

section 7

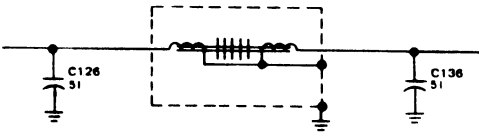
illustrations

SCHEMATIC CHANGES

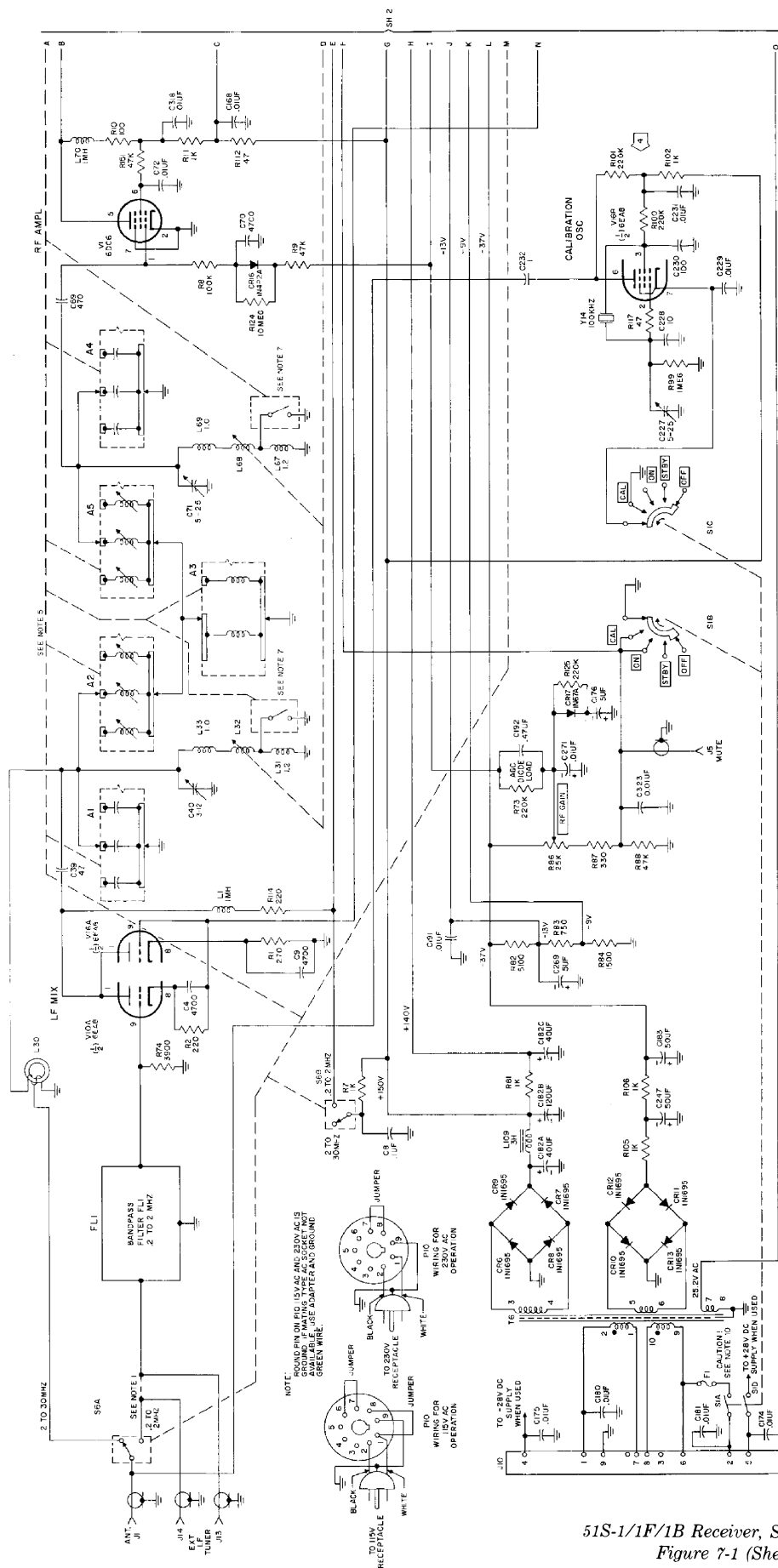
REVISION IDENTIFICATION	DESCRIPTION OF REVISION AND REASON FOR CHANGE	SERVICE BULLETIN	EFFECTIVITY
1	Reduced number of fixed capacitors at the input of filter circuits by removing C128, C129, C131, and C132 and relocating C130 and C127.		
2	Changed BFO circuit to reduce spurious 500-kHz signal by adding C337, relocating L114, relocating and changing value of C224, changing value of R156, adding capacitor 327, and replacing R99 with R155.		
3	Added additional selection of replacement transistor for Q1.		
4	Decreased value of R100 from 330 k Ω to 220 k Ω to ensure that calibration oscillator will oscillate.		
5	Changed LF crystal oscillator screen grid bypass capacitor C263 from 0.01 to 0.001 to prevent parasitic oscillations.		
6	Changed C224 from 470 to 390 to improve resonance.		
7	Added CR501, CR502, R502, R505, C510, C509, and L504 to reduce higher harmonic output of VFO and stabilize against voltage changes; changed C505 from 68 to 27 picofarads, and R503 from 56 to 33 k Ω ; and deleted C506.		

51S-1/1F/1B Receiver, Schematic Diagram
Figure 7-1 (Sheet A)

SCHEMATIC CHANGES

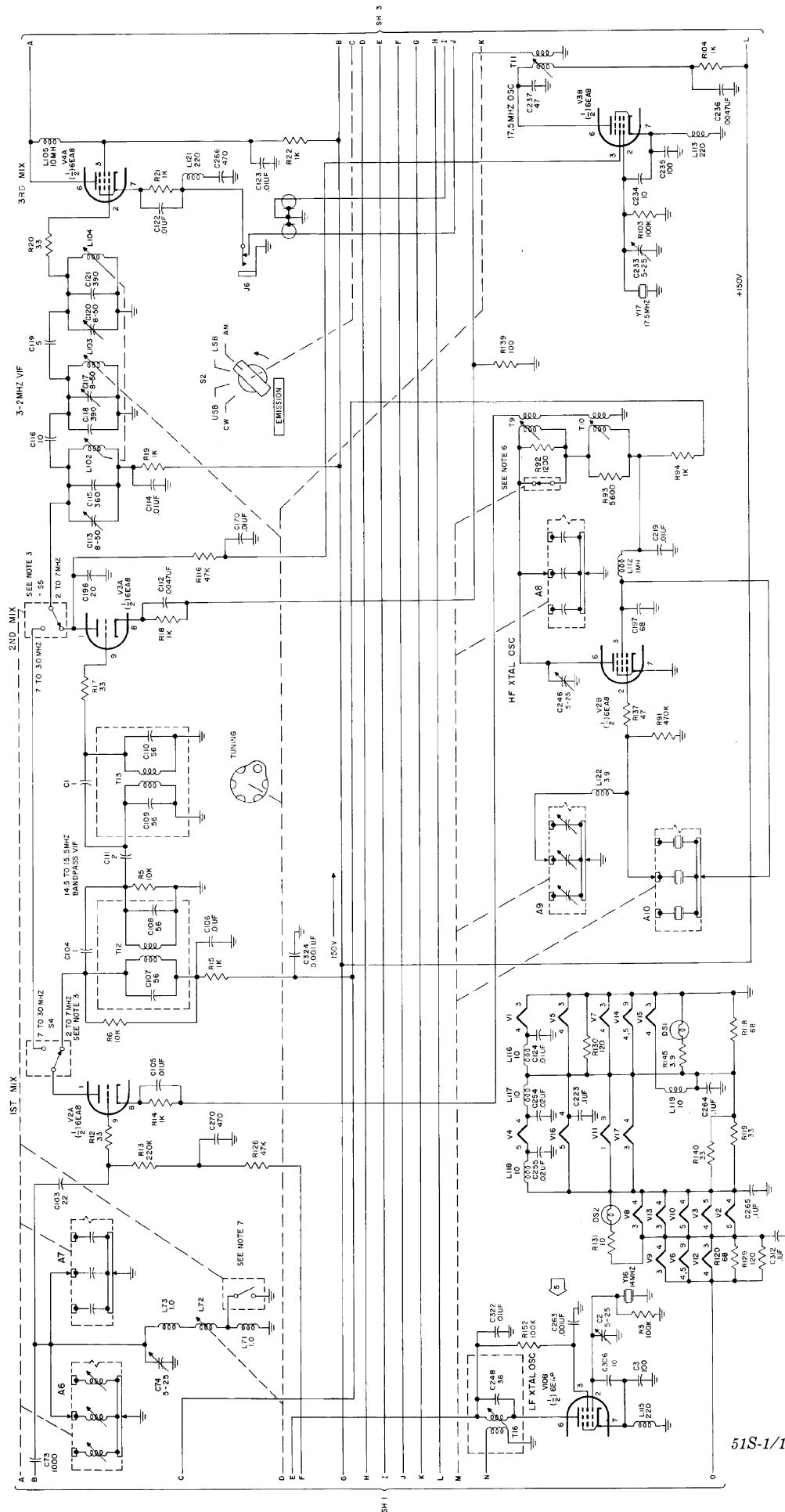
REVISION IDENTIFICATION	DESCRIPTION OF REVISION AND REASON FOR CHANGE	SERVICE BULLETIN	EFFECTIVITY
8	Optional 0.3-kHz CW filter may be used.		
9	T14, T15, and C134 may be optionally replaced with 6-kHz mechanical filter; schematic representation is as follows: 		
10	FL2 and FL3 may be optionally replaced with 3.1-kHz mechanical filters.		

51S-1/1F/1B Receiver, Schematic Diagram
 Figure 7-1 (Sheet B)

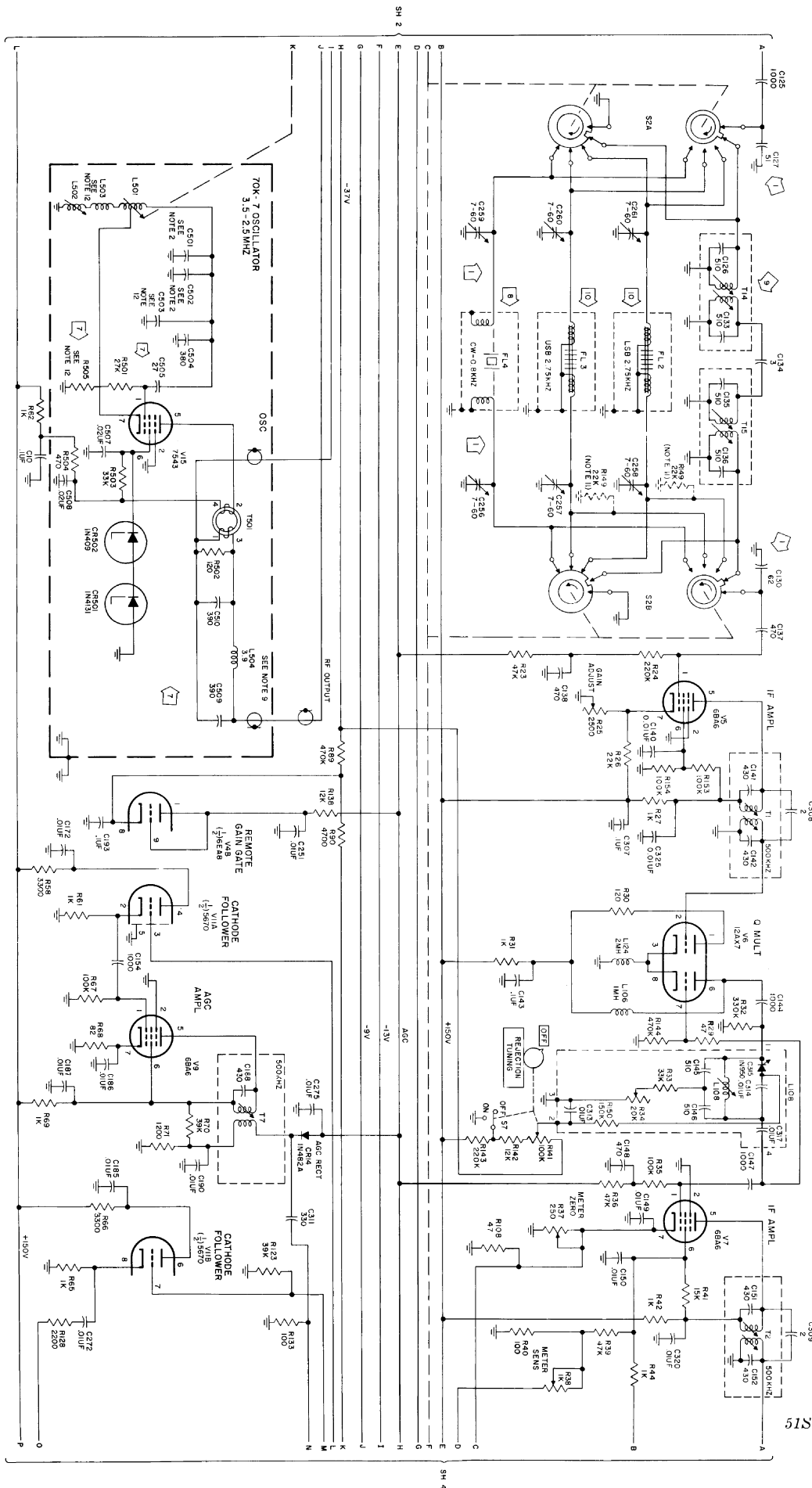


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51S-1/1F/1B Receiver, Schematic Diagram
Figure 7-1 (Sheet 1 of 4)

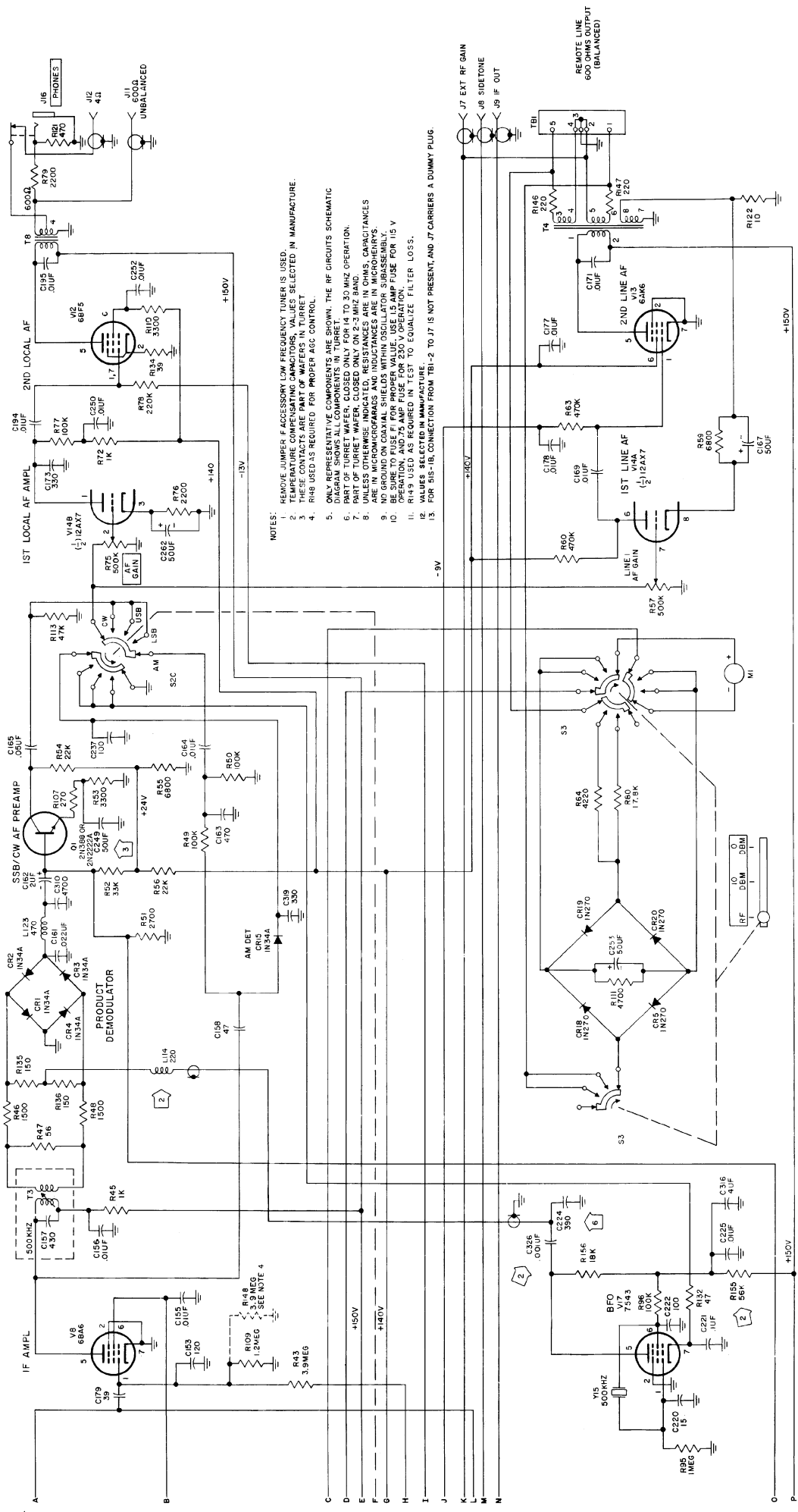


51S-1/1F/1B Receiver, Schematic Diagram
Figure 7-1 (Sheet 2)



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51S-1/1F/1B Receiver, Schematic Diagram
Figure 7-1 (Sheet 3)



51S-1/1B Receiver, Schematic Diagram
Figure 7-1 (Sheet 4)

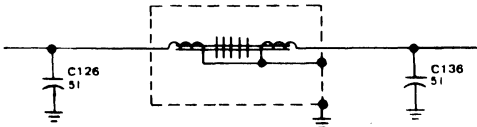
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SCHEMATIC CHANGES

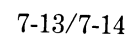
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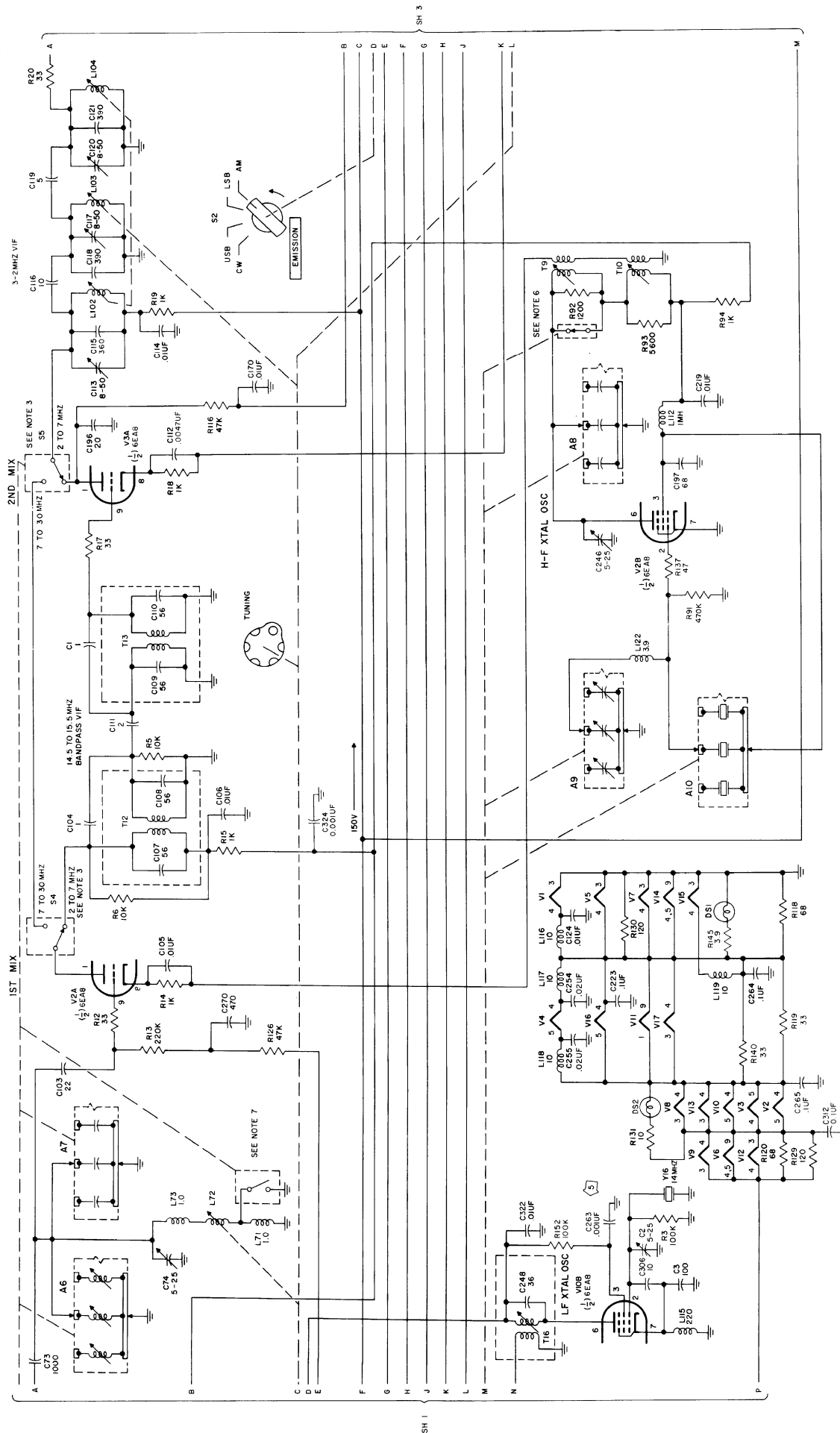
51S-1A/1AF Receiver, Schematic Diagram
Figure 7-2 (Sheet A)

SCHEMATIC CHANGES

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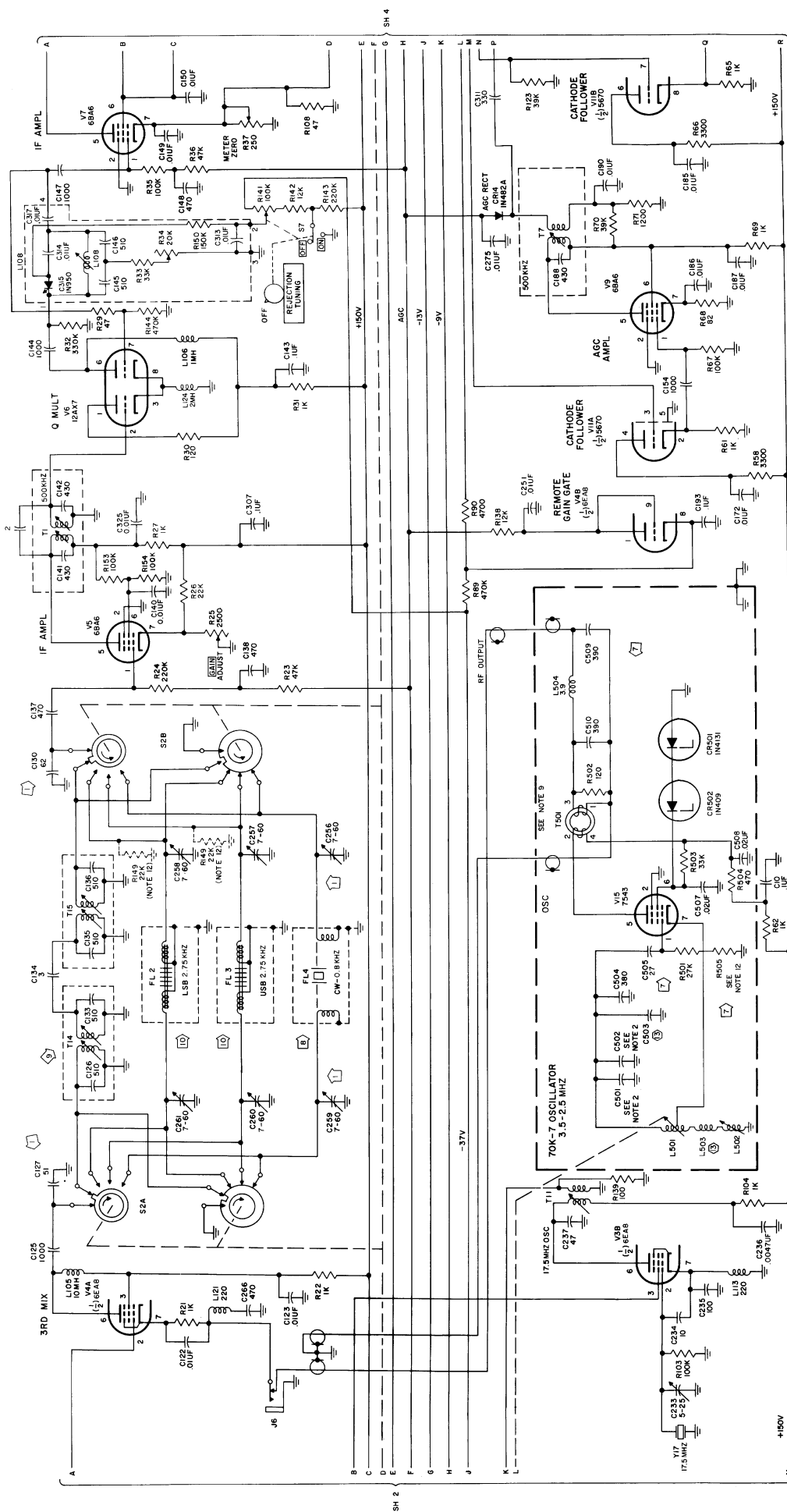
51S-1A/1AF Receiver, Schematic Diagram
Figure 7-2 (Sheet B)





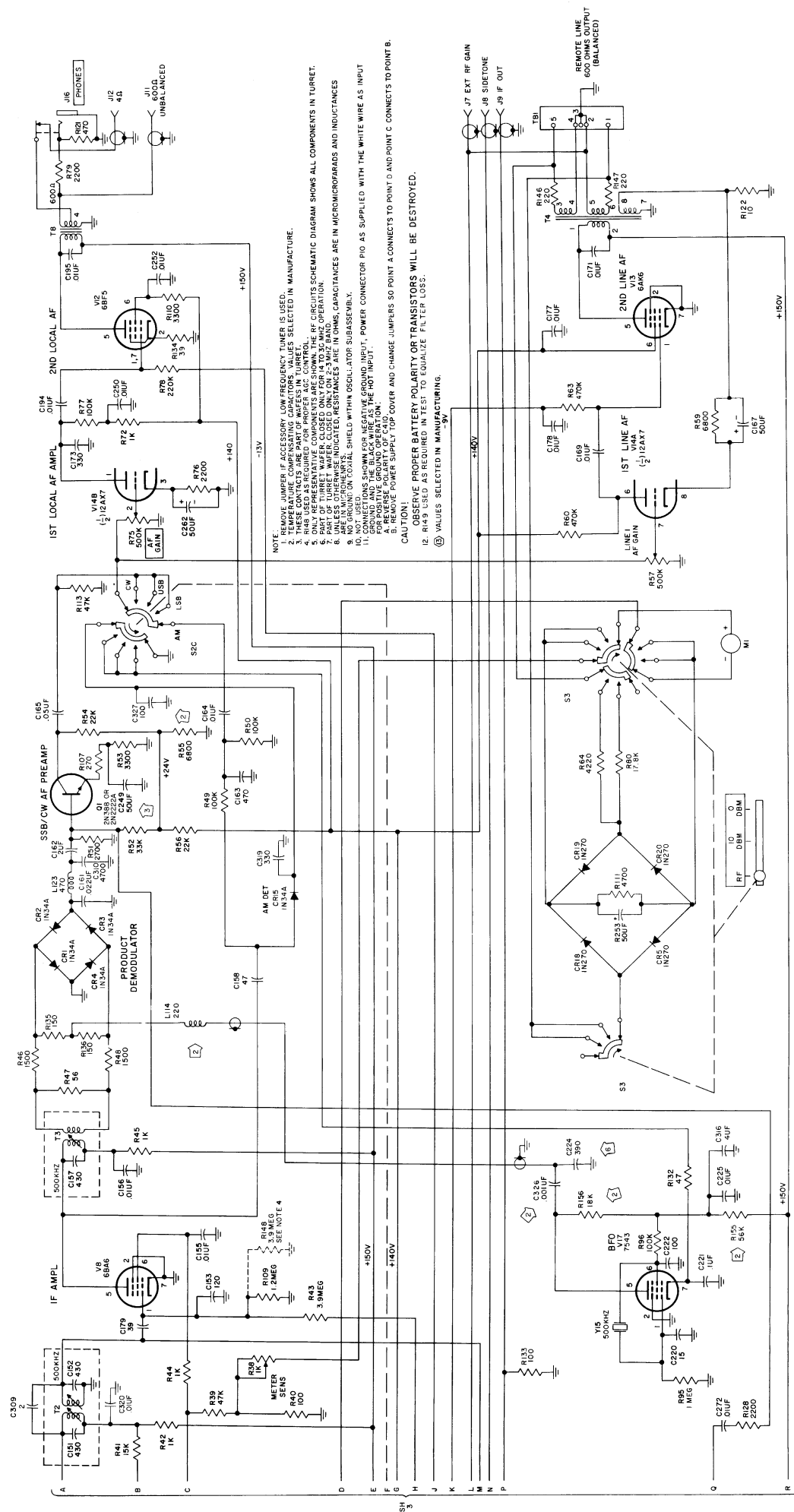
51S-1A/1AF Receiver, Schematic Diagram
Figure 7-2 (Sheet 2)

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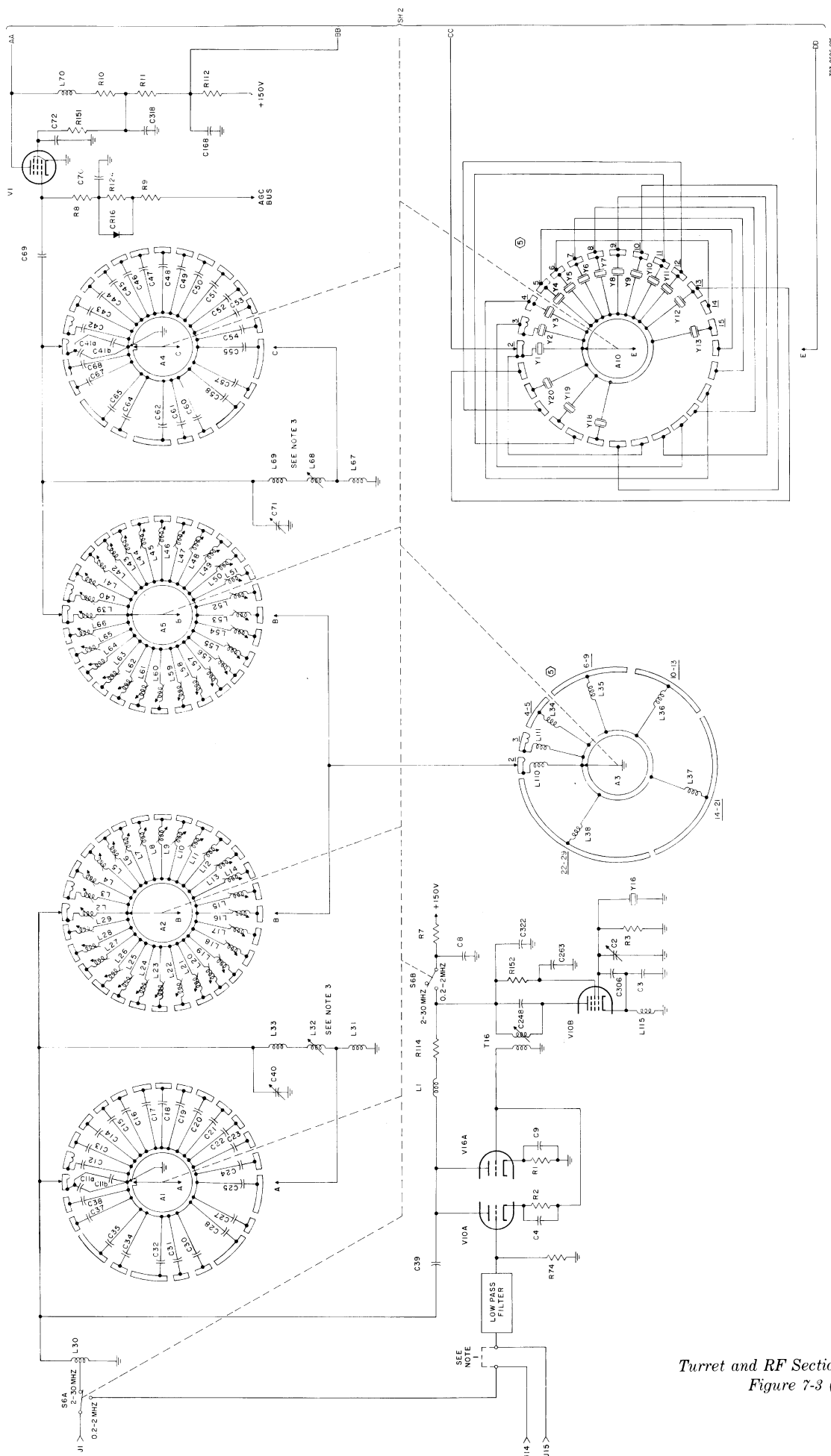
51S-1A/1AF Receiver, Schematic Diagram
Figure 7-2 (Sheet 3)

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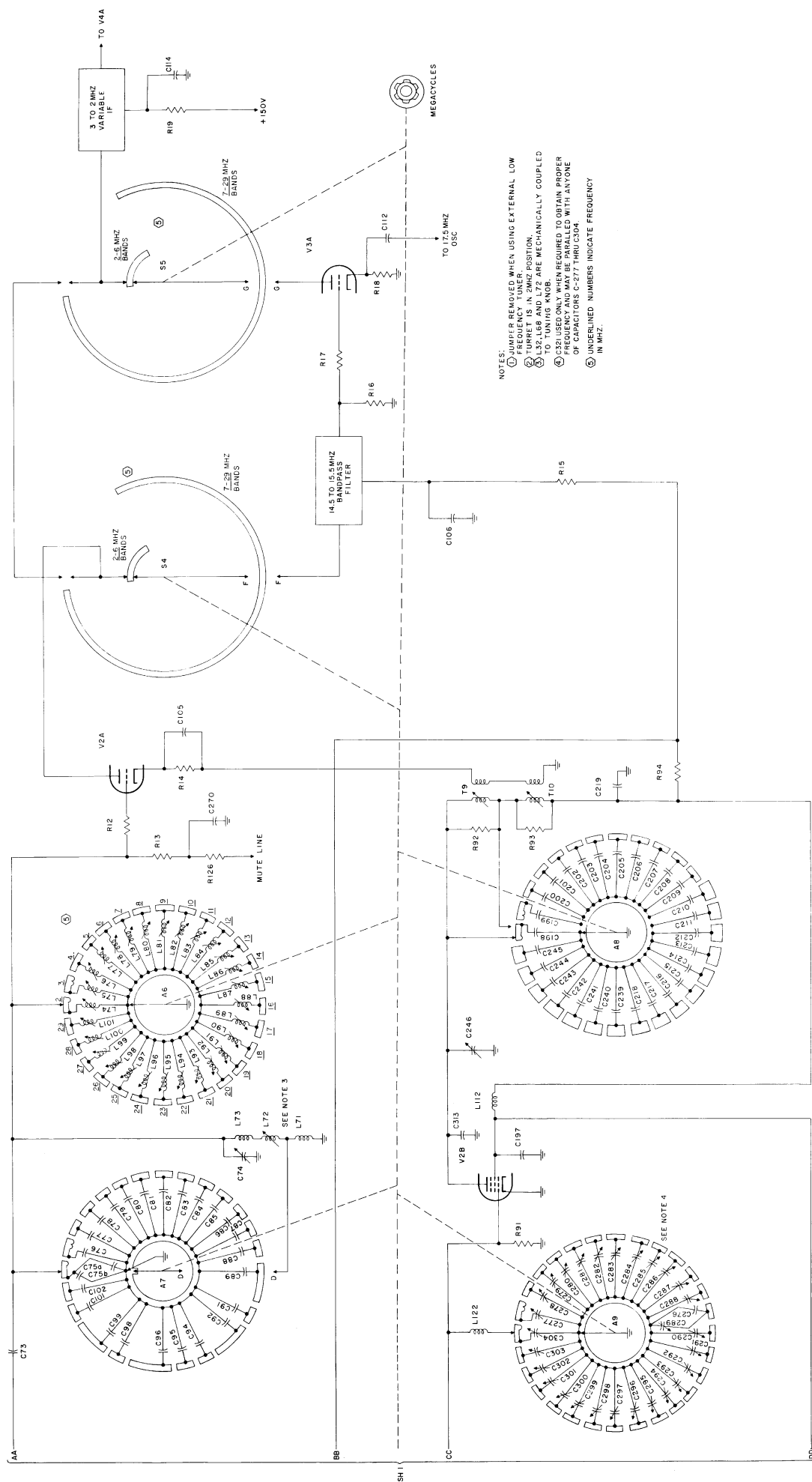


51S-1A/1AF Receiver, Schematic Diagram
Figure 7-2 (Sheet 4)

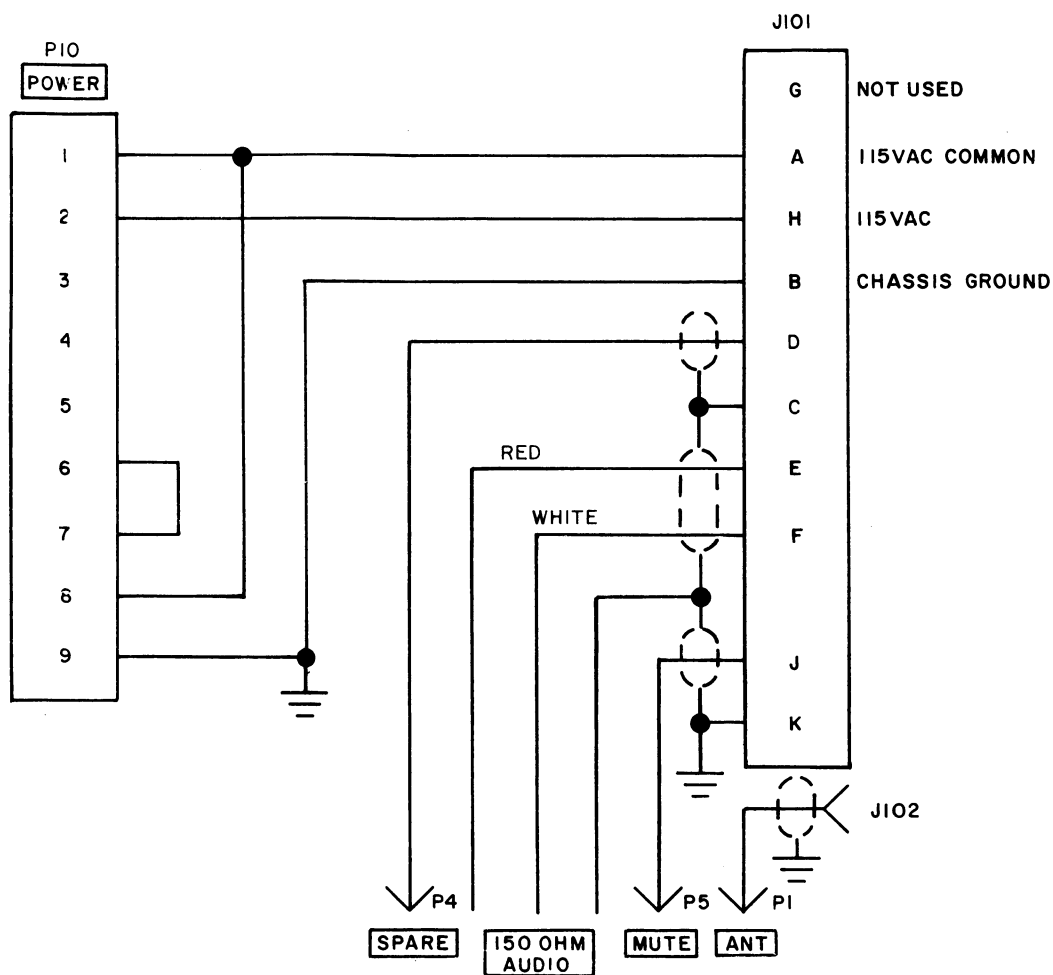
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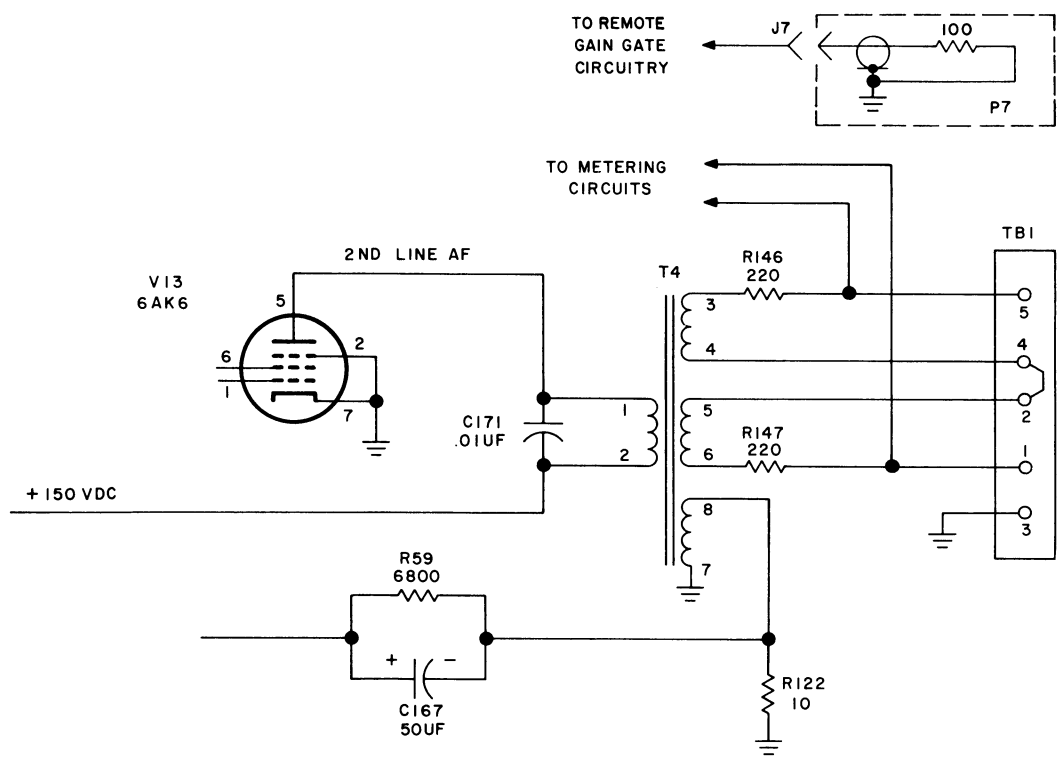
Turret and RF Section, Schematic Diagram
Figure 7-3 (Sheet 1 of 2)



Current and RF Section, Schematic Diagram
Figure 7-3 (Sheet 2)



51S-1B Junction Box, Schematic Diagram
Figure 7-4



51S-1B Output Circuit, Partial Schematic Diagram
Figure 7-5



Rockwell
International