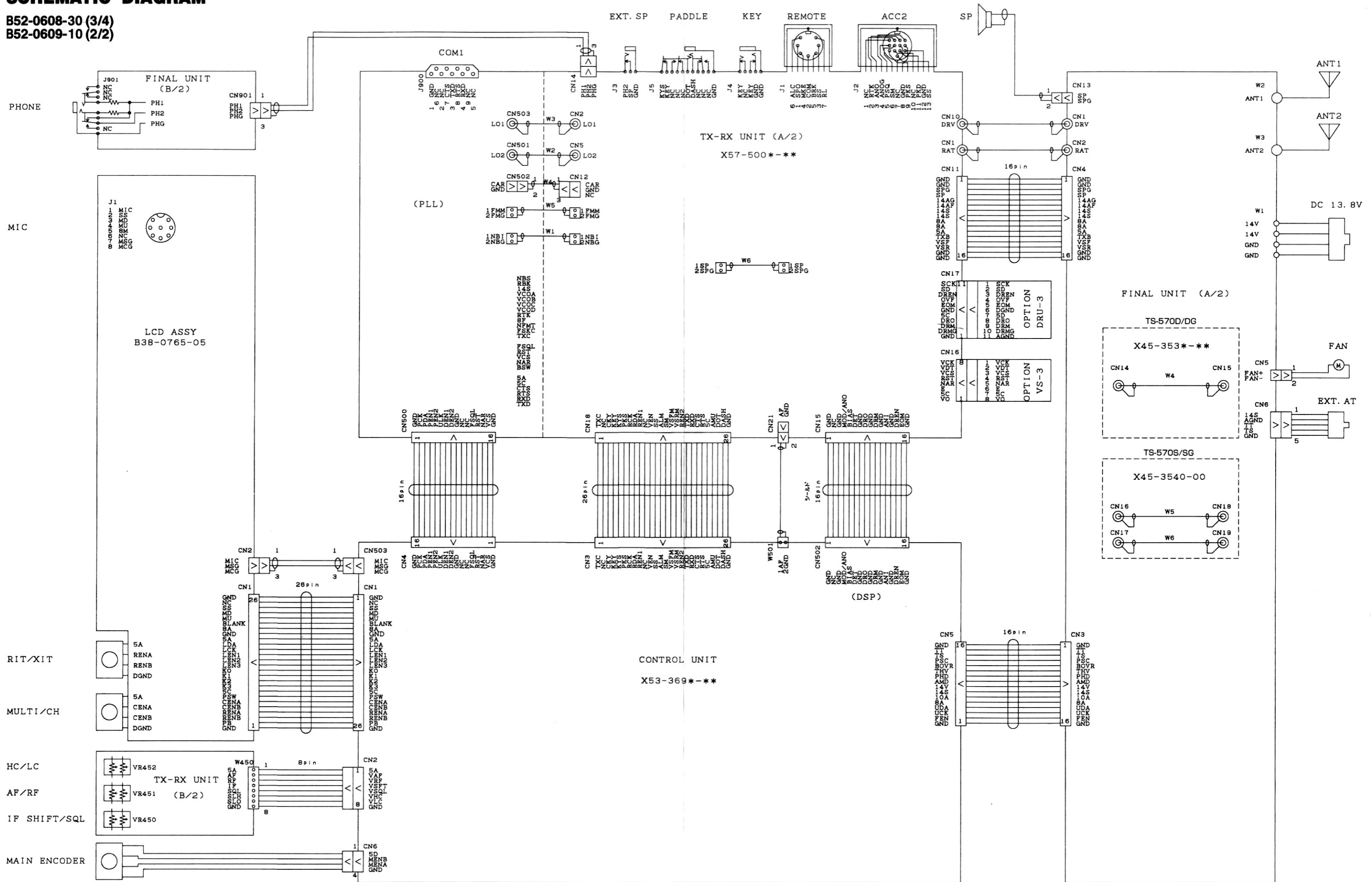


# SCHEMATIC DIAGRAM

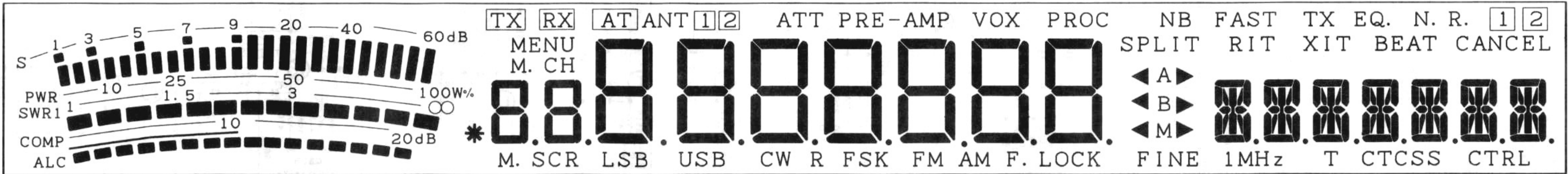
B52-0608-30 (3/4)  
B52-0609-10 (2/2)



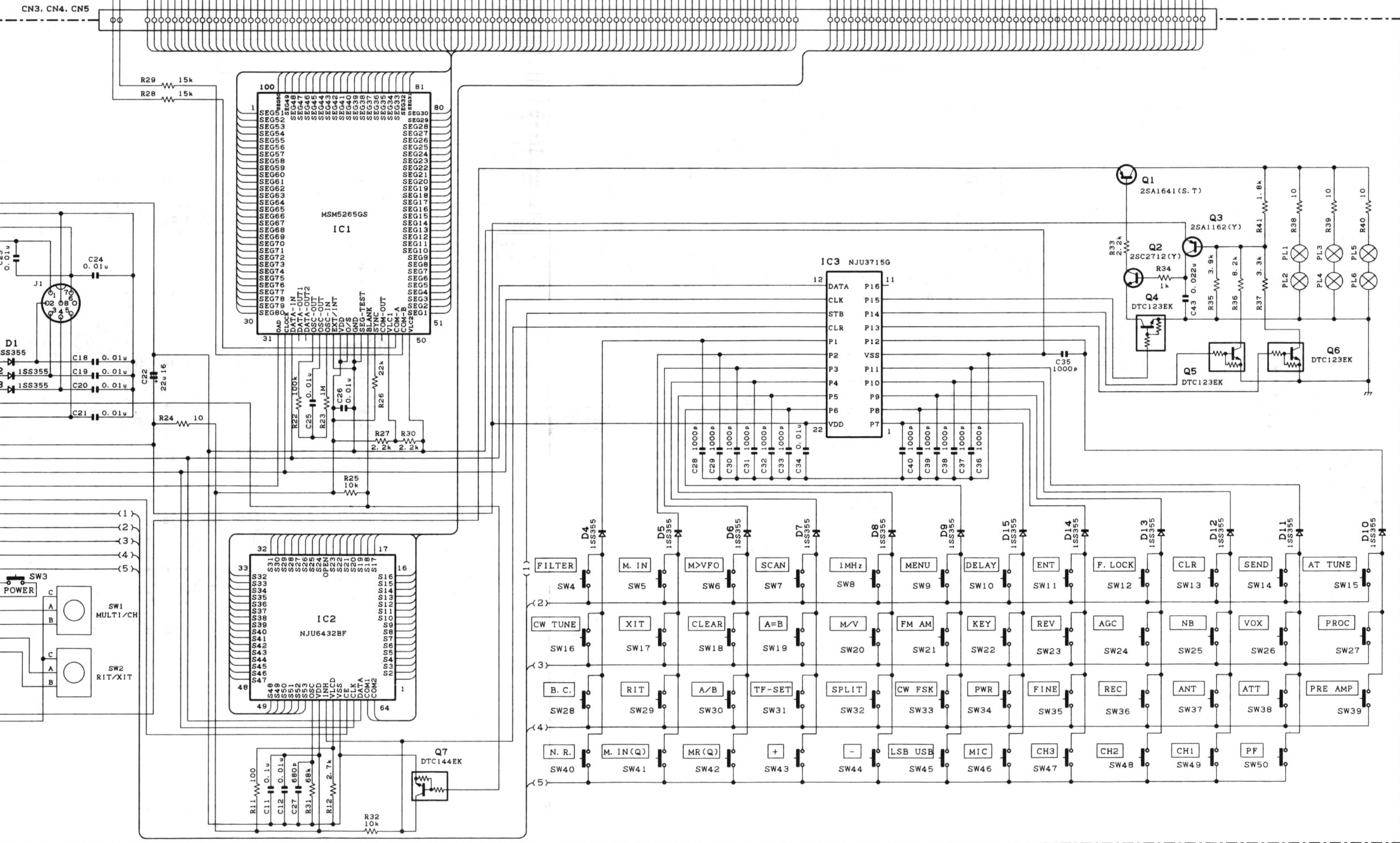
TS-570D/S/DG/SG

**Note:** Circuits are subject to change without notice due to advancements in technology.

LCD1  
(B38-0779-08)



LCD ASSY  
(B38-0765-05)

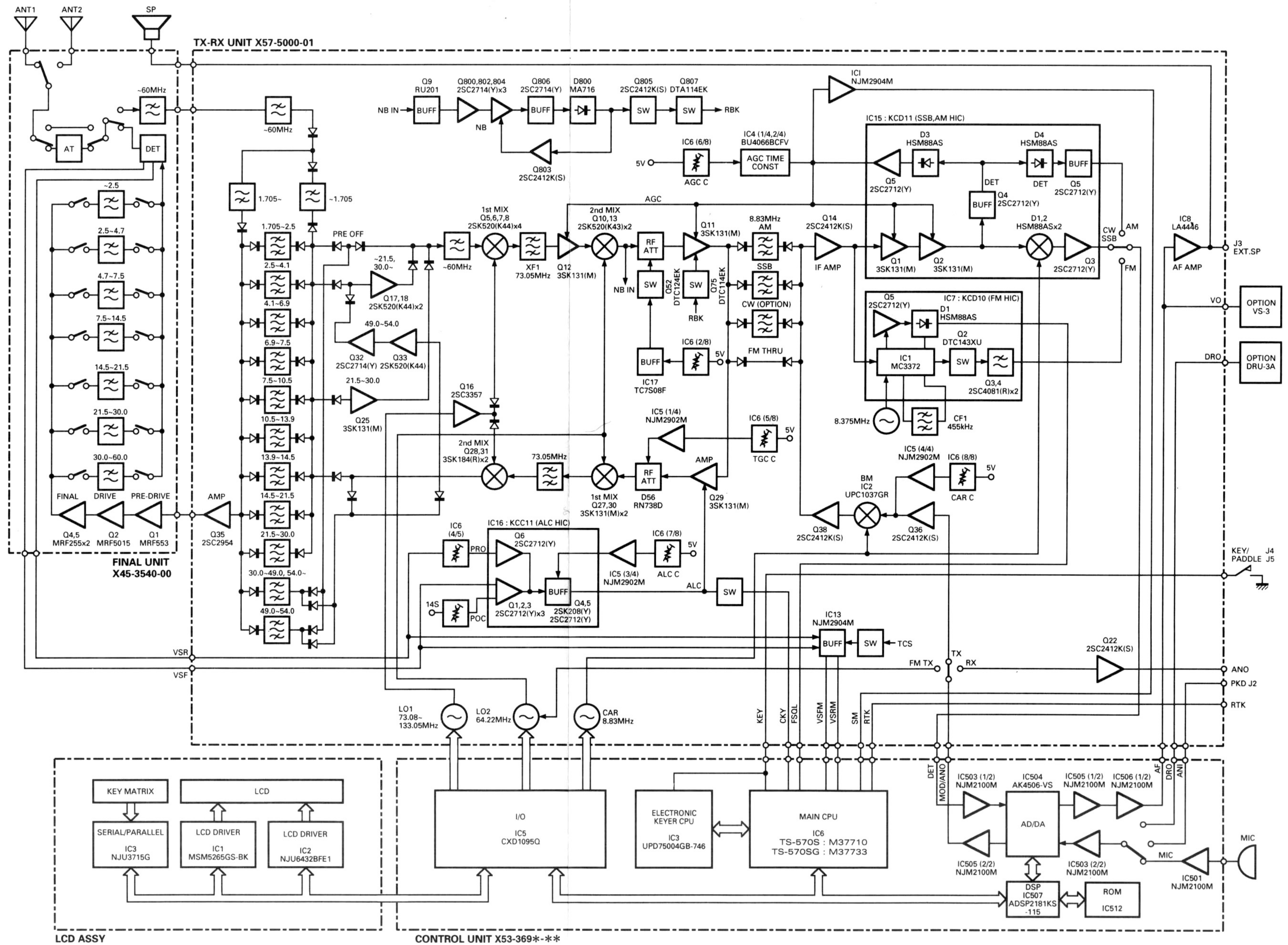


TS-570D/S/DG/SG LCD

Note: Circuits are subject to change without notice due to advancements in technology.

# BLOCK DIAGRAM

B52-0608-30 (4/4)

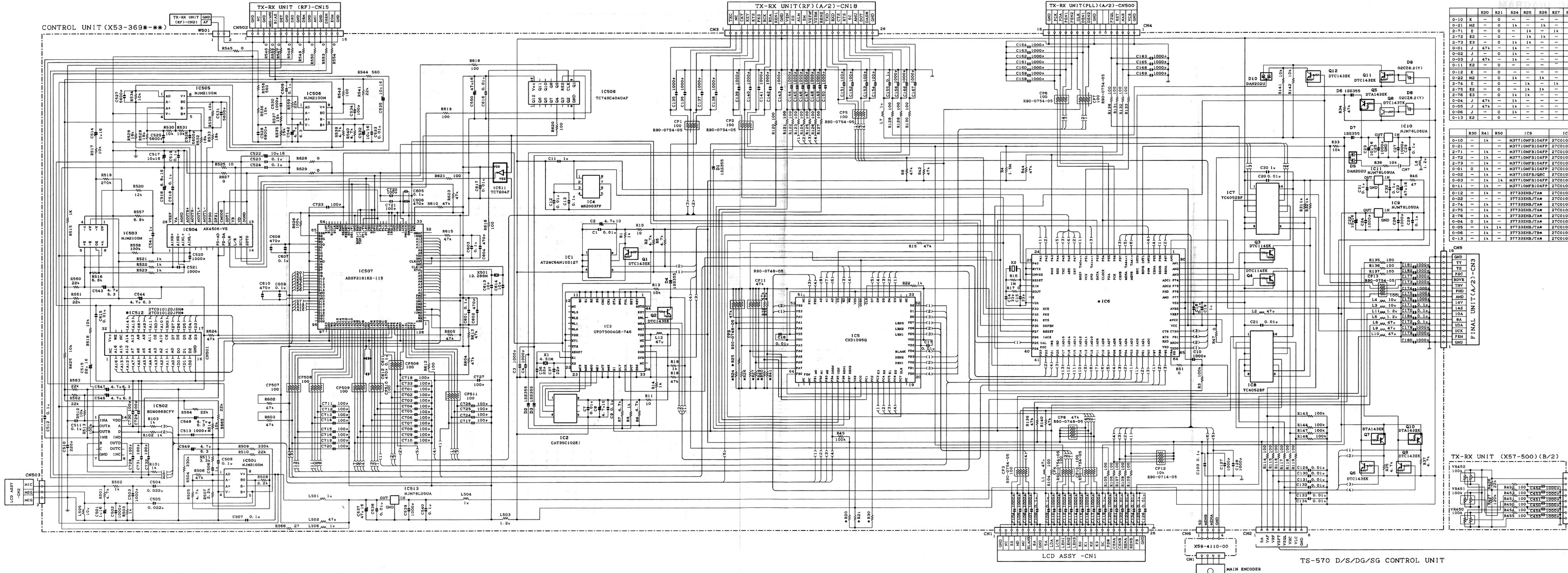


TS-570S/SG

**Note:** Circuits are subject to change without notice due to advancements in technology.

# SCHEMATIC DIAGRAM

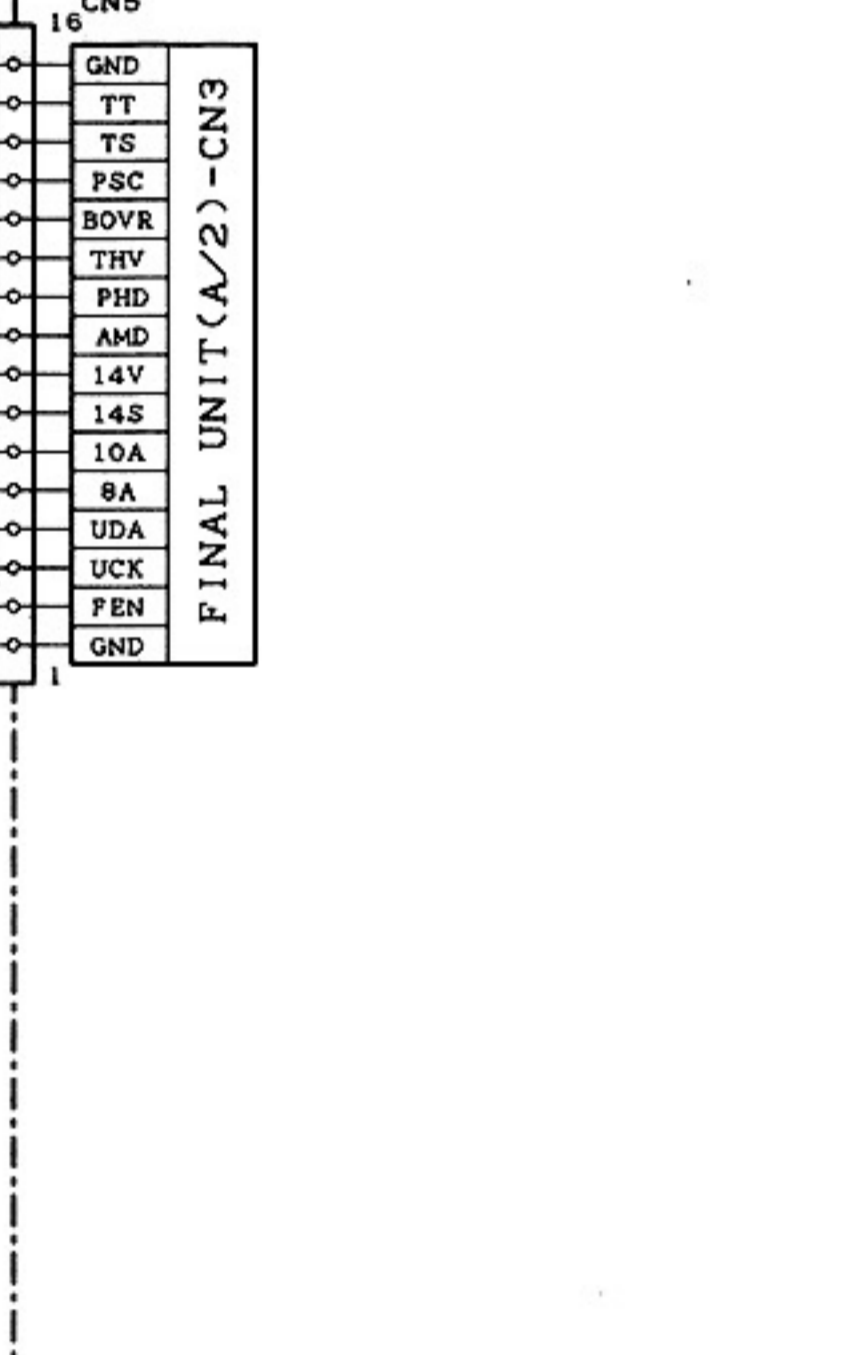
B52-0608-30 (2/4)



	R20	R21	R24	R25	R26	R27	R29
0-10	X	0	0	0	0	0	0
0-21	M2	0	1k	0	1k	0	0
2-71	E	0	0	1k	0	1k	0
2-72	E2	0	0	1k	1k	0	0
2-73	E3	0	0	1k	1k	0	0
0-01	J	47k	0	1k	0	0	1k
0-02	J	0	0	1k	0	0	1k
0-03	J	47k	0	1k	0	0	1k
0-11	K2	0	0	0	0	0	1k
0-12	K	0	0	0	0	0	1k
0-22	M2	0	0	1k	0	1k	0
2-74	E	0	0	1k	0	1k	0
2-75	E2	0	0	1k	1k	0	0
2-76	E3	0	0	1k	1k	0	0
0-04	J	47k	0	1k	0	0	1k
0-05	J	47k	0	1k	0	0	1k
0-06	J	0	0	1k	0	0	1k
0-13	K2	0	0	0	0	0	1k

	R30	R41	R50	IC6	IC512
0-10	0	1k	0	M37710MPB104FF	27C01012D
0-21	0	1k	0	M37710MPB104FF	27C01012D
2-72	0	1k	0	M37710MPB104FF	27C01012D
2-73	0	1k	0	M37710MPB104FF	27C01012D
0-01	0	1k	0	M37710MPB104FF	27C01012D
0-02	0	1k	0	M37710MPB104FF	27C01012D
0-03	0	1k	0	M37710MPB104FF	27C01012D
0-11	0	1k	0	M37710MPB104FF	27C01012D
0-12	0	1k	0	M37710MPB104FF	27C01012D
0-22	0	1k	0	M37710MPB104FF	27C01012D
2-74	0	1k	0	M37710MPB104FF	27C01012D
2-75	0	1k	0	M37710MPB104FF	27C01012D
2-76	0	1k	0	M37710MPB104FF	27C01012D
0-04	0	1k	0	M37710MPB104FF	27C01012D
0-05	0	1k	0	M37710MPB104FF	27C01012D
0-06	0	1k	0	M37710MPB104FF	27C01012D
0-13	0	1k	0	M37710MPB104FF	27C01012D

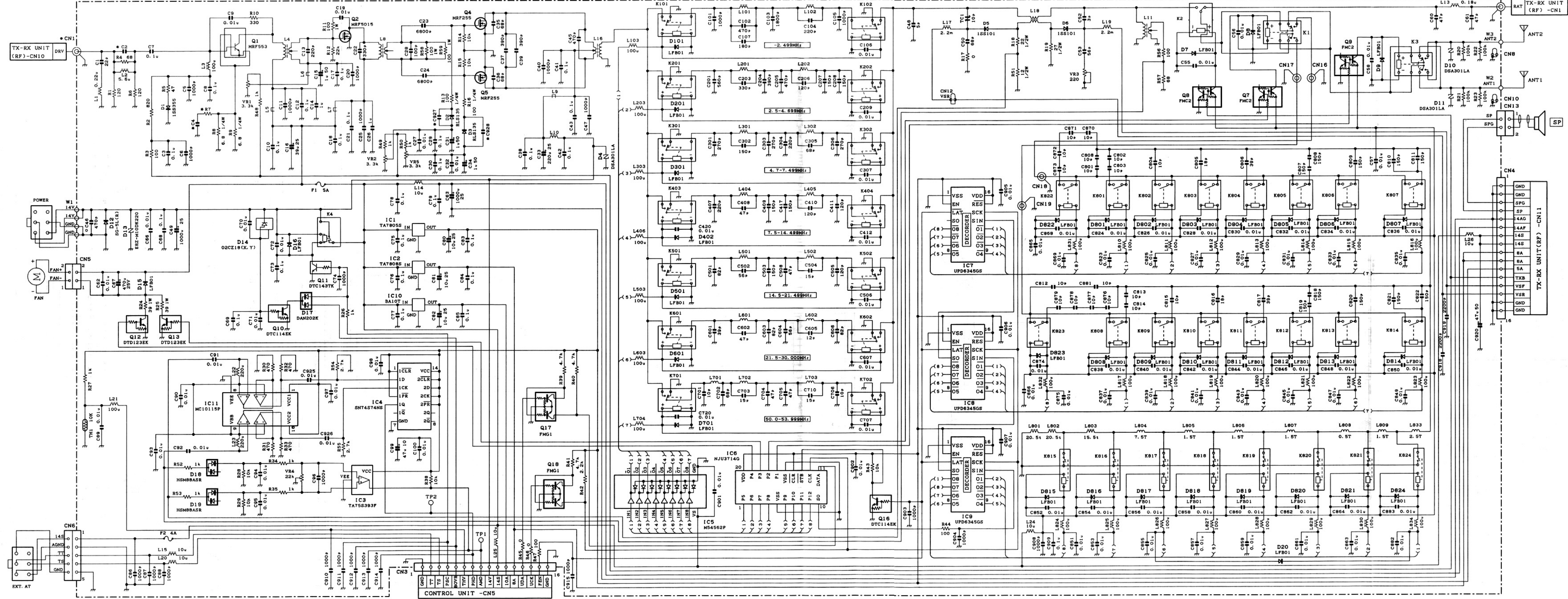


TS-570 D/S/DG/SG CONTROL UNIT

Note: Circuits are subject to change without notice due to advancements

# SCHEMATIC DIAGRAM

FINAL UNIT (X45-354\*\*\*) (A/2)



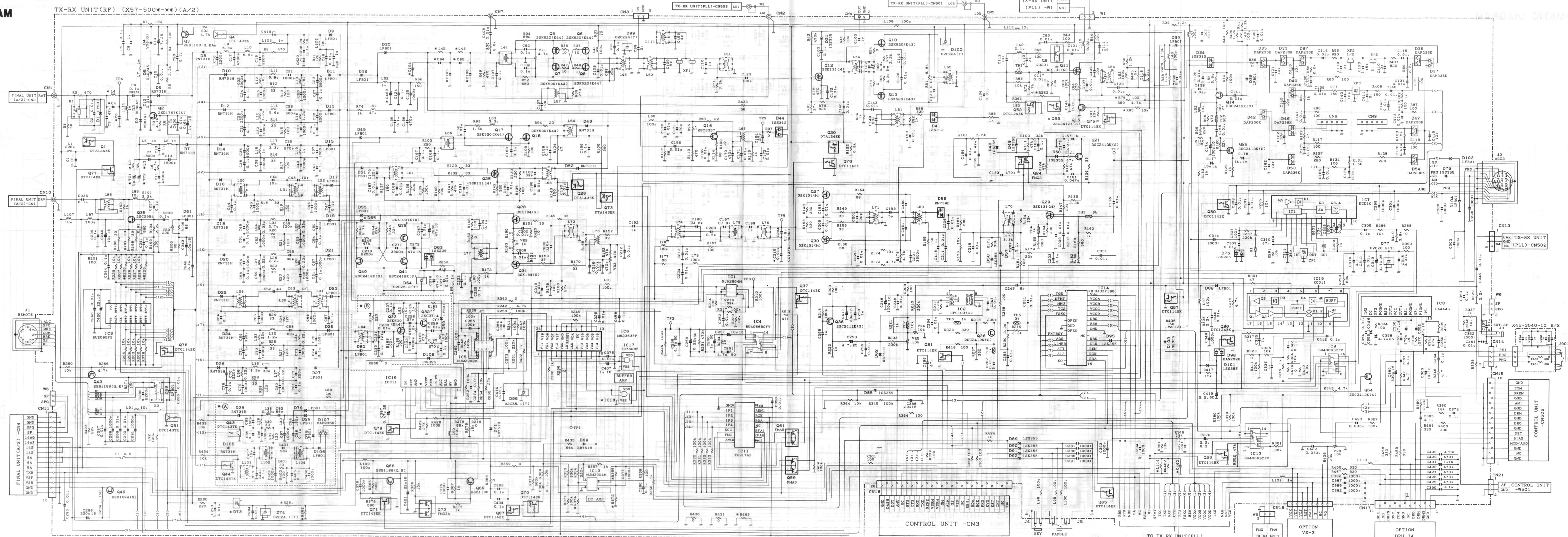
	C2	C4	C927	C928	R7
0-00	TS-570S/M/SG/MG	220p	820p	-	6.8
0-01	TS-570V/VG	100p	-	0.01u	0.01u

Note: Circuits are subject to change without notice due to advancements in technology.

TS-570S/SG FINAL UNIT

**SCHEMATIC DIAGRAM**

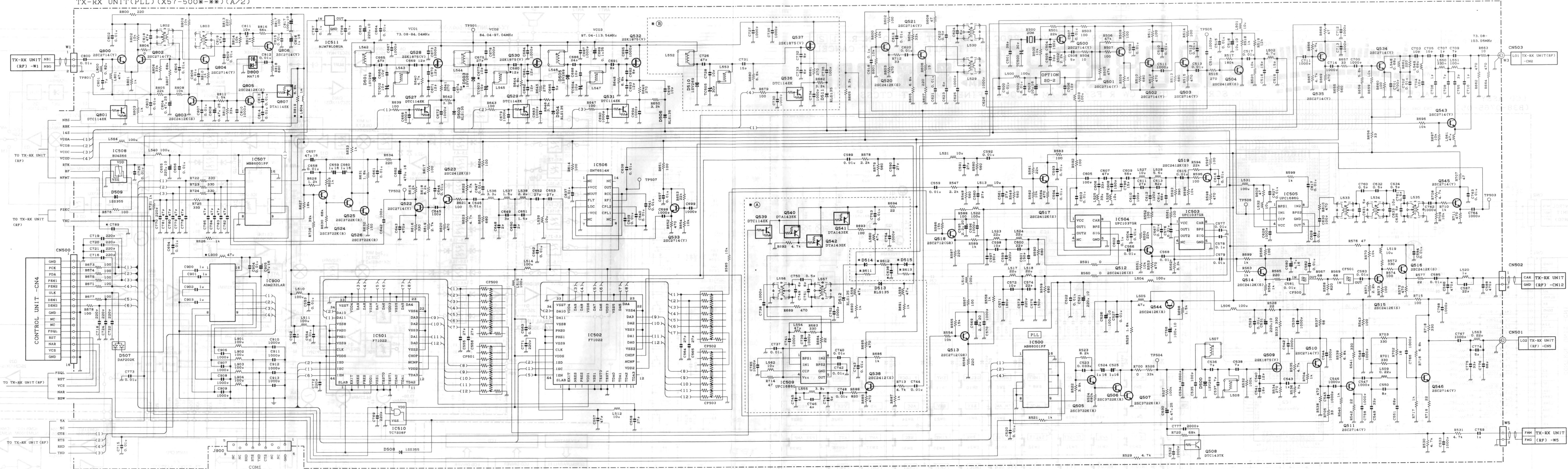
B52-0608-30 (1/4)  
B52-0609-10 (1/2)



Q10	STOD/DG	K. K2	X	B	L2	L3	TC1	TC2	C4	C5	C9	C10	C14	C18	C16	C40	C41	C42	C43	D55	D73	IC18	Q53	L4	L42	L43	L14	R206	R225	R262	R281	R282	R355	R372	R374	R452	R463
2-71	STOD/DG	E. E2, E3	X	X	6.8	3.9	30	30	7	39	6	18	68	150	68	22							0.27	0.27	0.27	1	2.7	100	100	100	0	0	0	0	0	0	
0-01	STOD/SG	J. K.	X	X	4.7	15	39	100	82	33						0.01	4.7	25	LFB01	0.2C24.7(Y)	TC750BF	DT124EK	0.18	0.15	0.1	10	10	10	120	560	560	100	100	100	0	0	
0-02	STOD/VG	J	O	O	4.7	15	39	100	82	33						0.01	4.7	25	LFB01	0.2C24.7(Y)	TC750BF	DT124EK	0.18	0.15	0.1	10	10	10	120	1	220	120	27	27	0	0	
0-03	STOM/MS	J	O	O	4.7	15	39	100	82	33						0.01	4.7	25	LFB01	0.2C24.7(Y)	TC750BF	DT124EK	0.18	0.15	0.1	10	10	10	120	1	2	560	100	100	100	0	0

Note: Circuits are subject to change without notice due to advancements in technology.

TX-RX UNIT(PLL) (X57-500\*~\*\*) (A/2)



0-10.2-71	TS-570D/DG	⊙	⊙	⊙	⊙	⊙	⊙	⊙
0-01.0-02.0-03	TS-570S/SV/VG/M/NG	○	○	○	○	○	○	○
			220			RLS135	RLS135	10μ 0.01μ

TS-570D/S/DG/SG TX-RX UNIT(PLL)

Note:  
Circuits are subject to change without notice due to advancements in technology.