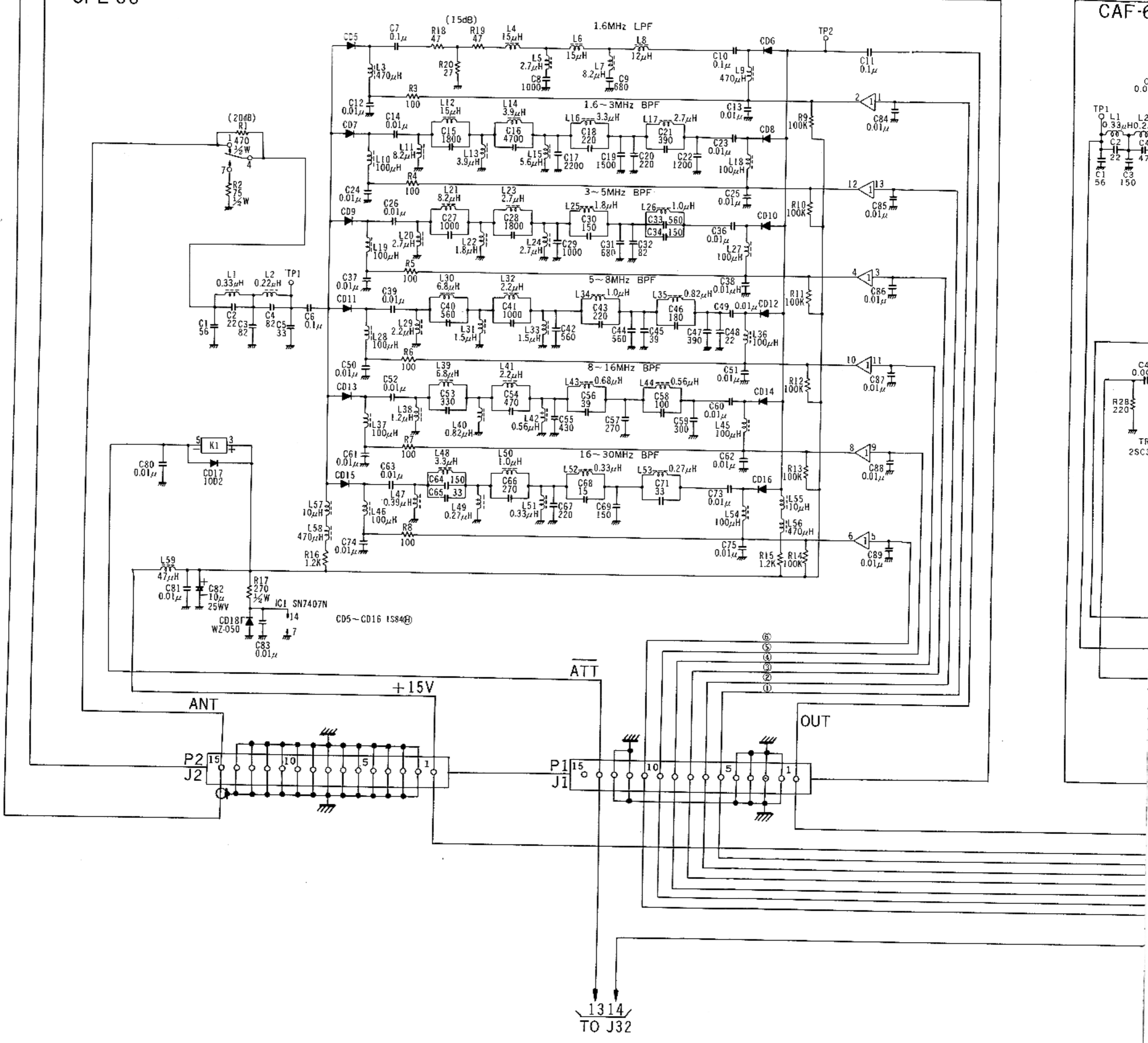


APPENDIX 1 MODEL NRD-505 ALL-WAVE RECEIVER FUNCTIONAL BLOCK DIAGRAM

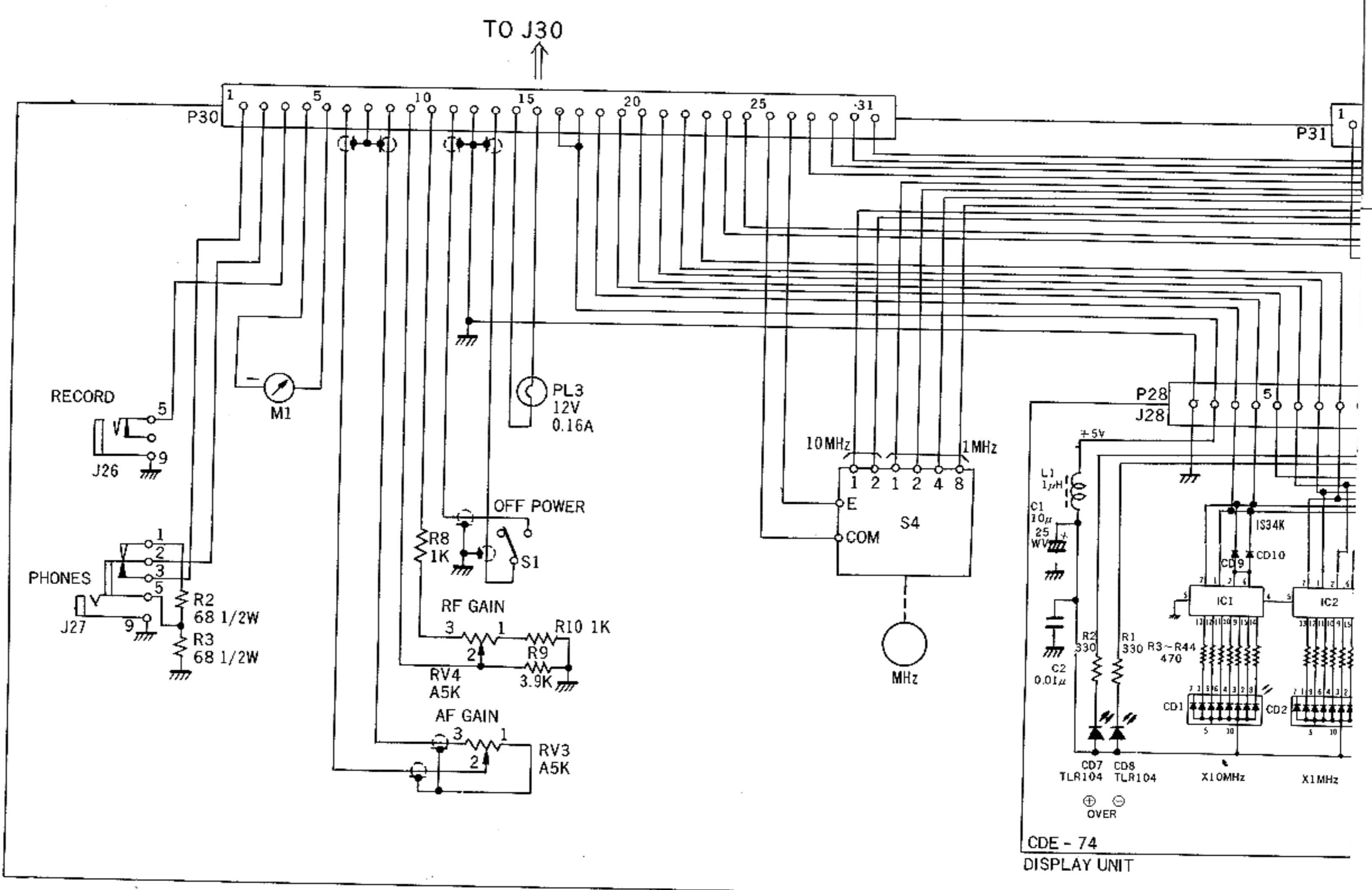
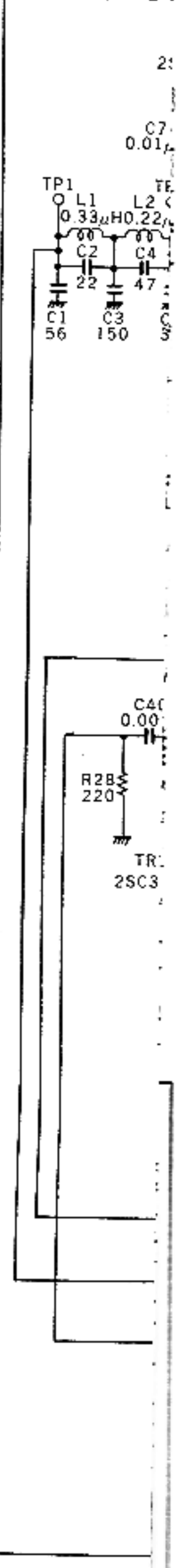
[...] OPTION

17 E  
J25

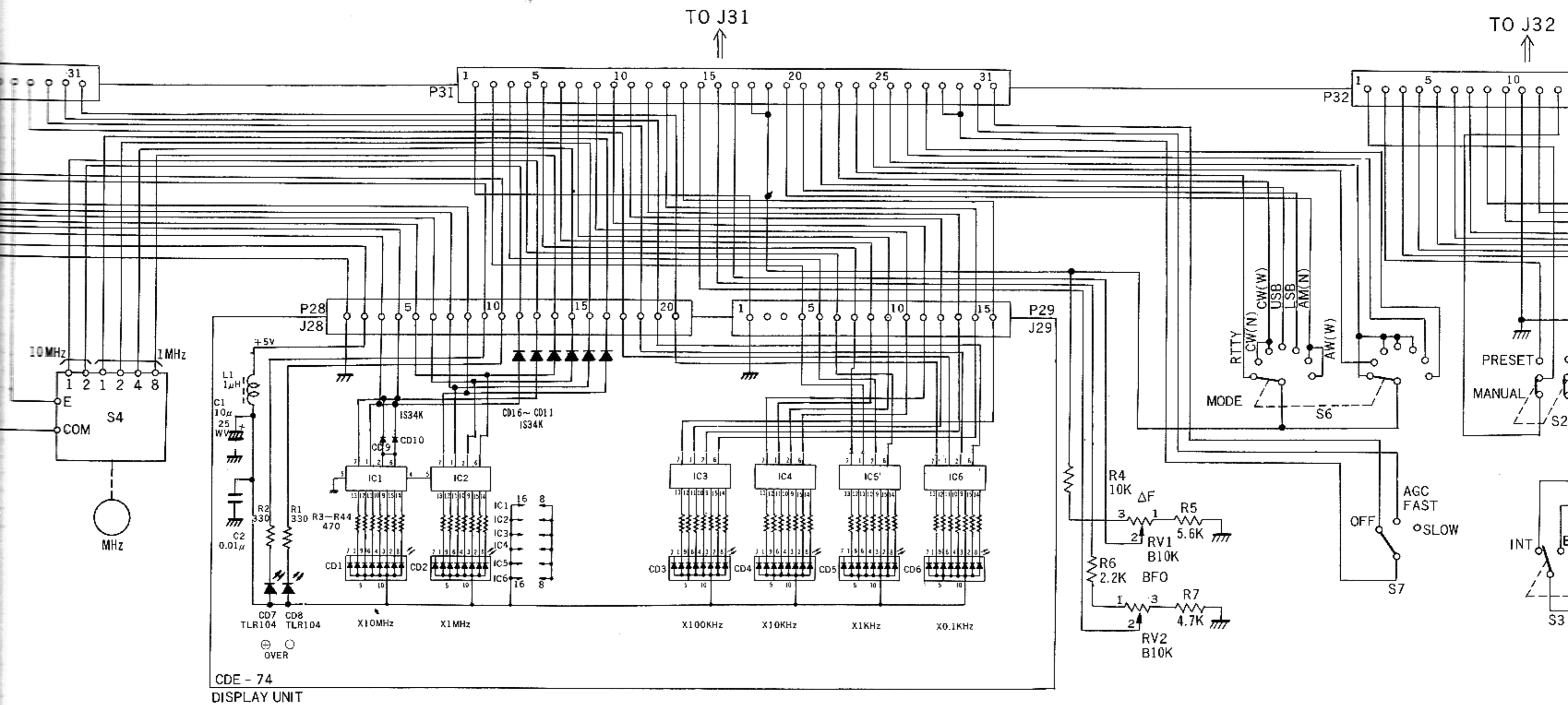
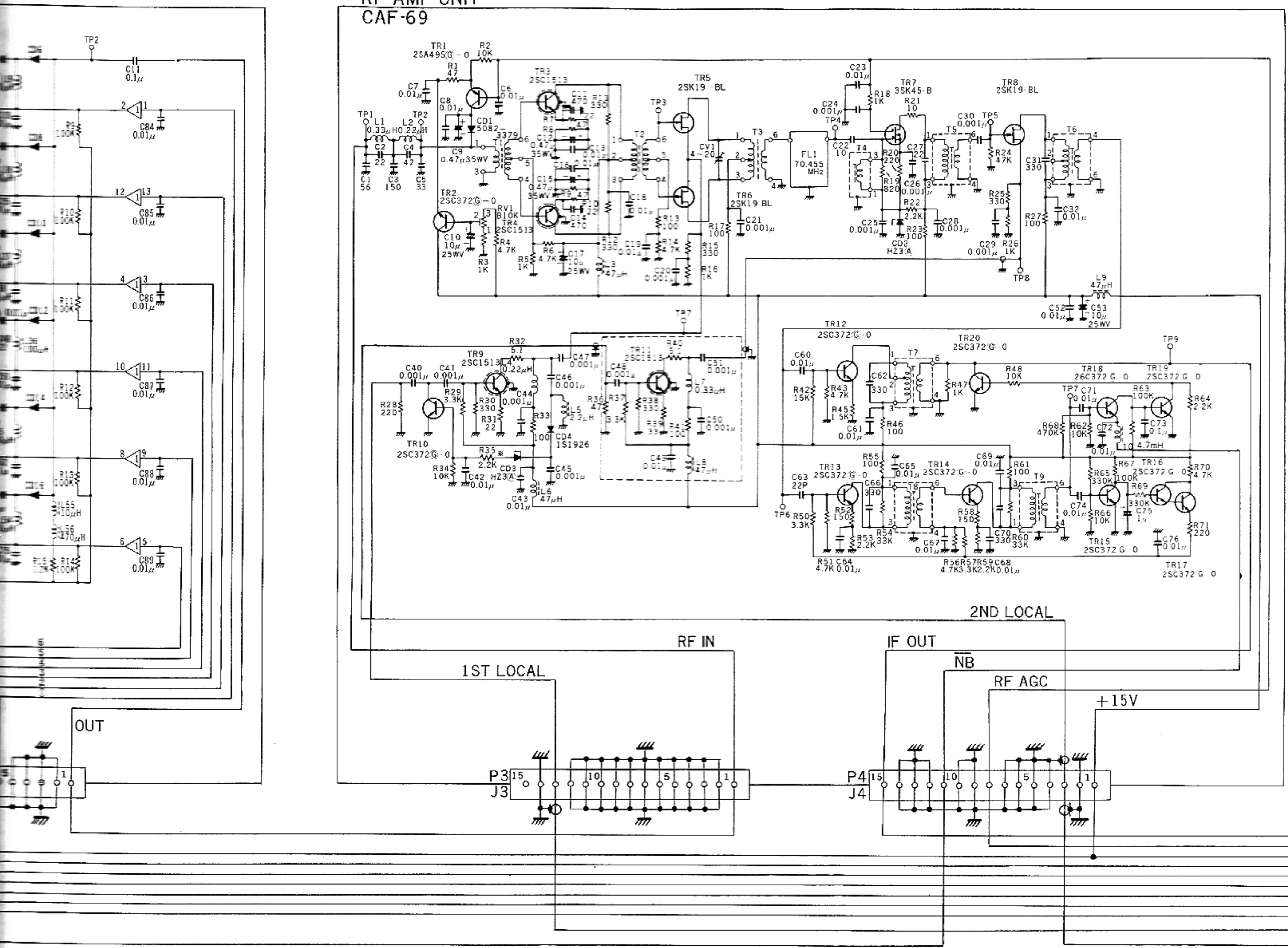
### RF INPUT FILTERS UNIT CFL-66



### RF AM CAF-66

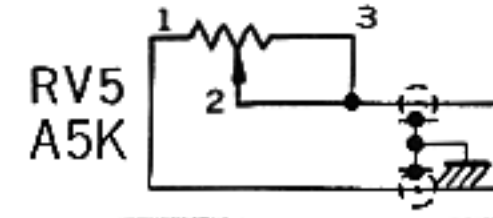
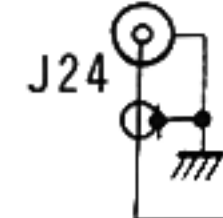


RF AMP UNIT  
CAF-69

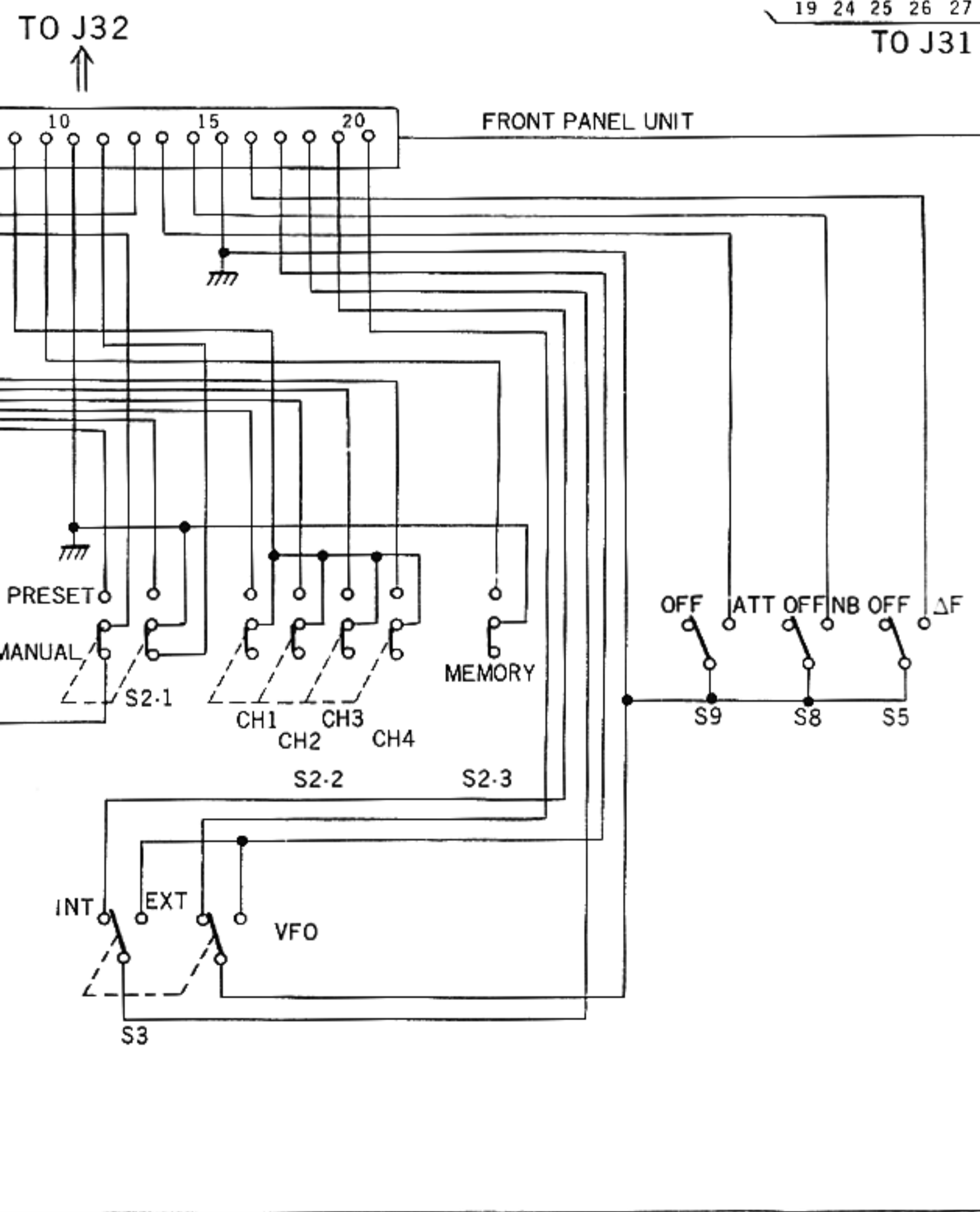
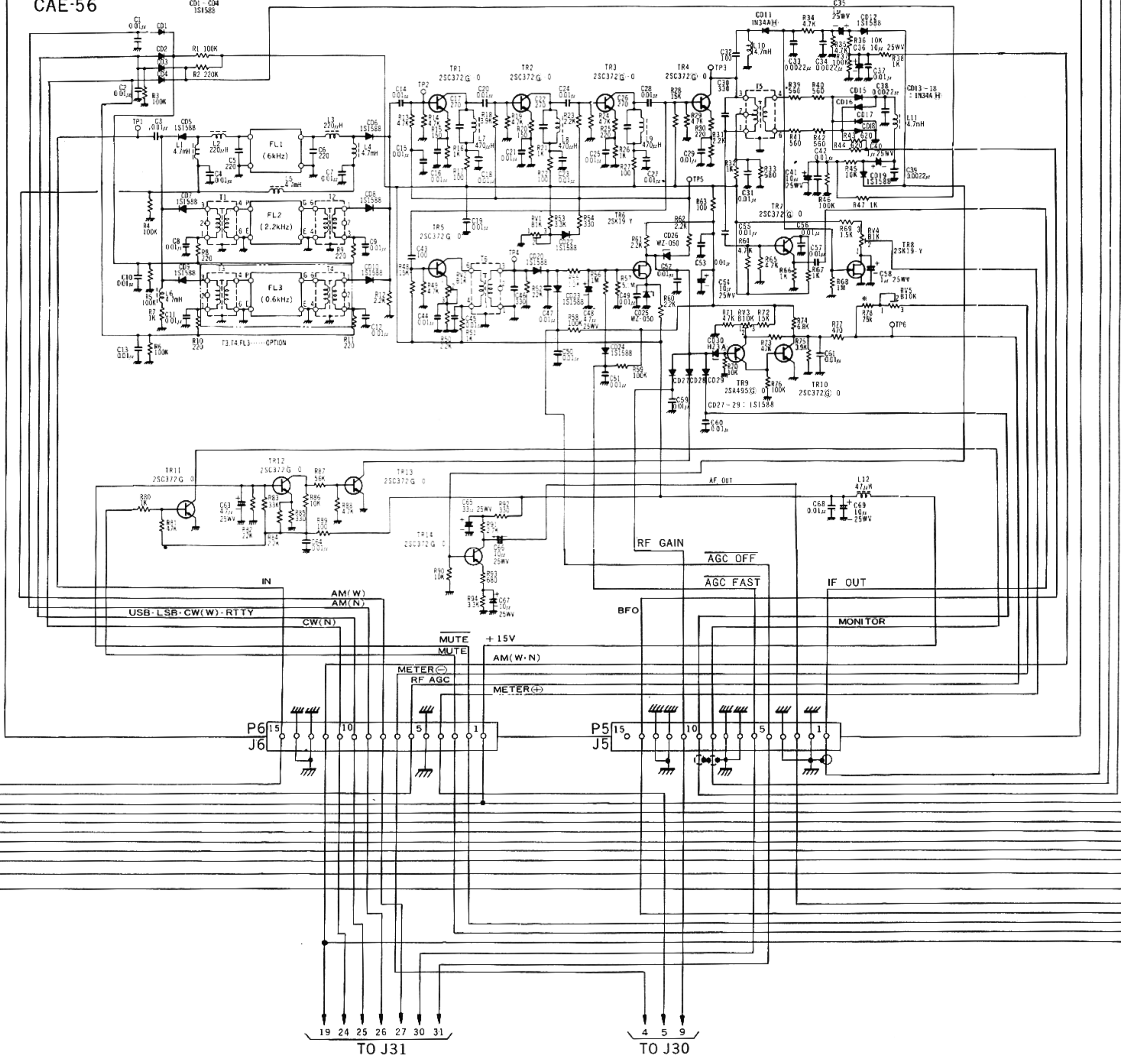


IF OUT

MONITOR



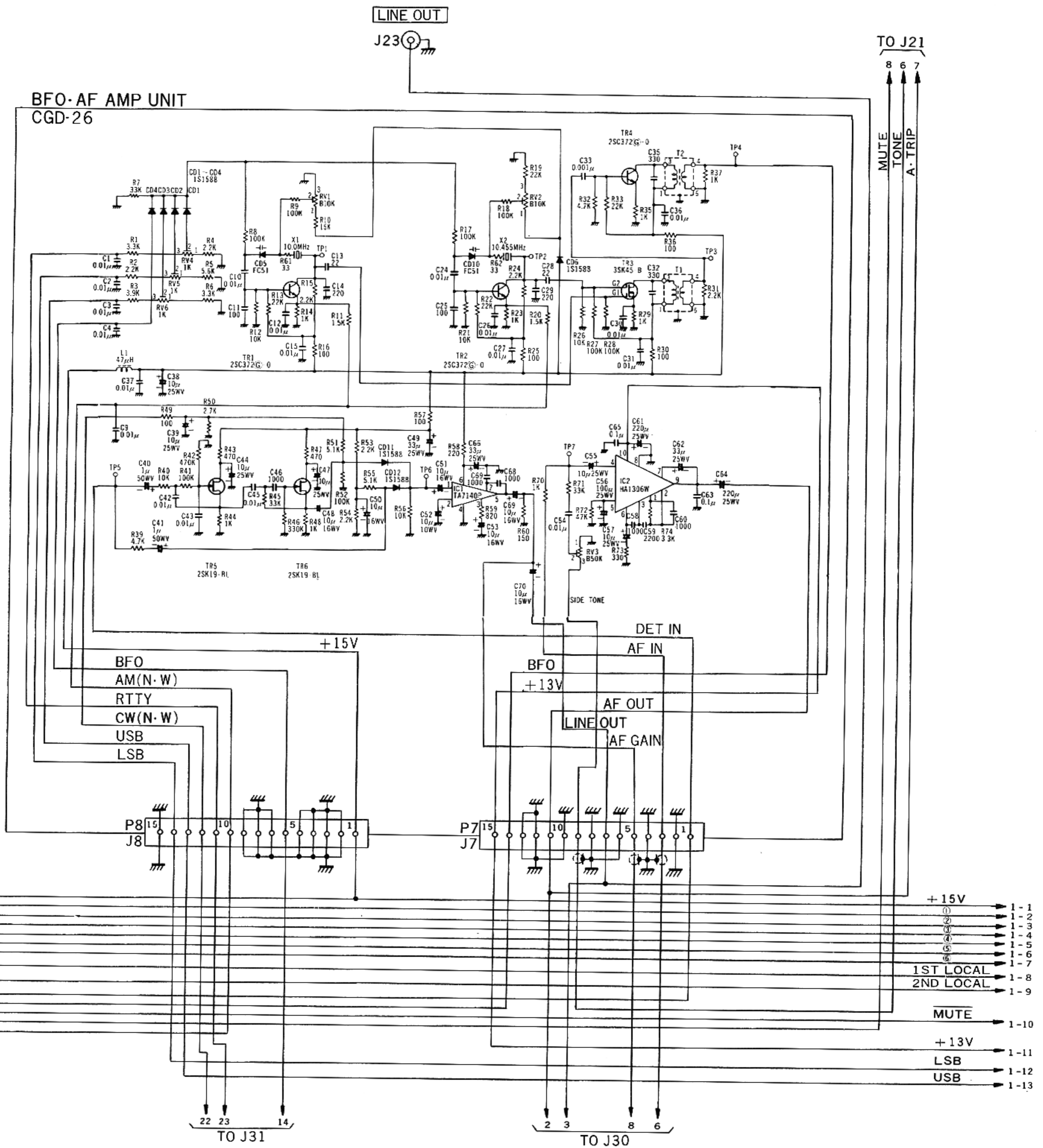
IF AMP UNIT  
CAE-56



- NOTES 1. Unless otherwise indicated resistances are in ohms  
 capacitances are in micro-micro farads.  
 2. Values selected in manufacture.

APPENDIX 2 RECEIVING SECTION AND FRONT PANEL CIRCUIT OF MODEL NRD-505 ALL-WAVE RECEIVER

**BFO-AF AMP UNIT  
CGD-26**



TO J21  
8  
6  
7  
MUTE  
TONE  
A-TRIP

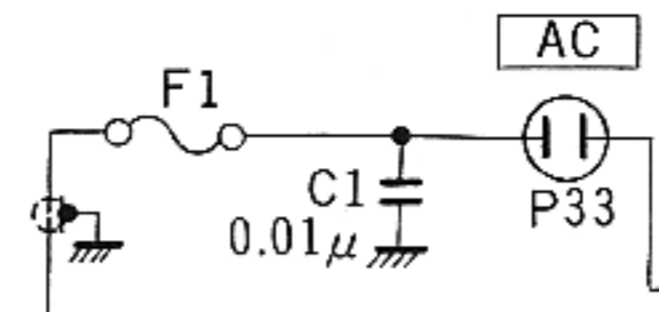
+15V  
1-1  
1-2  
1-3  
1-4  
1-5  
1-6  
1-7  
1-8  
1-9  
1ST LOCAL  
2ND LOCAL  
MUTE  
1-10  
+13V  
1-11  
LSB  
1-12  
USB  
1-13

TO J31  
22 23 14

TO J30  
2 3 8 6

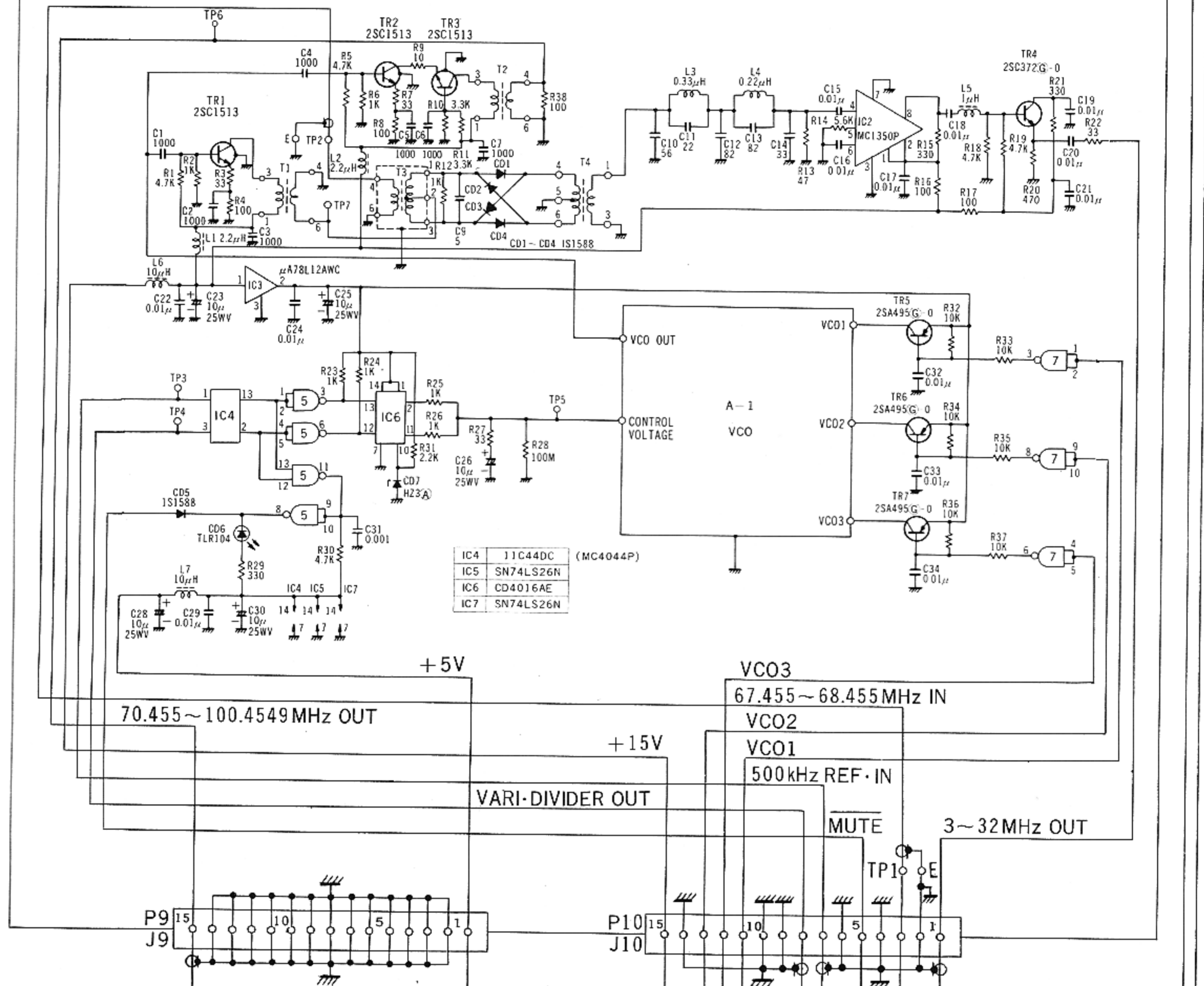
ams

**RECEIVER  
PANEL CIRCUIT CONNECTION DIAGRAM**

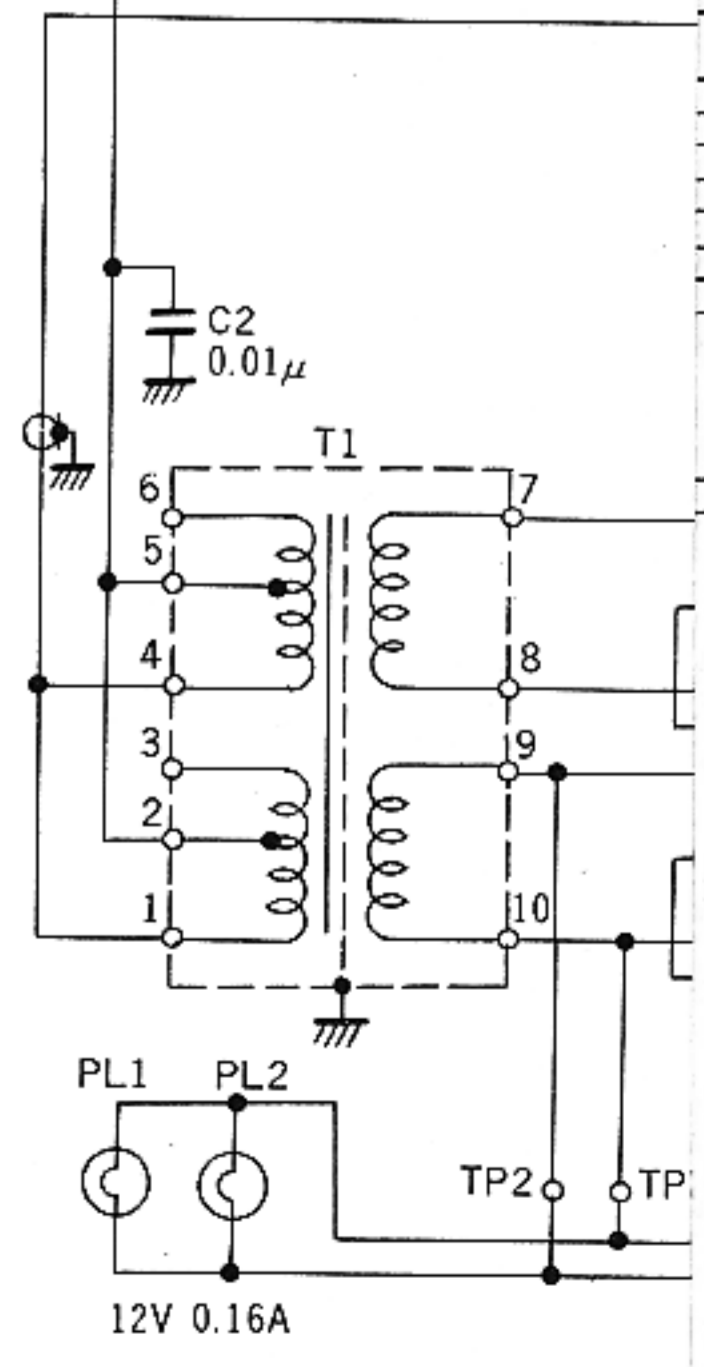


LOOP1 UNIT  
CGA-23

LOOP2 UN  
CGA 24



- 1-1 +15V
- 1-2 ①
- 1-3 ②
- 1-4 ③
- 1-5 ④
- 1-6 ⑤
- 1-7 ⑥
- 1-8 1ST LOCAL
- 1-9 2ND LOCAL
- 1-10 MUTE
- 1-11 +13V
- 1-12 LSB
- 1-13 USB

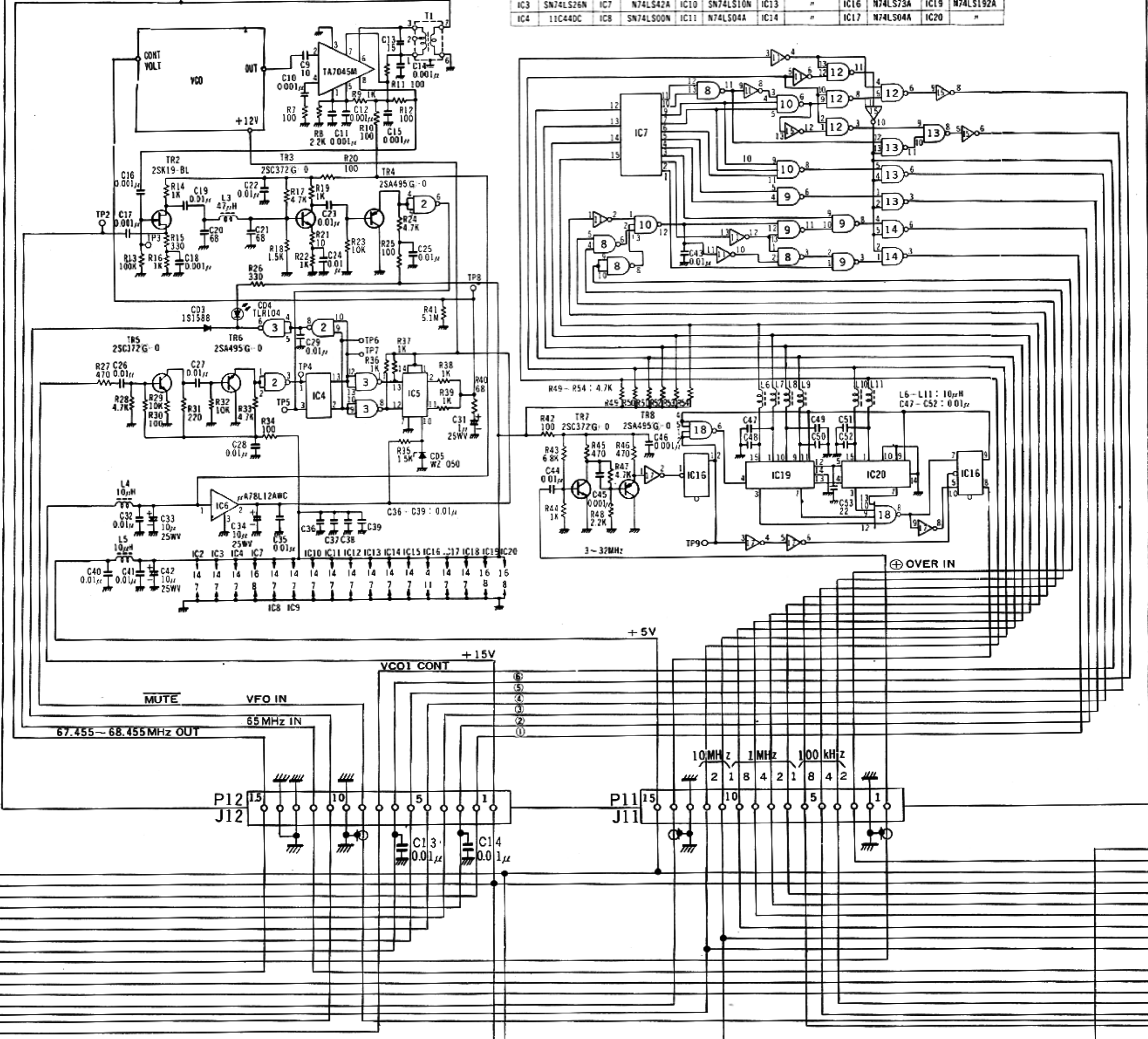


SP

J22

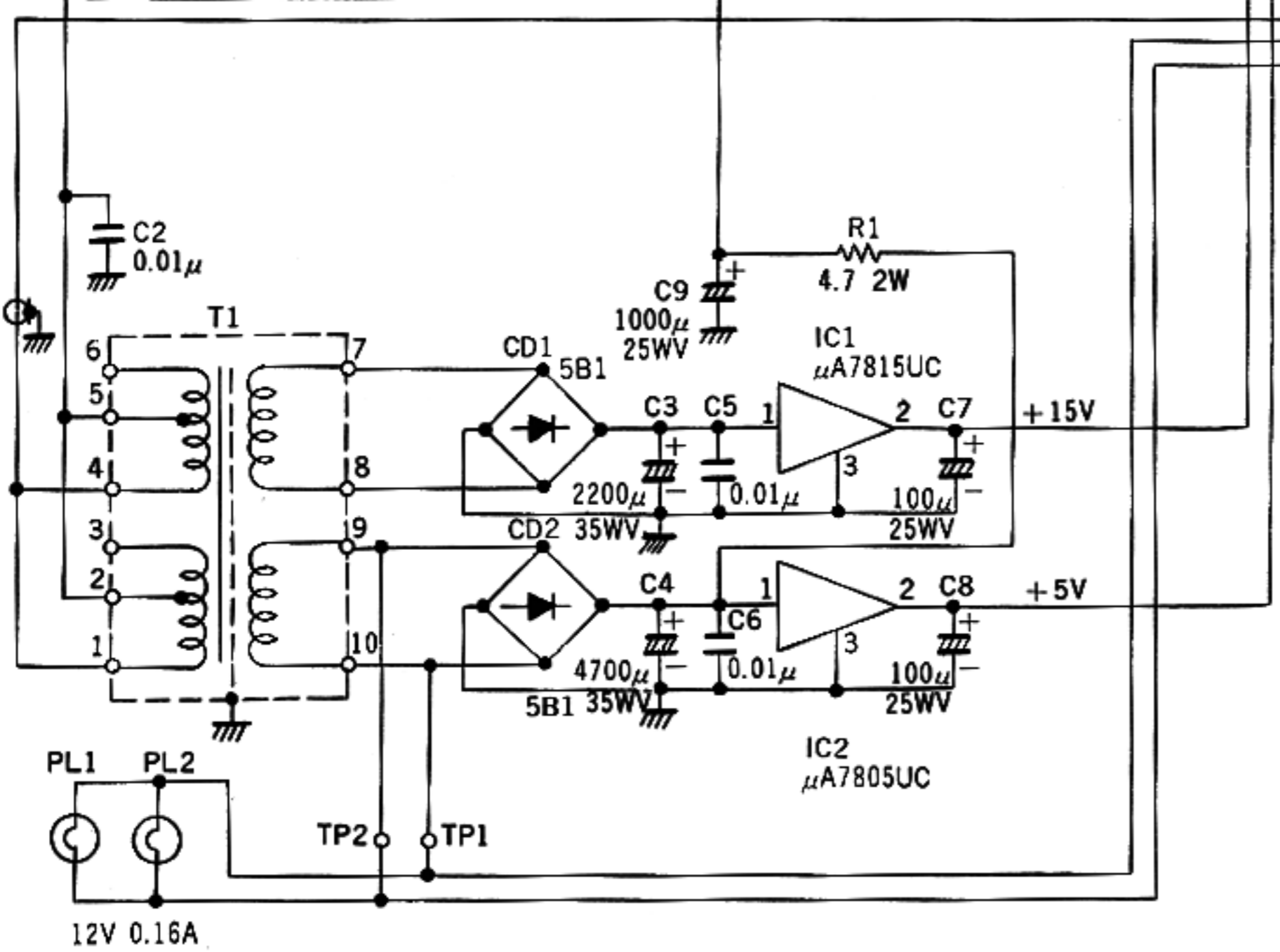
# LOOP2 UNIT CGA 24

IC2	SN74LS00N	IC5	CD4016AE	IC9	SN74LS00N	IC12	SN74LS00N	IC15	N74LS04A	IC18	SN74LS20N
IC3	SN74LS26N	IC7	N74LS42A	IC10	SN74LS10N	IC13	"	IC16	N74LS73A	IC19	N74LS192A
IC4	11C44DC	IC8	SN74LS00N	IC11	N74LS04A	IC14	"	IC17	N74LS04A	IC20	"

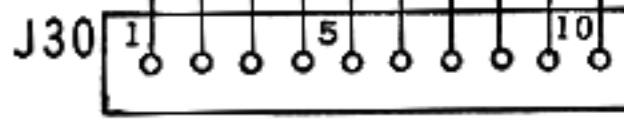


Hz OUT

VF  
CH



- (RF GAIN) TO J5 ①
- TO J7 ⑤
- (AF GAIN) TO J7 ③
- (S ⊕) TO J6 ④
- (S ⊖) TO J6 ②
- (LINE OUT) TO J7 ⑦
- (AF OUT) TO J7 ①

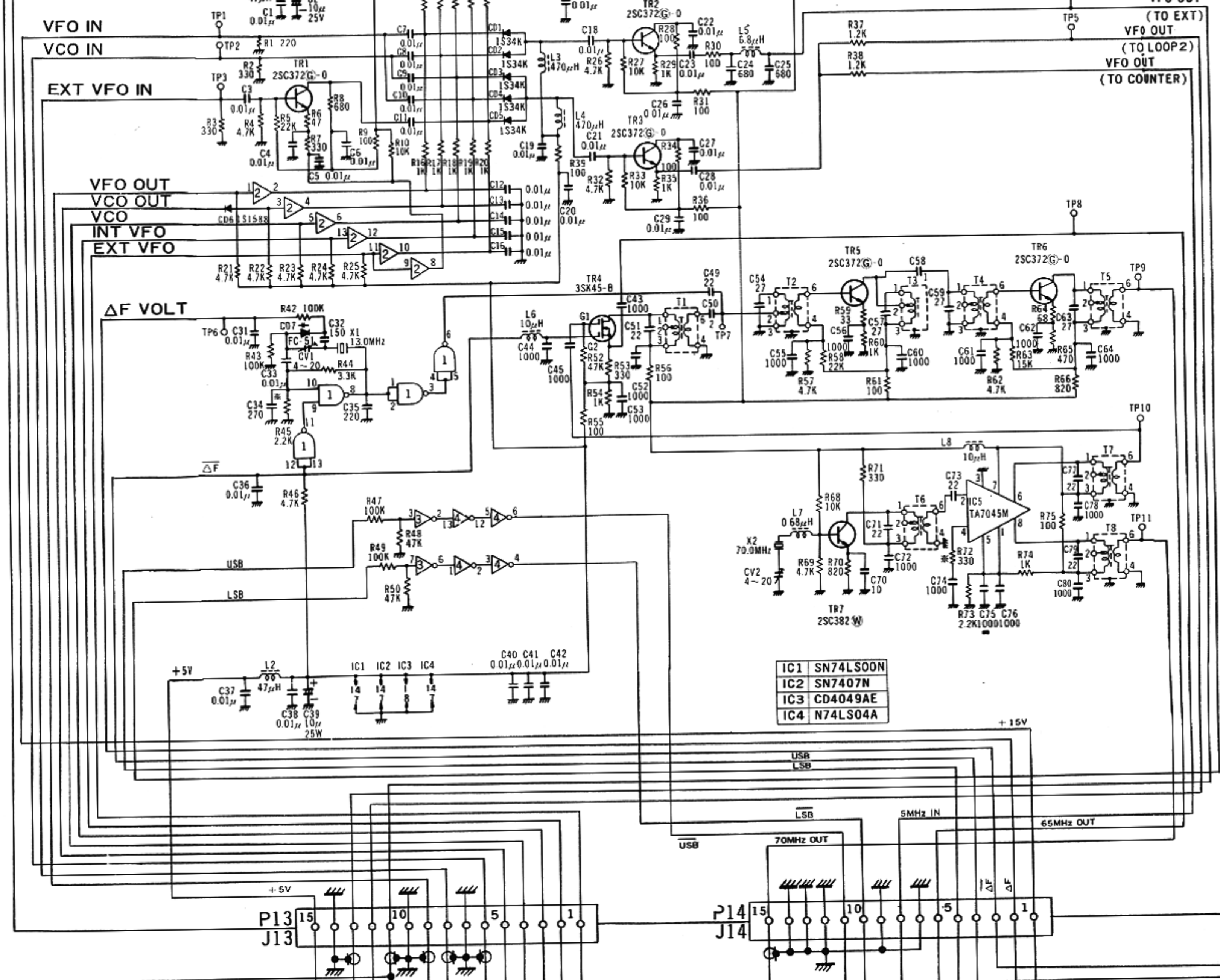


S20N  
192A

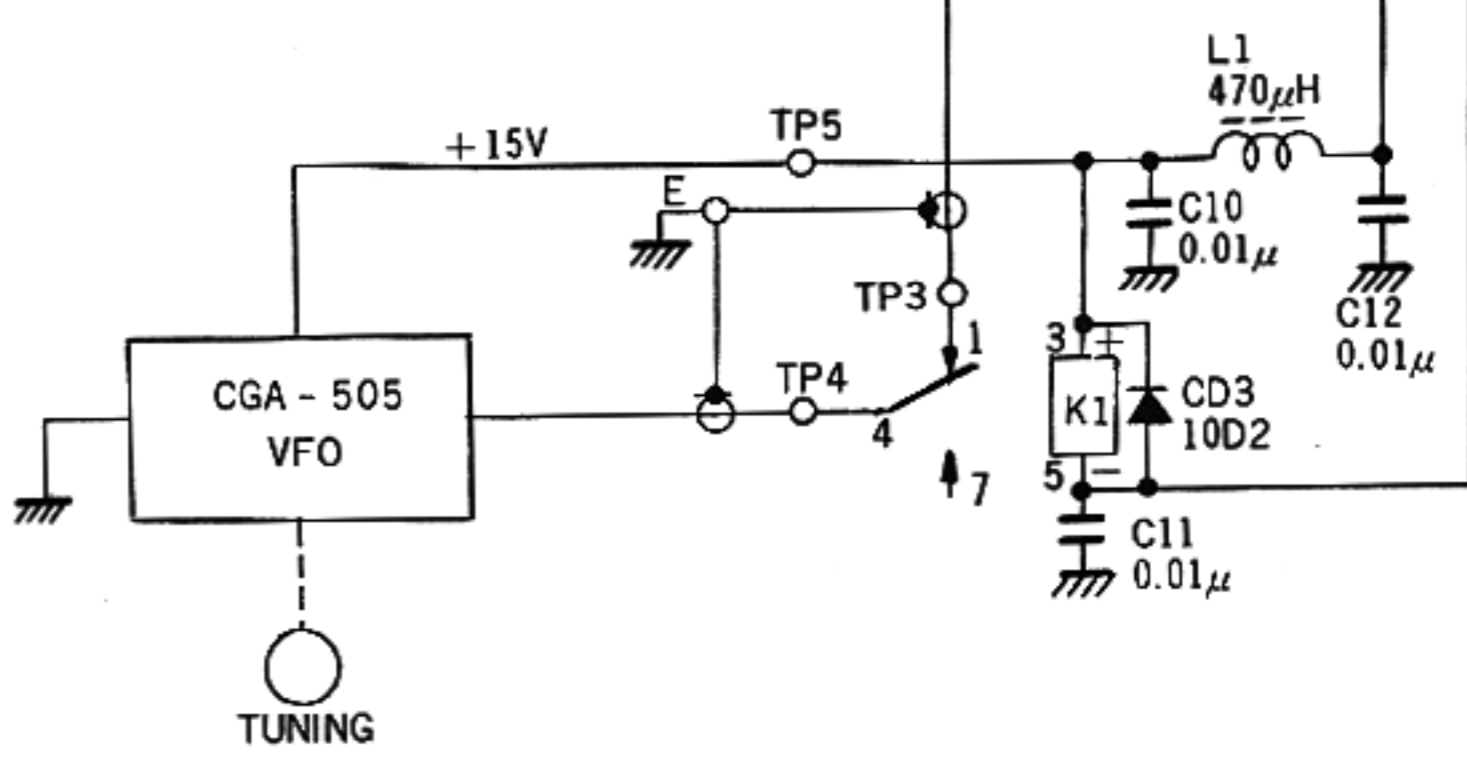
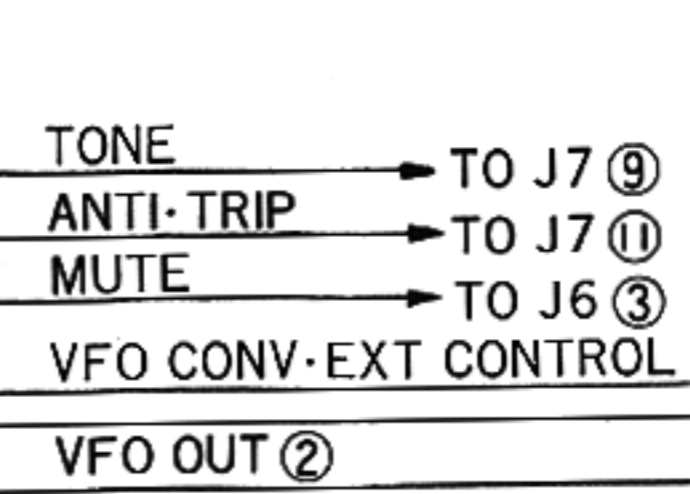
### VFO SELECT-LOCAL OSC UNIT

CHC-4

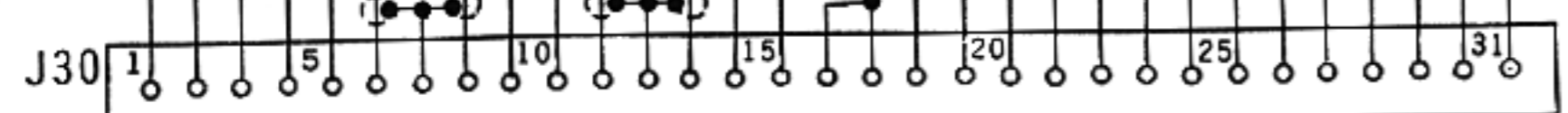
+15V



IC1	SN74LS00N
IC2	SN7407N
IC3	CD4049AE
IC4	N74LS04A



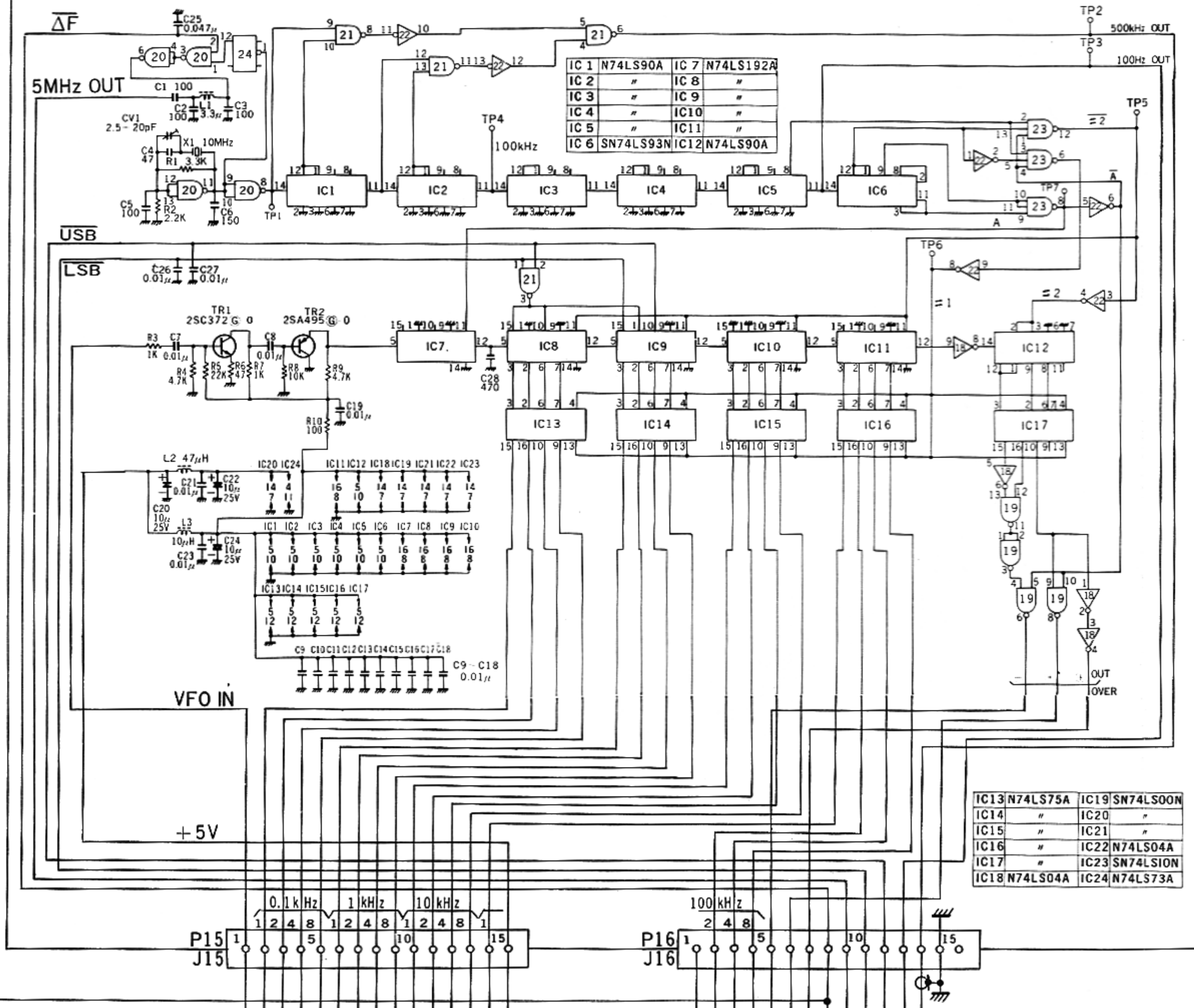
### APPENDIX 3 SYNTHESIZER CIRCUIT CONNECTION DIAGRAM NRD-505 ALL-WAVE RECEIVER



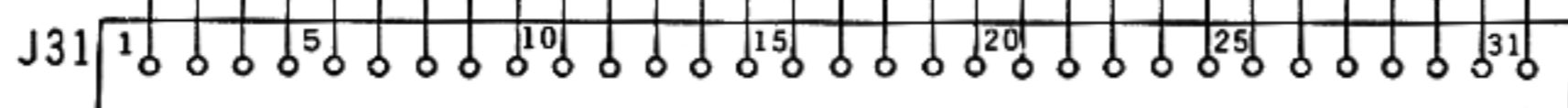
TO P30



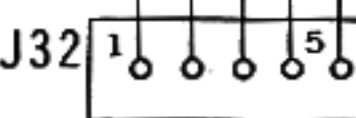
REF. VFO COUNTER UNIT  
CDB-49



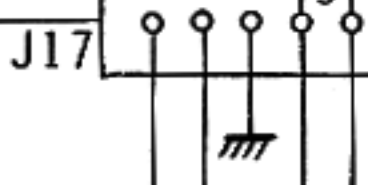
GRAM OF MODEL



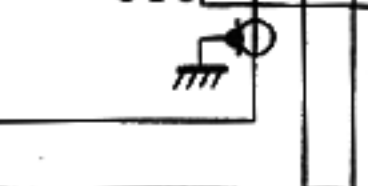
TO P31



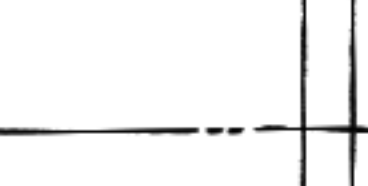
MEMORY



VFO IN



OUT OVER



100 kHz



0.1 kHz 1 kHz 10 kHz

- NOTES 1. Unless otherwise indicated resistances are in ohms  
capacitances are in micro-micro farads.  
2. Values selected in manufacture.

