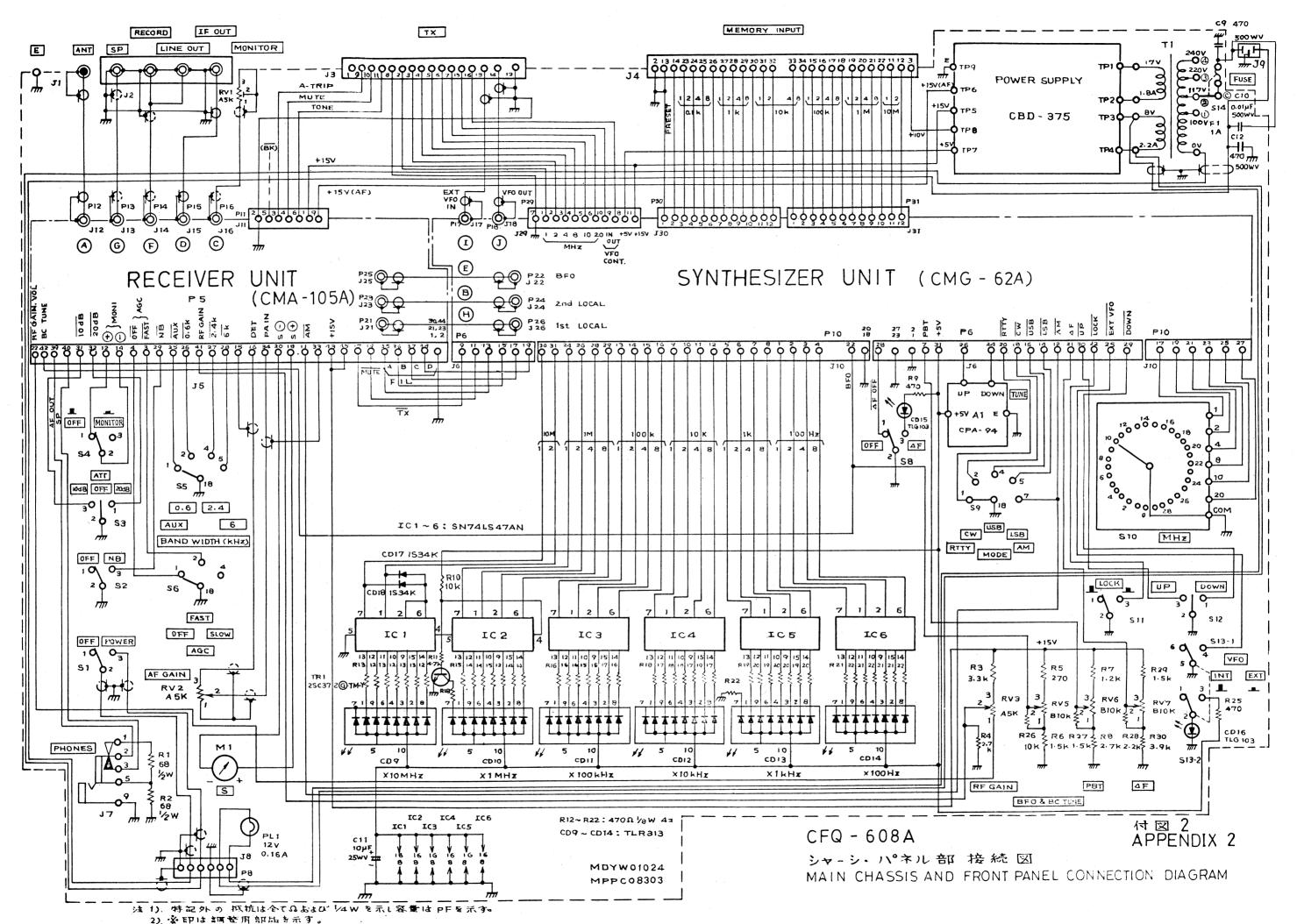


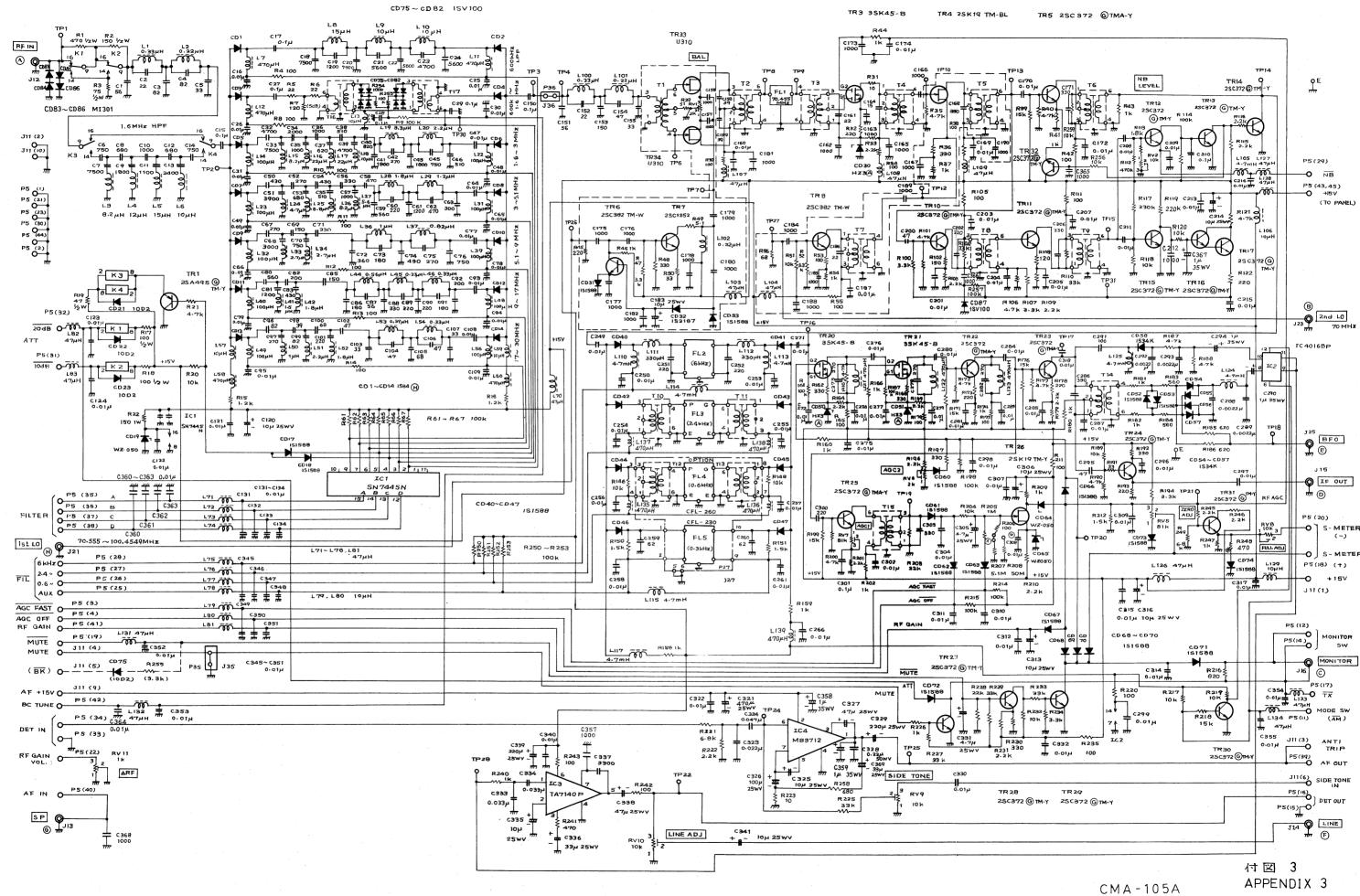
※ [\_\_\_] OPTION を示します。

NRD-515 全 波 受 信 機 系 統 図 ALL-WAVE RECEIVER FUNCTIONAL BLOCK DIAGRAM

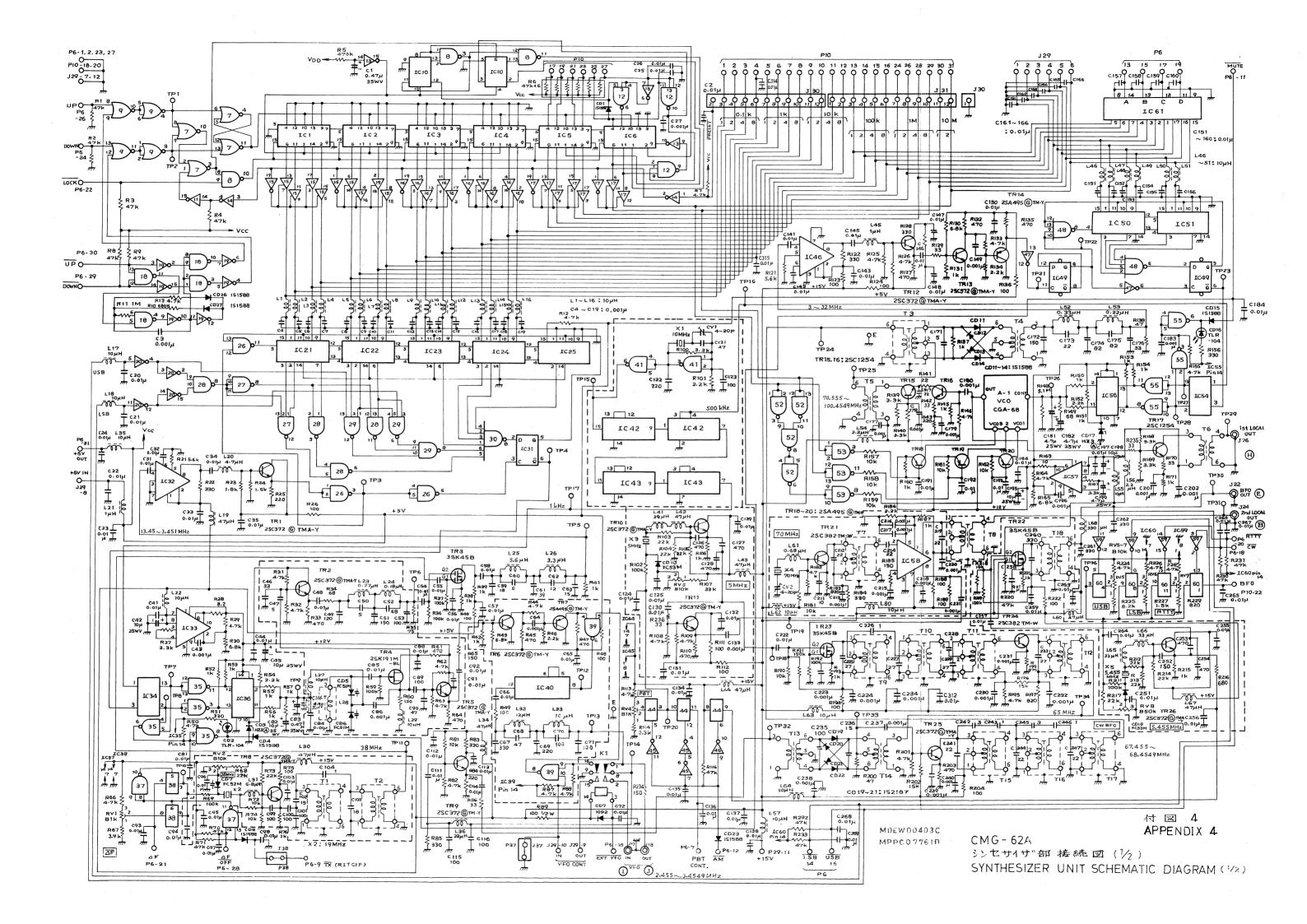


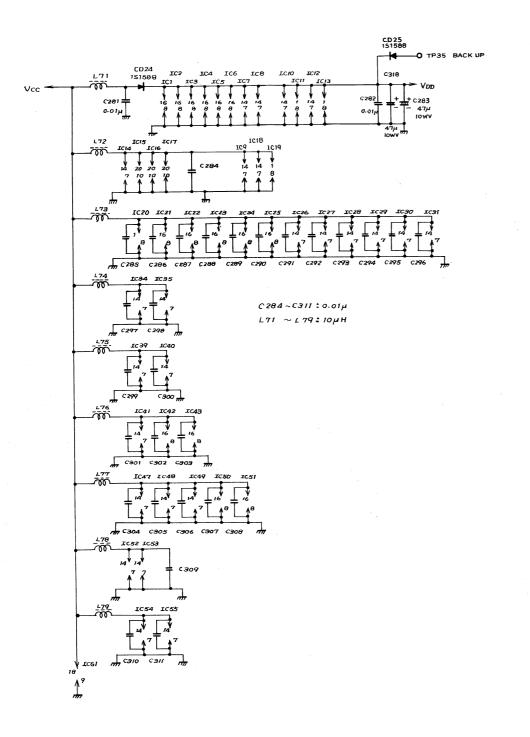
NOTES 1. UNLESS OTHERWISE INDICATED RESISTANCES ARE IN OHMS CAPACITANCES ARE IN MICRO-MICRO FARADS.

2. \* VALUES SELECTED IN MANUFACTURE.



MDMW 00552.C MPPC 07760 D 受信部 接続図 RECEIVER UNIT SCHEMATIC DIAGRAM





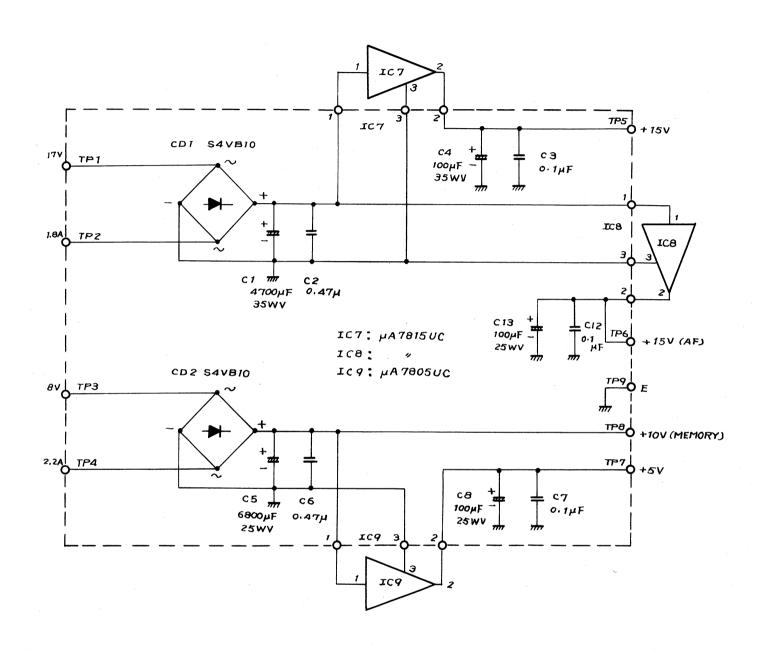
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付図 5 APPENDIX 5

CMG-62A シンセサイサ"部 接続図(²/2) SYNTHESIZER UNIT SCHEMATIC DIAGRAM(²½)



MDBW00795 MPPC07962

> 付図 6 APPENDIX 6

CBD-375 電源部接続図 RECTIFIER UNIT SCHEMATIC DIAGRAM