

19 Series

# Schematic

**B I A M P<sup>®</sup>**

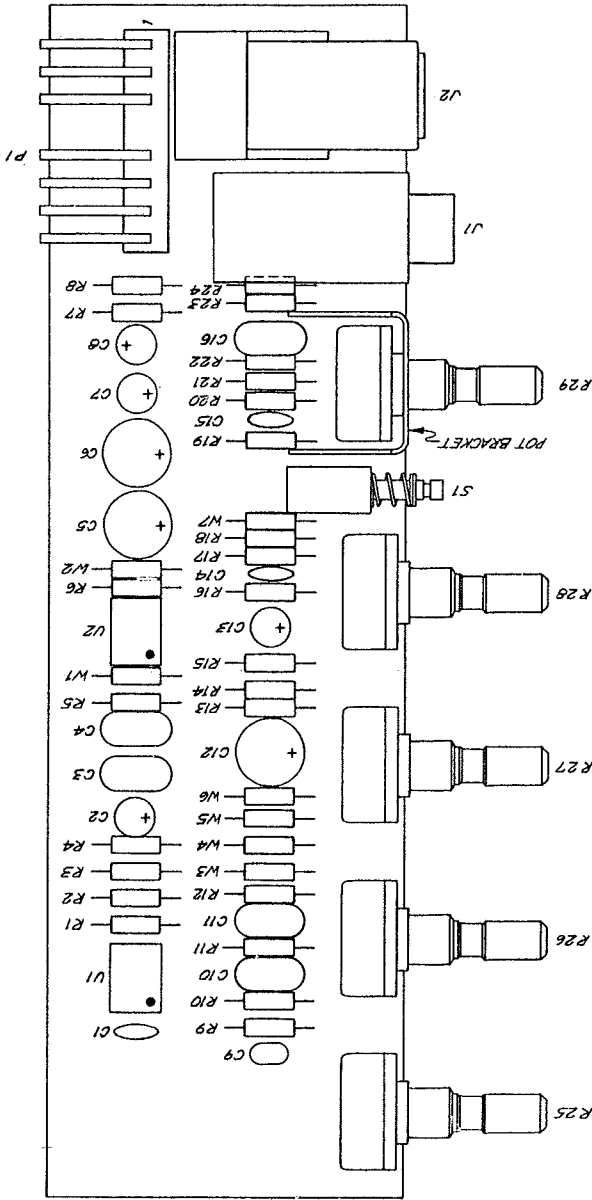
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S Y S T E M S

10074 SW Arctic Drive      Beaverton, OR 97005      503-641-7287

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- P1 PIN ASSIGNMENTS:
1. EFX BUSS
  2. MONITOR BUSS
  3. MAIN BUSS
  4. PWD
  5. SIGNAL GROUND
  6. POWER GROUND
  7. -15V
  8. +15V



REV.	E.C.O.	CHANGE	DATE	BY

ITEM	PART NO.	DESCRIPTION	SIZE	QTY.
1	✓	BIAMP SYSTEMS INC.		
2	✓	ASSEMBLY A1, 619 JURUT PCB		

DATE	DRAWN	CHECKED	ENGINEER	CREATOR
3/6/82	JD	JD		

FINISH	MODEL	NEXT DWG.	APPLICATION

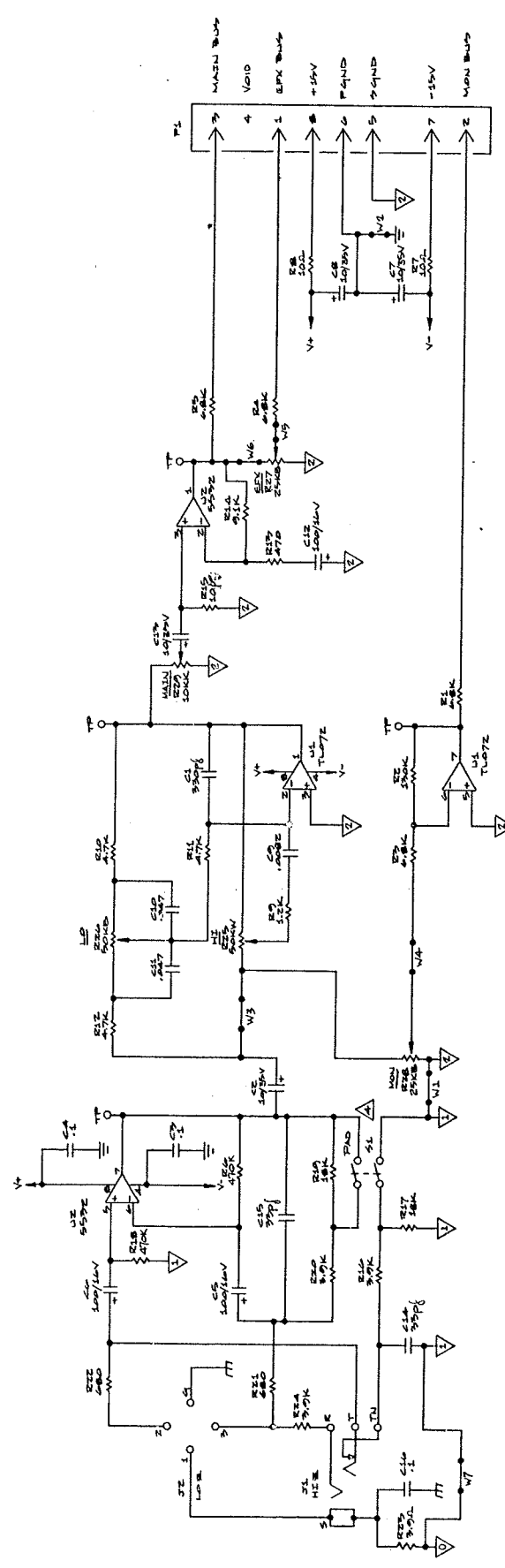
  

SCALE	SCALE 2X	SCALE 1X	SCALE 1/2X	SCALE 3X	SCALE 4X	SCALE 5X	SCALE 6X	SCALE 7X	SCALE 8X	SCALE 9X	SCALE 10X

DO NOT SCALE DRAWING	SHEET 1 OF 1

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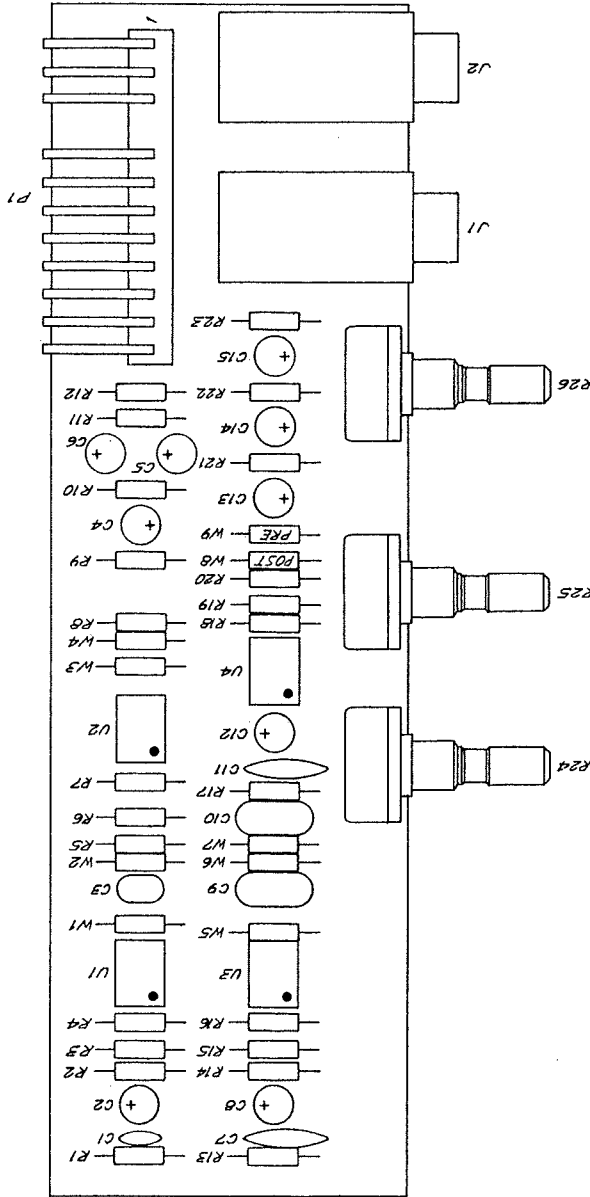
- LAST G
- LAST J
- LAST K
- LAST L
- LAST M
- LAST N
- LAST O
- LAST P
- LAST Q
- LAST R
- LAST S
- LAST T
- LAST U
- LAST V
- LAST W
- LAST X
- LAST Y
- LAST Z

NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS IN OHMS  
 2. ALL CAPACITORS IN MICROFARADS  
 3. ALL RESISTORS 1/4W ± 5% /  
 4. AND ATTENUATES AS FOLLOWS WHEN  
 DEPRESSSED:  
 HI Z JACK USED: -50dBm  
 HI Z JACK UNUSED: -58dBm

ITEM	PART NO.	DESCRIPTION	SIZE	QTY
1	075-22	BIAMP SYSTEMS INC		
2	075-22	SCHEMATIC DIAGRAM		
3	075-22	4/19 INPUT		
4	075-22	ASSEMBLY		
5	075-22	ASSEMBLY		
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99	075-22	ASSEMBLY		
100	075-22	ASSEMBLY		

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- P1 PIN ASSIGNMENTS:**
1. EFFECTS BUSS
  2. MONITOR BUSS
  3. MAIN BUSS
  4. VOID
  5. SIGNAL GROUND
  6. POWER GROUND
  7. -15V
  8. +15V
  9. TAPE OUT
  10. MAIN OUT
  11. REVERSE DRIVE
  12. REVERSE DRIVE



REV.	E.C.D.	CHANGE	DATE	BY

ITEM	PART NO.	DESCRIPTION	SIZE	QTY.

DATE	DATE	DATE	DATE
3/12/82	3/12/82	3/12/82	3/12/82

DESIGNED	CHECKED	ENGINEER	DESIGN	APPROVED	DWG. NO.	REV.

MATERIAL	FINISH	MODEL	NEXT DWG.	APPLICATION

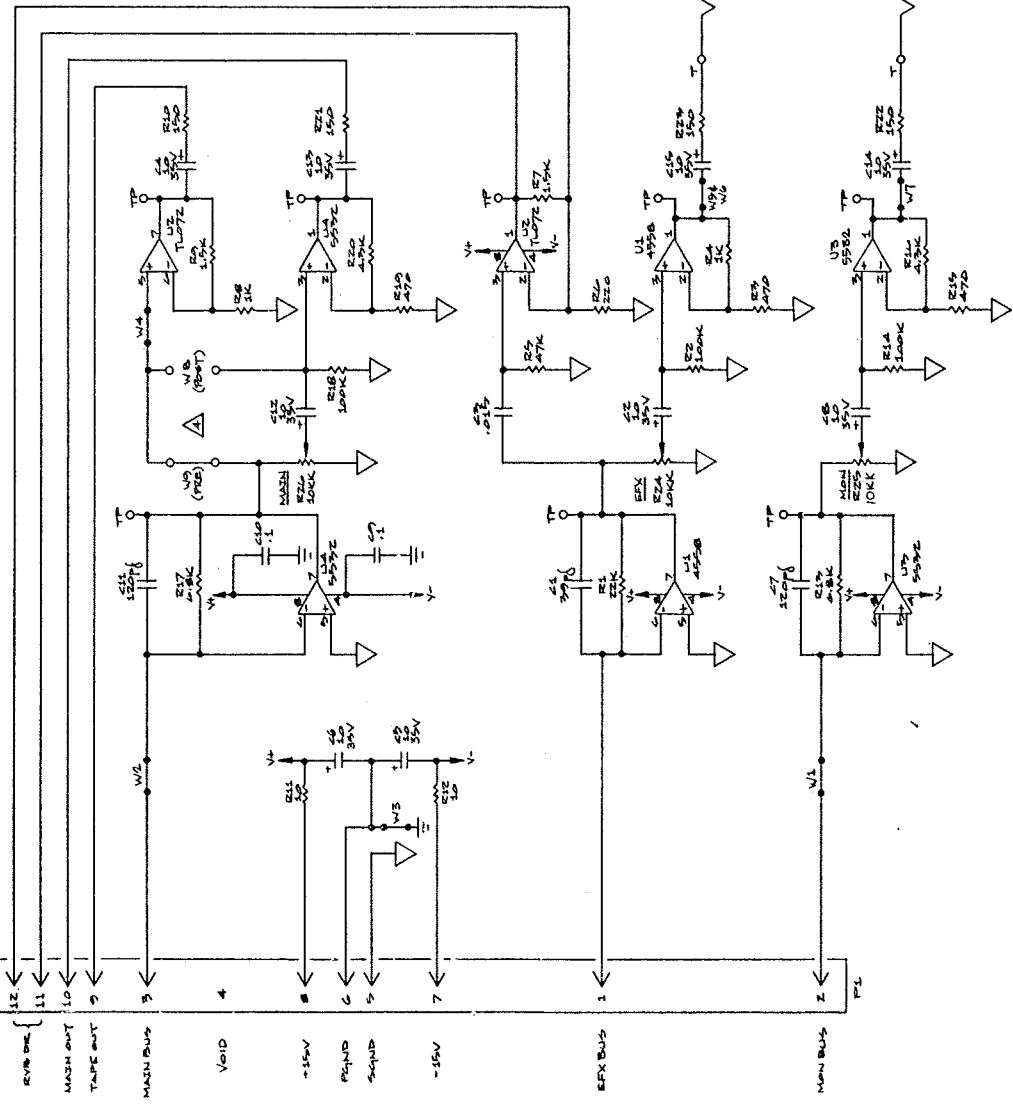
DESCRIPTION	SCALE	DO NOT SCALE DRAWING	SHEET 1 OF 1
BIAMP SYSTEMS INC. ASSEMBLY A3, 619 MAIN PCB	SCALE 2 X		

A B C D

1 2 3 4 3

A B C D

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NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS 1/4W, 5%  
 2. ALL RESISTOR VALUES IN OHMS  
 3. ALL CAPACITOR VALUES IN MICROSECONDS

⚠ W4 FUSES - INSTALLED FOR PRE-MAIN-FADE TAPE OUT; FOR POST-MAIN-FADE TAPE OUT, REMOVE W4, INSTALL W5.

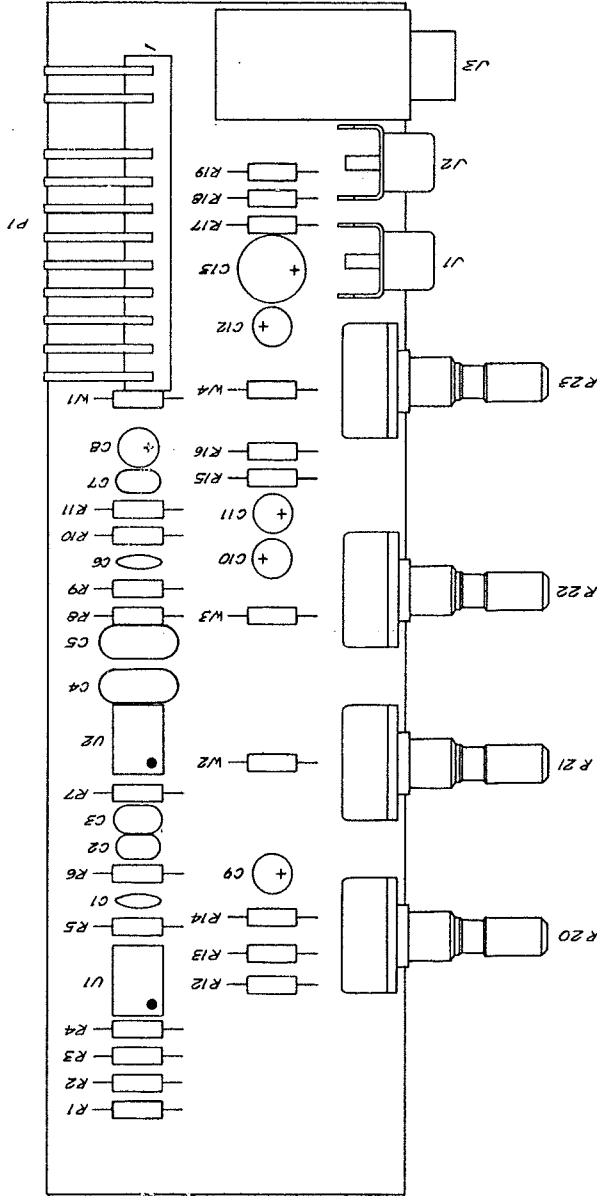
- W4 4 C15
- W5 5 C15
- W6 6 C15
- W7 7 C15
- W8 8 C15
- W9 9 C15

DESCRIPTION		PART NO.		REV.	DATE
BIAMP SYSTEMS I		BIAMP SYSTEMS I			
SCHEMATIC DIAGRAM		SCHEMATIC DIAGRAM			
C/S MAIN ASSY A3		C/S MAIN ASSY A3			
DRAWN: [signature]		CHECKED: [signature]			
MATERIAL: N/A		MATERIAL: N/A			
FINISH: N/A		FINISH: N/A			
MODEL: [signature]		MODEL: [signature]			
TEST ENG: [signature]		TEST ENG: [signature]			
DESIGNER: [signature]		DESIGNER: [signature]			
DATE: [signature]		DATE: [signature]			
SCALE: N/A		SCALE: N/A			
DO NOT SCALE DRAWING		DO NOT SCALE DRAWING			
SHEET 1 C		SHEET 1 C			

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**P1 PIN ASSIGNMENTS:**

- 1. MONITOR
- 2. MAIN
- 3. PWD
- 4. SIGNAL GROUND
- 5. POWER GROUND
- 6. -15V
- 7. +5V
- 8. TAP/AL LEFT & RIGHT
- 9. REVERB FOOTSWITCH
- 10. REVERB PICK-UP +
- 11. REVERB PICK-UP -
- 12. REVERB PICK-UP -



REV. E.C.O. CHANGE DATE BY

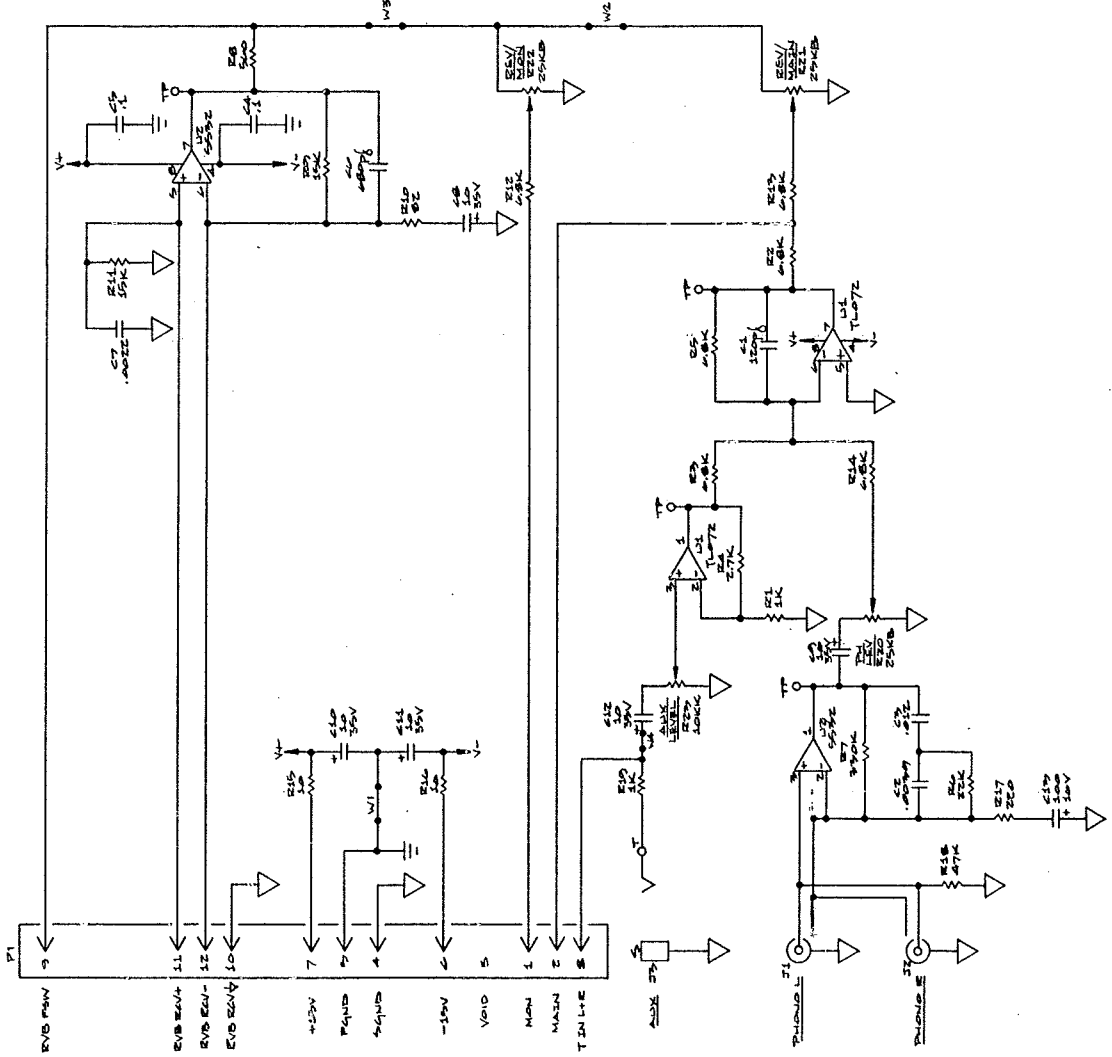
ITEM	PART NO.	DESCRIPTION	SIZE	QTY.
UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES. TOLERANCES ARE: .XX ± .XXX ± .000 ±				
DRAWN BY: <i>[Signature]</i> DATE: 03/28/82				
CHECKED BY: <i>[Signature]</i> DATE: 03/29/82				
ENGINEER: <i>[Signature]</i>				
DESIGN: <i>[Signature]</i>				
MATERIAL: <i>[Signature]</i>				
FINISH: <i>[Signature]</i>				
MFG. DWG. NO.: 04-4-81				
SCALE: 2X				
REV. A				
DO NOT SCALE DRAWING				
SHEET 1 OF 1				

BIAMP SYSTEMS INC.  
ASSEMBLY A2,619 AUX PCB

A B C D

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REV.	ECO.	CHANGE	A	B	C	D	E	F	G	H

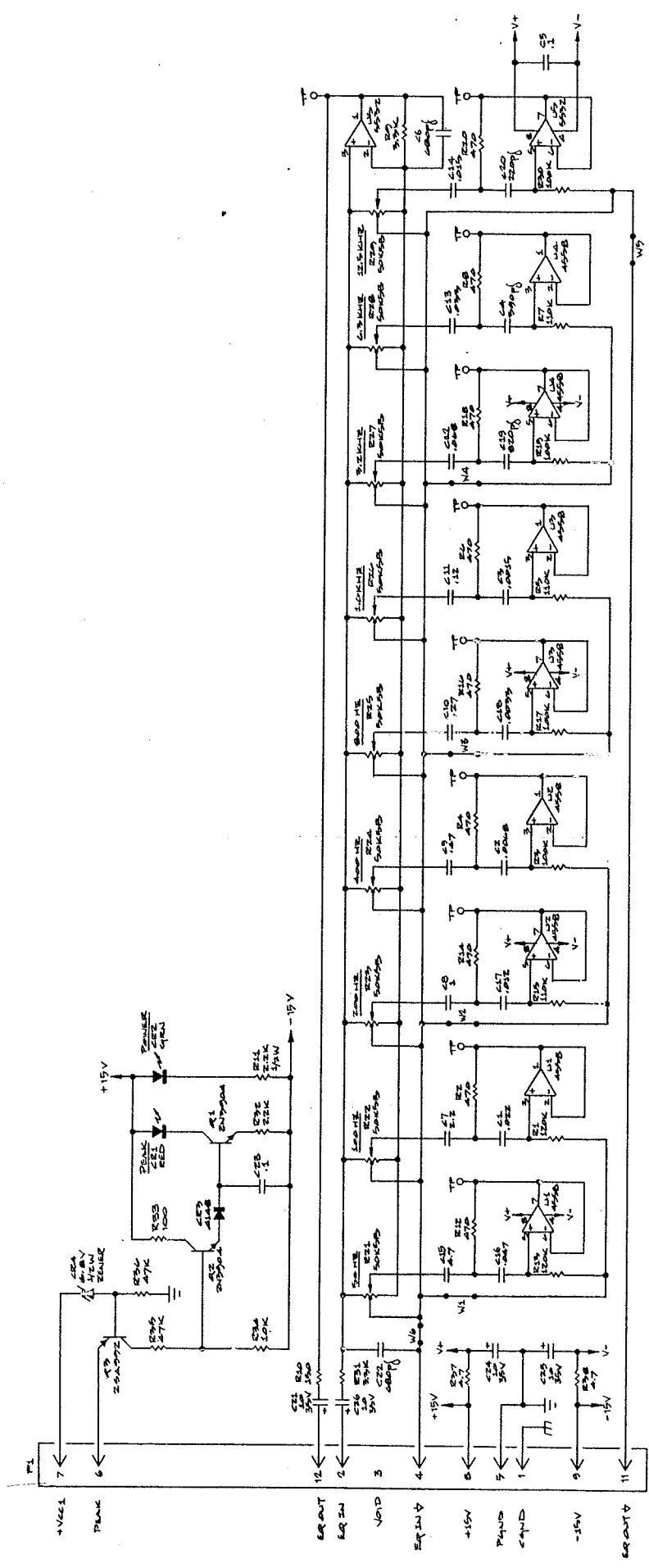


NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS 1/4W, 5%  
 2. ALL RESISTOR VALUES IN OHMS  
 3. ALL CAPACITOR VALUES IN MICROFARADS

- LAST 4 0.1u
- LAST 5 0.1u
- LAST 6 100k
- LAST 7 50k
- LAST 8 50k
- LAST 9 50k
- LAST 10 50k

DESCRIPTION				PART NO.				SIZE			
REV.	ECO.	CHANGE	DATE	REV.	ECO.	CHANGE	DATE	REV.	ECO.	CHANGE	DATE
BIAMP SYSTEM!				BIAMP SYSTEM!				BIAMP SYSTEM!			
MATERIAL: BARRON				MATERIAL: BARRON				MATERIAL: BARRON			
FINISH: N/A				FINISH: N/A				FINISH: N/A			
SCALE: N/A				SCALE: N/A				SCALE: N/A			
DO NOT SCALE DRAWING				DO NOT SCALE DRAWING				DO NOT SCALE DRAWING			

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NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS 1/4W, 5%  
 2. ALL RESISTOR VALUES IN OHMS  
 3. ALL CAPACITOR VALUES IN MICROSECONDS

LAST C  
 LAST CE  
 LAST F  
 LAST G  
 LAST H  
 LAST I  
 LAST J  
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 LAST W  
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 LAST Z

REV.	DATE	DESCRIPTION
1	1/10/72	SCHEMATIC DRAW
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100	1/10/72	SCHEMATIC DRAW



REV.	EGG.	CHANGE	DATE	BY
9	DB6-B2	REMOVE R41 ADD NOT USED	6-82	JD

D

C

B

A

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J1 PIN ASSIGNMENTS

1. OUTPUT
2. THERMAL RESISTOR - TS
3. THERMAL RESISTOR - T
4. +Vcc 1
5. +Vcc 2
6. +Vcc 2
7. +Vcc 2
8. +Vcc 1

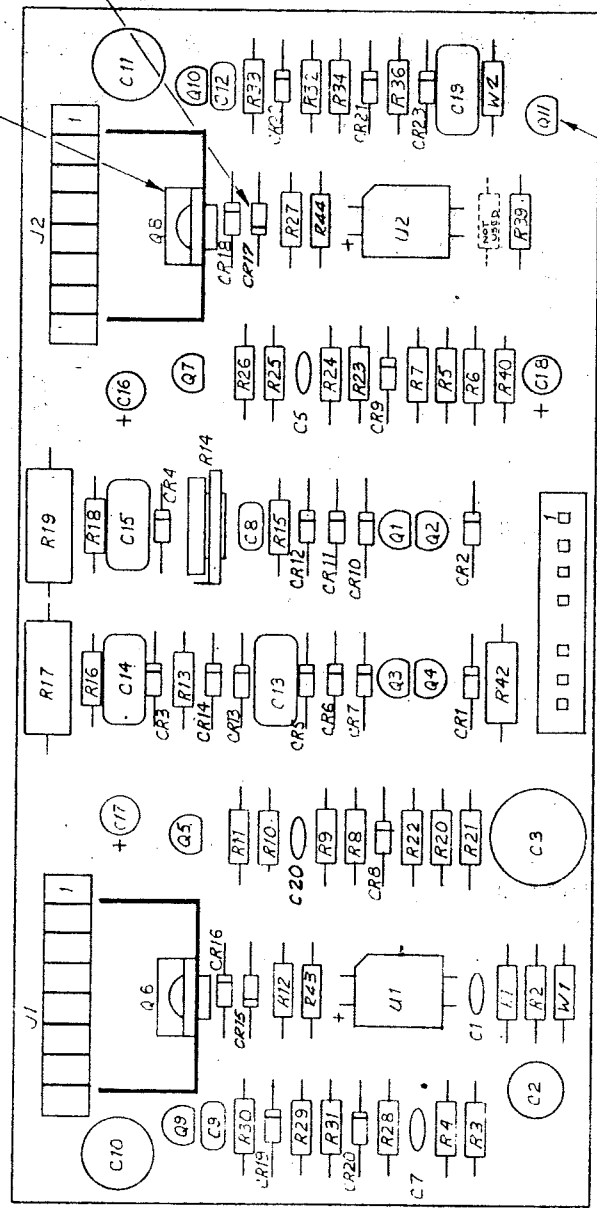
J2 PIN ASSIGNMENTS

1. GROUND
2. -Vcc 1
3. -Vcc 2
4. -Vcc 2
5. OUTPUT
6. -VI SENSE
7. -DRIVE / BIAS
8. BIAS

HEIGHT (FROM BOARD TO TOP OF TRANSISTOR HEAT SINK) TO BE 7/8" MAX. (FOR Q6 & Q8)

NOTE POLARITY ON CR15 & CR17

HEIGHT (FROM BOARD TO TOP OF CASE) TO BE 3/8" (FOR Q1-Q3, Q7, Q9-Q11)



J3 PIN ASSIGNMENTS

1. FILTERED LED CONTROL VOLTAGE
2. LED REFERENCE VOLTAGE
3. FAST OFF CONTROL
4. +Vcc 1 LED REFERENCE VOLTAGE
5. -Vcc 1 LED REFERENCE VOLTAGE
6. +PE INPUT
7. INPUT GROUND
8. SIGNAL INPUT

2. ALL COMPONENTS TO BE FULLY SEATED DOWN ON BOARD EXCEPT AS NOTED.  
1. SEE SHEET 1 FOR SCHEMATIC.  
NOTE:

ITEM	PART NO.	DESCRIPTION	SIZE	QTY.
<p>UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES. TOLERANCES ARE DECIMALS FRACTIONS ANGLES MATERIAL</p>				
DATE	8/28/81	DATE	8/28/81	SCALE
DRAWN	TP	CHECKED	TP	ENGINEER
DESIGN		DESIGN		
<p>BIAMP SYSTEMS INC. ASSEMBLY #11 19 SERIES DRIVER BOARD</p>				
FINISH	221 1229	MODEL		
APPROVED		SCALE	2:1	DO NOT SCALE DRAWING
DATE	7/1/81	SIZE	701-0063-00	SHEET 2 OF 2

REV.	ECO	CHANGE
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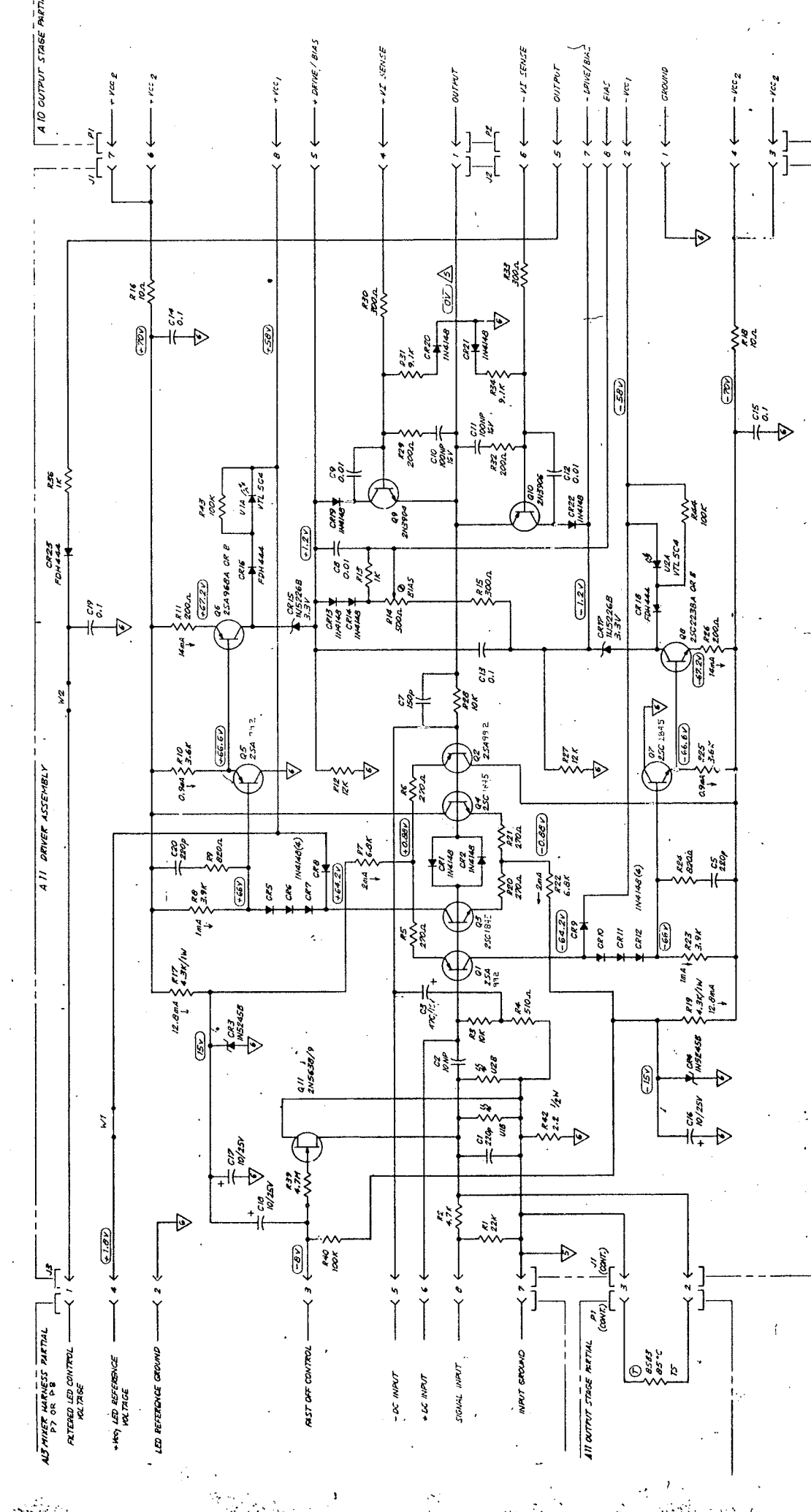
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REV.	ECO	CHANGE
1	3	37584
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REV.	ECO	CHANGE
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REV.	ECO	CHANGE
1	3	37584
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**BIAMP SYS**  
SCHEMATIC - ASSEMBLY  
19 SERIES DRIVER BOA

UNLESS OTHERWISE SPECIFIED, DIMENSIONS AND TOLERANCES ARE IN INCHES.  
DIMENSIONS ARE TO CENTER UNLESS OTHERWISE SPECIFIED.  
MAX. VOLTAGE VARIATION CAN BE ±50% VDC.  
SEE SHEET FOR PCB ASSEMBLY.  
DIMENSIONS ARE TO CENTER UNLESS OTHERWISE SPECIFIED.  
ALL DIMENSIONS ARE IN INCHES UNLESS NOTED.

LAST R: R44 OMIT R41, 55, 57, 436  
 \* C1, C20  
 \* C1, C20  
 \* C1, C20  
 \* C1, C20

REV. NO.	DATE	BY	CHKD	APP. NO.
1	2/10/84	J. J. JF		
2	2/10/84	J. J. JF		
3	2/10/84	J. J. JF		
4	2/10/84	J. J. JF		
5	2/10/84	J. J. JF		
6	2/10/84	J. J. JF		
7	2/10/84	J. J. JF		
8	2/10/84	J. J. JF		
9	2/10/84	J. J. JF		
10	2/10/84	J. J. JF		

DO NOT SCALE DRAWING

10

**A** **B** **C** **D**

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**P1 PIN ASSIGNMENTS**

1. OUTPUT
2. THERMAL RESISTOR - T
3. THERMAL RESISTOR - T
4. +VI SENSE
5. DRIVE/BIAS
6. +VCC 2
7. +VCC 1
8. GROUND

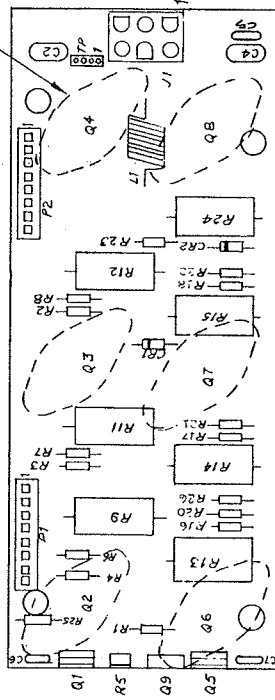
**P2 PIN ASSIGNMENTS**

1. GROUND
2. -VCC 1
3. -VCC 2
4. OUTPUT
5. +VI SENSE
6. DRIVE/BIAS
7. GROUND
8. +VCC 1

HAND ADD SOCKETS ON BACK OF BOARD AT TIME OF AMP MODULE ASSEMBLY.

**J1 PIN ASSIGNMENTS**

1. -VCC 2
2. AMP GROUND
3. +VCC 2
4. -VCC 1
5. AMP OUT
6. +VCC 1



1

2

3

4

11

REV.	E.C.O.	CHANGE	DATE	BY
B		REMOVED C1 & C2 (D.I.T.P)	4/84	NL
C	230-82	R10, R20, R17, R21, R18, R22	6/82	FD

ITEM	PART NO.	DESCRIPTION	SIZE	QTY.
DESCRIPTION	DATE	BIAMP SYSTEMS INC.		
DRAWN	8/24/81			
CHECKED	10-27-81			
ENGINEER				
DESIGN				
APPROVED	11/2/81			
DWG. NO.				
MATERIAL				
FINISH				
BREK SHARP EDGES 45°				
UNLESS OTHERWISE SPECIFIED				
DECIMALS	2			
FRACTIONS	2			
ANGLES	2			
PLANNING				
SCALE				
SCALE				
DO NOT SCALE DRAWING				
SHEET				
SHEET 2 OF 2				

2. ALL COMPONENT TO BE FULLY SEATED DOWN ON BOARD.  
1. SEE SHEET 1 FOR SCHEMATIC.  
NOTE :

**A**

**5**

**D1**

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A11 DRIVER PARTIAL A10 OUTPUT STAGE ASSEMBLY A14 AMP HARNESS PARTIAL

REV. B  
E.C.D.  
CHANGE C.1 & C.3 (0.1MFD) ADD TP 4

DATE 4/82  
BY JL

PI OR P2  
+Vcc 2  
+Vcc 1

J1  
3  
6

J2  
5  
6

J3  
1  
4

J4  
1  
4

J5  
1  
4

J6  
1  
4

J7  
1  
4

J8  
1  
4

J9  
1  
4

J10  
1  
4

J11  
1  
4

J12  
1  
4

J13  
1  
4

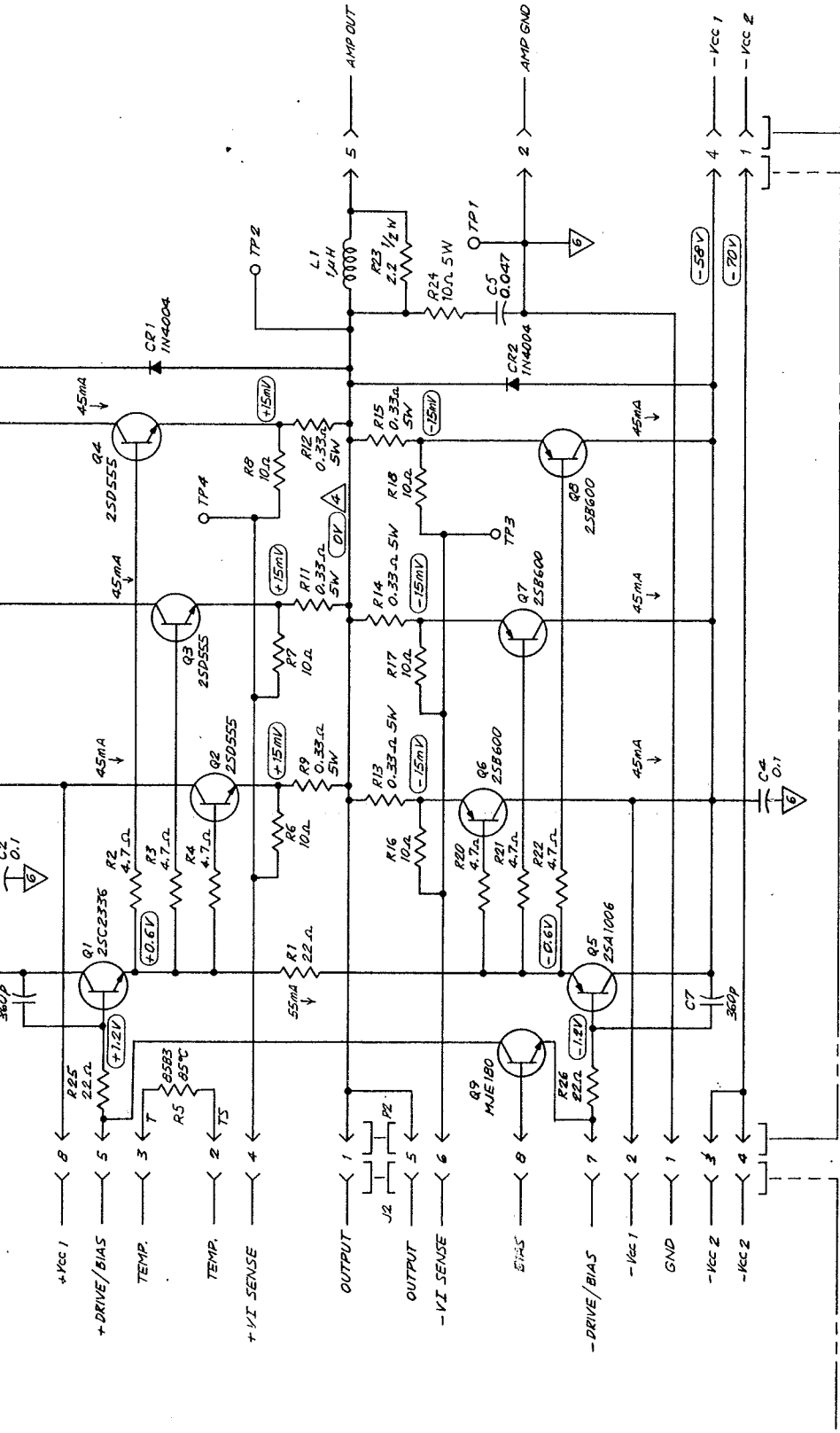
J14  
1  
4

J15  
1  
4

J16  
1  
4

J17  
1  
4

J18  
1  
4



- LAST R: R26
- C: C7
- CR: CR2
- Q: Q9
- L: L1
- J: J1
- P: P2

5. ALL VOLTAGES ARE DC AND MEASURED WITH NO SIGNAL; LINE VOLTAGE = 120VAC.
4. MAX. VOLTAGE VARIATION CAN BE ±50%VDC.
3. SEE SHEET 2 FOR PCB ASSEMBLY.
2. ALL RESISTORS ARE 1/4 W, 5% UNLESS NOTED.
1. ALL CAPACITORS IN µFD UNLESS NOTED.

NOTES:

REV.	DESCRIPTION	DATE	BY
B	REVISED CIRCUITRY, REWORK C.1 & C.3 (0.1MFD) ADD TP 4	4/82	JL

ITEM	DESCRIPTION	SIZE	QTY.
UNLESS OTHERWISE SPECIFIED, DIMENSIONS SHALL BE IN INCHES AND DECIMALS FRACTIONS UNLESS NOTED.			
1	BIAMP SYSTEMS INC.		
2	SCHMATIC - ASSEMBLY A10		
3	29 SERIES OUTPUT STAGE BOARD		
4	DWG. NO.		
5	SCALE		
6	DO NOT SCALE DRAWING		
7	SHEET 7 OF 3		

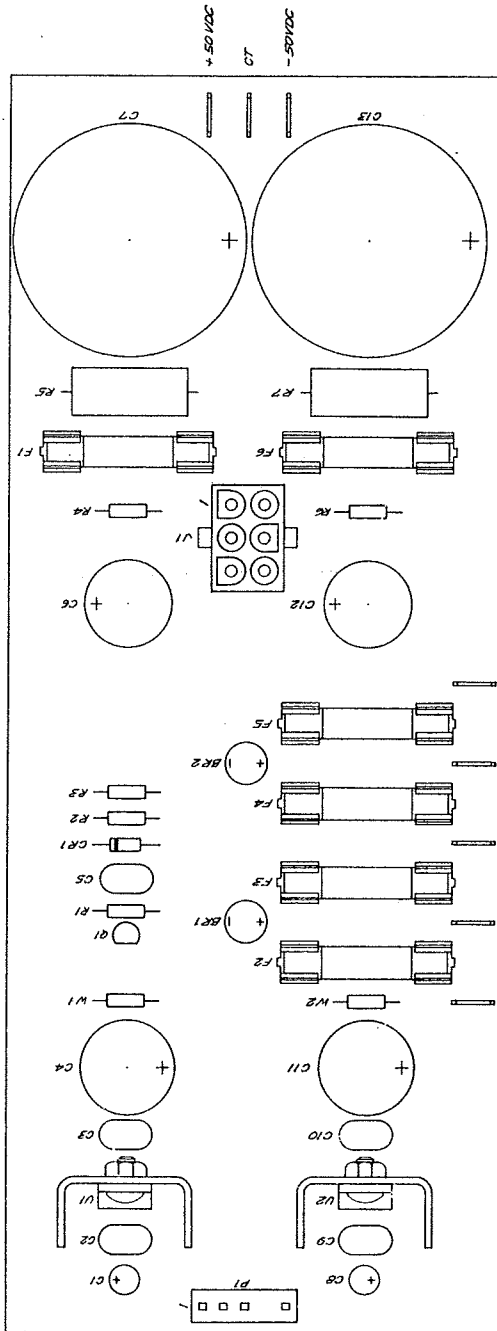
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**P1 PIN ASSIGNMENTS:**

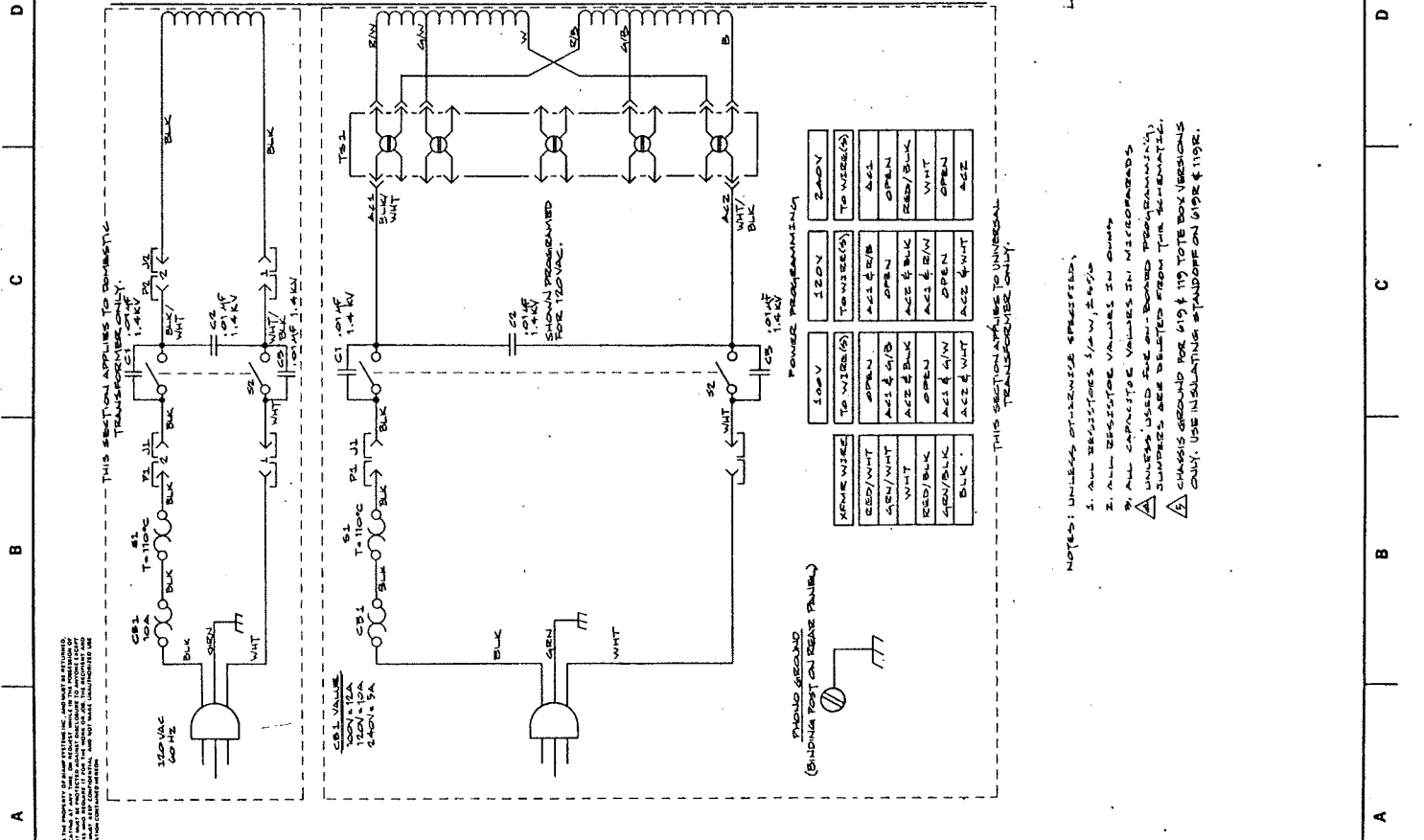
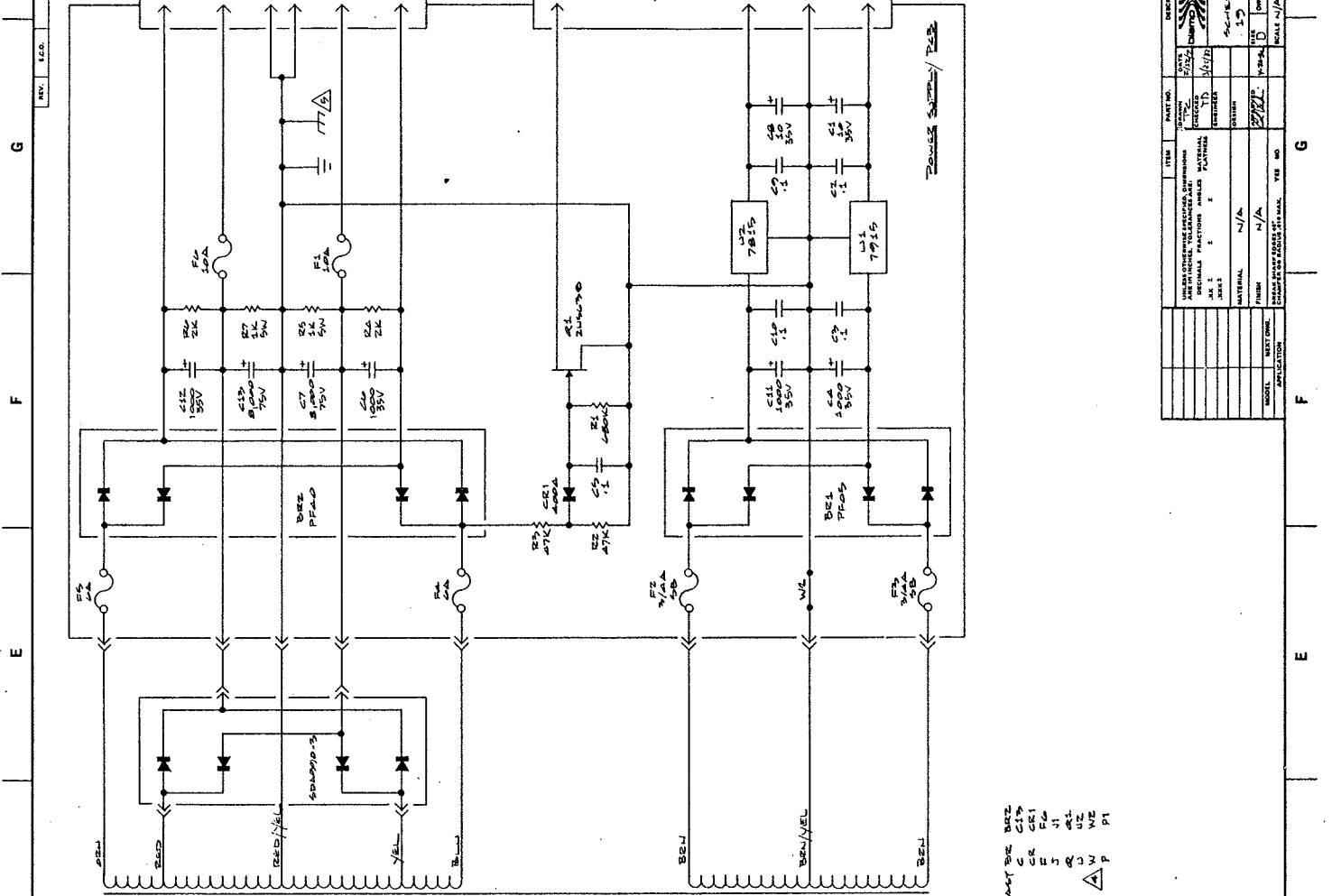
1. FAST OFF
2. -15V
3. POWER GROUND
4. PWD
5. +15V

**V1 PIN ASSIGNMENTS:**

1. AMP GROUND
2. -VCC 1
3. -VCC 2
4. SPEAKER GROUND
5. +VCC 1
6. +VCC 2



ITEM	QTY.	DESCRIPTION	SIZE
BIAMP SYSTEMS INC.			
ASSEMBLY A 9, 19 POWER SUPPLY			
DATE	REV.	BY	DATE
1977	1	JLD	
1977	2	JLD	
1977	3	JLD	
1977	4	JLD	
1977	5	JLD	
1977	6	JLD	
1977	7	JLD	
1977	8	JLD	
1977	9	JLD	
1977	10	JLD	
1977	11	JLD	
1977	12	JLD	
1977	13	JLD	
1977	14	JLD	
1977	15	JLD	
1977	16	JLD	
1977	17	JLD	
1977	18	JLD	
1977	19	JLD	
1977	20	JLD	
1977	21	JLD	
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1977	23	JLD	
1977	24	JLD	
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1977	26	JLD	
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1977	91	JLD	
1977	92	JLD	
1977	93	JLD	
1977	94	JLD	
1977	95	JLD	
1977	96	JLD	
1977	97	JLD	
1977	98	JLD	
1977	99	JLD	
1977	100	JLD	



THIS SECTION APPLIES TO DOMESTIC TRANSFORMERS ONLY.

THIS SECTION APPLIES TO UNIVERSAL TRANSFORMERS ONLY.

ARMED WIRE

100V	TO WIRE(S)	200V	TO WIRE(S)
200V	OPEN	ACE & R/B	ACE
250V	ACE & W/B	OPEN	RED/BLK
300V	ACE & BLK	ACE & BLK	WHT
	ACE & B/W	ACE & B/W	OPEN
	ACE & W/W	OPEN	ACE & W/W
	ACE & W/T	ACE & W/T	AGE

POWER PROGRAMMING

100V	TO WIRE(S)	200V	TO WIRE(S)
200V	OPEN	ACE & R/B	ACE
250V	ACE & W/B	OPEN	RED/BLK
300V	ACE & BLK	ACE & BLK	WHT
	ACE & B/W	ACE & B/W	OPEN
	ACE & W/W	OPEN	ACE & W/W
	ACE & W/T	ACE & W/T	AGE

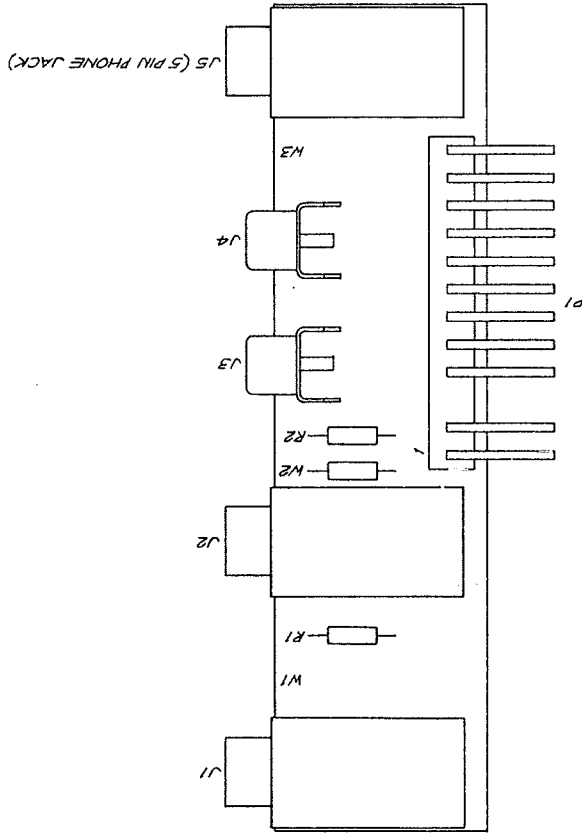
NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS 1/4 W, 5%  
 2. ALL CAPACITORS VALUES IN MICROFARADS  
 UNLESS USED FOR AN-BOARD PROGRAMMING;  
 SUMPS ARE DELETED FROM THIS SCHEMATIC.  
 CHASSIS GROUND FOR 619 & 119 TOTE BOX VERSIONS  
 ONLY. USE INSULATING STANDOFF ON 619 & 119R.

GROUND POINT ON REAR PANEL  
 (SOLDER POINT ON REAR PANEL)

- WHT
- RED
- BLK
- AGE
- W/T
- W/B
- R/B
- W/T
- P1

ITEM	DESCRIPTION	QTY	REV
1	7815	1	1.0
2	7915	1	1.0
3	1000 50V	1	1.0
4	1000 50V	1	1.0
5	1000 50V	1	1.0
6	1000 50V	1	1.0
7	1000 50V	1	1.0
8	1000 50V	1	1.0
9	1000 50V	1	1.0
10	1000 50V	1	1.0
11	1000 50V	1	1.0
12	1000 50V	1	1.0
13	1000 50V	1	1.0
14	1000 50V	1	1.0
15	1000 50V	1	1.0
16	1000 50V	1	1.0
17	1000 50V	1	1.0
18	1000 50V	1	1.0
19	1000 50V	1	1.0
20	1000 50V	1	1.0
21	1000 50V	1	1.0
22	1000 50V	1	1.0
23	1000 50V	1	1.0
24	1000 50V	1	1.0
25	1000 50V	1	1.0
26	1000 50V	1	1.0
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28	1000 50V	1	1.0
29	1000 50V	1	1.0
30	1000 50V	1	1.0
31	1000 50V	1	1.0
32	1000 50V	1	1.0
33	1000 50V	1	1.0
34	1000 50V	1	1.0
35	1000 50V	1	1.0
36	1000 50V	1	1.0
37	1000 50V	1	1.0
38	1000 50V	1	1.0
39	1000 50V	1	1.0
40	1000 50V	1	1.0
41	1000 50V	1	1.0
42	1000 50V	1	1.0
43	1000 50V	1	1.0
44	1000 50V	1	1.0
45	1000 50V	1	1.0
46	1000 50V	1	1.0
47	1000 50V	1	1.0
48	1000 50V	1	1.0
49	1000 50V	1	1.0
50	1000 50V	1	1.0
51	1000 50V	1	1.0
52	1000 50V	1	1.0
53	1000 50V	1	1.0
54	1000 50V	1	1.0
55	1000 50V	1	1.0
56	1000 50V	1	1.0
57	1000 50V	1	1.0
58	1000 50V	1	1.0
59	1000 50V	1	1.0
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62	1000 50V	1	1.0
63	1000 50V	1	1.0
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65	1000 50V	1	1.0
66	1000 50V	1	1.0
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68	1000 50V	1	1.0
69	1000 50V	1	1.0
70	1000 50V	1	1.0
71	1000 50V	1	1.0
72	1000 50V	1	1.0
73	1000 50V	1	1.0
74	1000 50V	1	1.0
75	1000 50V	1	1.0
76	1000 50V	1	1.0
77	1000 50V	1	1.0
78	1000 50V	1	1.0
79	1000 50V	1	1.0
80	1000 50V	1	1.0
81	1000 50V	1	1.0
82	1000 50V	1	1.0
83	1000 50V	1	1.0
84	1000 50V	1	1.0
85	1000 50V	1	1.0
86	1000 50V	1	1.0
87	1000 50V	1	1.0
88	1000 50V	1	1.0
89	1000 50V	1	1.0
90	1000 50V	1	1.0
91	1000 50V	1	1.0
92	1000 50V	1	1.0
93	1000 50V	1	1.0
94	1000 50V	1	1.0
95	1000 50V	1	1.0
96	1000 50V	1	1.0
97	1000 50V	1	1.0
98	1000 50V	1	1.0
99	1000 50V	1	1.0
100	1000 50V	1	1.0

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- P1 PIN ASSIGNMENTS:
- 1. MAIN OUT
  - 2. BLANK
  - 3. VOID
  - 4. ER IN
  - 5. ER IN GROUND
  - 6. TAPE IN
  - 7. MIXER GROUND
  - 8. TAPE OUT
  - 9. BLANK
  - 10. BLANK
  - 11. HEADPHONE OUT
  - 12. HEADPHONE GROUND

REV.	E.C.D.	DATE	BY	CHANGE

ITEM	PART NO.	DESCRIPTION	SIZE	QTY.
		BIAMP SYSTEMS INC.		
		ASSEMBLY A-4, 619 JACK PCB, UPPER		

DATE	CHKD	ENGR	SIZE	DWG. NO.	REV.
5-24-68	SD		C	4-24-68	A

ITEM	DESCRIPTION	MATERIAL	FINISH	MODEL	NEXT DWG.	APP. NO.	YES	NO

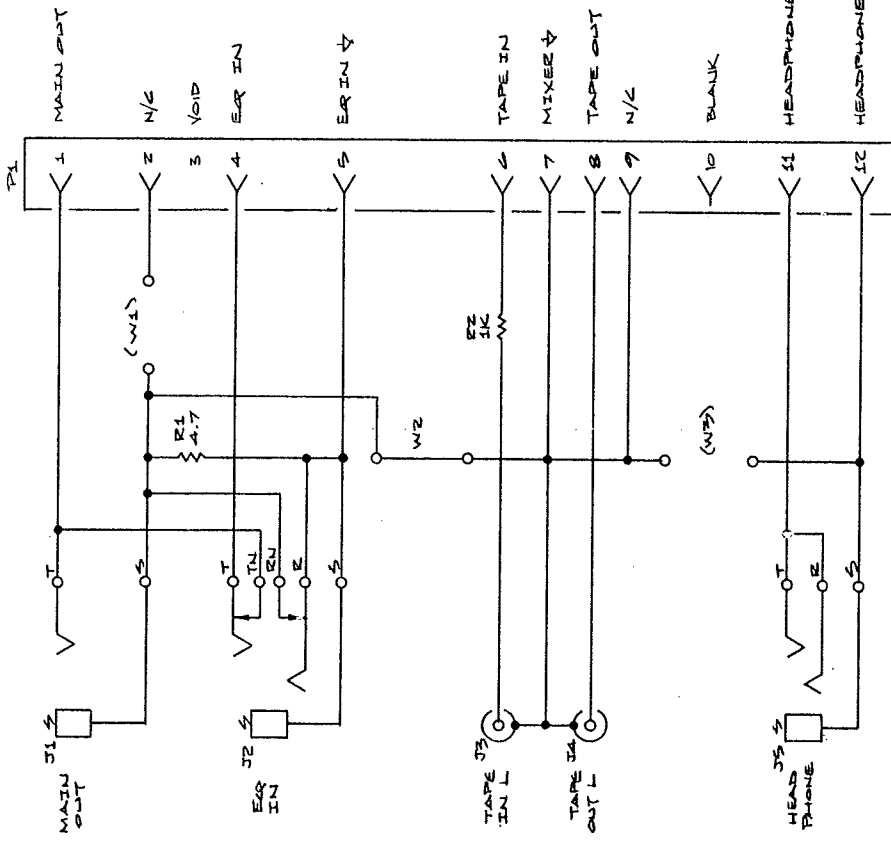
SCALE	SCALE 2X	DO NOT SCALE DRAWING	SHEET 1 OF 1

A B C D

1 2 3 4

A B C D

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NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS 1/4W, ±5%  
 2. ALL RESISTOR VALUES IN OHMS

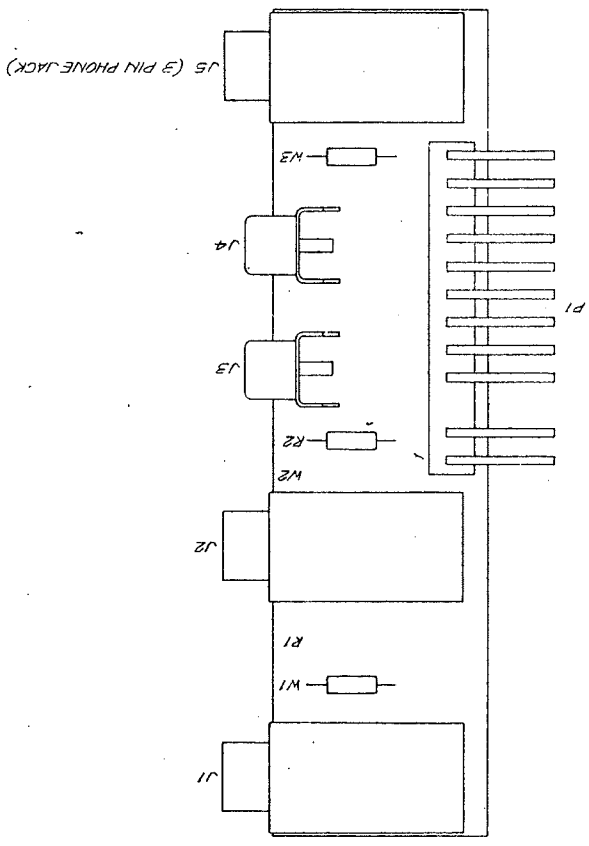
LAST S JS  
 LAST P P1  
 LAST R R2  
 LAST W W3

REV.	E.C.O.	CHANGE	DATE	BY

DATE 7/2/73	PART NO. 201	DESCRIPTION DIGITAL BIAMP SYSTEMS INC.	SIZE C	QTY.
CHECKED TD	DESIGNED TD	SCHEMATIC DIAGRAM 5/19 UPPER DE BD ASSY A4		
ENGINEER	DESIGN	DWG. NO.		
MATERIAL N/A		SCALE N/A		
FINISH N/A	BREAK SHARP EDGES 1/16"		DO NOT SCALE DRAWING	
MODEL	APPLICATION	SHEET 1 OF 1		



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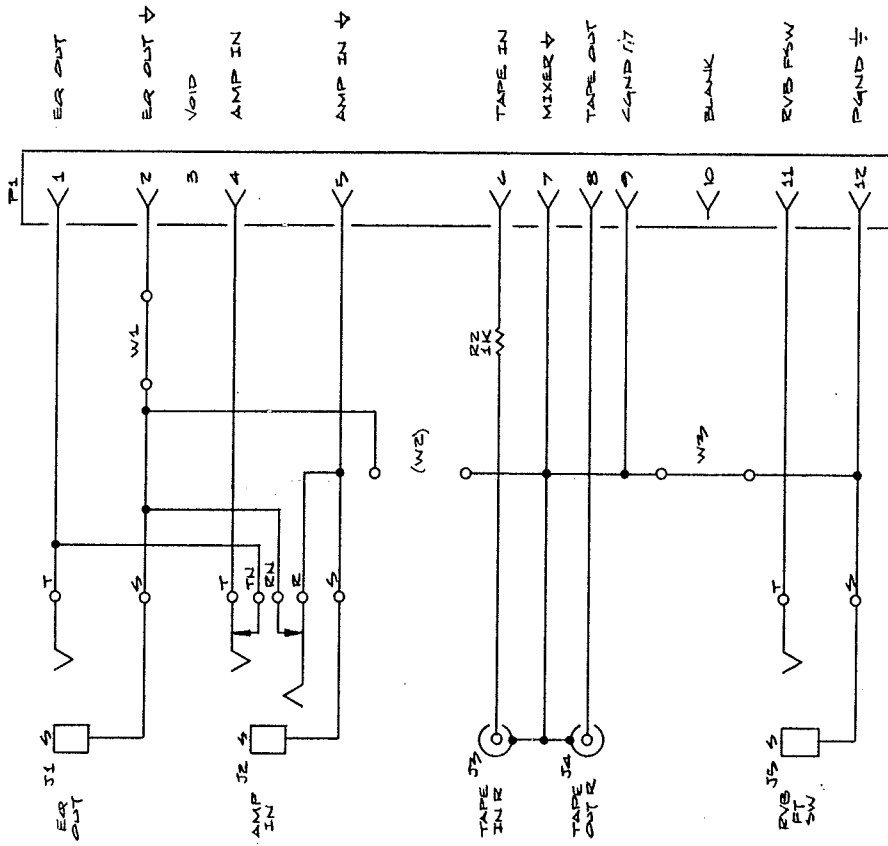
**P1 PIN ASSIGNMENTS:**

1. ER OUT
2. ER OUT GROUND
3. VOID
4. AMP IN
5. AMP IN GROUND
6. TAPE IN
7. MIXER GROUND
8. TAPE OUT
9. CHASSIS GROUND
10. BLANK
11. REVERB FOOTSWITCH
12. POWER GROUND

ITEM	PART NO.	DESCRIPTION	SIZE	QTY.
UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES. TOLERANCES ARE:	DATE	REV. <td>BY <td></td> </td>	BY <td></td>	
DECIMALS FRACTIONS ANGLES	3-24-82			
PLATNESS	JD			
SCALE	ENGINEER			
MATERIAL	DESIGN			
FINISH	APPROVED			
MODEL	W44-06			
NEXT DWG.				
APPLICATION				
BREAK SHARP CORERS AT 45° MAX.				
CHAMFER OR RADIUS .015" MAX.				
YES				
NO				
DO NOT SCALE DRAWING				
SHEET 1 OF 1				

**BIAMP SYSTEMS INC.**  
ASSEMBLY A5, #19 JACK PCB, LOWER

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NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS 1/4 W, ±5%  
 2. ALL RESISTOR VALUES IN OHMS

LAST J JS  
 LAST P PI  
 LAST R RZ  
 LAST W WP

REV.	E.C.O.	CHANGE	DATE	BY

ITEM	PART NO.	DESCRIPTION	SIZE	QTY.

DATE	BY	CHECKED	ENGINEER	DESIGN	SCALE	DWG. NO.	REV.
7/1/72							
11/1/72							

DESCRIPTION	BIAMP SYSTEMS INC.

SCHEMATIC DIAGRAM	2/19 LOWER STACK BO DSSY AS

SCALE	N/A	DO NOT SCALE DRAWING	SHEET 1 OF 1

A B C D

1 2 3 4

18

A

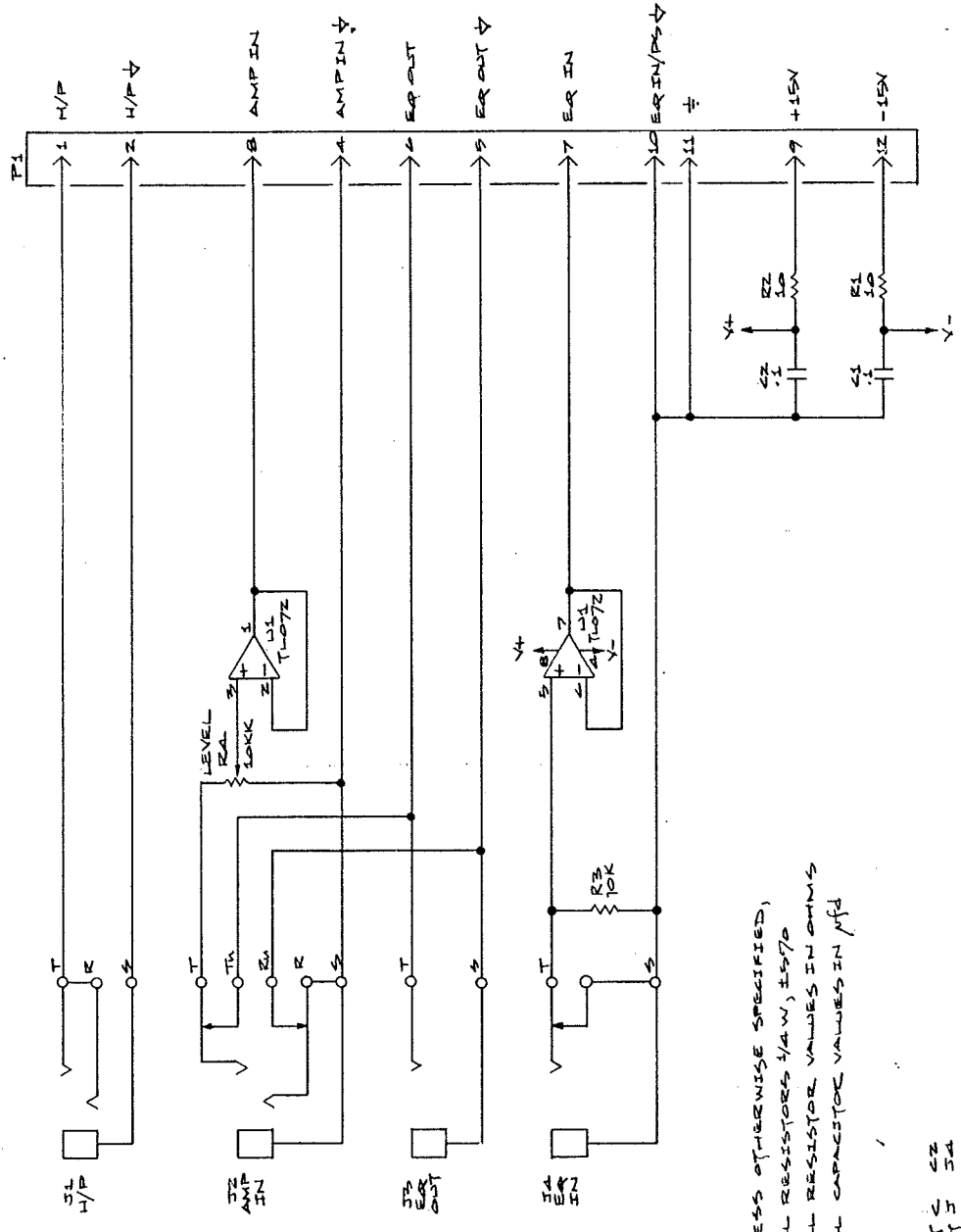
B

C

D



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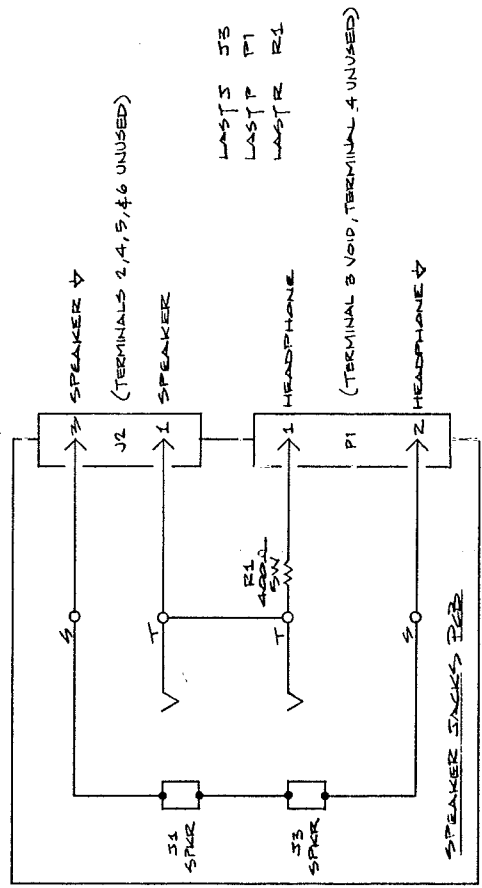
NOTES: UNLESS OTHERWISE SPECIFIED,  
 1. ALL RESISTORS 1/4W, 5%  
 2. ALL RESISTOR VALUES IN OHMS  
 3. ALL CAPACITOR VALUES IN  $\mu$ F

- LAST C
- LAST J
- LAST P
- LAST R
- LAST U
- LAST W

REV.	EQD.	DATE	BY

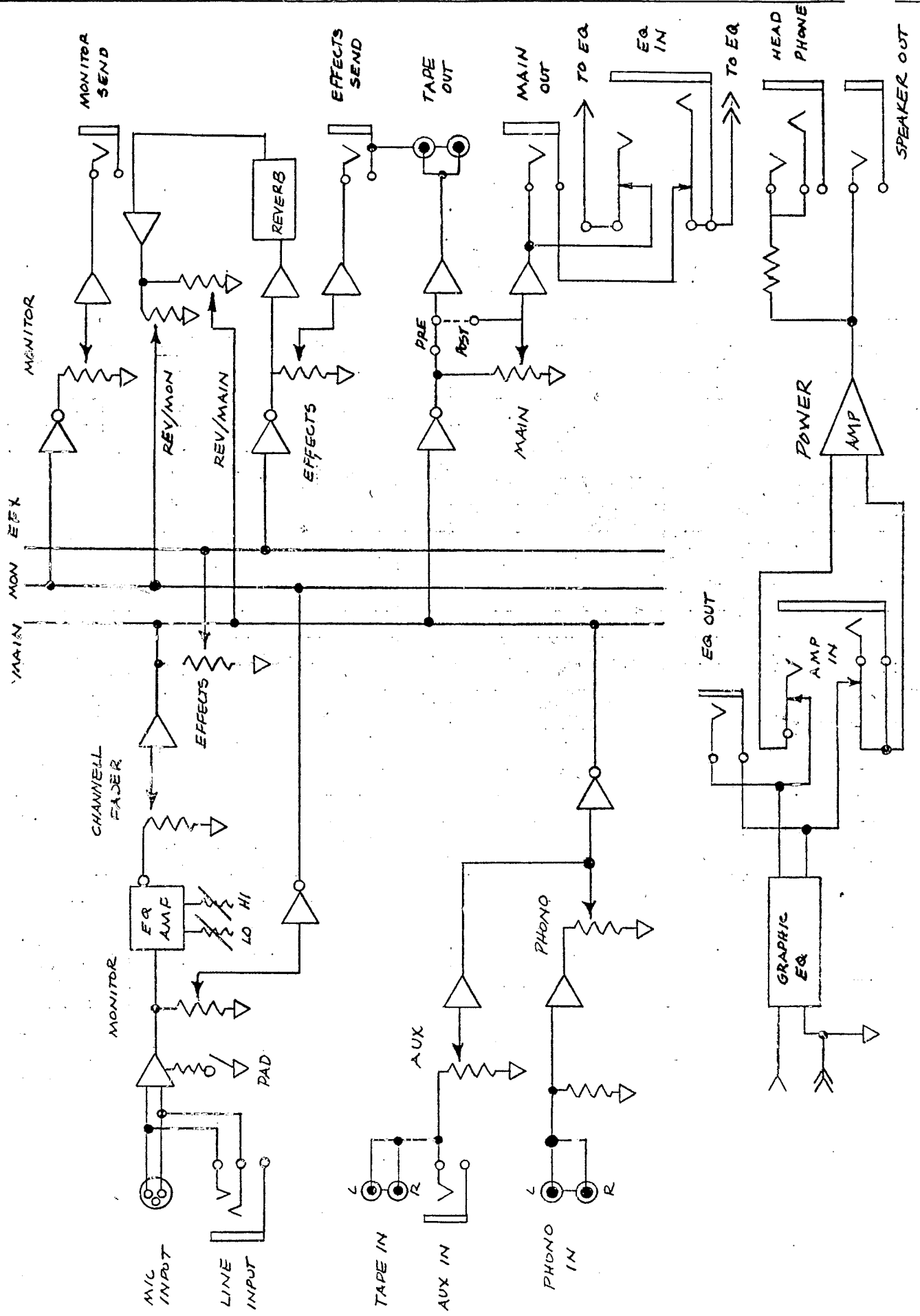
PART NO.		DESCRIPTION		QTY.	
DRAWN		DATE		BIAMP SYSTEMS INC.	
CHECKED		BY		SHEETS	
ENGINEER		DESIGN		SCHEMATIC DIAGRAM	
MATERIAL		FINISH		SCALE N/A	
N/A		N/A		DO NOT SCALE DRAWING	
APPLICATION		REV.		SHEET 1 OF 1	

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LASTS JS  
 LAST P P1  
 WASTE R1

PART NO.		DESCRIPTION		SIZE		QTY.		
DRAWN	DATE	BIAMP SYSTEMS INC.						
CHECKED	2/25/72							
DESIGNED	2/14/72	SCHEMATIC DIAGRAM						
ENGINEER		19 SPEAKER STACKS ASSY/A20						
MATERIAL	N/A							
FINISH	N/A							
MODEL	N/A							
APPLICATION								
UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES, TOLERANCES ARE DECIMALS FRACTIONS ANGLES DEGREES								
JES 1								
JES 2								
JES 3								
MATERIAL	N/A							
FINISH	N/A							
ENCLOSURE FOR ROOMS 91'S MAX.								
YES								
NO								
SCALE	N/A							
SHEET	1 OF 1							

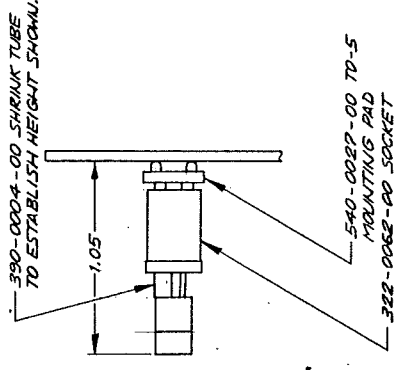


C

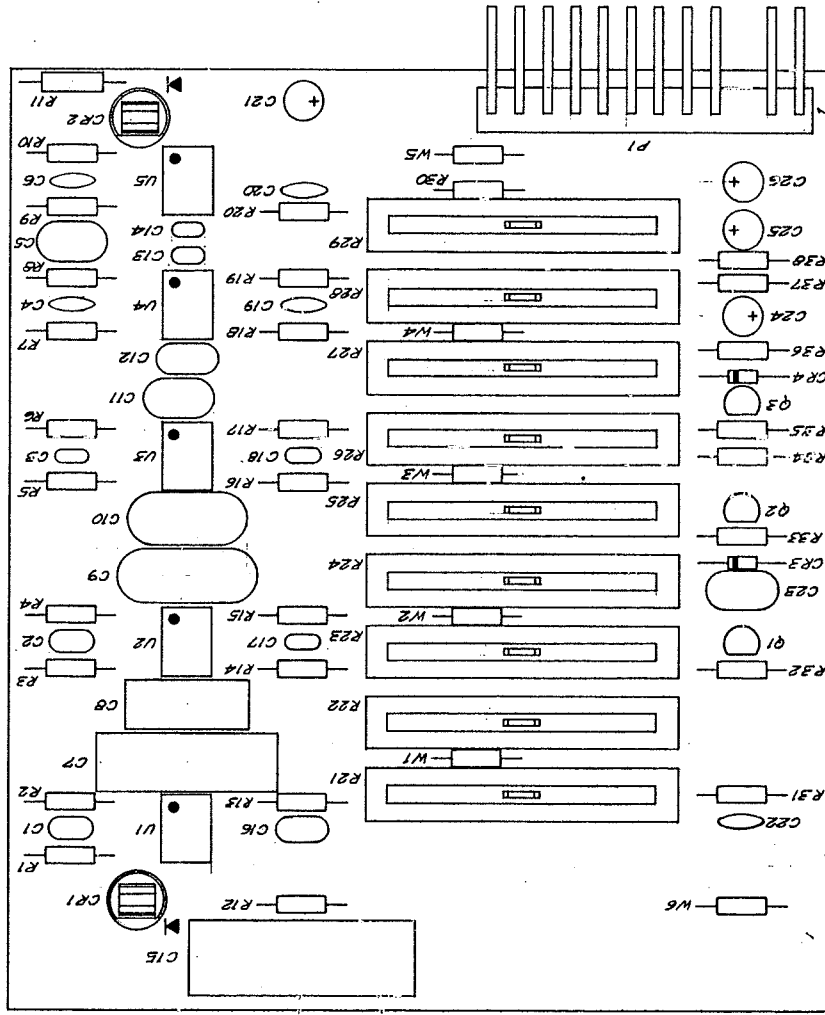
B

A

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- PI PIN ASSIGNMENTS:
1. CHASSIS GROUND
  2. ER IN
  3. VOID
  4. ER IN GROUND
  5. POWER GROUND
  6. PEAK
  7. +VCC 1
  8. +15V
  9. -15V
  10. BLANK
  11. ER OUT GROUND
  12. ER OUT



ITEM	PART NO.	DESCRIPTION	SIZE	QTY.
	390-0004-00	SHRINK TUBE TO ESTABLISH HEIGHT SHOWAL.		
	540-0022P-00	TD-5 MOUNTING PAD		
	322-0062-00	SOCKET		
<b>BIAMP SYSTEMS INC.</b>				
ASSEMBLY 46, 619 GRAPHIC EQ				
DRAWN: J.L. 7/10/82		DESIGNED: J.D. 5-21-82	DATE: 7-23-82	REV: A
CHECKED: J.D. 5-21-82		DESIGNED: J.D. 5-21-82	DATE: 7-23-82	REV: A
ENGINEER: J.D. 5-21-82		DESIGNED: J.D. 5-21-82	DATE: 7-23-82	REV: A
DESIGNER: J.D. 5-21-82		DESIGNED: J.D. 5-21-82	DATE: 7-23-82	REV: A
MATERIALS: J.D. 5-21-82		DESIGNED: J.D. 5-21-82	DATE: 7-23-82	REV: A
FINISH: J.D. 5-21-82		DESIGNED: J.D. 5-21-82	DATE: 7-23-82	REV: A
MODEL: J.D. 5-21-82		DESIGNED: J.D. 5-21-82	DATE: 7-23-82	REV: A
APPLICATION: J.D. 5-21-82		DESIGNED: J.D. 5-21-82	DATE: 7-23-82	REV: A
BREAK PARTS RADIUS 48°		YES: NO		
CHAMFER OR RADIUS 48° MAX.		YES: NO		
SCALE: 2X				
DO NOT SCALE DRAWING				
SHEET / OF 7				

C

B

A