

# **XA Series Power Amplifiers Schematic**

**B I A M P**<sup>®</sup>  
S Y S T E M S

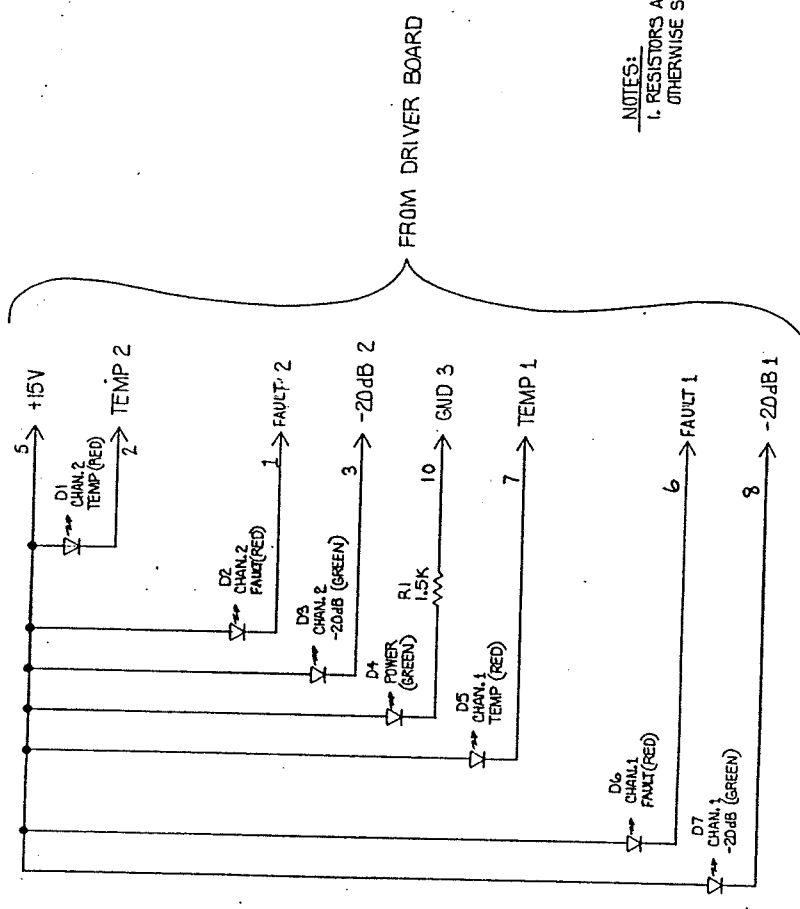
XA Series

# Schematic

**B I A M P<sup>®</sup>**

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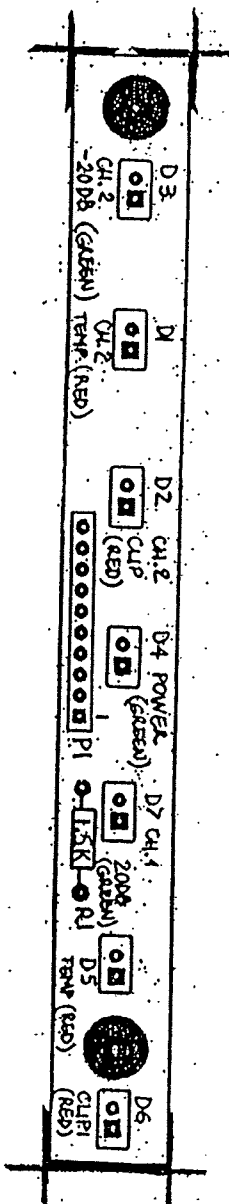
S Y S T E M S



NOTES:  
 1. RESISTORS ARE GIVEN IN OHMS UNLESS OTHERWISE SPECIFIED.

blimp		PROJECT	PROTOTYPE 9.4.24
DATE	REV	NR	
9-5-86			

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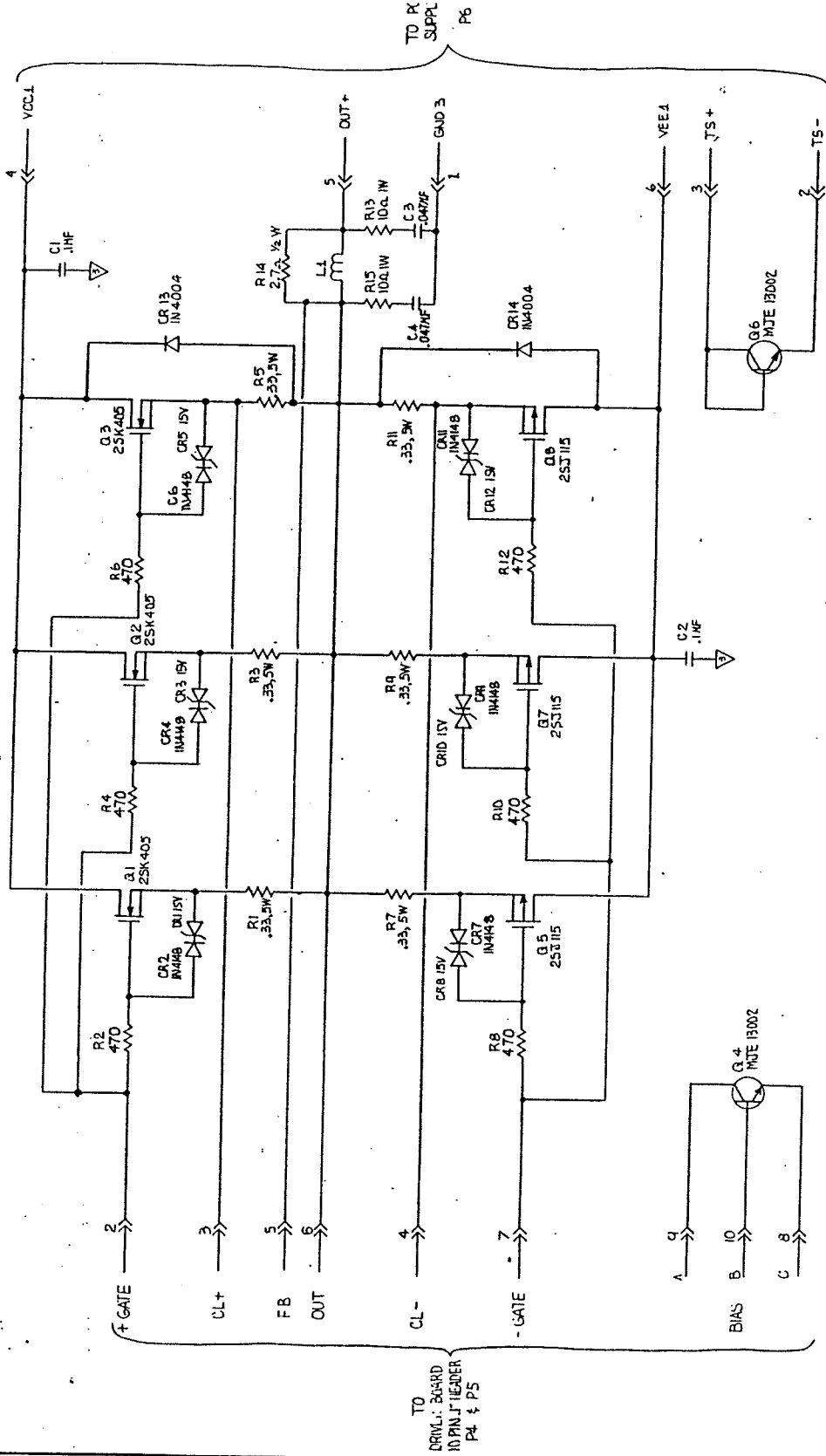


ITEM	PART NO.	DESCRIPTION	SIZE	QTY.
		<b>BIAMP SYSTEMS INC.</b>		
		<b>COMPONENT LAYOUT</b>		
		<b>X A 300 / 600 LED</b>		
		<b>DATE 12/21/20</b>		
		<b>SCALE A1:T5</b>		
		<b>DO NOT SCALE DRAWING</b>		
		<b>SIZE B</b>		
		<b>DWG. NO. 701-0174-00</b>		
		<b>REV.</b>		
		<b>APPROVED</b>		
		<b>DESIGN</b>		
		<b>ENGINEER</b>		
		<b>CHECKED</b>		
		<b>DRAWN</b>		
		<b>ND</b>		
		<b>UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES, TOLERANCES ARE: DECIMALS FRACTIONS ANGLES MATERIAL FLATNESS</b>		
		<b>.XX ±</b>		
		<b>.XXX ±</b>		
		<b>MATERIAL</b>		
		<b>FINISH</b>		
		<b>BREAK SQUARE CORNER CHAMFER OR RADIUS IS MAX.</b>		
		<b>YES</b>		
		<b>NO</b>		
		<b>APPLICATION</b>		
		<b>MODEL</b>		
		<b>NEXT DWG.</b>		

REV.	E.C.O.	CHANGE	DATE	BY

A B C D

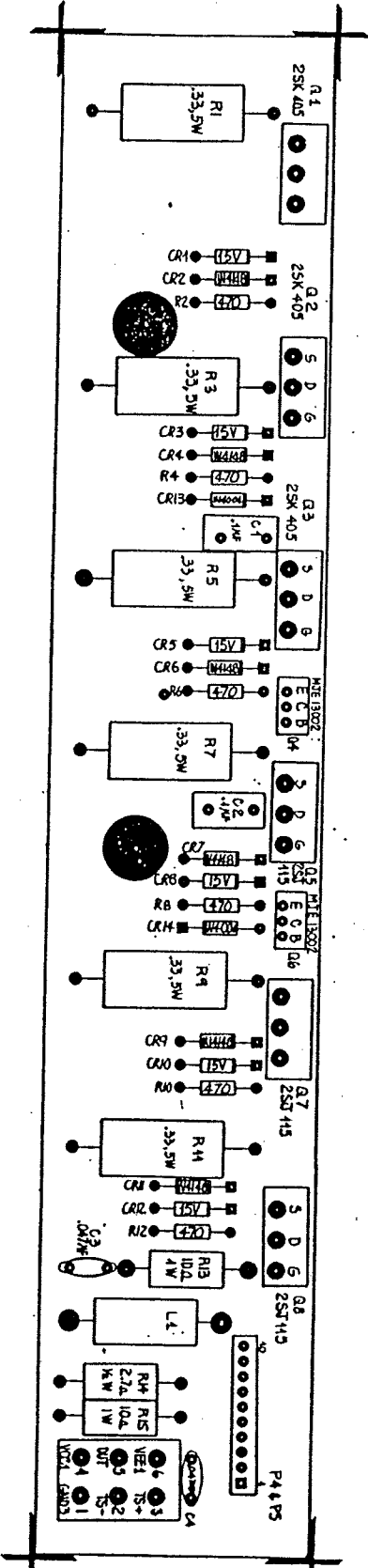
REV.	ECO	CHANGE
A.	1/11-87	CHGD 04-06 FROM TIP24 TO MJE1300Z
DATE		7/81



TO  
DRIVER BOARD  
IN PIN J LEADER  
P4 & P5

NOTE:  
1. FOR XA300, Q2, G7, R4, R10, R3, R4,  
CR3, CR4, CR1, CR10, ARE NOT STUFFED.

biamp	
REV.	1.1
DATE	10-09-86
DESIGNER	PL0T
NO.	
79-07-00	



REDUCE TO 8.000"



REV	DATE	CHANGE	PAGE
A	171-51	QUC'D 84+06 FROM TRPM TO MJE 1302	22947

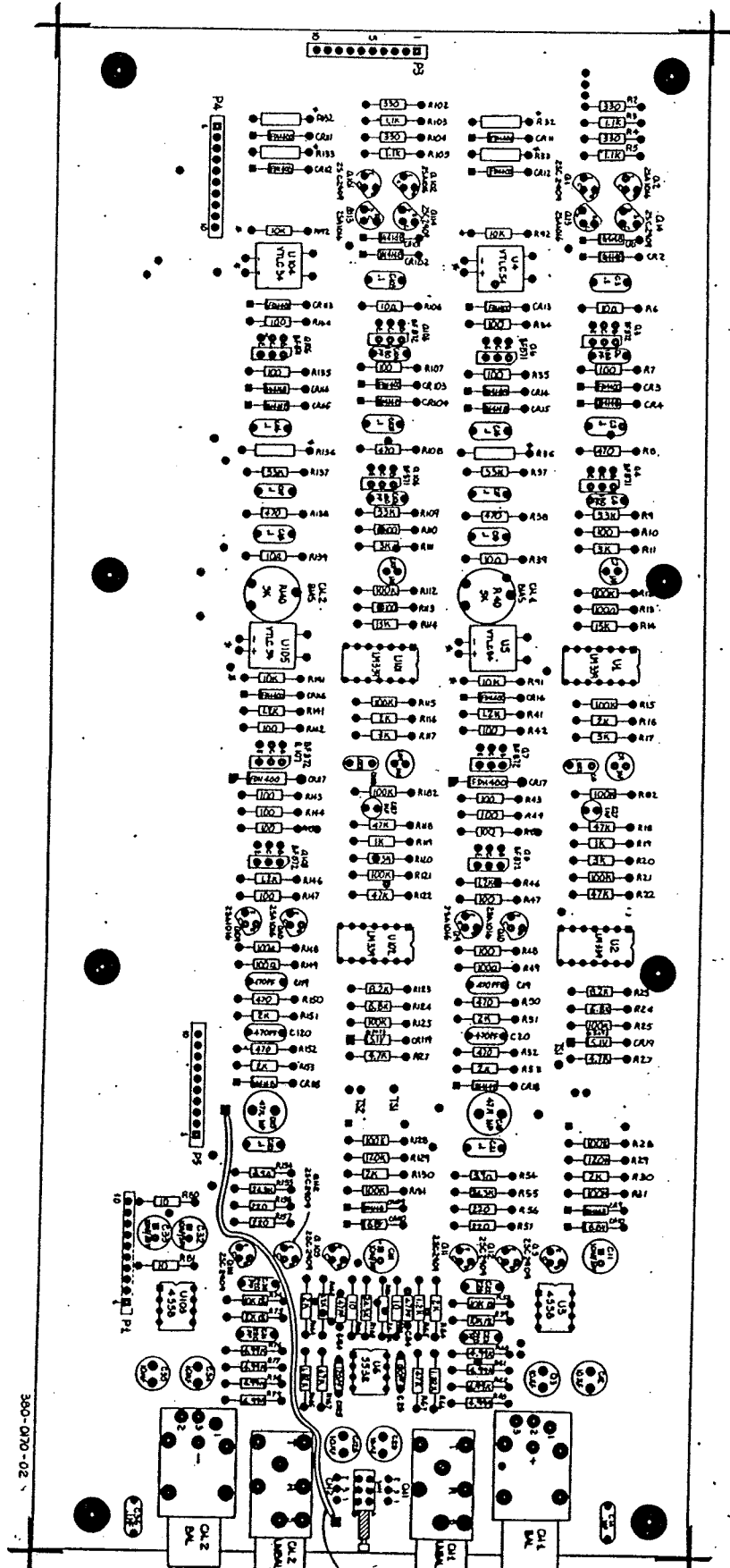
NOTE:  
 1. FOR XA300, Q2, Q7, R4, R10, R3, R9,  
 CR3, CR4, CR7, CR10, ARE NOT STUFFED.

**BIAMP SYSTEMS INC.**  
 1275 E. 12th St.  
 Grand Rapids, MI 49508  
 (616) 231-0001  
 FAX (616) 231-0002



REDUCE TO 7.000"

REDUCE TO 15.000"



U100	74LS00	1K	10K
U101	74LS00	1K	10K
U102	74LS00	1K	10K
U103	74LS00	1K	10K
U104	74LS00	1K	10K
U105	74LS00	1K	10K
U108	74LS00	1K	10K
U109	74LS00	1K	10K
U110	74LS00	1K	10K
U112	74LS00	1K	10K
U113	74LS00	1K	10K
U114	74LS00	1K	10K
U116	74LS00	1K	10K
U117	74LS00	1K	10K
U118	74LS00	1K	10K
U120	74LS00	1K	10K
U121	74LS00	1K	10K
U122	74LS00	1K	10K
U124	74LS00	1K	10K
U125	74LS00	1K	10K
U126	74LS00	1K	10K
U128	74LS00	1K	10K
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U133	74LS00	1K	10K
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U200	74LS00	1K	10K
U201	74LS00	1K	10K
U202	74LS00	1K	10K

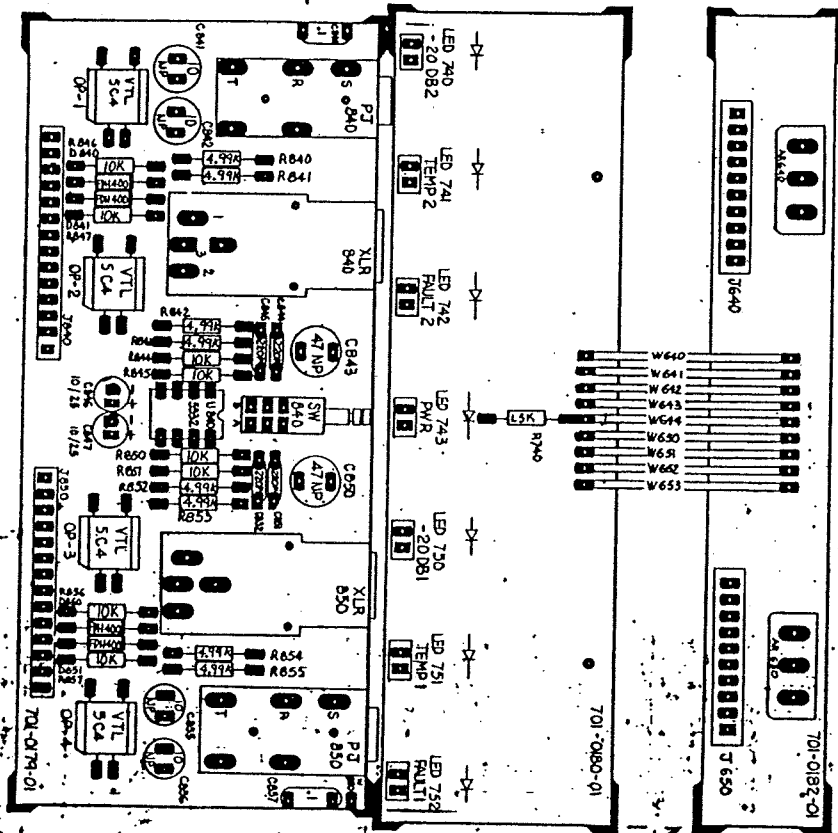
NO.	REV.	DATE	BY
1	1	10/1/80	J. K.
2	1	10/1/80	J. K.
3	1	10/1/80	J. K.
4	1	10/1/80	J. K.
5	1	10/1/80	J. K.
6	1	10/1/80	J. K.
7	1	10/1/80	J. K.
8	1	10/1/80	J. K.
9	1	10/1/80	J. K.
10	1	10/1/80	J. K.

REV.	DATE	DESCRIPTION
001	08-21	PCB COMPONENTS 100% TESTED
002	08-21	SHORT CIRCUIT TEST
003	07-31	UPDATED FOR 300-070-02
004	02-87	FORME ADDITIONAL PAGES 2-4, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100
005	08-27	DO NOT TEST THIS BOARD

Blom Klam  
 COMPONENT LAYOUT  
 YA 300 / 000 PRINTER II







REV	DATE	DESCRIPTION	CHANGE
01		CHG'D CIRCUIT BOARD	
02			
03			

**Blom**

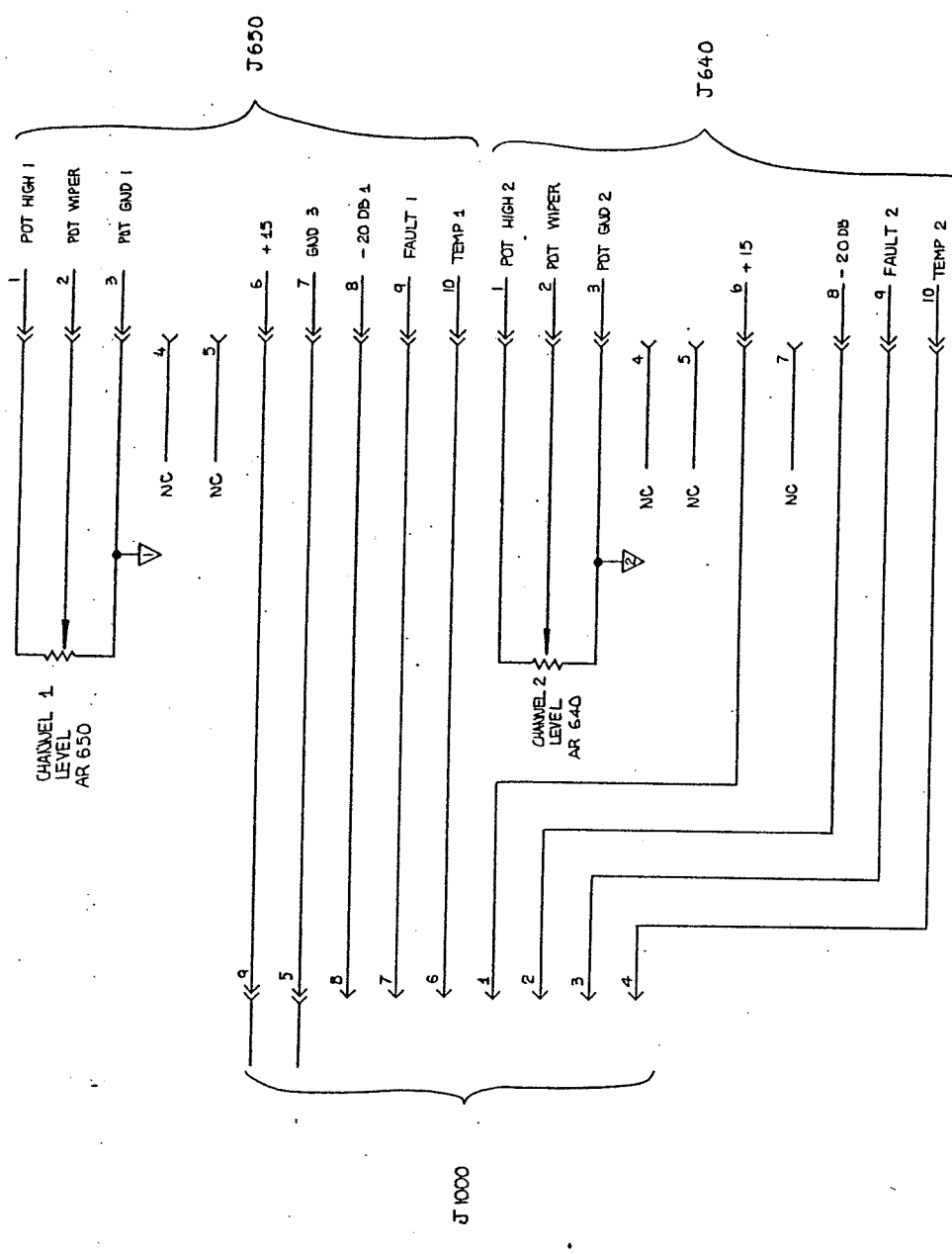
701-0182-01 REV. 01

2 X

3-18-87

IND

REV.	ECN	CHECKED	CHANGE	DATE
-01			CHG'D CIRCUIT BOARD	4-
01A			CORRECTED DRAWING J650 AR 640 WAS J640 & J641.	6-1

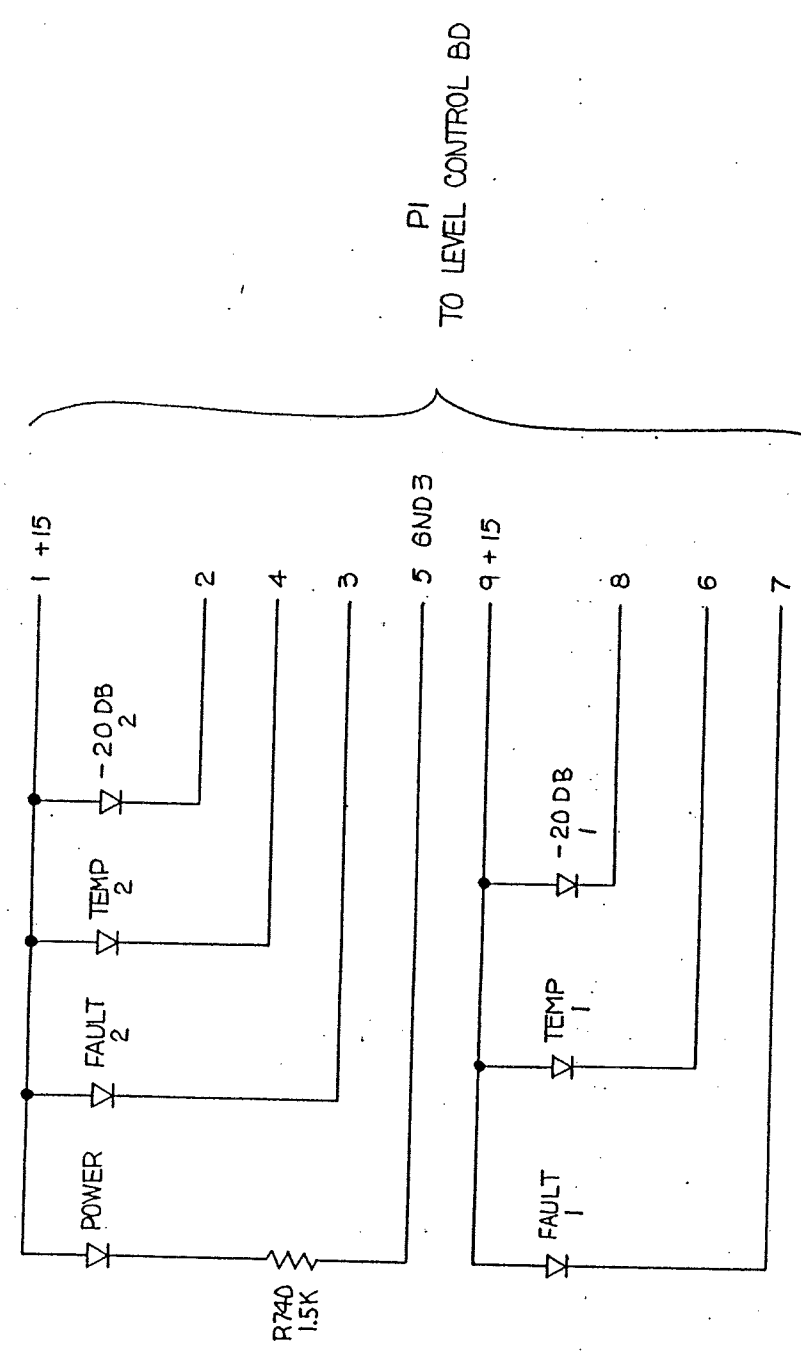


<b>blompp</b>		REV
DATE	BY	RE
11-15-86	ND	

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REV. E.C.D. CHANGE DATE BY

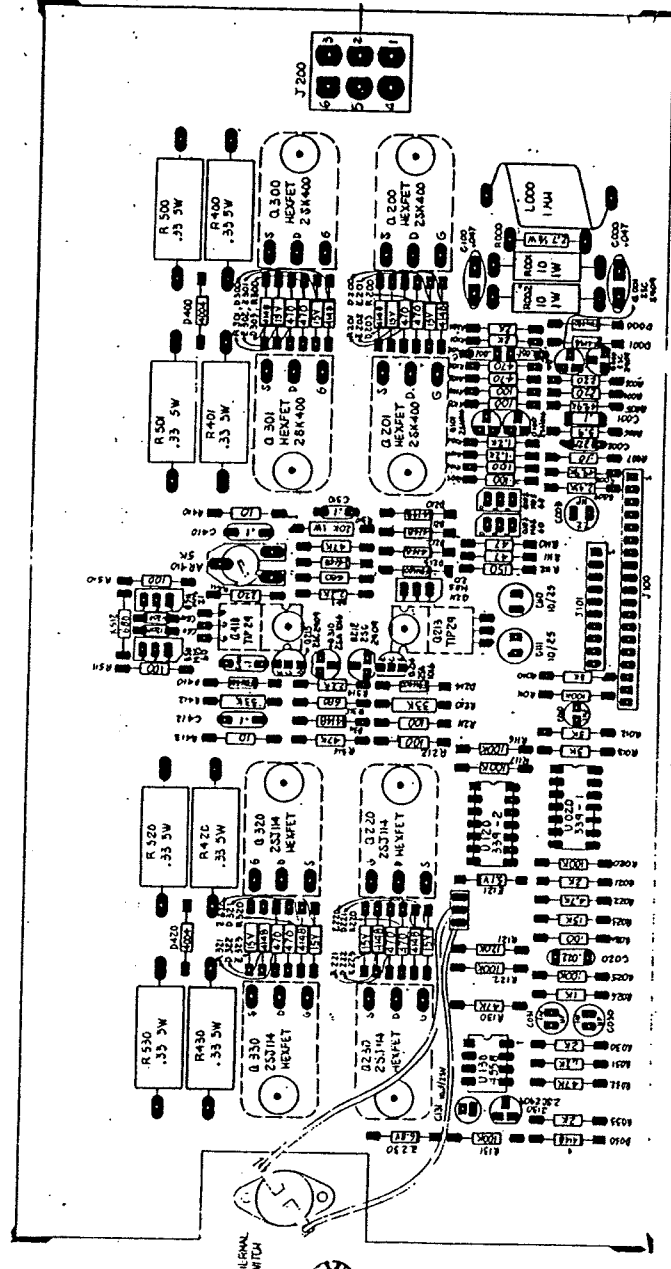
A B C D



PART NO.		DESCRIPTION		SIZE	QTY.
DRAWN	JD	DATE	BIAMP SYSTEMS INC.		
CHECKED		8-3-87	XA 1000		
ENGINEER			LED BOARD		
DESIGN					
APPROVED					
MODEL	NEXT DWG.				
APPLICATION					
				SCALE	REV.
				XXXIE	700-0180-00

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES. TOLERANCES ARE:		MATERIAL	
DECIMALS	FRACTIONS	ANGLES	PLATNESS
.XX ±	±	±	
.XXX ±			
MATERIAL		FINISH	
SHARP EDGES 45°		YES NO	
MINIMUM RADIUS .018 MAX.			

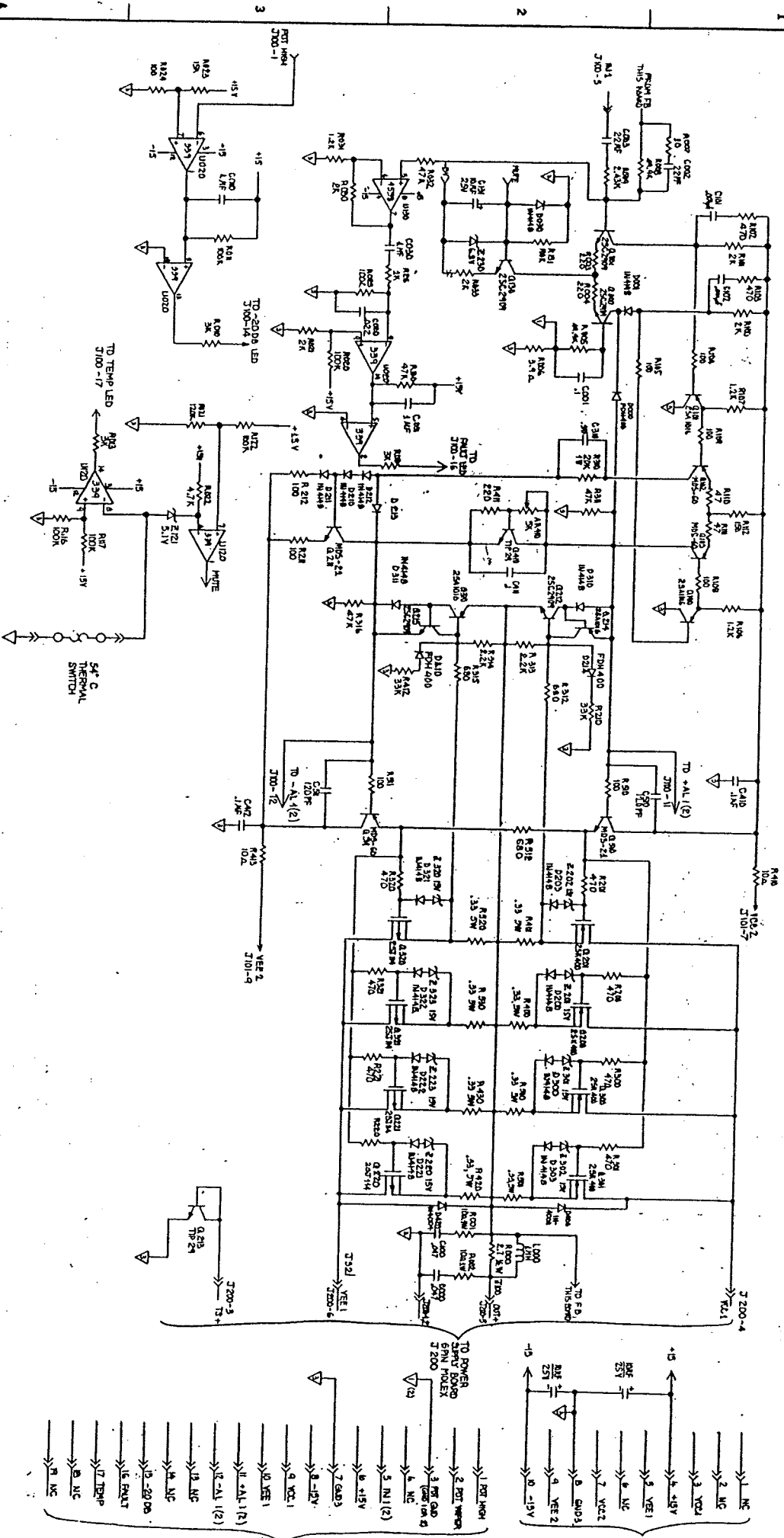
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2					DATE
3					BY
4					CHKD
5					APP'D
6					DESCRIPTION
7					DATE
8					BY
9					CHKD
10					APP'D
11					DESCRIPTION
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13					BY
14					CHKD
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16					DESCRIPTION
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18					BY
19					CHKD
20					APP'D
21					DESCRIPTION
22					DATE
23					BY
24					CHKD
25					APP'D
26					DESCRIPTION
27					DATE
28					BY
29					CHKD
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31					DESCRIPTION
32					DATE
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96					DESCRIPTION
97					DATE
98					BY
99					CHKD
100					APP'D



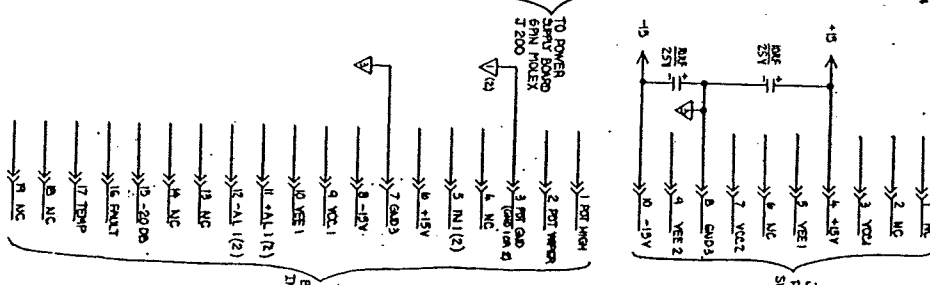
REV	DATE	BY	CHKD	APP'D	DESCRIPTION
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7					DATE
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100					APP'D

REV	DATE	BY	CHKD	APP'D	DESCRIPTION
1					INITIALS
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52					DATE
53					BY
54					CHKD
55					APP'D
56					DESCRIPTION
57					

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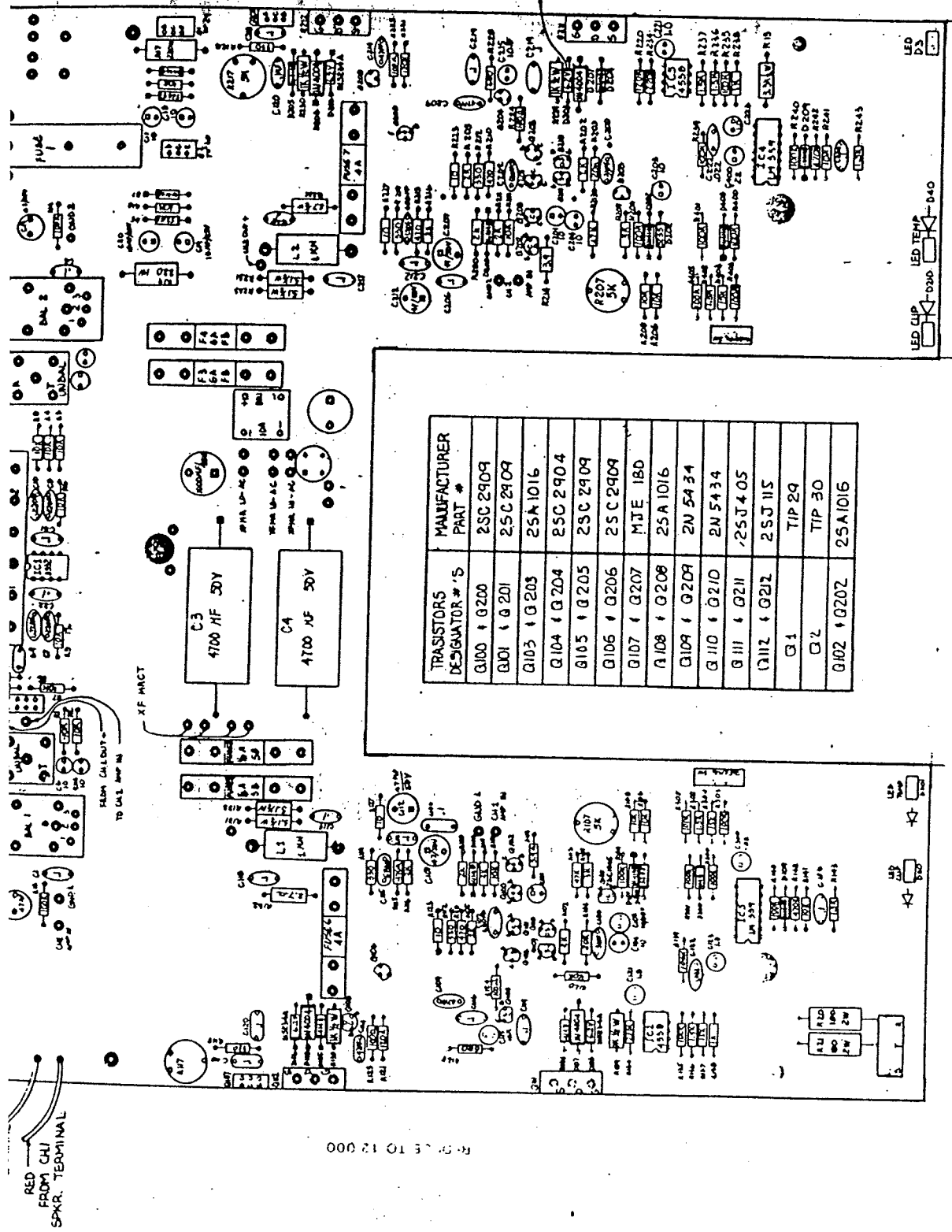
NO.	REV.	DESCRIPTION
1		CHG'S SMART BOARD
2		SMART-CIRCUIT M89











**RAMPA**

N.T.S.

10-13-88

NDT

COMPONENT LAYOUT

XA 1M DARY AMD

