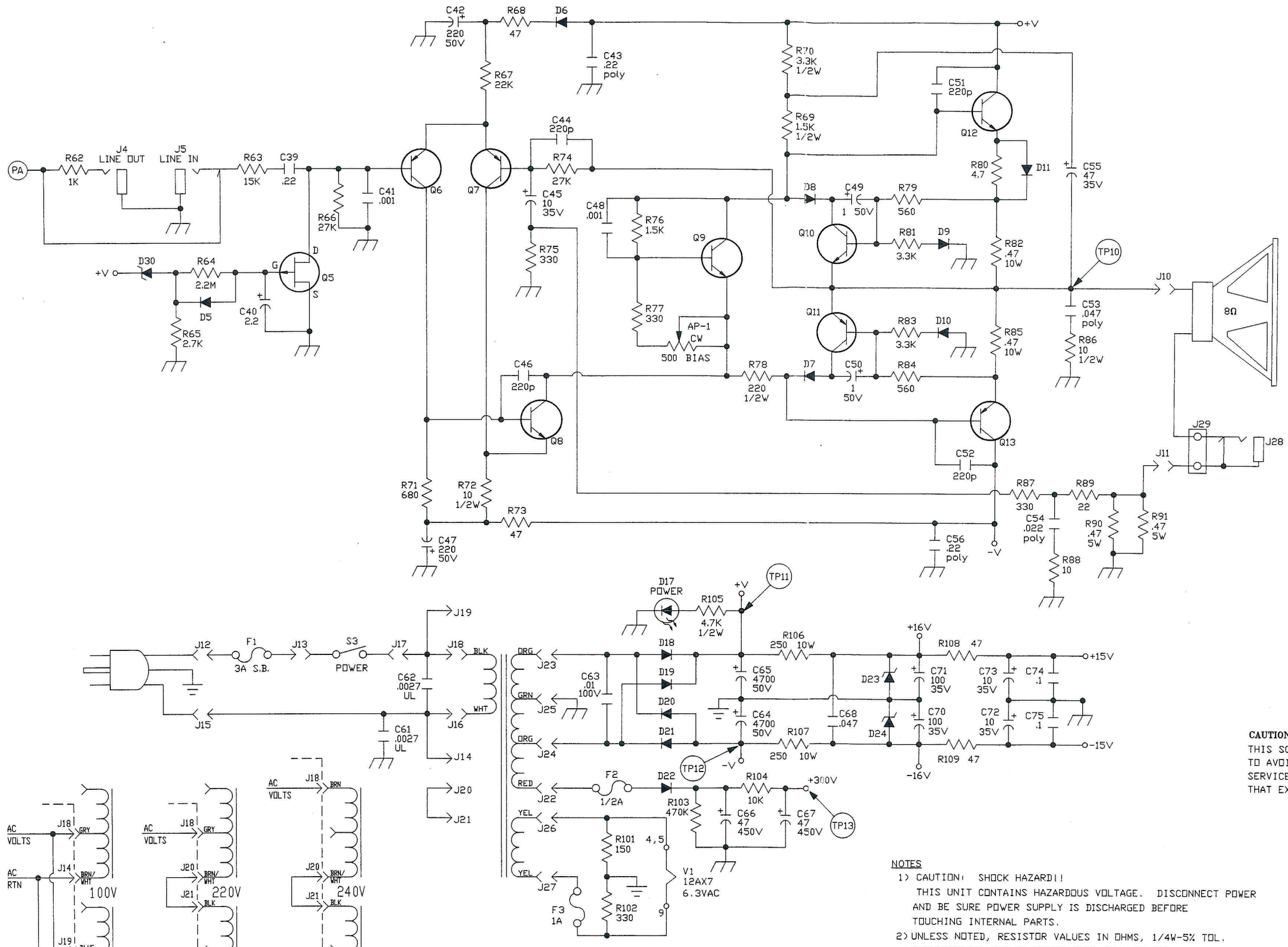

DESIGNATOR	DESCRIPTION	P/N
D1,D2,D31	1N754	21A754-01
D3,D4	1N746	21A146-01
D5-D10	1N914	21A914-01
D11	GI752	21-752-01
D12-D14	1N914	21A914-01
D15	GREEN R/A LED	21-225-01
D16,D17	RED R/A LED	21-224-01
D18-D21	1N5392	21A192-01
D22	1N4007	21A407-01
D23,D24	1N5353B	21-553-01
D25-D29	1N914	21A914-01
D30	1N4747	21A447-01
IC1,IC2	RC4558	37-458-01
IC3	5532	37-552-01
IC4	TL072	37-072-01
Q1,Q4,Q3	J112 N-CH FET	96-112-01
Q2,Q5	J176 FET	96-176-01
Q6,Q7	2N5087	96-587-01
Q8	2N3440	96-340-01
Q9	2N3402	96-342-01
Q10	2N5088	96-588-01
Q11	2N5087	96-587-01
Q12	TIP 142	96-142-01
Q13	TIP 147	96-147-01
Q14,Q15	MPS-A-55	96-055-01
Q16	MPS-A-06	96-006-01
Q17,Q19	J176 FET	96-176-01
Q18	J112 FET	96-112-01
Q20	J176 FET	96-176-01
AP1	500 ohm TRIMPT	71-501-02
P1	100KRA POT	70-104-23
P2,P8	10KA POT	70-103-22
P3,P6	50KL POT	70-133-01
P4	50KRA	70-503-23
P5,P9	250KL POT	70-306-01
P6,P7	250KA POT	70-254-22
P10	10KL POT	70-132-01
J1-J3	CLIFF T/R/S JACK	39-117-01
J4,J5	CLIFF T/S JACK	39-116-01
J6-J11	.187 ST. PC TAB	17-894-01
J12	.250 ST. PC TAB	17-836-01
J13,J17	.187 ST. PC TAB	17-894-01
J14-J16	.250 ST. PC TAB	17-836-01
J18-J27	.250 ST. PC TAB	17-836-01
J28	CLIFF T/S JACK	39-116-01
J29	2 PIN HEADER	17-310-02
F1	3A SB 3AG FUSE	23-303-05
F2	1/2A 3AG N.B.	23-300-01
F3	1A 3AG N.B.	23-301-01
S1	PC SWITCH-BLACK	88-302-03
S2	PC SWITCH-BLACK	88-302-03

CAUTION:

THIS SCHEMATIC IS PROVIDED FOR USE BY QUALIFIED PERSONNEL. TO AVOID RISK OF ELECTRIC SHOCK, REFER SERVICING TO QUALIFIED SERVICE PERSONNEL. DO NOT PERFORM ANY SERVICING BEYOND THAT EXPLAINED IN THE OPERATING INSTRUCTIONS.

NOTES

1) CAUTION! SHOCK HAZARD!! THIS UNIT CONTAINS HAZARDOUS VOLTAGE. DISCONNECT POWER AND BE SURE POWER SUPPLY IS DISCHARGED BEFORE TOUCHING INTERNAL PARTS.
2) UNLESS NOTED, RESISTOR VALUES IN OHMS, 1/4W-5% TOL. CAPACITOR VALUES IN MICROFARADS, 50V-10% TOL.
3) VOLTAGES ARE MEASURED WITH 1 MEGOHM OSCILLOSCOPE AND 10 MEGOHM DIGITAL VOLTMETER.
4) CIRCUIT GROUND ⏚ CHASSIS GROUND ⏚
5) SET BIAS TO 15mV FOR MOTOROLA OUTPUT TRANSISTORS, (10mV FOR TI) ACROSS R65 OR R66 WITH NO SIGNAL AND 8Ω LOAD.

REV	DATE	BY	CHK'D	DESCRIPTION
C				
B				
A	03/21/91	SWR		SEE SHEET 1 of 2

SIGNATURES:		DATE:	11880 BORMAN DR. ST. LOUIS, MISSOURI 63146
DRAWN:	ML	7-25-90	
CHK'D:			PROJECT NAME: TD-70 AMP
APP'D:			
ISSUED:			DRAWING NAME: SCHEMATIC
PLOT DATE:		03/21/91	
PLOT TIME:		10:13:28	DRAWING NO. 16-117-01
FILE NAME:		1611711A	SCALE: NONE SHEET: 2 of 2

NOTES

1) CAUTION: SHOCK HAZARD!! THIS UNIT CONTAINS HAZARDOUS VOLTAGE. DISCONNECT POWER AND BE SURE POWER SUPPLY IS DISCHARGED BEFORE TOUCHING INTERNAL PARTS.
2) UNLESS NOTED, RESISTOR VALUES IN OHMS, 1/4W-5% TOL. CAPACITOR VALUES IN MICROFARADS, 50V-10% TOL.
3) VOLTAGES ARE MEASURED WITH 1 MEGOHM OSCILLOSCOPE AND 10 MEGOHM DIGITAL VOLTMETER.
4) CIRCUIT GROUND ⏚ CHASSIS GROUND ⏚
5) SET BIAS TO 15mV FOR MOTOROLA OUTPUT TRANSISTOR (10mV FOR TI) ACROSS R65 OR R66 WITH NO SIGNAL AND AN 8Ω LOAD.

CAUTION:

THIS SCHEMATIC IS PROVIDED FOR USE BY QUALIFIED PERSONNEL. TO AVOID RISK OF ELECTRIC SHOCK, REFER SERVICING TO QUALIFIED SERVICE PERSONNEL. DO NOT PERFORM ANY SERVICING BEYOND THAT EXPLAINED IN THE OPERATING INSTRUCTIONS.

	CONDITION A: Gain, Level, Sub, Focus, Lead @ "10" 1.6mV rms input signal		CONDITION B: Vol, Low, Mid, High @ "10" 5mV rms input signal	
T.P.	WAVE	VOLTAGES	WAVE	VOLTAGES
1		5mV pp		75mV pp
2		760mV pp		50mV pp
3		36V pp/+150VDC		2.2V pp/+150VDC
4		2.2V pp		— —
5		2.4V pp		— —
6		— —		1V pp
7		— —		.5V pp
8		32V pp		32V pp
9		7.6V pp		1.7V pp
10		74V pp		55V pp
11	—	+38Vdc	—	+38Vdc
12	—	−38Vdc	—	−38Vdc
13	—	+300Vdc	—	+300Vdc
14	—	+14.5Vdc	—	0Vdc
15	—	0Vdc	—	−14.5Vdc

REV	DATE	BY	CHK'D	DESCRIPTION
C				
B				
A	03/21/91	SWR		CHANGED VALUE OF C58 & Q3 P/N ADDED D31 & R128

SIGNATURES:		DATE:	11680 BORMAN DR. ST. LOUIS, MISSOURI 63146
DRAWN:	M.L.	7-25-90	
CHK'D:			PROJECT NAME:
APP'D:		5-28-91	TD-70 AMP
ISSUED:			DRAWING NAME:
PLOT DATE:		03/21/91	SCHEMATIC
PLOT TIME:		10:01:12	DRAWING NO. 16-117-01
FILE NAME:		1611701A	SCALE: NONE SHEET: 1 of 2