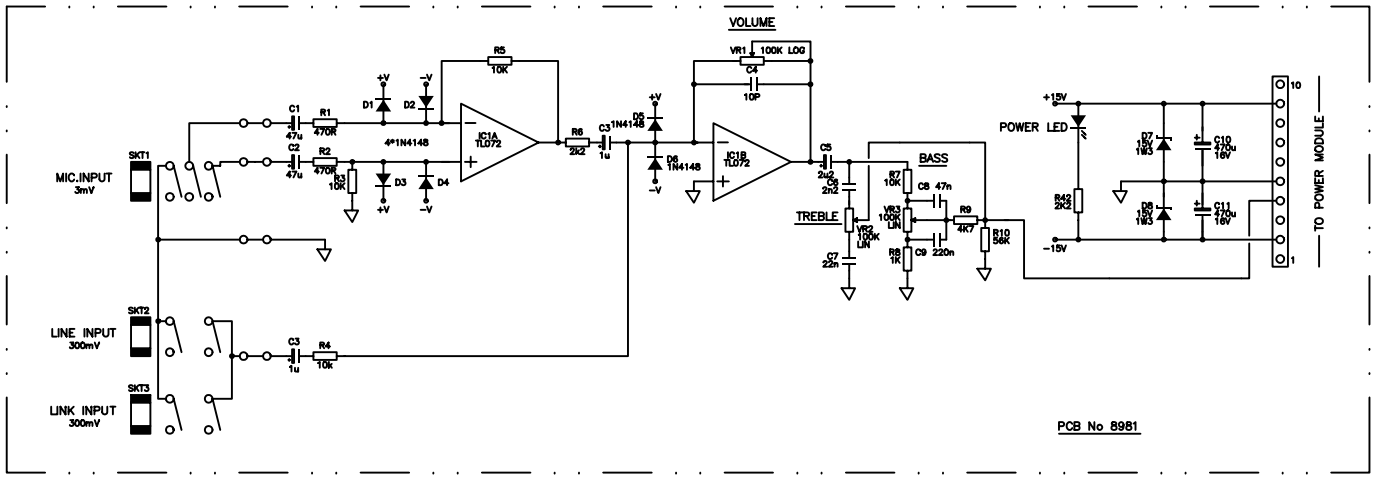
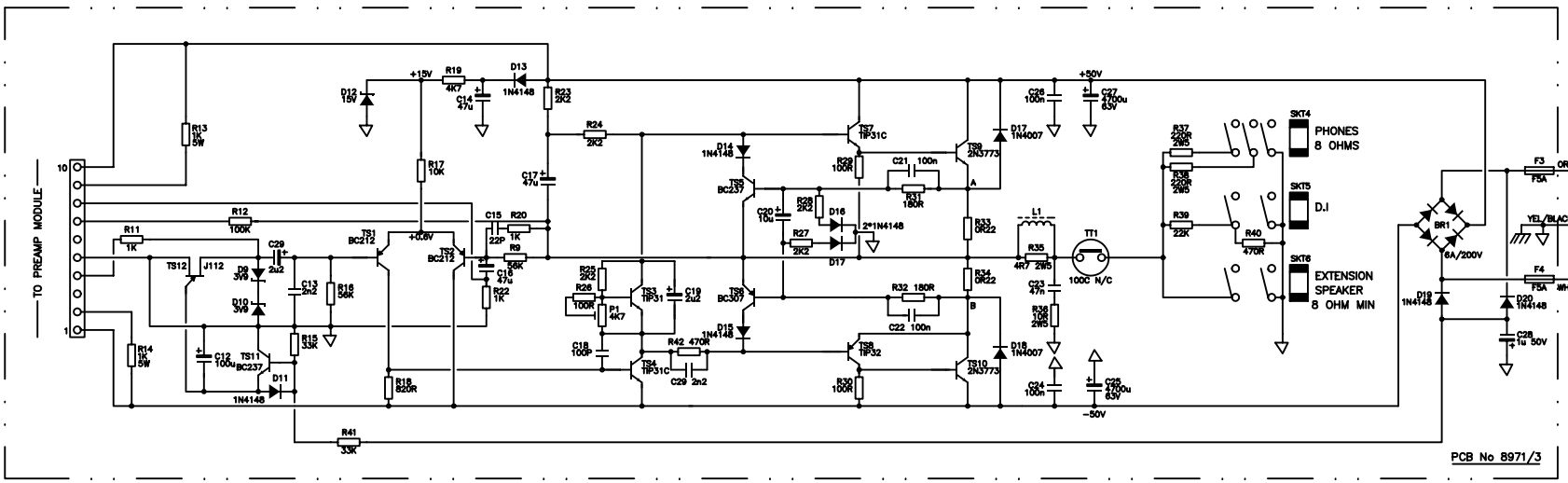
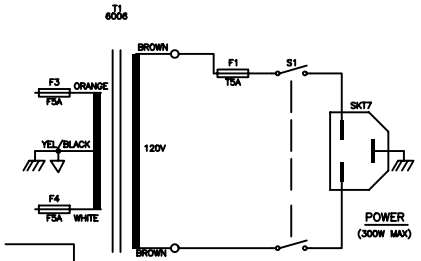
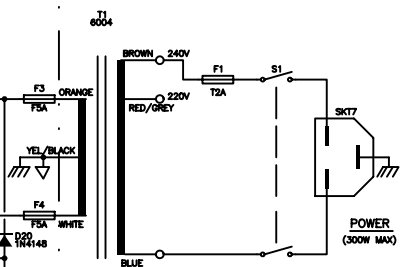
DWG.No. 2098









QUIESCENT CURRENT ADJUSTMENT :- SET P1 FOR 10-20mV BETWEEN POINTS A&B.

ISSUE	DESCRIPTION	APPD.	DATE
	Minor resistor changes, Refer to change note 231 - 8-3-99		

BLT Industries Ltd.

TITLE

IS115P
CIRCUIT DIAGRAM
PART No

DRAWN	TRACED	CHECKED	APPROVED	DATE
R.S.T				28/11/88

DRAWING No. 2098