

Studer OnAir 1000

Digital Broadcast Mixing Console, SW V4.0/4.03



Operating and Service Instructions

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Subject to change

A Safety Information

| CAUTION RISK OF ELECTRIC SHOCK DO NOT OPEN ATTENTION RISQUE DE CHOC ELECTRIQUE NE PAS OUVRIR ACHTUNG GEFAHR: ELEKTRISCHER SCHLAG NICHTÖFFNEN | To reduce the risk of electric shock, do not remove covers. No user- serviceable parts inside. Refer servicing to qualified service personnel (i.e., persons having appropriate technical training and experience neces- sary to be aware of hazards to which they are exposed in performing a repair action, and of measures to minimize the danger of themselves). |
|--|---|
| Â | This symbol alerts the user to the presence of un-insulated <i>dangerous voltage</i> within the equipment that may be of sufficient magnitude to constitute a risk of electric shock to a person. |
| \bigwedge | This symbol alerts the user to <i>important instructions</i> for operating and maintenance in this documentation. |
| CLASS I LED PRODUCT CLASS I LASER PRODUCT | Assemblies or sub-assemblies of this product can contain opto-electronic devices. As long as these devices comply with Class I of laser or LED products according to EN 60825-1:1994, they will not be expressly marked on the product. If a special design should be covered by a higher class of this standard, the device concerned will be marked directly on the assembly or sub-assembly in accordance with the above standard. |

A1 First Aid

In Case of Electric Shock:

If the Person is Unconscious:

Separate the person as quickly as possible from the electric power source:

- By switching off the equipment,
- By unplugging or disconnecting the mains cable, or
- By pushing the person away from the power source, using dry, insulating material (such as wood or plastic).
- After having suffered an electric shock, *always* consult a doctor.



Do not touch the person or his clothing before the power is turned off, otherwise you stand the risk of suffering an electric shock as well!

- Lay the person down
- Turn him to one side
- Check the pulse
- Reanimate the person if respiration is poor
- Call for a doctor immediately.

| В | General Instal | ation Instructions |
|------------|-------------------|---|
| | | Please consider besides these general instructions also any product-specific instructions in the "Installation" chapter of this manual. |
| B1 | Unpacking | |
| | | Check the equipment for any transport damage. If the unit is mechanically damaged, if liquids have been spilled or if objects have fallen into the unit, <i>it must not be connected to the AC power outlet, or it must be immediately disconnected by unplugging the power cable.</i> Repair must only be performed by trained personnel in accordance with the applicable regulations. |
| B2 | Installation Site | |
| | | Install the unit in a place where the following conditions are met: The temperature and the relative humidity of the environment must be within the specified limits during operation of the unit. Relevant values are the ones at the air inlets of the unit. Condensation must be avoided. If the unit is installed in a location with large variation of ambient temperature (e.g. in an OB-van), appropriate precautions must be taken before and after operation (for details on this subject, refer to Appendix 1). Unobstructed air flow is essential for proper operation. Air vents of the unit are a functional part of the design and must not be blocked in any way during operation (e.g. by objects placed upon them, placement of the unit on a soft surface, or installation of the unit within a rack or piece of furniture). The unit must not be heated up by external sources of heat radiation (sunlight, spot lights). |
| B 3 | Earthing and Pov | wer Supply |
| | | Earthing of units with mains supply (class I equipment) is performed via the protective earth (PE) conductor integrated in the mains cable. Units with battery operation (< 60 V, class III equipment) must be earthed sepa- rately. Earthing the unit is one of the measures for protection against electrical shock hazard (dangerous body currents). Hazardous voltage may not only be caused by a defective power supply insulation, but may also be intro- duced by the connected audio or control cables. If the unit is installed with one or several external connections, its earthing must be provided during operation as well as while the unit is not operated. If the earthing connection can be interrupted, for example, by unplugging the mains plug of an external power supply unit, an additional, permanent earthing connection must be installed using the provided earth terminal. Avoid ground loops (hum loops) by keeping the loop surface as small as possible (by consequently guiding the earth conductors in a narrow, paral- lel way), and reduce the noise current flowing through the loop by inserting an additional impedance (common-mode choke). |

Class I Equipment (Mains Operation)

Should the equipment be delivered without a matching mains cable, the latter has to be prepared by a trained person using the attached female plug (IEC320/C13 or IEC320/C19) with respect to the applicable regulations in your country.

Before connecting the equipment to the AC power outlet, check that the local line voltage matches the equipment rating (voltage, frequency) within the admissible tolerance. The equipment fuses must be rated in accordance with the specifications on the equipment.

Equipment supplied with a 3-pole appliance inlet (protection conforming to class I equipment) *must* be connected to a 3-pole AC power outlet so that the equipment cabinet is connected to the protective earth.

For information on mains cable strain relief please refer to Appendix 2.



Class III Equipment (Battery Operation up to 60 V_{DC})

Equipment of this protection class must be earthed using the provided earth terminal, if one or more external signals are connected to the unit (see explanation at the beginning of this paragraph).

B4 Electromagnetic Compatibility (EMC)

The unit conforms to the protection requirements relevant to electromagnetic phenomena that are listed in guidelines 89/336/EC and FCC, part 15.

- The electromagnetic interference generated by the unit is limited in such a way that other equipment and systems can be operated normally.
- The unit is adequately protected against electromagnetic interference so that it can operate properly.

The unit has been tested and conforms to the EMC standards of the specified electromagnetic environment, as listed in the following declaration. The limits of these standards ensure protection of the environment and corresponding noise immunity of the equipment with appropriate probability. However, a professional installation and integration within the system are imperative prerequisites for operation without EMC problems.

For this purpose, the following measures must be followed:

- Install the equipment in accordance with the operating instructions. Use the supplied accessories.
- In the system and in the vicinity where the equipment is installed, use only components (systems, equipment) that also fulfill the EMC standards for the given environment.
- Use a system grounding concept that satisfies the safety requirements (class I equipment must be connected with a protective ground conduc-

tor) and that also takes into consideration the EMC requirements. When deciding between radial, surface, or combined grounding, the advantages and disadvantages should be carefully evaluated in each case.

- Use shielded cables where shielding is specified. The connection of the shield to the corresponding connector terminal or housing should have a large surface and be corrosion-proof. Please note that a cable shield connected only single-ended can act as a transmitting or receiving antenna within the corresponding frequency range.
- Avoid ground loops or reduce their adverse effects by keeping the loop surface as small as possible, and reduce the noise current flowing through the loop by inserting an additional impedance (e.g. common-mode choke).
- Reduce electrostatic discharge (ESD) of persons by installing an appropriate floor covering (e.g. a carpet with permanent electrostatic filaments) and by keeping the relative humidity above 30%. Further measures (e.g. conducting floor) are usually unnecessary and only effective if used together with corresponding personal equipment.
- When using equipment with touch-sensitive operator controls, please take care that the surrounding building structure allows for sufficient capacitive coupling of the operator. This coupling can be improved by an additional, conducting surface in the operator's area, connected to the equipment housing (e.g. metal foil underneath the floor covering, carpet with conductive backing).

C Maintenance

All air vents and openings for operating elements (faders, rotary knobs) must be checked on a regular basis, and cleaned in case of dust accumulation. For cleaning, a soft paint-brush or a vacuum cleaner is recommended. Cleaning the surfaces of the unit is performed with a soft dry cloth or a

Cleaning the surfaces of the unit is performed with a soft, dry cloth or a soft brush.

Persistent contamination can be treated with a cloth that is slightly humidified with a mild cleaning solution (soap-suds).

For cleaning display windows, commercially available computer/TV screen cleaners are suited. Use only a slightly damp (never wet) cloth.

Never use any solvents for cleaning the exterior of the unit! Liquids must never be sprayed or poured on directly!

For equipment-specific maintenance information please refer to the corresponding chapter in the Operating and Service Instructions manuals.

D Electrostatic Discharge during Maintenance and Repair

Caution:



Observe the precautions for handling devices sensitive to electrostatic discharge!

Many semiconductor components are sensitive to electrostatic discharge (ESD). The life-span of assemblies containing such components can be drastically reduced by improper handling during maintenance and repair work. Please observe the following rules when handling ESD sensitive components:

- ESD sensitive components should only be stored and transported in the packing material specifically provided for this purpose.
- When performing a repair by replacing complete assemblies, the removed assembly must be sent back to the supplier in the same packing

material in which the replacement assembly was shipped. If this should not be the case, any claim for a possible refund will be null and void.

- Unpacked ESD sensitive components should only be handled in ESD protected areas (EPA, e.g. area for field service, repair or service bench) and only be touched by persons who wear a wristlet that is connected to the ground potential of the repair or service bench by a series resistor. The equipment to be repaired or serviced as well as all tools and electrically semi-conducting work, storage, and floor mats should also be connected to this ground potential.
- The terminals of ESD sensitive components must not come in uncontrolled contact with electrostatically chargeable (voltage puncture) or metallic surfaces (discharge shock hazard).
- To prevent undefined transient stress of the components and possible damage due to inadmissible voltages or compensation currents, electrical connections should only be established or separated when the equipment is switched off and after any capacitor charges have decayed.

E Repair

Removal of housing parts, shields, etc. exposes energized parts. For this reason the following precautions must be observed:

- Maintenance may only be performed by trained personnel in accordance with the applicable regulations.
- The equipment must be switched off and disconnected from the AC power outlet before any housing parts are removed.
- Even if the equipment is disconnected from the power outlet, parts with hazardous charges (e.g. capacitors, picture tubes) must not be touched until they have been properly discharged. Do not touch hot components (power semiconductors, heat sinks, etc.) before they have cooled off.
- If maintenance is performed on a unit that is opened and switched on, no un-insulated circuit components and metallic semiconductor housings must be touched, neither with your bare hands nor with un-insulated tools.

Certain components pose additional hazards:

- *Explosion hazard* from lithium batteries, electrolytic capacitors and power semiconductors (watch the component's polarity. Do not short battery terminals. Replace batteries only by the same type).
- Implosion hazard from evacuated display units.
- *Radiation hazard* from laser units (non-ionizing), picture tubes (ionizing).
- *Caustic effect* of display units (LCD) and components containing liquid electrolyte.

Such components should only be handled by trained personnel who are properly protected (e.g. safety goggles, gloves).

E1 SMD Components

Studer has no commercially available SMD components in stock for service purposes. For repair, the corresponding devices have to be purchased locally. The specifications of special components can be found in the service manual.

SMD components should only be replaced by skilled specialists using appropriate tools. No warranty claims will be accepted for circuit boards that have been damaged. Proper and improper SMD soldering joints are illustrated below.



F Disposal

Disposal of Packing Materials
 The packing materials have been selected with environmental and disposal issues in mind. All packing material can be recycled. Recycling packing saves raw materials and reduces the volume of waste. If you need to dispose of the transport packing materials, please try to use recyclable means.
 Disposal of Used Equipment
 Used equipment contains valuable raw materials as well as materials that must be disposed of professionally. Please return your used equipment via an authorized specialist dealer or via the public waste disposal system, ensuring any material that can be recycled is. Please take care that your used equipment cannot be abused. To avoid abuse, delete sensitive data from any data storage media. After having disconnected your used equipment from the mains supply, make sure that the mains connector and the mains cable are made useless.

G Declarations of Conformity

G1 Class A Equipment - FCC Notice

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide a reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference, in which case the user will be required to correct the interference at his own expense.

G2 CE Declaration of Conformity

We,

Studer Professional Audio GmbH, CH-8105 Regensdorf, declare under our sole responsibility that the product Studer OnAir 1000, Digital Mixing Console, (starting with serial no. 1000) to which this declaration relates, according to following regulations of EU directives and amendments

- Low Voltage (LVD): 73/23/EEC + 93/68/EEC
- Electromagnetic Compatibility (EMC): 89/336/EEC + 92/31/EEC + 93/68/EEC

is in conformity with the following standards or normative documents:

- Safety:
 - EN 60950:2000 (Class I equipment)
- Safety of laser products: EN 60825-1:1994 + A11 + A2, EN60825-2:2000
 - EMC: EN 55103-1/-2:1996, electromagnetic environments E2 and E4.

Regensdorf, December 18, 2000



B. Hochstrasser, President

P. Fiala, Manager QA

Caution: Any changes or modifications not expressly approved by the manufacturer could void the user's authority to operate the equipment. Also refer to relevant information in this manual.

Appendix 1: Air Temperature and Humidity

| General |
|---------|
|---------|

Normal operation of the unit or system is warranted under the following ambient conditions defined by EN 60721-3-3, set IE32, value 3K3. This standard consists of an extensive catalogue of parameters, the most important of which are: ambient temperature +5...+40 °C, relative humidity 5...85% (i.e., no formation of condensation or ice); absolute humidity 1...25 g/m³; rate of temperature change < 0.5 °C/min. These parameters are dealt with in the following paragraphs. Under these conditions the unit or system starts and works without any problem. Beyond these specifications, possible problems are described in the following paragraphs. **Ambient Temperature** Units and systems by Studer are generally designed for an ambient temperature range (i.e. temperature of the incoming air) of +5...+40 °C. When rack mounting the units, the intended air flow and herewith adequate cooling must be provided. The following facts must be considered: • The admissible ambient temperature range for operation of the semiconductor components is 0 °C to +70 °C (commercial temperature range for operation). • The air flow through the installation must provide that the outgoing air is always cooler than 70 °C. Average heat increase of the cooling air shall be about 20 K, allowing for an additional maximum 10 K increase at the hot components. In order to dissipate 1 kW with this admissible average heat increase, an air flow of 2.65 m³/min is required. A rack dissipating P = 800 W requires an air flow of $0.8 * 2.65 m^3/min$ **Example:** which corresponds to 2.12 m^3/min . If the cooling function of the installation must be monitored (e.g. for fan failure or illumination with spot lamps), the outgoing air temperature must be measured directly above the modules at several places within the rack. The trigger temperature of the sensors should be 65 to 70 °C.

Frost and Dew

The unsealed system parts (connector areas and semiconductor pins) allow for a minute formation of ice or frost. However, formation of dew visible with the naked eye will already lead to malfunctions. In practice, reliable operation can be expected in a temperature range above -15 °C, if the following general rule is considered for putting the cold system into operation:

If the air within the system is cooled down, the relative humidity rises. If it reaches 100%, condensation will arise, usually in the boundary layer between the air and a cooler surface, together with formation of ice or dew at sensitive areas of the system (contacts, IC pins, etc.). Once internal condensation occurs, trouble-free operation cannot be guaranteed, independent of temperature.

Before putting into operation, the system must be checked for internal formation of condensation or ice. Only with a minute formation of ice, direct evaporation (sublimation) may be expected; otherwise the system must be heated and dried while switched off.

A system without visible internal formation of ice or condensation should be heated up with its own heat dissipation, as homogeneously (and subsequently as slow) as possible; the ambient temperature should then always be lower than the one of the outgoing air.

If it is absolutely necessary to operate the cold system immediately within warm ambient air, this air must be dehydrated. In such a case, the absolute humidity must be so low that the relative humidity, related to the coldest system surface, always remains below 100%.

Ensure that the enclosed air is as dry as possible when powering off (i.e. before switching off in winter, aerate the room with cold, dry air, and remove humid objects as clothes from the room).

These relationships are visible from the following climatogram. For a controlled procedure, thermometer and hygrometer as well as a thermometer within the system will be required.

Example 1: An OB-van having an internal temperature of 20 °C and relative humidity of 40% is switched off in the evening. If temperature falls below +5 °C, dew or ice will be forming.





721-3-3 © CEI:1994

Appendix 2: Mains Connector Strain Relief

For anchoring connectors without a mechanical lock (e.g. IEC mains connectors), we recommend the following arrangement:



- **Procedure:** The cable clamp shipped with your unit is auto-adhesive. For mounting please follow the rules below:
 - The surface to be adhered to must be clean, dry, and free from grease, oil, or other contaminants. Recommended application temperature range is +20...+40 °C.
 - Remove the plastic protective backing from the rear side of the clamp and apply it firmly to the surface at the desired position. Allow as much time as possible for curing. The bond continues to develop for as long as 24 hours.
 - For improved stability, the clamp should be fixed with a screw. For this purpose, a self-tapping screw and an M4 bolt and nut are included.
 - Place the cable into the clamp as shown in the illustration above and firmly press down the internal top cover until the cable is fixed.

Rig

Appendix 3: Software License

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|----------------------------|---|
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Warranty, Disclaimer, and Liability

For all issues not covered herewithin, refer to the "General Terms and Conditions of Sales and Delivery" being part of the sales contract.

NEW FEATURES WITH SW V4.02/V4.03

1 Momentary/Latching Key Functions

1.1 **PFL and Talkback Keys**

| Latching: | If a PFL key or one of the talkback keys (N–1 A, N–1 B, AUX 1, AUX 2, or STUDIO) is pressed for less than 0.2 s, the function is now latching, and the key is illuminated. To release the function, an other short press of the key is required. The same functionality applies for the F1F5 keys if configured as additional talkback keys. <i>For this purpose, please refer to the "MONITOR EXPANDER page, Talkback Functions" paragraph below.</i> |
|---|---|
| Momentary: | When pressing a PFL or TB key longer than 0.2 s, it acts as a momentary key, i.e., the function is automatically canceled upon releasing the key. |
| | Basic information on this subject to be found in chapters: 3.1.4 / 6.1 / 6.3 |
| 1.2 Monitoring Keys | |
| Mutually Releasing / Latching: | If one of the Monitoring Selector keys is pressed for less than 0.2 s, the former monitoring source selection is canceled, and the new monitoring source selection becomes active. The selection is latching and the key is illuminated until any other Monitoring Selector key is pressed. |
| Mutually Releasing / Momentary: If | one of the Monitoring Selector keys is pressed for longer than 0.2 s, it acts as a momentary key. The former monitoring source selection is canceled, and the new monitoring source selection becomes active. Upon releasing the key, the former monitoring source selection is reactivated. |
| Uhm | <i>This may sound a bit confusing, but in everyday use it is a very convenient feature – just give it a try!</i> |

2 Talkback and PFL Signaling

2.1 Talkback Signaling from CR to Studio, and Vice Versa

| CR to Studio: | When talkback from the control room (CR) to the studio is activated, pin 23 of the STUDIO MON CTRL connector is activated (i.e. pulled to ground). This pin was formerly labeled as "Spare OUT 1". Now it can be used for illuminating the CR lamp on an external talkback box. <i>Please refer to the pin assignment diagram and table on the next page</i> . |
|---------------|--|
| Studio to CR: | When talkback from the studio to the CR is activated (e.g. when using the external talkback box), the TALK BACK STUDIO key in the console's central section is illuminated. |
| | Basic information on this subject to be found in chapters: $3.1.4 / 6.3 / 14.11$ |

2.2 **PFL Signaling**

When a PFL key is activated, pin 11 of the STUDIO MON CTRL connector is activated (i.e. pulled to ground). This pin was formerly labeled as "Spare OUT 2".

| 25 | Pin | Signal | Pin | Signal | Pin | Signal |
|----------|-----|-------------------|-----|-----------------|-----|-------------------|
| 24 | 1 | +5 V SUPPLY | 10 | Lamp EXTERN | 19 | Switch TB TO TEL2 |
| 23 | 2 | COMMON | 11 | Lamp PFL SIGN. | 20 | Lamp PGM |
| 21 | 3 | Switch AUX1 | 12 | n.c. | 21 | Lamp AUX2 |
| 20 | 4 | Switch OFF AIR | 13 | GND | 22 | Lamp PFL |
| 19 | 5 | Switch EXTERN | 14 | +5 V SUPPLY | 23 | Lamp TB TO STUDIO |
| 18 17 | 6 | Switch TB TO TEL1 | 15 | Switch PGM | 24 | Lamp STUDIO MIC |
| 16 | 7 | n.c. | 16 | Switch AUX2 | | ON |
| 15 | 8 | Lamp AUX1 | 17 | Switch PFL | 25 | GND |
| 14 | 9 | Lamp OFF AIR | 18 | Switch TB TO CR | | |

STUDIO MON CTRL (D-type, 25 pin, male):

3 MONITOR EXPANDER Page, Talkback Functions

The MONITOR EXPANDER page, possibly known from your experience with OnAir 2000M2 consoles, can now be opened on the OnAir 1000 consoles as well. For this purpose, the SYSTEM CONFIG. Page has an additional "MONITOR EXPANDER" button, as shown below:



When touching this button, the MONITOR EXPANDER page opens and allows to assign three different functions to any of the F1...F5 buttons:

| MONITOR | EXPANDER | SAVE | EXIT | LOGOUT | 09:42:39 |
|-------------------|--------------------------|------|------|---------------------|---------------------------|
| FUNCTION OF F5 | NONE | | | AUX & INSERT | |
| FUNCTION OF F4 | NONE | | | TEL N-1 AUDIT | |
| FUNCTION OF F3 | NONE | | | STUDIO MON | TH 29.JAN.02 |
| FUNCTION OF F2 | TB FROM CR TO ALL AUX | | | MIXER SETUP | 00:00:00 |
| FUNCTION OF F1 | TB FROM CR TO ALL N-1 | | | HOME | 00:00:00 START RESET |

TB FROM CR TO ALL AUX, TO ALL N–1, or TO ALL (meaning all AUX and all N–1), or NONE (no function at all).

However, no Monitor Extension module can be installed in an OnAir 1000 console, which means that no other functions (as known from OnAir 2000M2 consoles) can be configured.

4 Additional REC Signaling Output

An additional REC signaling output has been provided on pin 9 of the SIGN. connector (formerly labeled as "Spare OUT") to extend the on-air signaling features.

| \sim | | Pin | Signal | Pin | Signal |
|--------|---|-----|-------------|---------|----------------------------------|
| ••) | 9 | 1 | +5 V SUPPLY | 6 | COMMON |
| • | 8 | 2 | ON AIR IN - | 7 | Spare IN – or Ext. CR DIM IN – * |
| | 7 | 3 | CR MIC OUT | 8 | STUDIO MIC OUT |
| • ツ | 6 | 4 | PGM OUT | 9 | REC OUT |
| | | 5 | GND | * depen | ding on Customer Code setting |

SIGN. (D-type, 9 pin, male):

For the ON AIR INDICATION setting on the COMMON SETTINGS page, a fourth selection item (Σ PROGRAM/ Σ REC) was created. Depending on this setting, the two signaling outputs are active according to the following table:

| | | | SIGN output: | | |
|-----------------------|--|---|--------------------|------------------------------------|--|
| INDICATION setting | PROGRAM REC output on-air output on-air | | Pin 4 (PGM OUT) | Pin 9 (REC OUT) (new output) | |
| 1) | 0 | 0 | 0 | 0 | |
| I) SDDOCDAM | 1 | 0 | 1 | 1 | |
| | 0 | 1 | 1 | 1 | |
| AND SREC | 1 | 1 | 1 | 1 | |
| 2) | 0 | Х | 0 | 0 | |
| SPROGRAM | 1 | х | 1 | 0 | |
| 3) | х | 0 | 0 | 0 | |
| SREC | х | 1 | 0 | 1 | |
| 4) | 0 | 0 | 0 | 0 | |
| SPROGRAM | 1 | 0 | 1 | 0 | |
| / SREC | 0 | 1 | 0 | 1 | |
| (new setting) | 1 | 1 | 1 | 1 | |

In other words:

- For the 1st setting, both signaling outputs are active whenever any audio signal is routed either to the program or the record output (or both).
- For the 2nd setting, only the PGM OUT signaling output is active while an audio signal is routed to the program output. An audio signal routed to the record output has no effect.
- For the 3rd setting, only the REC OUT signaling output is active while an audio signal is routed to the record output. An audio signal routed to the program output has no effect.
- For the 4th (new) setting, only the PGM OUT signaling output is active while an audio signal is routed to the program output, and only the REC OUT signaling output is active while an audio signal is routed to the record output. If audio signals are routed to both the program and the record outputs, both the PGM OUT and REC OUT signaling outputs are active.

Basic information on this subject to be found in chapter 11.2.2

5 Additional Customer Codes

| Code 0x00000800: | Used to disable dimming of the CR monitor speakers during talkback from the studio to the control room. |
|------------------|--|
| Code 0x00001000: | Used to disable dimming of the studio monitor speakers during talkback from the control room to the studio. |
| Code 0x00002000: | Used to swap the functions of the PFL and the OFF keys next to the faders, regardless whether they are used for the standard PFL and OFF functions, or whether other functions are configured for any of these keys. <i>Basic information on this subject to be found in chapter 9</i> |
| Code 0x00004000: | If this code is active, whenever a channel is activated by moving its fader up from its lower stop while it is switched ON (or the other way round, by switching the channel ON while its fader is positioned above the lower stop), this channel's Channel Control page pops up automatically and allows for immediate changes of the channel settings. This Channel Control page re- mains displayed until another channel is activated, or until another page is selected using one of the five buttons next to the clock dial. |
| Code 0x00008000: | If this code is active, the status of the channel ON/OFF switch of channels currently not routed to the console surface will be saved in the flash memory when powering the console off. At power on, the ON or OFF status of these channels will automatically be re-established. If this code is inactive, the channels not routed to the console surface will always be set to OFF at power on. |

Basic information on Customer Code to be found in chapter 11.2.11

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1 INTRODUCTION



The OnAir 1000 is a smart but yet powerful digital mixing console for "on-air" and small recording and editing studio applications. It has ten input channels, each with two physical inputs, and two master faders.

There are two different basic versions available:

- An analog-biased version with five analog stereo line and two digital input channels, and
- A digital-biased version with two analog stereo line and five digital input channels.

Both versions feature three mono microphone input channels with switchable high-pass filter and phantom power. Both versions can be ordered with double, redundant mains power supply. For more information on this subject, please refer to chapter 14.2.

The OnAir 1000 can be integrated seamlessly with a broadcast automation system (CAB) like Studer's DigiMedia.

The OnAir 1000 is based on a touch-screen user interface. Only the most important functions have hardware control elements, making it very easy to use. It has everything integrated in a single, lightweight console and does not need any external racks or power supplies (except the Redundant Supply versions, refer to chapter 14.2).

Since it is fully digital, it can individually be adapted to the current user using snapshot automation. Extensive configuration possibilities allow the OnAir 1000 to be integrated into almost any broadcast studio environment.

For an overview, please refer to the OnAir 1000 block diagram located on the following two pages.

1.1 Block Diagram OnAir 1000, SW V4.0





1.2 Definition of Terms

Terms used in this manual:

Inputs: The physical inputs of the input unit; all input units have mono or stereo inputs with A/B selection.
 Analog inputs are equipped with 3-pin XLR connectors.
 Digital inputs are equipped with AES/EBU inputs on XLR connectors, and S/PDIF inputs on Cinch/RCA and optical (TOSlink) connectors.

Input Unit: An input unit is a hardware assembly installed within the console. All input units contain two selectable mono or stereo inputs; all input signals to the input units are connected to the input connectors at the rear of the console. Microphone input units have transformer-balanced mono inputs on XLRs,

with selectable 48 V phantom supply and A/B selection.

Analog Line input units have two electronically balanced stereo inputs on XLRs, with A/B selection.

On Digital Line input units, there is a selection of AES/EBU inputs on XLR connectors, and S/PDIF inputs on Cinch/RCA and optical (TOSlink) connectors available.

Each (A or B) input of every input unit is equipped with general-purpose control inputs and outputs (GPIO) that can be used for fader start, mute, signaling, etc.; these control inputs and outputs are available on D-type connectors.

Input Channel: An input channel is the combination of all signal processing functions that can be assigned to a fader strip, as A/B input selection, gain, filter, EQ, pan/balance etc.; each input channel can be routed to any fader strip thanks to the input channel routing performed in the DSP section of the console.

Fader Strip: A fader strip contains the operating elements for an input channel, as a linear fader and a few keys and LEDs (e.g. ON, OFF, and PFL keys, and OVL and AUTO TAKEOVER LEDs).As the number of operating elements is reduced to a minimum, the adjustment of some less often used input channel functions, as e.g. EQ setting, is performed on the center touch-screen and the rotary encoders located next to this screen.

2 GENERAL

2.1 Utilization for the Purpose Intended

is reserved to skilled technicians.

The OnAir 1000 mixing console is intended for professional use. It is presumed that the unit is operated only by trained personnel. Servicing



The electrical connections may be connected only to the voltages and signals designated in this manual.

2.2 First Steps

2.2.1 Unpacking and Inspection

Your new mixing console is shipped in a special packing that protects the unit against mechanical shock during transit. Care should be exercised when unpacking so that the surfaces do not get marred.

Verify that the content of the packing agrees with the items listed on the enclosed shipping list.

Check the condition of the equipment for signs of shipping damage. If there should be any complaints you should immediately notify the forwarding agent and your nearest Studer distributor.

Please retain the original packing material because it offers the best protection in case your equipment ever needs to be transported.

2.2.2 Installation

Primary Voltage:

DC Operation:

e: The power supply unit is auto-ranging; it can be used for mains voltages in a range of 100 to 240 V_{AC} , 50 to 60 Hz.

The console can be operated from a 24 V_{DC} source through the respective 24 V_{DC} connector provided on the rear panel.



For DC operation it is mandatory that a UL approved, external fuse is connected in series with one of the supply lines (T 10 A H 250 V UL/CSA). The power switch next to the power inlet only switches the mains voltage; for DC operation, an external power switch has to be foreseen by the installer.

For information on the Dual Power Supply versions refer to chapter 14.2.

Dual (Redundant) Power Supply:

General Precautions:



Cleaning:

Do not use the unit in conditions of excessive heat or cold, near any source of moisture, in excessively humid environments, or in positions where it is likely to be subjected to vibration or dust. The ambient temperature range for normal operation of the unit is $+5...+40^{\circ}$ C.

Unobstructed air flow is essential for proper operation. The air vents on the top and bottom of the unit are a functional part of the design and must not be blocked in any way (e.g. by a manual or a computer screen).

Do not use any liquids to clean the exterior of the unit. A soft, dry cloth or brush will usually do.

For cleaning the touch-screen display window, most of the commercially available window or computer/TV screen cleaners are suited. *Use only a slightly damp (never wet) cloth. Never use any solvent!*



STLINFO

Power Connection:



The attached female IEC 320/C13 mains cable socket has to be connected to an appropriate mains cable by a trained technician, respecting your local regulations. Refer to the "Installation, Operation, and Waste Disposal" chapter at the beginning of this manual.

In case of 24 V_{DC} operation, use an appropriate connection to the external DC supply unit or battery, equipped on one end with the attached 10-pole cable socket.



For DC operation it is mandatory that a UL approved, external fuse is connected in series with one of the supply lines (T 10 A H 250 V UL/CSA).

For pin assignment of the 24 V_{DC} connector please refer to chapter 14.1.



Please check your DC supply cable for correct polarity before connecting it to the console.

Earthing:

This equipment must be earthed, due to the mains input filter network being connected to the mains earth. Also in case of DC operation, earthing of the unit is mandatory.

Some consideration should be given to the earthing arrangement of the system, at the center of which is the console. The console chassis is earthed to the mains earth via the power supply and/or the dedicated earth connection bolt. Ground loops may occur where signal processing equipment, patched to the console, has its signal earth commoned to the equipment chassis.

2.2.3 **Adjustments, Repair**

Danger:

Replacing the Supply Unit:

Fuse for DC Supply:



All internal adjustments as well as repair work on this product must be performed by trained technicians!

The primary fuse is located within the power supply unit and cannot be changed. In case of failure, the complete supply unit must be replaced. Please contact your nearest Studer representative.

For DC operation there is a second fuse located on the PSU Sub Board (please refer to chapter 14.1 for details). In case of failure, this fuse must be replaced by a spare fuse of exactly the same type and value.

2.2.4 PC-Card

A standard OnAir 1000 mixing console is equipped with a PC-Card socket. Using the industry-standard SRAM PC-cards, the user can save important console information on a card. This information can then be used to restore the console to the same state at a later moment. Since the information stored on the card is DOS compatible, it is easily transferred to an IBMcompatible PC for data storage and backup.

Note: Although it is possible to format an SRAM card in a PC, the card must be re-formatted in the OnAir 1000; therefore, the write protect tab must be set to OFF when the card is used in the mixing console for the first time. It is also possible for new software releases to be downloaded from an SRAM PC-card. This feature is only available for users in possession of

the correct files. For more information on this subject, refer to chapter 12. The mixing console PC-card support is restricted to 5 V, Type 1, SRAM memory cards with capacities of 64 kbytes to 32 Mbytes. Hot-swapping is supported, too.

2.3 Technical Specifications (subject to change without notice)

| General | |
|--|---|
| Level specs, digital, in dB _{FS} : Level specs, analog, in dBu: Level specs, analog, in dBu _{FS} : Sampling rate: Headroom adjustable: Default setting: Output level: | dB, referenced to full modulation (dB _{FS} , dB Full Scale) 0 dBu \triangleq 0.775 V _{rms} Level in dBu for full modulation (\triangleq 0 dB _{FS}) 48 kHz ±100 ppm (internally synchronized) 9 to 19 dB 9 dB 15 dBu @ 0 dB _{FS} |
| | All input faders set to their 0 dB position. External analog sources: Source impedance < 200 Ω . Frequency range: 20 Hz to 20 kHz, if not stated otherwise. |
| Microphone Input Units | Microphone input units have A/B-switchover and are equipped with a balancing transformer and XLR connectors. The signal is routed to an electronically balanced, analog insert point before the A/D-converter. |
| Input sensitivity Gain setting Phantom power, switchable Frequency response High-pass filter (12 dB/Octave) Input impedance Insert level A/D converter Dynamics THD+N THD+N Noise figure Common mode rejection | $\begin{array}{l} -60 \text{ to } +20 \text{ dBu}_{FS} \text{ (with 9 dB headroom: } -69 \text{ to } +11 \text{ dBu}) \\ \text{in steps of 1 dB} \\ 48 \text{ V} \\ \pm 0.5 \text{ dB} \\ -3 \text{ dB} @ 75 \text{ Hz} \pm 5 \text{ Hz}, \text{ switchable} \\ > 1 \text{ k}\Omega \\ +15 \text{ dBu}_{FS} \text{ (with 9 dB headroom: } +6 \text{ dBu}) \\ 24 \text{ bit (Delta-Sigma, } 64 \times \text{ oversampling}) \\ \text{typ. } 102 \text{ dB (unweighted)} \\ < -85 \text{ dB}_{FS}, 20 \text{ Hz to } 20 \text{ kHz}, @ -1 \text{ dB}_{FS} \\ < -100 \text{ dB}_{FS}, 20 \text{ Hz to } 20 \text{ kHz}, @ -30 \text{ dB}_{FS} \\ \text{typ. } < 4 \text{ dB } @ \text{ max. gain, bandwidth } 20 \text{ kHz}, \text{ R}_{s} = 200 \Omega \\ > 50 \text{ dB} @ 15 \text{ kHz}, > 75 \text{ dB} @ 50 \text{ Hz} \end{array}$ |
| Line Level Input Units | Analog line level input units have A/B-switchover and are equipped with an elec- tronically balanced input and XLR connectors. |
| Input sensitivity for 0 dB _{FS} Gain setting Frequency response Input impedance A/D converter Dynamics THD+N THD+N Common mode rejection | 0 to +24 dBu _{FS} (with 9 dB headroom: -9 to +15 dBu) in steps of 1 dB ± 0.1 dB > 10 k Ω 24 bit (Delta-Sigma, 64 × oversampling) typ. 102 dB @ +15 dBu _{FS} (unweighted) < -94 dB _{FS} , 20 Hz to 20 kHz @ -1 dB _{FS} < -100 dB _{FS} , 20 Hz to 20 kHz @ -30 dB _{FS} > 47 dB @ 50 Hz to 15 kHz |
| Digital InputUnits | Digital input units have A/B-switchover. AES/EBU (AES3-1992) and S/PDIF (IEC 958) formats are supported; XLR, Cinch/RCA, and TOSlink connectors are available. All digital input units are equipped with a sampling frequency converter (SFC). |
| SFC resolution Input sampling rate THD+N Frequency response Input impedance | 20 Bit 30 to 54 kHz $< -105 \text{ dB}_{FS} @ 1 \text{ kHz}, 0 \text{ dB}_{FS}$ $\pm 0.1 \text{ dB}$ 110 Ω (XLR connectors); 75 Ω (Cinch/RCA connectors) |

| Analog Outputs | Analog outputs are transformer-balanced with XLR connectors. |
|--|---|
| Output level D/A converter Dynamics THD+N Frequency response Output impedance | +4 to +24 dBu @ $R_L = 100 k\Omega$; +4 to +23 dBu @ $R_L = 300 \Omega$ 24 bit (Delta-Sigma, 128 × oversampling) typ. 101 dB (unweighted) < -90 dB _{FS} @ 1 kHz, -1 dB _{FS} ±0.2 dB < 40 Ω |
| Digital Outputs | Digital outputs are equipped with two independent outputs according to the AES/EBU standard (AES3-1992). On each of these outputs, the same signal is fed to two XLR sockets with individual buffers. |
| Output level Output impedance | 2 to 5 V 110 Ω |
| Equalizer Treble control (High) Equalizer (Mid) Bass control (Low) | 5 kHz/10 kHz: ±15 dB (depending on Customer Code setting; refer to 11.2.11) 200 Hz to 10 kHz: ±15 dB 200 Hz/400 Hz: ±15 dB |
| Crosstalk Attenuation Between channels Input fader attenuation | > 90 dB > 100 dB |
| Power Supply Mains voltage Power consumption DC operation | Redundant power supply available on request. 100 to 240 V, 50/60 Hz (auto-ranging) 130 VA typ. 24 V _{DC} +15/–10 %; typ. 5 A @ 24 V |
| Note: | The abovementioned values are typical values; in an individual case, the values may differ. We reserve the right to make changes as technological progress may warrant. |



3 OPERATING CONCEPT

The normal operation of the console is as simple as possible. This has been achieved by reducing the number of operating elements to the minimum. Nevertheless, high flexibility has been achieved thanks to the use of touchscreen technology that just shows those operating elements needed at a certain time.

A linear fader and some keys (ON, OFF, PFL, SEL, EQ, A, B) are the only hardware operating elements within a fader strip. All other functions are available through the touch-screen user interface. To maintain a good console overview, several LEDs are provided, together with four-character channel label displays indicating the label for each channel. Pressing either one of the SEL or EQ keys assigns the touch-screen to this function for that specific channel. Parameters can now be entered via the four rotary encoders (e.g. equalizer parameters), or directly through the assigned touch-screen (e.g. EQ ON/OFF). This innovative user surface is called the "Touch'n'action" concept and is patented by Studer.

In broadcast applications many DJs and operators without a special technical education work on the same mixing console. Every DJ has his preferred console settings, mainly EQ parameters for his microphone. In the OnAir 1000, individual settings can be stored; by simply pressing a few keys, every operator can recall his particular, tailor-made setting and concentrate afterwards upon his essential task – presenting a radio program meeting the needs of the listeners.

Radio stations often use the same mixing console type for on-air and production. The "Snapshot" feature easily turns the production console into an on-air console within seconds, if required.

Thanks to the integrated input channel router, the user/administrator can place any input unit's signal to any fader strip on the console surface at his convenience. Through a straightforward representation of the available signals and faders on the screen, the input channel routing is easily done.

3.1 Operating Elements

3.1.1 Power Switch



For the two standard (single power supply) versions of the OnAir 1000 console, the mains power switch is located on the rear side of the console next to the power inlet.

In case of DC operation of the console using an external power supply or a battery, this power switch is without function; an external power switch has to be foreseen by the installer. Please refer to chapter 14.1 for details on DC operation.

Information on connection and operation of the two redundant power supply versions of the OnAir 1000 with their external power supply unit is given in chapter 14.2.

3.1.2 Touch-Screen Unit


3.1.3 Metering Section



The OnAir 1000 mixing console is equipped with two dual 30-LED stereo level meters and one correlator meter for each of the level meters.

The meters can be set to simultaneously display either VU or PPM. Two LEDs below the respective scale indicate the selected mode (for mode setting, please refer to the OnAir 1000 Service Manual).

It is configurable for both meters which signal they display; the following sources can be selected in the configuration menu:

| Meter 1 | Meter 2 | Source |
|---------|---------|------------|
| Х | Х | CR MONITOR |
| Х | Х | PROGRAM |
| Х | Х | RECORD |
| Х | Х | AUX 1 |
| Х | Х | AUX 2 |
| Х | | AUDIT |
| Х | | N–1 A |

For configuration details refer to chapter 11.2.4.

Below the meters an electret talkback microphone is integrated into the console that can be used as a talkback source. If desired, an external talkback microphone can be connected to the rear of the console (for details, refer to chapters 14.6 and 15.2).

If the console is powered by the mains voltage and an additional $24 V_{DC}$ supply is used for redundancy, or if the (optional) redundant power supply is used, the red POWER ALARM LED indicates a failure of one of the power supplies, allowing for timely repair without interruption of the continuity program.

3.1.4 Monitoring and Talkback Section



[1] Control Room (CR) Monitoring Selector

Ten mutually releasing keys for selecting the control room monitoring signal from the following sources:

- AUX 2/AUX 1: Auxiliary, general purpose outputs;
 - AUDIT: Auxiliary, general purpose output with configurable inputs;
 - Σ **REC:** Recorder output;
 - **Σ PGM:** Program (on-air) output;
 - **PFL:** PFL (pre-fader listening);
 - **EXT 3:** External analog input signal;
 - **EXT 2:** External analog input signal;
 - **EXT 1:** External analog input signal;
 - **OFF AIR:** External analog input signal (e.g. tuner for off-air listening).

[2] CR Monitor Speaker Control

MON \square : Monitoring volume control.

DIM: If pressed, the monitor speaker level is reduced by 20 dB. The DIM function is activated automatically if one of the TALK BACK keys is pressed. If "CR DIM WHEN AUDITION SEL" is set in the COMMON SETTINGS page (details: refer to chapter 11.2.2), the monitoring source is AUDIT, and the DJ microphone is on, the AUDIT level is not muted but reduced by 20 dB.

If DIM is active, the DIM key is illuminated.

CUT: If pressed, the monitor speakers are muted, and the CUT key is illuminated. As long as one of the CR or DJ microphones is on, the monitor speakers are automatically muted, and the CUT key is illuminated.

| [3] PFL/TB P EX PFL | FL/TB: T PFL: >MON: | Volume control for the built-in PFL/TB speaker. The EXT PFL input can be used as an external PFL input to the monitor- ing unit. If pressed, the key is illuminated, the external signal is routed to the PFL output, and the external PFL control output is active. If PFL is active on any channel, the PFL signal is routed to the main monitor speakers. If no PFL is selected, the monitoring signal is the stereo CR signal as selected. The PFL>MON key is illuminated if active. |
|---------------------------|---------------------------|--|
| [4] Headphones PHON | ES 🎧 : | Headphones volume control. The headphones socket (6.3 mm TRS jack) is located below the hand rest on the left side of the console. |
| МО | PFL: N/PFL: | Three keys allow different monitoring modes: PFL and TB signals are routed to the headphones. The left-channel headphones signal is the mono PFL signal, while the right-channel headphones signal is the mono CR signal as selected by the monitoring source selector [1]. If no PFL is selected, the headphones sig- nal is the stereo CR signal as selected. |
| | MON: | The headphones signal is the same stereo signal as selected by the moni- toring source selector [1]. It is, however, not affected by the DIM and CUT functions. |
| [5] Talkback N–1 B/ | /N-1 A: | Selector for five talkback destinations: Connection between the talkback microphone* in the control room and the corresponding clean-feed (N–1) output; this allows talking e.g. to the con- nected telephone hybrid(s) or communication lines |
| AUX 2/ | AUX 1: | Connection between the talkback microphone* in the control room and the AUX 2 or AUX 1 outputs |
| ST | TUDIO: | Connection between the talkback microphone* in the control room and the studio speakers and headphones. The built-in PFL/TB loudspeaker and/or the headphones are used for talk-back listening in the control room |
| | * | If a console is equipped with an external talkback microphone, this micro- phone is used instead of the integrated talkback microphone. |
| [6] F1 to F5 | | Spare keys for future functions. |
| [7] Signaling Lamps C | ON-AIR | Is turned on by a control signal on the "SIGN." control connector on the monitoring part of the connector panel |
| C | R-MIC | Is on if at least one microphone in the control room is routed to the pro- gram or to the record output (i.e. the corresponding output is selected, the input channel is ON, the input channel fader and the master fader are opened) |
| STUDIO-MIC | | Is on if at least one microphone in the studio is routed to the program or to the record output (i.e. the corresponding output is selected, the input chan- nel is ON, the input channel fader and the master fader are opened). |

Fader Section 3.1.5



| [1] | Channel Label Display | Four-character display, indicating the label of the input channel momen- tarily selected. The channel label can be set in the INPUT CONFIG. page; refer to chapter 11.2.1. When pressing the A or B key, the label will scroll by in full length. |
|------------|---|--|
| [2] [3] | OFF ("key no. 3") ON ("key no. 2") | Keys for deactivating or activating an input channel. Can be reconfigured. For details please refer to chapters 8 and 14.3. |
| [4] | Linear Fader | The audio path is fully digital, therefore the signal does not pass through the fader; stroke length is 100 mm. For details concerning fader start control please refer to chapters 8 and 14.3. |
| [5] | PFL ("key no. 1") | Key for activating/deactivating the PFL (pre-fader listening) function. |
| [6] | OVL | Channel overload indicator. |

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C (1

| [7] SEL | | Multi-function key: For activating the Channel Control page on the touch-screen for this respective input channel. For details please refer to chapters 4.4 and 4.5. If the INPUT CONFIG. page is already displayed on the central touch-screen, pressing the SEL key in a fader strip selects the corresponding input channel for configuration. If the Mixer Setup page (SNAPSHOTS/MIC SETTING) is already displayed on the central touch-screen, pressing the SEL key in a microphone input fader strip selects the corresponding input channel for configuration. |
|-----------|---------------|--|
| [8] EQ | | For activating the EQ/filter page on the touch-screen for this respective input channel. For details please refer to chapters 4.6 and 4.7. |
| [9] A, B | | For selecting the physical inputs A or B of this input unit. Each time one of these keys is pressed (even if this physical input is currently active), the full-length label scrolls by in the four-character channel label display. |
| [10] LEDs | AUTO TAKEOVER | Up/Down Arrow LEDs. After having modified the channel routing or during radio automation (CAB) operation, they indicate that the fader has to be moved upward/downward to match the physical and the logical positions. |
| | PGM | LED indicating that this input channel signal is routed to the Program output. |
| | REC | LED indicating that this input channel signal is routed to the Record output. |
| | AUX1 | LED indicating that this input channel signal is routed to the AUX1 output. |
| | AUX2 | LED indicating that this input channel signal is routed to the AUX 2 output. |
| | INS1 | LED indicating that this input channel signal is routed over the analog insert 1. |
| | INS2 | LED indicating that this input channel signal is routed over the analog insert 2. |

STUDER

3.2 Connector Panels

Analog-biased (left) and digitally-biased (right) versions



4 CHANNEL FUNCTIONS

The OnAir 1000 has ten input channel faders and two master faders on the surface. The input channel fader strips consist of a four-character channel label display, two input selector keys, a SEL key, an EQ key, an OVL overload indication, a PFL key, the fader, an ON and an OFF key. All keys are equipped with LED indicators. Six LEDs (PGM, REC, AUX1, AUX2, INS1, INS2) display the signal routing of this respective input channel. If the console is remote-controlled by a radio automation system (CAB), the two AUTO TAKEOVER arrow LEDs indicate in which direction the fader has to be moved in order to match the physical and the logical position of the fader.

Input Router: Through the integrated input router, the output signal of every individual input unit can be assigned to any input fader strip on the console surface (see chapter 5.8). The input channel parameters relate to the input unit and not to the fader strip. They will be re-routed when the input unit is assigned to another fader. Exception: The position of the two master faders is fixed and cannot be re-routed.

Access Permission: In the following chapters we assume that the user has access permission to all functions, unless otherwise noted. Access permission may be limited to some users by the administrator, causing that certain screen parts are hidden. Full access permission is in such a case only obtained when entering a password. For a description of how to set up the access permission, refer to chapter 10.

4.1 Keys

Channel Functions:

| P | F | L |
|---|---|---|
| | | |

ON

OFF

В

Α

The channel key functions are configurable; this configuration is valid for the whole console (and not per input channel). The different configuration possibilities are:

| Configuration | n Key 1 ("PFL") | Key 2 ("ON") | Key 3 ("OFF") |
|---------------|-----------------|--------------|---------------|
| 1 (Default) | PFL | ON | OFF |
| 2 | PFL | ON/OFF | LOCATE |
| 3 | PFL | ON | LOCATE |
| 4 | PFL | ON/OFF | NEXT |

For details on the key configuration refer to chapter 8.

For microphone, analog line, and digital input units, the input keys A and B directly select one of the two physical input signals. The selected input is indicated by an LED integrated in the respective key, and the source name appears in the channel label display. If the input channel is active (i.e. input channel ON, fader open, and output selected), the input selection is disabled.

If the input channel is connected to an automation system, scrolling of the channel label display indicates that this input channel is "ON AIR". For more details on this topic, please refer to chapter 9.

By pressing one of the SEL (select) keys, the Channel Control page is opened on the touch-screen. Parameters for this input channel can now easily be entered with the four rotary encoders and the touch-screen fields. The SEL key of the corresponding fader strip is illuminated.

SEL Key:

| EQ Key: | The EQ key opens the EQ/filter page on the touch-screen, and the corre- sponding SEL key is illuminated, indicating that parameters of this input channel are displayed. The EQ key is illuminated if the equalizer is on. By observing if the EQ keys are illuminated, it can be determined at a glance in which input channels the equalizer settings are active. | |
|------------------------|--|--|
| 4.2 Faders | | |
| Master Faders: | Since the audio path is fully digital, the signal does not pass through the faders. Therefore, linear-track mono faders are used. The stroke length is 100 mm. When opening the fader, fader start or signaling commands can be released, depending on the configuration (refer to chapter 8). On the right-hand side of the input channel faders, a master fader for each the program and the record output is provided (refer to chapter 5.14). These two master faders can be enabled or disabled at the user's convenience. | |
| 4.3 LED Indicators | | |
| | In each fader strip, LEDs display the actual routing of the signals. The selected physical input being (either A or B) is indicated by the illumi- nated A or B selector key, and the filtering of the signal by the equalizer function is indicated by the illuminated EQ key. With all these displays, the operator has an immediate overview of the major console settings at a single glance. | |
| Channel Label Display: | The four-character channel label display shows the name of the selected physical input. The channel label is entered in the Mixer Setup menu of the INPUT CONFIG. page, using a soft keyboard appearing on the touch-screen. | |
| OVL (Overload): | A red OVL indicator per fader strip signalizes an overload condition any- where within the corresponding input channel's signal processing path. Signals in the OnAir 1000 are processed with sufficient digital headroom through the entire signal processing chain. Besides, most outputs (PGM, REC, AUX1, AUX2, INSERT SEND1/2) are equipped with limiters in or- der to prevent overloads. All limiters are set to 0 dB _{FS} with fixed attack and release times. Overload in an input channel is indicated if the signal level reaches 0 dB _{FS} . Due to the output limiters, normally distortions will be avoided even in case of an overload. | |
| PGM / REC: | The PGM and/or REC LEDs are on if the respective input channel's signal is routed to the PGM and/or the REC output(s). The output routing can be changed in the Channel Control page on the touch-screen that is opened by pressing the SEL key. The " Σ PROGRAM" and/or " Σ RECORD" touch-screen fields are highlighted if selected. | |
| AUX1 / AUX2: | The AUX1 and AUX2 LEDs are on if the respective input channel's signal is routed to the AUX output(s). The AUX settings can be changed in the Channel Control page on the touch-screen that is opened by pressing the | |

SEL key. There, both auxiliaries can be independently set as "AF" (after-

INS1 / INS2:

INS1 INS2

AUTO TAKE OVER

 \bigcirc

highlighted if selected.

AUTO TAKEOVER Arrow LEDs: After having modified the channel routing or during radio automation (CAB) operation, they indicate that the fader has to be moved upward/downward to match the physical and the logical positions.

fader), "PF" (pre-fader), "ON" or "OFF". These touch-screen fields are

The INS1 or the INS2 LED is on if the respective input channel's signal is

routed trough one of the analog inserts 1 or 2. The inserts are controlled in

the AUX MASTER/INSERTS page on the touch-screen.

If the console is used together with an automation system, the fader value can be set by the automation system via the "Monitora" protocol. In case the user would like to take-over the control of the signal with the fader, the AUTO TAKEOVER arrow LED(s) of the input channels being controlled by the automation system indicate in which direction the fader has to be moved to match its physical and its logical position. If the positions are matched, or if, by chance, the fader is in the exact position, both arrow LEDs remain illuminated as long as this input channel is on air.

If an input channel is currently on air, its label continuously scrolls by in the channel label display.

| AUTO AUTO TAKE OVER OVER OVER | | TAKE OVER | A SAME |
|--|------------------------|----------------------|----------------|
| Channels <i>not</i> con- | Audio level above | Audio level below | Audio level |
| trolled by a CAB | the fader position – | the fader position – | equal to the |
| system | move fader <i>down</i> | move fader <i>up</i> | fader position |

4.4 Channel Control Page, Microphone Input

| INP 1A | | | | LOGOUT AUX & INSERT | 16:23:00 |
|-------------------|-------|--------|----------|---------------------------|---------------------------|
| | AUX 1 | AUX 2 | ΣPROGRAM | TEL N-1 AUDIT | T. 🔨 T |
| PHANTOM ON OFF | ON | ON OFF | ΣRECORD | STUDIO Mon | TH 12.APR.02 |
| PHASE NORM INV | AF PF | AF PF | | MIXER SETUP | 00:00:00 |
| GAIN: ØdB | AUX1: | AUX2: | PAN: | HOME | 00:00:00 START RESET |

The Channel Control page for a microphone input channel is opened by pressing the SEL key of a mic channel's fader strip.

- **PHANTOM** Touching the "ON" part of the PHANTOM field turns the 48 V phantom power on; a touch on the "OFF" part of the field turns it off again. The status is indicated by highlighting the corresponding field.
 - **PHASE** The phase of the microphone signal is inverted by touching the "INV" part of the PHASE touch-screen field; "INV" is highlighted. Touching "NORM" de-activates the phase inversion.
 - **GAIN** The microphone preamp's gain is set with the first rotary encoder (in a -5...+75 dB range); the adjusted value is indicated in the GAIN field.
- AUX 1 / AUX 2 The AUX 1/2 ON and OFF touch-screen fields route the input channel signal to the corresponding auxiliary bus, if "ON" is selected (and high-lighted). The AF/PF touch-screen fields allow selection whether the signal is taken after- (AF) or pre-fader (PF). The current selections are highlighted. The AUX levels (-∞ to +10 dB; 0 dB position is marked with a dash) are adjusted with the second and third rotary encoder. The settings are graphically displayed in the fields right above the rotary encoders. The output routing of this particular input channel is performed with the "∑PROGRAM" and "∑RECORD" touch-screen fields.
 - **PAN** The fourth rotary encoder positions the source within the stereo image, the PAN (-ning) indicator shows the adjusted position.

4.5 Channel Control Page, Line Input

INP 1A LOGOUT 16:25:41 MODE AUX & INSERT ST MONO AUX 1 AUX 2 ΣPROGRAM LR RL Ń-Ĩ AUDIT LL RR OFF ΣRECORD ΟN ON OFF TH 12.APR.02 PHASE MIXER AF PF ΑF PF 00:00:00 NORM INV AUX1 AUX2 BAL 00:00:00 CAL HOME ØdB START | RESET

The Channel Control page for a line input channel is opened by pressing the SEL key of a digital or an analog line input channel's fader strip.

MODE Line level inputs have a MODE field defining whether the input is processed in "ST"(-ereo) or in "MONO" mode. In mono mode the stereo input signal from the input unit is added to a mono signal and attenuated by 3 dB.

Stereo signals are processed in the following modes:

LR: Normal stereo mode;

- *RL:* Left/right stereo channels swapped;
- *LL:* Left signal on both stereo channels;
- *RR:* Right signal on both stereo channels.
- **PHASE** The phase (*of the left input path only*) is inverted by touching the "INV" part of the PHASE touch-screen field; "INV" is highlighted. Touching "NORM" de-activates the phase inversion. Only in the "RR" case, the phase inversion takes place in the R input path.
 - **CAL** In the CAL field, the gain deviation referred to nominal level setting is displayed; max. deviation: ± 15 dB, adjustment with the first rotary encoder.
- AUX 1 / AUX 2 The AUX 1/2 "ON" and "OFF" touch-screen fields route the input channel signal to the corresponding auxiliary bus, if "ON" is selected (and high-lighted).

The "AF" / "PF" touch-screen fields allow selection whether the signal is tapped after- (AF) or pre-fader (PF). The current selections are high-lighted.

The AUX levels ($-\infty$ to +10 dB; 0 dB position is marked with a dash) are adjusted with the second and third rotary encoder. The settings are graphically displayed in the fields right above the rotary encoders.

The bus assignment is performed with the " Σ PROGRAM" and " Σ REC-ORD" touch-screen fields.

BAL The fourth rotary encoder adjusts the position of the stereo image, the BAL indicator shows the adjusted position.

Access Permission: Depending on the access permission (see chapter 10) of the user currently logged in, the functions not available to this user will not be displayed on the Channel Control page; an example is given below. This user cannot access the following parameters:

- PHASE
- AUX 1
- Output summing buses (ΣPROGRAM, ΣRECORD)
- PAN/BAL.



If the user has no access permission at all, the Channel Control page will not be displayed.

4.6 EQ/Filter Page, Microphone Input

The EQ/filter page for a microphone input channel is opened by pressing the EQ key of a microphone input channel's fader strip. If the user has no access permission to the EQ and filter parameters, the EQ/filter page will not be displayed.



- **HPF** The HPF "ON" / "OFF" touch-screen field is a high-pass filter switch. The filter is part of the analog front end and is available on microphone input channels only. "ON" is highlighted if the filter is active.
 - **EQ** The EQ "ON" / "OFF" touch-screen fields activate or bypass the equalizer. If the equalizer is active (i.e. "ON"), the EQ key will be illuminated. If the EQ is bypassed (i.e. "OFF"), the preset frequency curve is displayed on the touch-screen nevertheless.

The four rotary encoders are the control elements for the equalizer. The low-frequency section is a shelving-type filter with two selectable turnover frequencies and variable boost/cut. The frequency is selected by the left-hand "CUTOFF" touch-screen fields, the gain is set with the first (left-most) rotary encoder. The second rotary encoder sets the frequency of the mid-frequency section which is a peaking-type filter. The third rotary encoder controls the mid-frequency boost/cut. The fourth (rightmost) rotary encoder controls the gain of the high-frequency section which again is a shelving-type filter with two selectable turnover frequencies. The right-hand "CUTOFF" touch-screen fields allow frequency selection of the high-frequency shelving filter.

Note: The HF cutoff frequencies can be reduced by approx. one octave (resulting in an increased filter effect) when activating Customer Code 0x00000002; refer to chapter 11.2.11.

| s: | Filter | Mode | Frequency | Gain range | Comment |
|----|-------------------|----------|-----------------------------------|-----------------------------|--|
| | High-pass filter | HPF | –3 dB @ 80 Hz | ON/OFF | Analog, 12 dB/oct., mic input ch only |
| | EQ low-frequency | Shelving | 200 Hz/400 Hz | -15 dB+15 dB, 1 dB steps | - |
| | EQ mid-frequency | Peaking | 200 Hz10 kHz, 1/6 octave steps | –15 dB+15 dB, 1 dB steps | Q = 0.7 |
| | EQ high-frequency | Shelving | 5 kHz/10 kHz | –15 dB+15 dB, 1 dB steps | see Customer Code (11.2.11) |

EQ Specs

4.7 EQ/Filter Page, Analog Line/Digital Input

The EQ/filter page for a line input channel is opened by pressing the EQ key of an analog or digital line input channel's fader strip. If the user has no access permission to the EQ parameters, the EQ/filter page will not be displayed at all.



The only difference between the analog line/digital and the microphone EQ/Filter pages is the missing HPF high-pass filter switch on analog line/digital input channels.

5 MASTER FUNCTIONS

Master functions are not input channel-related but global functions. These are audio functions (AUX master level, studio monitoring), non-audio functions (telephone hybrid control, clock, stopwatch), snapshot management, and system configuration.

Touching one of the LOGIN, AUX & INSERT, TEL, STUDIO MON, MIXER SETUP, or HOME–CHN ON/OFF fields will always lead to the corresponding page.

5.1 Login/Logout

The console always starts up in default user mode. If the user wants to work in his own environment, he must log-in. He has to touch the LOGIN field on the touch-screen. The LOGIN page will appear where the user can touch the field with his own name.

| LOGIN | | ADMIN. | EXIT | LOGIN | 16:36:02 |
|------------------------|------------------|--------------|-----------------------|---------------------|---------------------------|
| IVO SAKOMURA | LISA GERBER | PETER GARRET | EMIL TISCHBEIN | AUX & INSERT | |
| ANTON GAST | LINDA DE MOL | DAVE SCOTT | PATRICIA DE NAPOLI | TEL N-1 AUDIT | T. 🔨 T |
| DAVID VAN DER PLOEG | MARTIN MAERKI | PAOLO SCANO | CHARLOTTE SAX | STUDIO Mon | TH 12.APR.02 |
| PAUL YOUNG | SASKIA SMITH | HANS POL | ESTHER VALERI | MIXER SETUP | 00:00:00 |
| ANNE SEBIRE | KATE LENERT | SYLVIA SEGON | GABOR JENKINS | HOME | 00:00:00 START RESET |

More information on the login procedure can be found in chapter 10.5.

After having logged in, the LOGIN field changes to LOGOUT; when the user leaves the console, this field must be used. After having touched LOGOUT, a dialog box appears, asking for confirmation. This is used as a protection against logging out inadvertently.



If a console is operated by one person only, the system administrator simply can enable all access rights for the default user. This is the way the user can control all console functions (except the administration functions) without having to log-in. A default user is always defined in the User administration table, his name is DEFAULT USER. The access permissions of this user can be edited. His record cannot, however, be deleted, and no password can be defined for him.

5.2 AUX and Insert Control

The AUX MASTER/INSERTS page is entered by touching the AUX & INSERT field on the touch-screen.

If the user has no access permission for the AUX and/or insert parameters, there will appear a text next to AUX MASTER and/or INSERTS, telling the user that he cannot modify the parameters.

| AUX MASTER | | INSERTS | | LOGOUT | 16:42:32 |
|-----------------|-----------------|----------------|--------------|---------------------|---------------------------|
| | | INSERT 1 | | AUX & | |
| | | UN OFF | I 2B | INSERT | |
| AUX 1 | AUX 2 | INSE ON OFF | RT 2 I 1A | TEL N-1 AUDIT | |
| ON OFF | ON OFF | | | STUDIO Mon | / TH 12.APR.02 |
| | | | | MIXER SETUP | 00:00:00 |
| AUX1: -00 10 | AUX2: -00 10 | | I 2B | HOME | 00:00:00 START RESET |

AUX MASTER: The two rotary encoders below the AUX 1 and AUX 2 gain indication symbols are used to adjust the desired level of the corresponding AUX master. The gain can be adjusted in a range of $-\infty$ to +10 dB; the 0 dB position is marked with a dash.

ON/OFF selection of the AUX master buses is done by touching the corresponding fields on the screen. A highlighted background indicates the current status.

The AUX 1 and AUX 2 gain indication symbols are always displayed, even if no output unit is jumper-configured for AUX 1 and/or AUX 2.

INSERTS: On the same page, the insert points can be assigned. Assignment is possible to any of the following signals:

- CH1 to CH10,
- PROGRAM bus,
- RECORD bus,
- Talkback to Studio output,
- Talkback to Control Room (CR) output,
- NONE.

A signal can only be assigned to an insert while the insert is set to OFF, by touching the channel/bus label field of the insert box. The label field is highlighted. Then it can be changed with the rightmost rotary encoder. If the insert is set to ON, the channel/bus label field cannot be highlighted, and the insert assignment cannot be changed.

The insert send always follows the setting as displayed on the screen; it remains active while the insert is switched OFF. If the insert is switched ON, a switchover from the internal connection to the insert return is performed.

Each signal can be assigned to one insert only. If a signal is already assigned to an insert, it will not appear in the channel/bus label field during signal selection for another insert.

The insert is assigned per input unit and not per physical input, which means that the insert assignment is always valid for both physical inputs A and B of an input channel.

The insert assignment and the insert ON/OFF setting are included in snap-shots.

The insert assignment is displayed in the corresponding fader strip by an illuminated INS LED for as long as the insert is active (ON).

5.3 N–1/Audition Bus and Telephone Hybrid Control

Two clean-feeds (N–1A, N–1B) are provided. The clean-feeds can be used as mono outputs to telephone hybrids, or as mono line outputs. The audition bus can either be used as a special stereo monitoring bus (for information on the special CUT and DIM conditions, please refer to chapter 11.2.2, "CR DIM WHEN AUDITION SELECTED"), or as a third, mono or stereo clean-feed.

If the user has no access permission for the clean-feed and/or audition bus parameters, there will appear a text next to the corresponding field, telling the user that he cannot modify the parameters.



The clean-feeds as well as the audition bus have master level controls that can be accessed on the N–1/audition master and telephone hybrid page (opened by touching the TEL/N–1/AUDIT field). The output levels are controlled with the rotary encoders; the output gain range is $-\infty$ to +10 dB, the 0 dB position is marked with a dash.

Two telephone hybrid interfaces to which the clean-feeds are fed can be controlled by the ON-AIR, HOLD, and TELEPHONE fields on this page. TELEPHONE switches the telephone line to the telephone. By touching HOLD, the telephone line is routed to the telephone hybrid interface. The return signal is connected, but the incoming signal is not fed to the console. Only when touching ON-AIR, both the incoming and the return signals are connected to the console.

5.3.1 N–1 and Audition Bus Routing



For bus routing, first open the INPUT CONFIG. page of the desired input channel (the example above shows the INPUT CONFIG. page of a digital input channel; for a mic input channel, the INPUT CONNECTOR field is not displayed, and for an analog line input channel, there will be the LEVEL and HEADROOM fields displayed instead).

When touching the BUS ASSIGNMENT field on this page, it changes as follows, allowing to select the N–1 A/B and audition bus routing:



In the TO N–1 A, TO N–1 B, and TO AUDIT fields, the input signal is routed to the N–1A, N–1B, or audition bus, with the options given in the table below.

| Setting | Meaning |
|------------|---|
| NEVER | The input channel is never routed to N–1 or AUDIT |
| | The input channel is routed to N–1 and/or AUDIT if the input channel is |
| WHEN FOREC | either assigned to the program or to the record bus |
| | The input channel is routed to N–1 and/or AUDIT if the input channel is |
| WHENFRO | assigned to the program bus |
| | The input channel is routed to N–1 and/or AUDIT if the input channel is |
| | assigned to the record bus |
| | The input channel is always routed to N–1 and/or AUDIT, regardless of |
| ALVIA13 | the bus assignment |

The audition bus is similar to the N–1 buses, but it is a stereo bus that can be selected as a monitoring source in the control room.

Once the bus routing is completed, return to the INPUT CONFIG. page by touching the BACK TO CTRL OUT/IN CFG. field.

The N–1 A/B and the audition output levels are set using the rotary encoders on the TEL/N–1/AUDIT page, as described in chapter 5.3 above.

5.4 Studio Monitoring

The STUDIO MONITORING SOURCE page is opened by touching the STUDIO MON field. This page allows selecting one of six signals to be routed to the studio monitoring loudspeakers and headphones. This selection can also be done via a pushbutton remote control from the studio (optional accessory "Studio Talkback Box" available). The field of the currently selected signal source is highlighted.

The signal selected with the EXT3 field is the same as the one selected with EXT3 on the CR monitor selector on the right hand side of the console; this signal is fed to the console via the 15-pin EXTERN MONITOR INPUT (B) connector at the rear of the console.

| STUDIO M SOUF | ONITORIN RCE | | 16:57:31 |
|------------------|-----------------|---------------------|---------------------------|
| ΣPROGRAM | PFL | TEL N-1 AUDIT | |
| AUX 1 | OFF AIR | STUDI Mon | TH 12.APR.02 |
| AUX 2 | EXT 3 | MIXER Setuf | 00:00:00 |
| | | Номе | 00:00:00 Start Reset |

For a more detailed description of the monitoring system, see chapter 6.

5.5 Mixer Setup

The Mixer Setup page (SNAPSHOTS/MIC SET/CHAN ROUT) gives access to several function groups used for setting the general status of the console. These are: Global snapshots, private snapshots, global microphone (mic) settings, private microphone settings, global channel routings.

| SNAPSHOTS | MIC SET | CHAN ROUT | SYSTEM ROUT- CONFIG ING LOGOU | 17:02:30 |
|-----------|---------------------|--------------|--------------------------------------|---------------------------|
| ON AIR | DJ PAUL | 1 TO 1 | SAVE DELETE AUX & CARD CARD INSER | |
| PROD. 1 | DJ BOBO | REVERSE | LOAD FROM CARD AUDIT |] - |
| PROD. 2 | DJ LUCIE | MASTER LEFT | SAVE DELETE STUDIO | TH 12.APR.02 |
| SPECIAL | OLIVER BONO | MASTER RIGHT | RECALL FROM MIXER MEMORY SETUR | 00:00:00 |
| | PREV NEXT 14 910 | PAUL BOCUSE | PRI- VATE GLOBAL HOME | 00:00:00 START RESET |

At the left of the Mixer Setup page there is the SNAPSHOTS area. The five fields allow snapshot selection.

Up to ten mic settings can be selected in the MIC SET area; either all of them are displayed, or – if the CHAN ROUT section is visible on this page as well – they are displayed in groups of four and can be browsed using the NEXT... and/or PREV... fields, as shown above.

Five global channel routings are accessible in the CHAN ROUT area. If no global channel routings should have been defined by the system administrator, the CHAN ROUT area is not displayed, as shown below.



The right part of the Mixer Setup page also provides the following command fields:

PRIVATE/GLOBAL: SYSTEM CONFIG:

ROUTING:

Users can select between private and global snapshots and mic settings.

This field leads to the second level of mixer setup functions. These are accessible only for users having the corresponding access right set in the user configuration. A new page is displayed, allowing to select from several groups of configuration parameters.

NG: This field leads to the CHANNEL ROUTING. page. This page is only accessible for users having the corresponding access right set in the user configuration.

The use of snapshots, mic settings, and channel routings is described in the following chapters, followed by some information on user administration and the watch/stopwatch functions.

5.6 Snapshots

A snapshot is a copy of a momentary console setup. It contains all parameters (as input selection, input gain, phase, phantom power, balance or panorama setting, EQ settings, AUX settings, input channel ON/OFF, and insert assignment), except fader positions and PFL. A snapshot does *not* contain any parameters being part of the console configuration (input channel routing, channel labels, control signal functions, clean-feed bus assignment, level meter assignment, etc.).

Each user having a personal password can save up to four private snapshots (max. 20 users), and up to five global snapshots (available for every user) can be saved in the console's flash memory. More snapshots can be saved on a PC-Card (refer to chapter 5.9) and loaded from the card into the console's flash memory if required.

A name can be given to every snapshot which is displayed in the leftmost field column.

5.6.1 Recall a Snapshot from Memory

Select a snapshot by touching one of the name fields. The name of the selected snapshot is highlighted. When touching the RECALL FROM MEMORY field, the snapshot will immediately be recalled from the console's internal flash memory, except if the current input channel is active (switched to the program or the record bus); then the recall operation is delayed until the input channel is closed.

During execution of the snapshot RECALL function, the system compares the configuration parameters of each input channel with the configuration parameters saved together with the snapshot. If there is a mismatch, the snapshot will not be executed for this input channel.

| SNAPSHOTS | MIC SET | CHAN ROUT | | LOGIN | 17:56:44 |
|-----------|---------------------|--------------|-----------------------|---------------------|---------------------------|
| ON AIR | DJ PAUL | 1 TO 1 | | AUX & INSERT | |
| PROD. 1 | DJ BOBO | REVERSE | | TEL N-1 AUDIT | |
| PROD. 2 | DJ LUCIE | MASTER LEFT | | STUDIO Mon | TH 12.APR.02 |
| SPECIAL | OLIVER BONO | MASTER RIGHT | RECALL FROM MEMORY | MIXER SETUP | 00:00:00 |
| | PREU NEXT 14 910 | | PRI- VATE GLOBAL | HOME | 00:00:00 START RESET |

5.6.2 Save a Snapshot to Memory

Saving a snapshot is done by touching a snapshot field, followed by SAVE MEM. The keyboard page appears where the snapshot name can be entered or edited (max. 20 characters). If the existing name is to be kept, just touch the <--1 ENTER field.



CLEAR is used to clear the text field for entering a new name with the keyboard.

<--- BACK is used to delete one character to the left of the cursor.

EXIT is used to leave the keyboard page without saving the new name.

When touching <--: ENTER, the keyboard page is left and the snap-shot data are saved.

Note: Global snapshots can be saved by the system administrator only. Loggedin users only can save their private snapshots.

5.6.3 Delete a Snapshot from Memory

When saving a snapshot, the previously saved snapshot data are overwritten. A snapshot can be deleted, too, without overwriting it with new data. For deleting a snapshot from the console's internal flash memory, the desired snapshot has to be selected. The selected field is highlighted. After touching the DELETE MEM field a dialog box appears. If it is confirmed, the snapshot will be permanently removed from the memory, and the name within the selected field is cleared.

5.7 Mic Settings

A mic (microphone) setting is a set of parameters (EQ, gain, phantom power) for a single microphone input channel. Thus, every DJ or announcer can store his preferred mic settings and recall them at any time. Up to four private mic settings for each user (max. 20 users), and up to ten global mic settings can be stored in the console's flash memory; additional mic settings can be stored on a PC-Card as well (refer to chapter 5.9).

If the CHAN ROUT section is also displayed on the Mixer Setup page, the global mic settings are displayed in groups of four. To display the other groups, use the NEXT... and/or PREV... fields below the MIC SET label fields.

Mic settings can be named, as the snapshots; these names are displayed on the corresponding touch field columns on the Mixer Setup page.

5.7.1 Recall a Mic Setting from Memory

To recall a mic setting from the console's internal flash memory, first open the Mixer Setup page (SNAPSHOTS/MIC SET/CHAN ROUT) by touching MIXER SETUP, then select a microphone input channel by pressing the SEL key of the desired channel, then touch the desired MIC SETTING field. The name of the selected mic setting is highlighted.

Then touch the RECALL FROM MEMORY field. The selected input channel's parameters are immediately set to the values of the mic setting. Selection of another type of input channel (line or digital) is treated as an error and will not be accepted by the system.

| SNAPSHOTS | MIC SET | CHAN ROUT | | | 7:29:51 |
|-----------|---------------------|--------------|-----------------------|-------------------------|---------------------------|
| ON AIR | DJ PAUL | 1 TO 1 | I | AUX & INSERT • | $\overline{}$ |
| PROD. 1 | DJ BOBO | REVERSE | | TEL - N-1 AUDIT . | N - |
| PROD. 2 | DJ LUCIE | MASTER LEFT | S | STUDIO MON | TH 12.APR.02 |
| SPECIAL | OLIVER BONO | MASTER RIGHT | RECALL FROM MEMORY | MIXER SETUP | 00:00:00 |
| | PREU NEXT 14 910 | | PRI- VATE GLOBAL | HOME | 00:00:00 START RESET |

If the last modification was made on a microphone input channel, this channel is automatically selected and shown in the top line of the Mixer Setup page. To change the input selection, just touch the SEL key of the desired fader strip. The physical input being currently active (A or B) of this input channel is now selected and shown in the top line of the Mixer Setup page.

5.7.2 Save a Mic Setting to Memory

Creating a mic setting means saving a set of equalizer and gain parameters for a single microphone input channel under a given name into the console's internal flash memory. This is done by first selecting the microphone input channel to be stored, then touching a MIC SET field, followed by SAVE MEM. The keyboard page appears where the mic setting's name can be edited. Any MIC SET field (also one of these that have already been used and have a name assigned) can be selected. It will, however, be overwritten by the new mic setting.



Now a new name can be generated, or the already present name can be overwritten or edited. When touching <--: ENTER, the mic setting data and its new name are saved.

5.7.3 Delete a Mic Setting from Memory

When saving a mic setting, the previously saved data are overwritten. A mic setting can be deleted, too, without overwriting it with new data. For deleting a mic setting from the console's internal flash memory, the desired mic setting has to be selected. The selected field is highlighted. After touching the DELETE MEM field, a dialog box will appear. If it is confirmed, the mic setting will be permanently removed from the memory, and the text within the selected field is cleared.

5.8 Routing

The OnAir 1000 features an input channel router that allows to route the output of any input unit (including the complete parameter set, as stereo mode, gain, filter, sends, bus assignment) to any fader strip. Please note that the two physical inputs A and B of the same input unit *cannot* be assigned to different fader strips, as the input selector switch is located before the preamplifier.

An input unit signal *cannot* be routed to more than one fader strip.



5.8.1 Channel Routing

The channel routing, i.e. the input unit-to-fader strip assignment, is accessed on the CHANNEL ROUTING page. There is only one user-specific channel routing per logged-in user available. Should the user have no access permission to the channel routing, this page is not displayed.

| CHANNEL ROUTING | | Channel 01 02 03 04 05 <mark>06</mark> 07 08 09 10 P R |
|------------------------------|-------|--|
| SAVE TO USER PROFILE EXIT | APPLY | Fader 02 |
| INPUT ASSIGNMENT | | Fader 05 |
| I 6A I 6B | | Fader 07 |
| CHANNEL - FADER ASSIGNMENT | | Fader 09 |
| ANALOG -128.0dB | | Fader 10 |
| CHANNEL FADER | OFT | Fader 11 |
| 06 05 | 361 | Fader 12 + + + + + + + + + + + + + + + + + + |

The right part of the CHANNEL ROUTING page displays a matrix with the corresponding number of faders and input channels. The current input unit-to-fader strip assignment is indicated by the crosspoints. The program and record master fader assignment is fixed (PGM MASTER to fader no. 11, REC MASTER to fader no. 12). These two faders can be disabled (CLEAR) and re-enabled (SET), but not re-assigned to input channels. They are located at the far right of the grid ("P" and "R"). An example of this particular CHANNEL ROUTING page is shown below:

| CHANNEL ROUTING | Channel 01 02 03 04 05 06 07 08 09 10 P R |
|---|---|
| SAVE TO USER EXIT APPLY | Fader 01 Fader 02 Fader 03 Fader 03 |
| INPUT ASSIGNMENT | Fader 04 |
| CHANNEL - FADER ASSIGNMENT | Fader 07 • |
| -128.0dB CHANNEL FADER PGM MASTER 11 CLEAR | Fader 10 |

Making a CrosspointOn the left part of the CHANNEL ROUTING page, the input unit-to-fader
strip assignment is performed with the two left rotary encoders below the
touch-screen. When turning these encoders, the highlighted horizontal and
vertical lines are moved within the grid. The input channel and fader num-
bers are displayed above the two rotary encoders, as well as the type of the
selected input unit (MIC/ANALOG/DIGITAL) and the position of the cur-
rently selected fader.
Above the input unit-to-fader strip assignment, the input labels are dis-

played. The channel routing relates to the physical input. This means that, for example, input B can be selected; when loading this set-up later, input B with all its input parameters (as input gain, EQ settings, etc.) is automatically selected. These parameters are input-related and will be automatically rerouted when the input channel is assigned to a different fader.

When the desired crosspoint is found, touch the SET field. A dot appears at this crosspoint in the grid, indicating the established connection.

The fader to which this input channel had been assigned before is free now, and its dot jumps outside the grid to the left (fader 07 in the picture above). The same happens when the CLEAR field is touched after having selected a connection.

When all settings are done, touch the APPLY field. The new channel routing is loaded to the work memory. There are two ways to proceed now:

Temporary Routing When touching the EXIT field, the page is left without saving, and the user can work with the new channel routing.

However, the new routing is not stored in the user's profile. When the user logs out, the temporary channel routing is lost, and his original channel routing will be loaded when logging in the next time.

Permanent Routing To store the new channel routing within the user's profile, the SAVE TO USER PROFILE field has to be touched before exiting the page with EXIT.

Clearing a CrosspointWhen the CLEAR field is touched after having selected a connection, then
this fader strip is free now, and its dot jumps outside the grid to the left.

 Protected Connections
 One or several connections can be protected by the administrator from being modified, even by users having access permission to the CHANNEL

ROUTING page. In such a case, a *dashed vertical line* is displayed for the corresponding input unit (channels 01, P, and R in the picture above).

Input Units not Connected to a Fader Strip

There are cases where the signals must be routed "to the background", i.e., it is possible for input channels to be active, but without operating elements on the console surface, e.g. if the user deliberately decides that some input channels must be in the background.

All input channels currently routed to the background can be displayed in the CHANNEL ON/OFF page; this page is selected by touching CHN ON/OFF in the HOME page.

When routing an input unit signal to the background, the audio path remains open, and the audio passes through the console with the fader level set at the moment of routing the signal to the background. This can be used, for example, for input channels being controlled in the background by a broadcast automation system (e.g. DigiMedia) using the serial port. External MUTE control for the input channel is still available.

Notes: This could be dangerous for a new user taking over the console, or for unexperienced users. Therefore being in the HOME page, the HOME field toggles to CHN ON/OFF. When touching this field, the CHANNEL ON/OFF page is opened (see below), displaying all input channels not assigned to fader strips (i.e., being in the background); the highlighted ones are open for audio signals ("ON"). Now, the user can switch any input channel ON or OFF on this page directly by touching the corresponding field on the screen.

When the PFL function is active at the moment of routing an input channel to the background, it is automatically switched off.



When powering the console off and on again, the input channels in the background are always set to OFF, to prevent audio from being played out unintentionally.

Auto Takeover When a new channel routing is loaded or when a user logs in, the input unit-to-fader strip assignment is modified. In this moment, the audio level of an input signal may not correspond to the physical position of the new fader. As the OnAir 1000 does not feature motorized faders, in such a case the Auto Takeover LEDs indicate in which direction the fader knob has to be moved to "catch" the audio level and to get the signal under control again.

5.8.2 Recall a Channel Routing from Memory

Automatical Recall: Every logged-in user has his own user channel routing that is loaded either automatically when logging-in, or manually. This is defined by customer code 0x00000200 (see chapter 11.2.11). Usually it is easier to load the user-specific channel routing automatically at log-in; in some cases, however, the channel routing must not be changed

(e.g. when the administrator makes some changes in a user domain).When the console is switched on, the last channel routing (i.e. the one that was active before power off) is automatically loaded.

Manual Recall: A user can recall one of the five global channel routings or, when logged in, his own user channel routing at any time. This is done in the Mixer Setup page, either after selection of GLOBAL (up to five channel routings can be recalled by selecting one of them and touching RECALL FROM MEMORY), or PRIVATE (only one user channel routing can be recalled for logged-in users by selecting RECALL USER ROUTING).

When the console is switched on, the last channel routing (i.e. the one that was active before power off) is automatically loaded.



5.8.3 Save a Channel Routing to Memory

After having configured the channel routing as desired (see chapter 5.8.1), touch the APPLY field. The new channel routing is loaded to the work memory. When touching the EXIT field, the CHANNEL ROUTING page is left, and the user can work with the new channel routing. However, this routing is not yet stored in the user's profile. When the user logs out, the temporary channel routing is lost; if customer code 0x00000200 is set (see chapter 11.2.11), his original channel routing will be loaded when logging in the next time.

To store a modified channel routing within the user's profile, the SAVE TO USER PROFILE field has to be touched before exiting the page with EXIT.

Note: The system administrator can set and save his own user channel routing as described above.

For administrator setting and saving of global channel routings or user channel routings, please refer to chapter 5.8.5 and chapter 5.10.2.

5.8.4 Delete a Channel Routing from Memory (Administrator Only)

Previously stored channel routing data are overwritten when saving a channel routing, as described in chapter 5.8.3.

For the administrator, it is also possible to delete a global channel routing *without* overwriting it by new data. To do this, first the desired channel routing has to be selected. The selected field is highlighted. After touching the DELETE MEM field, a dialog box will appear. If it is confirmed, the channel routing will be permanently removed from the memory, and the text within the selected field is cleared.

For more information on deleting global channel routings please refer to chapter 5.10.2.

5.8.5 Channel Routing Administration

In addition to the user's own user channel routing, the administrator can save the current channel routing to one of the five globally-accessible channel routings. These can be recalled in the Mixer Setup page. The default and global channel routings can be saved or edited by the administrator; the default user's user routing can also be saved or edited by any user not logged-in (i.e. the default user), if access permission is given.

Channel Routings:

Global Channel Routings

User Channel Routing The procedure for the system administrator changing a user's channel routing is described in chapter 5.10.2.

Five globally accessible channel routings can only be created by the administrator, allowing users to set-up the channel routing for specific transmissions, e.g. news broadcasts or sports events. Users do not have access to the global channel routings configuration. However, access permission given, a user could load a global channel routing, edit it, and store it in his own user profile.

Administrator Protection: The administrator can protect specific connections within the channel routing by selecting the desired cross-point with the horizontal and vertical lines, and then touching the PROT field. A protected connection is indicated by a dashed vertical line in the grid (see below); this connection cannot be changed by the users, even if they are allowed to change their channel routing. Only the administrator may UNPROT this connection again.



However, the user can change the input selection (A/B) of this input channel, if he has the INPUT SEL access permission.

5.9 Using PC-Cards

Snapshots, mic settings, and global channel routings can be saved to a PC-Card or loaded from the card into the console's flash memory. Using PC-Cards, parameter settings of a console can also be copied to an other console, provided that both have the same configuration. A PC-Card with a capacity of 64 kB can hold about 20 snapshots.

Notes: Using PC-Cards, it is also possible to exchange snapshot, mic setting, and/or channel routing data between OnAir 1000, OnAir 2000, and OnAir 2000M2 consoles. However, some restrictions must be considered if the consoles have different hardware configuration (e.g. number of channels, number of fader strips, different input unit types).

Only the administrator is allowed to load, save, and delete global channel routings from/to PC-Card.

File Name Extensions: The three-character DOS file name extensions on the PC-Card will be generated automatically. The relationship between the file name extension and the file content is shown in the following table.

| Data type | File name extension |
|------------------------|---------------------|
| Global snapshot | .gss |
| Private snapshot | .pss |
| Global mic setting | .mis |
| Private mic setting | .mip |
| Global channel routing | .gfm |

5.9.1 Load a Snapshot/Mic Setting/Channel Routing from PC-Card

To load a snapshot, a mic setting, or a global channel routing from a PC-Card, touch MIXER SETUP followed by LOAD FROM CARD. The page below will be displayed *(please note that the CHAN ROUT area is only displayed for the administrator)*. The USER FILTER allows to select different user's data with the fourth rotary encoder.



Snapshots, mic settings, or a global channel routing can only be loaded as long as there is sufficient empty space available in the console's flash memory; if not, some of them must be deleted first. They can be selected for loading by scrolling with the corresponding << or >> fields, followed by LOAD. After having loaded a snapshot, a mic setting, or a channel routing, it is not automatically active, but it is available in the Mixer Setup page and must be recalled with RECALL FROM MEMORY, as described in chapters 5.6.1, and 5.7.1, or 5.8.2, respectively.

5.9.2 Save a Snapshot/Mic Setting/Channel Routing to PC-Card

To save a snapshot, a mic setting, or a channel routing to a PC-Card, first select a snapshot, a mic setting, or a channel routing in the Mixer Setup page. Then touch the SAVE CARD field. A keyboard appears for entering a file name (max. 8 characters, no dots, no spaces), as described in chapters 5.6.2, 5.7.2, and 5.8.3.

After confirmation with ENTER, the snapshot, the mic setting, or the channel routing is saved on the card.

If an empty file name is entered or if EXIT is touched, the action is cancelled without saving anything to the PC-Card.

Please note that only the administrator is allowed to save channel routings on the PC-Card.

Exceptions: If the file name already exists on the PC-Card, the action must be confirmed before the file is overwritten.

If a user tries to store a file under a file name that is already existing but has been given by an other user, the existing file will (after confirmation) be overwritten; it will no more be visible for the initial user.

If the card is full, a dialog box appears, telling that the file cannot be saved.

5.9.3 Delete a Snapshot/Mic Setting/Channel Routing from PC-Card

To delete a snapshot, a mic setting, or a channel routing from the PC-Card, DELETE CARD must be touched. The page below is displayed; *(please note that the CHAN ROUT area is only displayed for the administrator)*.



A snapshot, a mic setting, or a channel routing can be selected for deleting by scrolling with the corresponding << or >> fields. The selected file and the snapshot, mic setting, or channel routing names contained in this file are displayed to the right of the << / >> fields. When touching DELETE, the console asks for confirmation. After confirmation, the file is deleted permanently. *Once deleted, the data cannot be recovered.*

A user can only delete his own, private data from the card. The administrator, however, has access to all (global and private) files on the card. Depending on what selection the administrator has made in the Setup page, either only the global or only the private files are displayed for him.

The DELETE FROM PC-CARD page remains until either EXIT or one of the main menu fields is touched.

5.10 Administrator

5.10.1 Features

- The administrator has access to *all* private and global snapshots, mic settings, and channel routings.
- The administrator has no private snapshots or mic settings, but one user channel routing.



The administrator's Mixer Setup page is shown above. In this case all SAVE, DELETE, RECALL, and LOAD actions correspond to *global* snapshots, mic settings, and channel routings.

When the administrator touches the PRIVATE field and selects one of the users, he can modify the private settings of this user (see chapter 5.10.2).

Summary: The displayed private snapshots, private mic settings, and user channel routing depend on the user logged-in to the console; global snapshots, global mic settings, and global channel routings are the same for all users. When a user is not logged-in, i.e. he has not entered any password, he is automatically treated as the default user. The default user, too, has private settings; these will, however, not be password-protected, so they can be overwritten by any other user which is *not* logged-in to the console.

Global parameter settings can be recalled by any user, but modified by the system administrator only.

| | Access Rights: | Global Snapshots | Private Snapshots | Global Mic Settings | Private Mic Settings | Global Channel Routings | User Channel Routings |
|---|--|---------------------|----------------------|---------------------------|-------------------------|-------------------------------|-----------------------------|
| | Administrator | read/write | read/write* | read/write | read/write* | read/write | read/write |
| | Logged-in user, default user | read | read/write | read | read/write | read | read/write |
| * | The administrator can read and write the private snapshots and mic settings of any user, but he has no own snapshots or mic settings. | | | | | | |

| Number of Parameter Sets: | | | | | | |
|---------------------------|------------|---------------|--------------------|--|--|--|
| Data Type | Number | Multiplied by | Total Data Records | | | |
| Global snapshots | 5 | 1 | 5 | | | |
| Global mic settings | 10 | 1 | 10 | | | |
| Global channel routings | 5 | 1 | 5 | | | |
| Private snapshots | 4 per user | 20 | 80 | | | |
| Private mic settings | 4 per user | 20 | 80 | | | |
| User channel routings | 1 per user | 22 | 22 | | | |

5.10.2 Admin Selection of Snapshots/Mic Settings/Channel Routings

The system administrator can display and modify the private snapshot, mic setting, and channel routing data of any user.

This feature has been implemented in order to enable the setting of parameters in a private snapshot, mic setting, or channel routing to which the particular user has no access. All actions, such as RECALL FROM MEM-ORY, SAVE (to) MEM, DELETE (from) MEM, LOAD FROM CARD, SAVE (to) CARD, and DELETE (from) CARD, concern the selected user's data. No other actions on the console can be affected.

Snapshots and Mic Settings: After selecting PRIVATE in the Mixer Setup page, the second rotary encoder from the left (the one below the user's name label) allows to select the user's name. In this mode the system administrator can LOAD, RE-CALL, SAVE, or DELETE all the data being displayed.



Channel Routing: With RECALL USER ROUTING, the system administrator can activate the user channel routing of the user selected with the rotary encoder. With RECALL ADMIN ROUTING he activates his own user channel routing.

The procedure for changing a user's channel routing is as follows:

- Log-in as administrator,
- touch MIXER SETUP,
- SYSTEM CONFIG,
- USER ADMIN,
- select the desired user,
- touch EDIT,
- SET CHANNEL ROUTING,
- modify the channel routing,
- touch BACK,
- SAVE,
- EXIT.

5.10.3 Users with and without a Password

When a normal user enters the Mixer Setup page before logging-in (which means he is treated as the *default user*), he works with the default user's data. He can LOAD, RECALL, SAVE, or DELETE all of the default user's data in the console's memory or on the PC-Card. These data can, however, be used, saved, overwritten, or deleted by any other user who is not logged in.

The Mixer Setup page offers a PRIVATE field next to the GLOBAL field, allowing the user – after having logged-in – to access his own, protected private snapshots and mic settings.

A user can recall his user channel routing in the Mixer Setup page, after selection of PRIVATE (only one user channel routing can be recalled by selecting RECALL USER ROUTING); for more information on this subject please refer to chapter 5.8.2.

Should the user have access permission to the system configuration and/or channel routing, the corresponding SYSTEM CONF./ROUTING fields would be displayed here as well.



A user can decide to work with the global data although he is already logged-in. If the GLOBAL field is touched, the Mixer Setup page will look as shown below, where he can recall (but not modify) the global snapshots, mic settings, and channel routings; the CHAN ROUT area is suppressed if no global channel routings are available. Should the user have access permission to the system configuration and/or channel routing, the corresponding SYSTEM CONF./ROUTING fields would be displayed here as well.

| SNAPSHOTS | MIC SET | CHAN ROUT | LOGIN 17:56:44 |
|-----------|---------------------|--------------|--|
| ON AIR | DJ PAUL | 1 TO 1 | AUX & INSERT |
| PROD. 1 | DJ BOBO | REVERSE | TEL • • N·1 AUDIT · |
| PROD. 2 | DJ LUCIE | MASTER LEFT | STUDIO MON TH 12.APR.02 |
| SPECIAL | OLIVER BONO | MASTER RIGHT | RECALL FROM MIXER MEMORY 00:00:00 |
| | PREU NEXT 14 910 | | PRI- VATE GLOBAL HOME 00:00:00 START RESET |

5.11 User Administration

| USER ADMINISTRATION | | | EXIT | LOGOUT | 17:58:48 |
|---------------------|------------------------|--------------------|-----------|---------------------|---------------------------|
| ADMIN. | ANTON GAST | MARTIN MAERKI | NEW | AUX & INSERT | \cdot |
| DEFAULT USER | LINDA DE MOL | PAOLO SCANO | EDIT | TEL N-1 AUDIT | |
| | DAVE SCOTT | CHARLOTTE YOUNG | DELETE | STUDIO MON | TH 12.APR.02 |
| | PATRICIA DE NAPOLI | PAUL SAX | | MIXER SETUP | 00:00:00 |
| | DAVID VAN DER PLOEG | SASKIA SMITH | USER 1120 | HOME | 00:00:00 START RESET |

USER ADMINISTRATION is only accessible for the system administrator. Refer to chapter 10 for details on how to set up function access permission and new user accounts.

5.12 System Configuration

System configuration is a separate function group that can be accessed only by the system administrator or by users having access permission. It is separately described in detail (refer to chapter 11).

5.13 Watch and Stopwatch

The watch and stopwatch functions are displayed on the right-hand side of the touch-screen (except when the keyboard or routing pages are active).

The term "watch" refers to the time-of-day in analog and digital format, the day-of-week, and the date in an abbreviated format.

The term "stopwatch" refers to both the fader and the user stopwatches. The fader stopwatch measures the time the most recently used channel fader has been opened. The user stopwatch works like a normal stopwatch with START/STOP and RESET keys located directly below the touchscreen.

Below, the HOME page with the watch and stopwatch displays and functions is shown.



5.13.1 Watch

The watch gives a time, day-of-week, and date display. The default time reference is an internal, battery-buffered real-time clock (RTC). The RTC continues to run even when the console is switched off. Therefore it is unnecessary to set the watch at power-up. The RTC can also by synchronized by the time signal fed to the TimeSync input connector. If synchronization is established, the top right corner of the touch-screen shows a small "S" (Sync OK).

Changes to the internal time reference and the time and date formats can be made by the system administrator on the TIME page (MIXER SETUP, SYSTEM CONFIG., TIME). For details, refer to chapter 11.2.5.

Both time and date can be displayed in two formats, as shown below:

| Time Format | Digital Time Indication | | |
|---------------|-------------------------|--|--|
| 05:00:00 p.m. | 12 hour time format | | |
| 17:00:00 | 24 hour time format | | |
| Date Format | Fxample | | |
| DD.MMM.YY | MO 20.MAR.02 | | |
| MMM-DD-YY | MO MAR-20-02 | | |
5.13.2 Fader Stopwatch

The upper one of the two stopwatches is called the fader stopwatch. It always restarts at 00:00:00 when a new audio channel is activated (e.g. channel ON, fader open, and output bus selected). In other words, it displays the elapsed time of the audio channel opened last.

This function can be enabled or disabled for any desired audio channel via the MIXER SETUP/SYSTEM CONFIG./INPUT menu, item TIMER START YES/NO.

The counter stops when the audio channel that started the stopwatch is switched OFF, or its fader is closed.

Note: If more than one audio channel has caused the fader stopwatch to restart, only the last audio channel's time is displayed.

5.13.3 User Stopwatch

The user stopwatch is located just below the fader stopwatch. It is controlled by two keys on the console's surface located below the digits of the display. One key starts and stops the timer, the other resets the timer.

| Кеу | Label | Description |
|-------|-------|----------------------------|
| START | START | Start timer if stopped |
| STOP | STOP | Stop timer if running |
| RESET | DECET | Reset timer regardless |
| ILSE! | RESET | whether stopped or running |

5.14 PGM and REC Master Faders

The console provides two master faders (PGM, fader no. 11, and REC, fader no. 12) located on the far right of the CHANNEL ROUTING page ("P" and "R"). Their assignment is fixed, they cannot be re-assigned to input channels.



- **Specifications:** In the audio path, the master faders are located before the insert points (also refer to the block diagram in chapter 1). If an insert is assigned to one of the master outputs, the corresponding IN1 or IN2 LED is on;
 - No ON/OFF function is available for the master outputs;
 - PFL function is not available;
 - Overload indication is available;
 - Master level can be controlled by Monitora extended commands;
 - Signaling (ON-AIR, CR-MIC, and STUDIO-MIC) as well as CR/Studio CUT/DIM are logically linked to the master faders.

5.14.1 Fixed Master Levels

If only fixed levels of the PGM and/or REC master output signal(s) are required, it is possible to enter these levels in the DEFAULT MASTER LEVEL window on the COMMON SETTINGS page in a -7...+5 dB range. The fixed level for a master output becomes active when the corresponding master fader is disabled by selecting CLEAR in the CHANNEL ROUTING page. For re-enabling, the CLEAR field then changes to SET.



Important: *This setting directly affects the level of the main output signals. Please be careful when changing this parameter.*

6 MONITORING

6.1 Control Room Monitoring

The CR monitoring source selector has 10 keys to select one out of six internal and four analog external sources. The key of the selected source is illuminated.

| Key | Source | urce Description | |
|---------------------------------|----------------------------|--|--|
| AUX 2 | Auxiliary 2 | General purpose output bus. AUX2 is illuminated if selected. | |
| AUX 1 | Auxiliary 1 | General purpose output bus. AUX1 is illuminated if selected. | |
| AUDIT | Audition | General purpose output bus. The input channels to the audition bus are configurable. AUDIT is illuminated if selected. | |
| | Record output | Output signal of the record master output. ΣREC is illuminated if selected. | |
| E PGM | Program output (on-air) | Output signal of the on-air master output. Σ PGM is illuminated if selected. | |
| PFL | PFL | PFL sum. PFL is illuminated if selected. | |
| EXT 3 | External 3 | General purpose analog stereo input signal. EXT3 is illuminated if selected. | |
| EXT 2 | External 2 | General purpose analog stereo input signal. EXT2 is illuminated if selected. | |
| EXT 1 | External 1 | General purpose analog stereo input signal. EXT1 is illuminated if selected. | |
| OFF AIR | Off-air | General purpose analog stereo input signal (e.g. tuner). OFF AIR is illuminated if selected. | |
| F 5 F 4 F 3 F 2 F 1 | Function keys F1 to F5 | Spare keys for future functions. | |

Two additional keys are used to control the monitoring loudspeakers in the control room. The functions of these keys are described in the table below.

| Key | Function | Description |
|-----|----------|--|
| DIM | –20 dB | The output level to the CR monitor loudspeakers is reduced by 20 dB. DIM is illuminated if selected. |
| CUT | Mute | The output to the CR monitor loudspeakers is muted. CUT is illuminated if selected. |

A volume control knob (MON \square) is used to set the level sent to the CR monitor speakers.

As long as one of the microphones in the control room (DJ or guest microphone) is on, the monitor speakers are muted. In this case the CUT key is illuminated. If the monitoring source is AUDIT (audition bus), the monitor speakers are either dimmed (DIM key illuminated), muted (CUT key illuminated), or not affected at all, according to the configuration (see chapter 11.2.2 for details).

If talkback is active, the level of the monitor speakers is dimmed by 20 dB and the DIM key is illuminated.

External CR DIM: Provided that the corresponding Customer Code has been activated, an additional control input signal on the "SIGN." connector allows to reduce the level of the CR monitor speakers by 20 dB (refer to chapters 11.2.11 and 14.11 for details).

The signal on the PFL bus is reproduced by the console's built-in speaker. The same speaker is used for talkback. The volume is adjusted with the PFL/TB potentiometer.

For monitoring purposes, a headphones connector is provided on the left side below the hand rest of the console. Three keys allow different monitoring modes, as described below:

| Кеу | Function | Description |
|------------|----------------|--|
| PFL | PFL | The headphones signal is the stereo PFL signal. PFL is illuminated if selected. |
| MON PFL | PFL/CR monitor | The left headphones signal is the mono PFL signal, while the right headphones signal is the mono CR signal as selected by the monitor source selector. If no PFL is selected, the headphones signal is the stereo CR signal as selected. MON/PFL is illuminated if selected. |
| MON | CR monitor | The headphones signal is the same stereo signals as selected by the source selector for the monitor speaker. MON is illuminated if selected. |

The CUT and DIM keys have no effect on the headphones signal. The headphones output is not muted if one of the control room microphones is on. The incoming talkback signal from the studio is mixed to the headphones output. For improved intelligibility, the monitoring signal is attenuated by 20 dB.

The PFL to MONITOR function feeds the PFL signal to the monitor loudspeakers, if PFL is selected on any channel. The normal monitoring signal is muted for as long as any PFL keys are active.

| Key | Function | Description |
|------------|---------------------|---|
| PFL MÖN | PFL to MONI- TOR | The PFL signal is routed to the monitor speakers if PFL on any channel is active. If no PFL is se- lected, the monitoring signal is the stereo CR signal as selected. PFL > MON is illuminated if active. |

A volume control knob (PHONES \bigcap) adjusts the headphones listening level.

6.2 Studio Monitoring

The built-in studio monitoring functions are based on a touch-screen menu on the control screen. The STUDIO MONITORING SOURCE page allows one of six sources to be selected for routing to the studio speakers and headphones.



The table below lists the available sources.

| Label | Function | Description | |
|----------|------------------|---|--|
| | Program output | Output signal to the transmitter. Σ PROGRAM is | |
| ZFRUGRAM | (on-air) | highlighted if selected. | |
| | Auxiliary | General purpose output sum. AUX1 is highlighted | |
| | output 1 | if selected. | |
| | Auxiliary | General purpose output sum. AUX2 is highlighted | |
| AUX 2 | output 2 | if selected. | |
| DEI | Pre-fader lis- | PFL sum. PFL is highlighted if selected. | |
| FIL | tening output | | |
| | External Off-air | General purpose input signal, usually from re- | |
| | input | ceiver. OFF AIR is highlighted if selected. | |
| EVT 2 | External input | General purpose input signal. EXT 3 is highlighted | |
| LXI J | | if selected. | |

As soon as one of the fields is touched, the corresponding signal is routed to the studio, and the field is highlighted.

The monitoring source selection can be controlled from the studio via external pushbuttons thanks to the parallel interface. The monitor speaker and headphones levels can independently be controlled by two potentiometers located in the studio.

The studio monitoring speakers are muted if one of the studio microphones is on. The headphones are not muted in this case.

During talkback from the CR to the studio, the volume for the studio speakers is reduced by 20 dB. The talkback signal from the CR to the studio is not reduced and has therefore the normal listening level on both the studio monitor speakers and the studio monitor headphones.

6.3 Talkback

There is a choice among five talkback destinations from any control room (CR) microphone. A key is assigned for each destination as can be seen in the table below.

| Кеу | Function | Description |
|--------|------------------------|--|
| N-1 B | TB to Tele- phone 2 | The connection between DJ mic and Clean-feed B (N–1B, e.g. telephone 2) is established for as long as this key is pressed. |
| N-1 A | TB to Tele- phone 1 | The connection between DJ mic and Clean-feed A (N–1A, e.g. telephone 1) is established for as long as this key is pressed. |
| AUX 2 | TB to Auxiliary 2 | The connection between DJ mic and Auxiliary 2 is established for as long as this key is pressed. |
| AUX 1 | TB to Auxiliary 1 | The connection between DJ mic and Auxiliary 1 is established for as long as this key is pressed. |
| STUDIO | TB to Studio | The connection between DJ mic and the Studio is established for as long as this key is pressed. |

Any microphone in the control room, including the integrated TB microphone, can be the CR microphone, if "TB SOURCE" is set to "CR" in the INPUT CONFIG. page.

The console's built-in PFL/TB loudspeaker and the headphones are used for talkback listening in the CR, while in the studio the monitoring speakers and the headphones are used.

The parallel studio interface on the "STUDIO MON CTRL" connector allows the connection of three pushbuttons for talkback target selection. The possible targets are: CR, Clean-feed 1 (N–1 A/TEL1), and Clean-feed 2 (N–1 B/TEL2). For talkback from the studio, the studio microphones are used ("TB SOURCE" is set to "STUDIO").

An additional, electronically balanced talkback line input on the "STUDIO MONITOR (AUDIO) B" connector as well as additional talkback to CR and to studio control inputs (on the "EXT PFL CTRL" connector) are provided.

Please refer to the pin assignment tables and the connection diagrams in chapter 14.11.

6.3.1 Talkback Settings

Signaling and talkback can be configured independently. The "SIGN." setting in the INPUT CONFIG. page is used only for signaling (red light) and monitoring (cut and dim).

The "TB SOURCE" parameter in the INPUT CONFIG. page sets the assignment of the talkback source, i.e. the input channel to the TB bus (NONE, from CR, or from Studio).



During talkback from the Studio, the studio speakers are dimmed only if at least one input channel is configured as "SIGN." = STUDIO *and* "TB SOURCE" = STUDIO. The same applies for talkback from the control room.

Note: Configuration of talkback and signaling is also possible for analog line or digital input channels as well as for a TB mic input channel, as shown by the following screenshots.

| INPUT CO | NFIG. | SAVE | EXIT | LOGOUT | 13:28:19 |
|------------------|---------------------|-------------|-----------------------|---------------------|---------------------------|
| CHANNEL << >> | 8 ANALOG | CTRL. OUT 1 | ON & FADER | AUX & INSERT | · · · · · |
| INPUT | Ĥ | CTRL. OUT 2 | LOCATE PFL∕CUE KEY | TEL N-1 AUDIT | T. K.T. |
| LABEL | I 8A | CTRL. IN | NOT ACTIVE | STUDIO MON | TH 16.APR.02 |
| SIGN. CR | TB SOURCE CR | TIMER START | NO | MIXER SETUP | 00:00:00 |
| LEVEL 6dBU | HEADROOM 9dB | | BUS ASSIGNMENT | HOME | 00:00:00 START RESET |
| | | | | | |
| INPUT CO | NFIG. | SAVE | EXIT | LOGOUT | 13:35:02 |
| CHANNEL | 9 DIGITAL | CTRL. OUT 1 | ON & FADER | AUX & INSERT | |
| INPUT | A | CTRL. OUT 2 | LOCATE PFL∕CUE KEY | TEL N-1 AUDIT | T. / T |
| LABEL | I 9A | CTRL. IN | NOT ACTIVE | STUDIO MON | TH 16.APR.02 |
| SIGN. STUDIO | TB Source Studio | TIMER START | NO | MIXER SETUP | 00:00:00 |
| INPUT | | | BUS | номе | 00:00:00 |

For deactivating the internal TB microphone and activating the external TB mic inputs, the INPUT CONFIG. page for the channels 11 and 12 is used:

| INPUT CO | NFIG. | SAVE | EXIT | ou⊤ 13:35:51 |
|------------------|-----------------|------|----------------|----------------------------|
| CHANNEL << >> | 11 ANALOG | | AUX INS | |
| | | | TI N AUI | |
| LABEL | TB to ST | | STU | DIO DN TH 16.APR.02 |
| | TB SOURCE CR | | MISE | VER 00:00:00 |
| | | | но | ME 00:00:00 Start Reset |

6.4 External PFL

The OnAir 1000 provides an external PFL audio input. If the EXT PFL key is pressed, the key is illuminated, and the signal at the EXT PFL input (EXTERNAL MONITOR INPUT A connector) is routed to the PFL bus. A control output is activated if EXT PFL is active. In addition, the EXT PFL function can be activated by a dedicated control input on the EXT PFL CTRL connector (refer to chapter 14.11).

With this feature, the OnAir 1000 is well-suited for the integration with a broadcast automation (CAB, computer-assisted broadcasting) system. These systems need a separate PFL input that can be activated by the CAB system itself.

| Key | Function | Description |
|------------|--------------|--|
| EXT PFL | External PFL | Switches the EXT PFL OUT control output on and off, and routes the EXT PFL audio input signal to the PFL bus. The key is illuminated if active, or if a control signal is applied to the EXT PFL input. |

7 SIGNALING

For control room and studio, signaling is provided by means of opencollector outputs (9-pin D-type connector "SIGN." on the rear of the console; for connection details, refer to chapter 14.11).

One output is available for each of the following statuses:

- Control Room microphone is on-air,
- Studio microphone is on-air, and
- Any signal from the console is on-air (i.e., a fader is open, this channel's ON key is active, its signal is routed to the PROGRAM and/or the REC-ORD bus, and, if configured, the corresponding master fader is open).

The CR-MIC and STUDIO-MIC indicators in the monitoring section of the console are illuminated when the corresponding control output is active.

| SIGN. Output | Active if: |
|----------------|--|
| CR MIC OUT | At least one input channel of a CR or DJ microphone is active (chan- nel ON, fader open, assigned to program or record bus, correspond- ing master fader open). |
| STUDIO MIC OUT | At least one input channel of a studio microphone is active (channel ON, fader open, assigned to program or record bus, corresponding master fader open). |
| PGM OUT | Depending on ON-AIR INDICATION condition selected in the COM-MON SETTINGS page: ΣPROGRAM AND ΣRECORD – at least one input channel is active (channel ON, fader open, assigned to program or record bus, corresponding master fader open). ΣPROGRAM – at least one input channel is active (channel ON, fader open, assigned to program bus, PGM master fader open). ΣRECORD – at least one input channel is active (channel ON, fader open, assigned to program bus, PGM master fader open). ΣRECORD – at least one input channel is active (channel ON, fader open, assigned to record bus, REC master fader open). |



The ON AIR INDICATION condition for the PGM OUT control signal can be set in the COMMON SETTINGS page (reached by touching MIXER SETUP followed by COMMON), if access permission is available.

An additional ON AIR IN opto-coupler input on the SIGN. connector can be used for illuminating the ON-AIR indicator in the monitoring section of the console.

Note: For information on the DEFAULT MASTER LEVEL window on the COMMON SETTINGS page, please refer to chapter 5.14.1.

8 MACHINE CONTROL

The OnAir 1000 provides different control inputs and outputs. These can be used e.g. for switching an input channel on and off, or for starting, stopping, and cueing of the connected source unit (as CD/cartridge/MD players, tape recorders, or a CAB system). The control inputs and outputs are input channel-related and are re-assigned together with the input channel signal to the respective fader strip if the channel routing is changed.

Each audio input holds two control outputs and one control input on D-type connectors. The signals are:

- CTRL OUT 1 (normally used for fader start, but configurable for other functions; see chapter 8.2.1)
- CTRL OUT 2 (configurable functions see chapter 8.2.2)
- CTRL IN (configurable functions see chapter 8.3.1)

The status of the output signals depends on the control elements of the fader strip (keys 1 to 3, fader, input selection, channel routing, and output bus assignment), as well as on the console configuration.

Each input channel has two physical audio inputs, each of them with own control input and two control outputs, allowing to control every external source separately.

8.1 Keys and LEDs

Key 1/LED 1



The first key (labeled "PFL") is always used to activate/deactivate the PFL function; if active, LED 1 is illuminated.

The PFL function can affect the CTRL OUT1 and/or CTRL OUT2 control signals, depending on the configuration. This configuration is performed in the center part of the INPUT CONFIG. page and is identical for all types of input units.



Key 2/LED 2



The second key (labeled "ON") is used either to switch the input channel ON or to toggle the input channel ON/OFF, depending on the configuration of key 3. This function affects the CTRL OUT1 control signal. LED 2 always indicates the input channel's ON/OFF status. Key 3/LED 3



The functionality of the third key (labeled "OFF") depends on the console configuration. It can act as channel OFF key or trigger a LOCATE function. The key 3 functionality is set in the COMMON SETTINGS page:



| Console configuration (valid for all channels): | | | |
|---|------------------|--|--|
| Key 3 ("OFF") function Key 2 ("ON") function | | | |
| (Channel) OFF | (Channel) ON | | |
| LOCATE | (Channel) ON/OFF | | |
| LOCATE | (Channel) ON * | | |

Channel ON/OFF affects the CTRL OUT1 control signal of the selected input channel, and the LOCATE function defines the status of the CTRL OUT2 control signal of the selected input channel.

* To protect a channel from being switched off inadvertently, the OFF function can be disabled. This setting is performed in the COMMON SET-TINGS page; *it is valid for all channels of the console simultaneously*.

Note: If this setting is selected, no input channel can be switched off by pressing a key – therefore, either closing the fader must do, or an external pushbutton per input channel has to be added. For this purpose, a CTRL IN control input per input channel is provided; please refer to chapter 8.3.1. Select the option "CTRL. IN – EXT. ON/OFF" in the INPUT CONFIG. page.

LED 3 is illuminated if an external device indicates ready status using the CTRL IN input signal of the selected input channel, or if the CAB system indicates "ready for playback" via the serial interface ("Monitora" protocol, refer to chapter 9).

8.2 Control Outputs

8.2.1 **CTRL OUT1**

This open-collector output is normally used to start external devices, such as CD players or a CAB system, to play the next track. On the INPUT CONFIG. page (see chapter 8.1), other functions can be assigned individually for each audio input.

| Input configuration (for each channel individually): | | | | | | |
|--|--|--|--|--|--|--|
| CTRL OUT1 mode | CTRL OUT1 signal | | | | | |
| NOT ACTIVE | Output always open (inactive). | | | | | |
| PFL/ON & FADER | Active if either PFL key is pressed (independent of fader position), or if ON key is pressed and fader is open (fader start function, causing the source to be started as well when PFL key is pressed). | | | | | |
| ON & FADER | Active if ON key is pressed and fader is open (standard fader start function). | | | | | |
| ON LAMP | Active if input channel is ON; used as acknowledgement if the input channel is remotely controlled. | | | | | |

8.2.2 CTRL OUT2

This open-collector output can be used to re-park an external device, such as a CD player, after cueing, or for signaling, or as an acknowledgement for ON status. It can be configured individually for each input in the IN-PUT CONFIG. page (see chapter 8.1).

| | ut configuration (for each channel individually): | | | | | |
|-------------------------|---|--|--|--|--|--|
| CIRL OUI2 mode | CTRL OUT2 signal | | | | | |
| NOT ACTIVE | Output always open (inactive). | | | | | |
| LOCATE KEY | Active if key 3 ("OFF", configured as LOCATE key) is pressed while the channel is closed (fader closed or channel OFF). Using this function, a source that has been pre-listened before can be reset to the start point. | | | | | |
| PREVIEW | Active as long as key 1 ("PFL") is pressed while the channel is closed (fader closed or channel OFF). Using this function, a source can be started for pre-listening. | | | | | |
| LOCATE PFL KEY | Active for approx. 0.3 s when key 1 ("PFL") is switched off while the channel is closed (fader closed or channel OFF). Using this function, a source can be given a locate command to return to the start point. | | | | | |
| LOCATE PFL / LOC KEY | Combination of the LOCATE KEY and LOCATE PFL KEY functions: Output is active for approx. 0.3 s when key 1 ("PFL") is switched off while the input channel is closed (fader closed or channel OFF). Using this function, a source can be given a locate command to return to the start point. Output is active if key 3 ("OFF", configured as LOCATE key) is pressed while the input channel is closed (fader closed or channel OFF). Using this function, a source that has been pre-listened before can be reset to the start point. | | | | | |
| ATTENTION | Active as long as key 3 ("OFF", configured as LOCATE key) is pressed; can be used for "ready" signaling. | | | | | |
| ON LAMP | Active if input channel is ON; used as acknowledgement if the channel is remotely controlled. | | | | | |
| FADER STOP_PULSE | Generates a fader start pulse (approx. 0.2 s) at CTRL OUT 1 and a fader stop pulse (approx. 0.3 s) at CTRL OUT 2, but only if CTRL OUT 1 is configured as ON & FADER or PFL/ON & FADER. | | | | | |

8.3 Control Inputs

8.3.1 CTRL IN

A CTRL IN control input is available for each audio input channel. This input channel's function can be selected in the INPUT CONFIG. page (see chapter 8.1).

| Input configuration (for each channel individually): | | | | | | |
|--|---|--|--|--|--|--|
| CTRL IN mode CTRL IN signal | | | | | | |
| NOT ACTIVE | No function (input signal is ignored) | | | | | |
| READY | Key 3 ("OFF") is illuminated as long as CTRL IN is active | | | | | |
| EYT MUTE | Input channel is muted as long as CTRL IN is active (e.g. cough | | | | | |
| | key) | | | | | |
| | Toggles the channel ON/OFF; used e.g. for ON/OFF button on | | | | | |
| EAT. UN/OFF | speaker's desk, or for remote control through CAB system | | | | | |

Note: An optional, external control interface (1.942.803.xx) can be used for the following functions of a single input channel:

- Electrical isolation between CTRL OUT and the connected source (relay with make contact);
- Conversion from the continuous fader start signal to electrically isolated start and stop pulses;
- Remote control of an input channel using separate ON and OFF keys, logically linked with the ON LAMP signal. The ON and OFF keys in the console remain functional;
- Remote control of an input channel through a CAB system sending a continuous control signal as long as an input channel must be on, logically linked with the ON LAMP signal. The external command is overridden by the ON and OFF keys in the console.

Additional information on this interface can be found in the circuit diagrams chapter of the OnAir 1000 Service Manual.

8.3.2 EXT PFL Input

An external PFL control input is available on the "EXT PFL CTRL" connector at the rear of the console (refer to chapter 14.11). It allows e.g. to open the EXT PFL pre-listening audio path by a CAB system.

8.4 CTRL OUT1/2 & CTRL IN Application Examples

| Application | Configuration CTRL OUT1; see Note 4 | Configuration CTRL OUT2; see Note 4 | Configuration of key 3 ("OFF"/LOCATE); see Note 4 | Connection(s) OnAir 1000 -> controlled source (e.g. CD player, cart/tape recorder |
|---|---|---|--|--|
| Fader start/stop with: Fader open/close, or channel ON/OFF | ON & FADER | _ | _ | CTRL OUT1 -> remote control input "fader start" |
| Fader start/stop with: Fader open/close, or channel ON/OFF, or PFL ON/OFF | PFL / ON & FADER | — | - | CTRL OUT1> remote control input "fader start" |
| Review function of Studer D730/D731: pre-listening with PFL and subsequent locate to the last cue address. Afterwards: PLAY with fader start; see Notes 1 and 2 | ON & FADER | PREVIEW | _ | CTRL OUT2 -> Studer D730/ D731, remote control input "SR_REVIEW"; <i>see fig. a)</i> |
| Pre-listening with PFL and subsequent locate to the start; console sends an 0.2 s locate pulse on CTRL OUT2. Afterwards: PLAY with fader start; see Note 1 | PFL / ON & FADER | LOCATE PFL KEY | _ | CTRL OUT1 -> remote control input "fader start", CTRL OUT2 -> remote control input "locate"; see fig. b) |
| Manual locate to the start, using OFF/LOCATE key 3; see Note 1 | — | LOCATE KEY | LOCATE | CTRL OUT2 -> remote control input "locate"; see fig. b) |
| Pre-listening with PFL and subsequent locate to the start. Manual locate also available; see <i>Note 1</i> | PFL / ON & FADER | LOCATE PFL / LOC KEY | LOCATE | CTRL OUT1 -> remote control input "fader start"; CTRL OUT2 -> remote control input "locate"; see fig. b) |
| Fader start pulse and fader stop pulse (on different control outputs) | ON & FADER or PFL / ON & FADER | FADER STOP PULSE | _ | CTRL OUT1 -> fader start pulse, CTRL OUT2 -> fader stop pulse; used for any remote control input using pulses instead of a static signal |
| Control of any external signal with key 3 ("OFF") | _ | ATTENTION | LOCATE | CTRL OUT2 -> any control input |
| Reflects the ON lamp status to CTRL OUT1 Reflects the ON lamp status to CTRL OUT2 | ON LAMP | ON LAMP | | CTRL OUT1 -> any control input CTRL OUT2 -> any control input |

Note 1: Function is available only if the corresponding console channel is **not** active; "channel active" = fader open **and** channel ON **and** channel assigned to PGM or REC bus.

Note 2: D730/D731 configuration checksum example, suited for this function: **4050631164470**. For more information on this subject, please refer to the D730/D731 operating instructions manual, order no. 10.27.1672

Note 3: Combination of different applications is possible if allowed by the CTRL OUT1/2 wiring.

Note 4: DO NOT change the settings while any of the control output signals is active!





9 AUTOMATION

9.1 Introduction

The OnAir 1000 supports external CAB (computer-assisted broadcasting) systems. A serial interface (RS232/RS422) is used for communication with the CAB system.

Studer DigiMedia: Information on how to connect and operate the OnAir 1000 mixing console with a DigiMedia CAB system is given in the current DigiMedia operating instructions.

The serial interface protocol is based on the "Schnittstellen-Spezifikationen Regiepult" of DSA (Digitale Steuerungs- und Automationstechnik, Thomas Volgmann) furthermore called [MONPROT], which was defined by VCS and Siemens.

The implementation in the OnAir 1000 is a subset of [MONPROT].

For detailed information on the telegrams implemented in the OnAir 1000, please refer to the "Communication Protocol for Broadcast Automation" document [BCACOM] (available on request).

9.2 Features of the OnAir 1000 CAB Support



- Serial interface: Serial RS232/RS422 standard communication interface based on [MONPROT].
- 2 x 12 character text display per channel.
- Music and speech output: The CAB can control the output assignment (program/record bus) of the currently selected input of a channel.

9.3 Application Handling

9.3.1 Configuration for Automation Control

Input channels must be assigned to the automation system in the console configuration in order to be controlled by an external CAB system. AUTOMATION CONFIG. is reached from the SYSTEM CONFIG. page:



The AUTOMATION CONFIG. page allows each input channel to be assigned to the CAB system.

Please note that these channels' labels *must* be named "DIGI 1" through "DIGI 3". Normally three input channels are used with the CAB system; a fourth "DIGI 4" input channel is possible, but unusual.

9.3.2 Communication Time-Out

If no valid telegram is received from the CAB within approx. 30 seconds, the OnAir 1000 assumes that the communication is interrupted.

In this case it stops sending telegrams and the following actions are performed:

- All input channels are deselected;
- A warning: "Communication to Broadcast Automation lost!" is displayed.

After the next or first telegram from the CAB, communication will be reestablished.

The information: "Communication to Broadcast Automation established!" is generated.

9.3.3 Output Selection

The CAB is able to control the output assignment (program/record bus) of the currently selected input of any input channel. This allows, for instance, to route speech to the program output, and music to the record output.

9.3.4 Start a New Title from Schedule

In automation mode, the CAB can start a new title automatically (it is also possible to start new titles manually; then, the operator opens the fader while the input channel is already switched ON, or switches the desired channel ON while the fader is already open).

Automatic New Title Start: The CAB can control the console automatically. This automatic mode must be activated from within the CAB and can, for example, be used for controlling a program during the night without any assistance.

If the CAB runs in automatic mode, it starts playback of the source and opens the appropriate input channel with the pre-defined level and fade-in time. This will send a new audio level to the DSP, and switch the input channel ON in any case.

If a take ends, the CAB performs a cross-fade on its audio card, the selected channels on the console remain open.

If the CAB changes the audio level, the channel changes into *auto takeover mode*. This means that the audio level (i.e. actual DSP level) and the fader level (i.e. physical position of the fader knob) differ for more than a specified auto takeover window. If the fader is moved now, nothing will change until the physical position of the fader knob is within the auto takeover window. Then the auto takeover mode is left.



Auto takeover mode is indicated in the fader strip by the two AUTO TAKEOVER arrow LEDs. The operator can recognize the direction in which he has to move the fader in order to leave auto takeover mode and take control of the signal level. If the physical position of the fader is brought within the auto takeover window or if it accidentally is situated within the window, both arrow LEDs will be illuminated.

| AUTO TAKE OVER | AUTO TAKE | TAKE OVER | |
|----------------------|----------------------|----------------------|----------------|
| Channels not con- | Audio level above | Audio level below | Audio level |
| trolled by a CAB | the fader position – | the fader position - | equal to the |
| system | move fader down | move fader up | fader position |

9.3.5 Indication of the Currently Playing Input Line

As the OnAir 1000 is not equipped with motor faders, a clear indication of the channel currently "playing" is necessary.

Under the following conditions the respective channel label is in continous scrolling mode, indicating "ON-AIR" if:

- Fader (audio level) is open;
- The channel is switched ON;
- The channel is assigned to program and/or record bus (as defined in the Common Settings page);
- The channel is selected by the CAB (SOURCE_SELECTION).

9.3.6 Pre-Listening

When starting the pre-listening function in the CAB system by clicking on the LISTEN button on the CAB screen, followed by selecting a title, the CAB system immediately sends the desired title over the fourth channel of its audio card. At the same time, it opens the external PFL input, so that the desired title is audible in the console's PFL loudspeaker.

9.3.7 Time Synchronization

The CAB is able to set the console's internal clock (time and date) if TIME SYNC. is set to AUTOMATION in the TIME & DATE FORMAT page; refer to chapter 11.2.5.



10 USER MODES

10.1 Purpose of User Modes

| | The console is used in different studios with different working practice and different personnel structure. A large part of users in broadcast studios is not technically oriented. A mixing console meeting their requirements must be simple to use, reliable and free of "unnecessary" controls and dis- plays. Setting of a wider range of console parameters, saving of console settings (snapshots), and modifications of the console configuration have to be left for technically more competent users (studio technicians, chief engineer). It is also necessary to protect parts of system data and give the permission to change them only to a restricted number of users. In order to satisfy these needs, the OnAir 1000 console supports individual user ac- cess rights. |
|-----------------------|--|
| Default User: | Three classes of users are defined for the OnAir 1000; these are: No password required; Private snapshots and mic settings; User channel routing: |
| Normal User: | Function access rights according to the configuration. Password-protected; Private snapshots and mic settings; User channel routing; |
| System Administrator: | Function access rights according to the configuration. Password-protected; User channel routing; Unlimited access rights to all functions; Defines new users; Defines access rights for all users; Stores global snapshots, global mic settings, and global channel routings. Stores private snapshots, private mic settings, and user channel routings for all users. |
| | The table in chapter 10.2 shows the functions to which access is configure |

The table in chapter 10.2 shows the functions to which access is configurable in the OnAir 1000.

10.2 Access to Configurable Functions of the Console

| Function Description | Default User | Normal User | Adminis- trator | |
|---|--------------|----------------|--------------------|--|
| Loglist management | 1 | | | |
| Accept an entry (delete entry from list) | configurable | configurable | accessible | |
| View the log list | accessible | accessible | accessible | |
| Aux Master | | | | |
| Inserts on AUX page | configurable | configurable | accessible | |
| Aux 1 and 2 parameters | configurable | configurable | accessible | |
| AUDIT master | | | | |
| AUDIT level on TEL N-1 AUDIT page | configurable | configurable | accessible | |
| N–1 master | J | <u> </u> | | |
| N–1A/N–1B levels on TEL N–1 AUDIT page | configurable | configurable | accessible | |
| Channel input parameters | · · · · | <u> </u> | | |
| Aux 1 parameters: | | | | |
| Set channel input as AF for Aux 1 Set channel input as PF for Aux 1 Add channel input to Aux 1 Remove channel input from Aux 1 Aux 1 level | configurable | configurable | accessible | |
| Aux 2 parameters: Set channel input as AF for Aux 2 Set channel input as PF for Aux 2 Add channel input to Aux 2 Remove channel input from Aux 2 Aux 2 level | configurable | configurable | accessible | |
| Phase parameters: Set phase to invert Set phase to normal | configurable | configurable | accessible | |
| Phantom parameters: Set phantom to ON Set phantom to OFF | configurable | configurable | accessible | |
| Stereo mode parameters: Set channel to stereo mode Set channel to mono mode Set stereo mode to LR Set stereo mode to LL Set stereo mode to RL Set stereo mode to RR | configurable | configurable | accessible | |
| Gain/cal parameters: | configurable | configurable | accessible | |
| Pan/balance parameters: | configurable | configurable | accessible | |
| Sum selection: Add/remove chn to/from ON-AIR bus Add/remove chn to/from RECORD bus | configurable | configurable | accessible | |

| Function Description | Default User | Normal User | Adminis- trator | |
|--|--------------|----------------|--------------------|--|
| EQ parameters: Switch EQ for this channel input on Switch EQ for this channel input off Set EQ high shelving corner freq. to low Set EQ high shelving corner freq. to high Switch high-pass filter off Switch high-pass filter on Set EQ low shelving corner freq. to low Set EQ low shelving corner freq. to high Switch phantom power off Switch phantom power on Set the low filter gain Set the peak filter corner frequency | configurable | User | trator | |
| Set the peak filter gain Set the high filter gain | | | | |
| Channel common parameters | | | | |
| Input selection | configurable | configurable | accessible | |
| All other parameters | accessible | accessible | accessible | |
| Mixer setup | | | | |
| Delete selected global snapshot/mic setting/ channel routing | - | - | accessible | |
| Create global snapshot/mic setting/ channel routing | - | - | accessible | |
| Recall global snapshot/mic setting/ channel routing | accessible | accessible | accessible | |
| Delete selected private snapshot/mic setting | accessible* | accessible* | accessible | |
| Create private snapshot/mic setting | accessible* | accessible* | accessible | |
| Recall private snapshot/mic setting | accessible* | accessible* | accessible | |
| Recall user channel routing | accessible* | accessible* | accessible | |
| Channel routings | | | | |
| Enter channel routing page | configurable | configurable | accessible | |
| User administration | | | | |
| Enter user administration | - | _ | accessible | |
| System configuration | | | | |
| Display system configuration page | configurable | configurable | accessible | |
| * accessible for a user = his own data only | | | | |

10.3 User Administration

The described access permission allows different functionality ranges to important console functions for each user. By assigning an appropriate access permission to each user, it is possible to fit the OnAir 1000 console to very different working environments.

This user administration is done with the help of a User Administration table that is part of the console configuration. This table can be edited by the system administrator only.

The users supposed to use the console with basic functionality do not need an account. To adjust the console to differently trained staff, it is possible to change the access permissions of the default user.

A user who wants to store his own, private data (snapshots, mic settings and/or channel routing) must have an account containing his name, his access permission table, and an optional password.



The USER ADMINISTRATION page can only be opened by the system administrator, by touching USER ADMIN in the SYSTEM CONFIG. page. Ten users will be listed. With the USER 11..20 field, the administrator can list the remaining users. When the second block of users is displayed, the USER 11..20 field changes to USER 1..10.

STUDER

10.4 Administration Functions

By the system administrator, a user record can be created (NEW), changed (EDIT), or deleted (DELETE).

Create User Record:

To create a new user record, touch NEW in the USER ADMINISTRA-TION page. The NEW USER page appears with empty input fields:

| NEW USER | | SAVE | | EXIT | LOGOUT | 14:04:07 |
|----------------------------|----------------------|------------------------|--------------|-------------------------------|---------------------|---------------------------|
| USER NAME | ME | | WORD | | AUX & INSERT | 1/2 |
| ACCESS PERMISSION | | SET CHANNEL ROUTING | | SET LIKE DEFAULT USER | TEL N-1 AUDIT | |
| Central functions | Channel functions | EQ | INPUT SEL | Configuration | STUDIO Mon | TH 16.APR.02 |
| AUDIT N-1 MASTER MASTER | GAIN CAL PHASE | SUM SEL | AUX 1 | ROUT- ING | MIXER SETUP | 00:00:00 |
| INSERT AUX MASTER | PHAN- TOM MODE | PAN BAL | AUX 2 | SYSTEM ACCEPT CONFIG ERROR | HOME | 00:00:00 START RESET |

The USER NAME and PASSWORD fields are filled in, using the keyboard page. The maximum length of the password is 22 characters. The user name may consist of up to 20 characters. If the new user is not accepted (e.g. because the user name already exists), an error message tells the system administrator that the new user will not be registered. A user name can be entered without a password, too.

Access permission is configured by touching the corresponding function fields on the NEW USER page. With SET LIKE DEFAULT USER, the access permission setting and the channel routing of the default user are copied to the user currently being edited and can subsequently be edited again. The new user record is stored with SAVE.

Two users are already predefined: The default user and the administrator. Both can only be edited, *but not created or deleted*.

Change User Record: A user record is selected by touching the appropriate name field in the USER ADMINISTRATION page, followed by EDIT. The USER EDIT page, containing the user's individual data, appears.

| USER EDI | SA | VE | EXIT | LOGOUT | 14:04:07 | |
|----------------------------|--------------------------|------------------------|--------------|-------------------------------|---------------------|---------------------------|
| USER NAME | PETER WOLF | PASSWORD | | ALPHA1OMEGA | AUX & INSERT | · 1/2 · |
| ACCESS PERMISSION | | SET CHANNEL ROUTING | | SET LIKE DEFAULT USER | TEL N-1 AUDIT | |
| Central functions | Channel functions | EQ | INPUT SEL | Configuration | STUDIO MON | TH 16.APR.02 |
| AUDIT N-1 MASTER MASTER | GAIN CAL PHASE | SUM SEL | AUX 1 | ROUT- ING | MIXER SETUP | 00:00:00 |
| INSERT AUX MASTER | PHAN- STEREO TOM MODE | PAN BAL | AUX 2 | SYSTEM ACCEPT CONFIG ERROR | HOME | 00:00:00 Start reset |

The data can be edited in the same way as described above, except that the user name is displayed but cannot be modified. The only way to rename a user while preserving his snapshots, mic settings, and channel routings is to save them to a PC-card, delete the user, create a new one with the de-

sired name, and then reload the snapshots, mic settings, and channel routing from the card. After touching SAVE, the old record is replaced by the new one. This procedure has to be performed this way in order to avoid name conflicts in the console's memory and on the PC-card.

When editing the default user, the USER EDIT page looks similar, except that no password field is displayed.

When editing the administrator, the USER EDIT page neither displays the access permission fields nor the USER NAME field, but the password field is enabled.

Set User Channel Routing: The system administrator can define the user channel routing (surface definition) for every user by touching the SET CHANNEL ROUTING field in the USER EDIT page. In addition, the administrator can allow a user to change his own channel routing.

When the SET CHANNEL ROUTING field is touched, the CHANNEL ROUTING page appears and displays this specific user's channel routing. Instead of the SAVE TO USER PROFILE field, a BACK field is displayed, leading back to the USER EDIT page.



The system administrator can protect specific connections within the channel routing by selecting the desired cross-point with the horizontal and vertical lines, and then touching the PROT(ect) field. A protected connection is indicated by a dashed vertical line in the grid (see below); this connection cannot be changed by the user, even if he might be allowed to change his channel routing. Only the administrator can UNPROT(ect) this connection again. However, the user can change the input selection (A/B) of this input unit, if he has the INPUT SEL access right.



Delete User Record: To delete a user record from the user administration table, a record must be selected by touching the desired name field. A dialog box appears; if deleting the user is confirmed there, the selected user is permanently removed from the memory, and the user record disappears from the USER ADMINISTRATION page.

The default user and the administrator can be edited only, but not deleted.

10.5 Log-in Procedure and Defaults

The console always starts up in default user mode. The default user's access permission is activated automatically when a user logs out. If the corresponding customer code is activated (refer to chapter 11.2.11), the default user's channel routing is also recalled automatically.

If the user is a registered user, and if he wants to work in his own, private environment, he must log-in. After touching the LOGIN field, the LOGIN page will appear where the user can touch the field with his own name.

| LOGIN | | ADMIN. | EXIT | LOGIN | 14:12:15 |
|------------------------|------------------|--------------|-----------------------|---------------------|-------------------------|
| IVO SAKOMURA | LISA GERBER | PETER GARRET | EMIL TISCHBEIN | AUX & INSERT | |
| ANTON GAST | LINDA DE MOL | DAVE SCOTT | PATRICIA DE NAPOLI | TEL N-1 AUDIT | |
| DAVID VAN DER PLOEG | MARTIN MAERKI | PAOLO SCANO | CHARLOTTE SAX | STUDIO Mon | TH 16.APR.02 |
| PAUL YOUNG | SASKIA SMITH | HANS POL | ESTHER VALERI | MIXER SETUP | 00:00:00 |
| ANNE SEBIRE | KATE LENERT | SYLVIA SEGON | GABOR JENKINS | HOME | 00:00:00 START RESET |

If a password is defined for this user, he has to enter it on the keyboard page.

| Passwor | ·d: _ | | | | | | | | | | |
|---------|-------|---|---|---|---|---|---|---|----|-------|--------|
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | Ø | < | - ВАСК |
| Q | Σ | Ε | R | Τ | Y | U | I | 0 | Ρ | CI | EAR |
| A | S | D | Ь | G | Η | J | K | L | <; | ENTER | |
| | Ζ | X | С | ► | В | Ν | Μ | • | | | |
| | | | | | | | | | | | EXIT |

If the password is correct, the system automatically returns to the page on which the user touched the LOGIN field. If the password is not valid, a dialog box will appear on the LOGIN page telling the user that the password was not correct. The user can leave the LOGIN page via the EXIT field which brings him back to the previous page.

If no password is defined for a user, the system does not show the keyboard page after a touch on a name field in the LOGIN page. The system directly jumps back to the page where the LOGIN field was touched. As the system does not check for a password in this case, this user's private data are not protected, and any other user can access them.

If a console is operated by one person only, the system administrator simply has to enable all access rights for the default user. This is the way the user can control all console functions (except the user administration functions) without having to log-in.

A default user is always defined in the user administration table. His name is DEFAULT USER. The access permission of this user can be edited, but the record cannot be deleted, and no login code can be defined for this user.

11 CONFIGURATION

In order to meet the requirements for different studio environments, the OnAir 1000 is highly configurable.

A configuration is "static", which means that it cannot be changed during normal operation. Although snapshots rely on a certain configuration, the configuration data are not stored together with the snapshots. Therefore, snapshots cannot change the console configuration.

11.1 Configuration Handling

System configuration can be changed by the system administrator, or by any user with access right to the SYSTEM CONFIG. page. System configuration includes data for:

- Channel labels
- Control signal modes
- Nominal input levels
- Level meter assignment, etc.

An OnAir 1000 configuration can be stored on a PC-Card, or loaded from the PC-Card into the console.

11.2 Configuration Procedure

| SYSTEM CONFIG. | EXIT LOGOUT 14:21:33 |
|-----------------------------------|---|
| | USER ADMIN. AUX & INSERT |
| | LEVEL METER |
| | STUDIO / MON TH 16.APR.02 |
| TIME & DATE SYSTEM TEST SW UPDATE | CONSOLE DUMP |
| SYSTEM SYNC. AUTOMATION | CUSTOMER CODE HOME 00:00:00 START RESET |

The console configuration can be changed in the SYSTEM CONFIG. page. This page can only be reached from the Mixer Setup page by an operator with system administrator permission.

The configuration parameters are subdivided in groups. Each group is edited on a separate page accessed from the SYSTEM CONFIG. page.



LOGOUT

AUX & INSERT

STUDIO

MON

13

TH 16.APR.02

11.2.1 Input

Microphone Input Channel

Analog Line Input Channel

There are three slightly different INPUT CONFIG. pages for editing input configurations, depending on the type of input unit. The pages for microphone, analog line, and digital line input channels are shown below.

EXIT

ON & FADER

LOCATE PFL/CUE KEY

NOT ACTIVE

SAVE

CTRL. OUT 1

CTRL. OUT 2

CTRL. IN

| | UNF16. | |
|----------|---|--|
| CHANNEL | 1 MIC | |
| INPUT | A | |
| LABEL | I 1A | |
| SIGN. CR | TB SOURCE NONE | |
| | CHANNEL << >> INPUT << >> LABEL SIGN. CR | |

TNBUT CONETC

| SIGN. CR | TB SOURCE NONE | TIMER START | NO | SETUP | 00:00:00 |
|---------------|-------------------|-------------|-----------------------|---------------------|---------------------------|
| | | | BUS ASSIGNMENT | HOME | 00:00:00 START RESET |
| | | | | | |
| INPUT CO | NFIG. | SAVE | EXIT | LOGOUT | 13:28:19 |
| CHANNEL | 8 ANALOG | CTRL. OUT 1 | ON & FADER | AUX & INSERT | |
| INPUT | A | CTRL. OUT 2 | LOCATE PFL∕CUE KEY | TEL N-1 AUDIT | T. K. |
| LABEL | I 8A | CTRL. IN | NOT ACTIVE | STUDIO MON | TH 16.APR.02 |
| SIGN. CR | TB SOURCE CR | TIMER START | NO | MIXER SETUP | 00:00:00 |
| EEVEL 6dBU | HEADROOM 9dB | | BUS ASSIGNMENT | HOME | 00:00:00 START RESET |
| | | | | | |

Digital Input Channel

| INPUT CONFIG. | | SAVE | EXIT | LOGOUT | 13:35:02 |
|--------------------|---------------------|-------------|-----------------------|---------------------|---------------------------|
| CHANNEL | 9 DIGITAL | CTRL. OUT 1 | ON & FADER | AUX & INSERT | |
| INPUT | A | CTRL. OUT 2 | LOCATE PFL/CUE KEY | TEL N-1 AUDIT | / . |
| LABEL | I 9A | CTRL. IN | NOT ACTIVE | STUDIO MON | TH 16.APR.02 |
| SIGN. STUDIO | TB SOURCE STUDIO | TIMER START | NO | MIXER SETUP | 00:00:00 |
| INPUT CONNECTOR | XLR | | BUS ASSIGNMENT | HOME | 00:00:00 Start reset |

Channel and input are selected with the CHANNEL and INPUT forward (>>) and backward (<<) fields. Channel and input can also be selected by pressing the SEL key of a channel. The display will show the current configuration data for the selected input and channel. To change a parameter, the corresponding field must be touched; it will change to the next possible value. Consecutive touching toggles through the available options.

Touching the LABEL field will open the keyboard page where an input label can be edited. The values in the LEVEL and HEADROOM fields (analog line input channels only) can be adjusted with the rotary encoders next to these fields. Touching BUS ASSIGNMENT opens the BUS AS-SIGNMENT page where the routing to the N–1 and to the AUDIT buses is performed.

Exceptions:

| Input 1 | abel: I | 1B_ | | | | | | | | |
|---------|---------|-----|---|---|---|---|---|---|----|--------|
| 1 | 2 | Э | 4 | ы | 6 | 7 | 8 | 9 | 0 | < BACK |
| Q | Σ | Ε | R | Τ | Y | | Ι | 0 | Ρ | CLEAR |
| A | S | D | F | G | Η | J | K | L | <; | ENTER |
| | Ζ | X | C | ► | В | N | M | • | | |
| | | | | | | | | | | EXIT |

LABEL The LABEL field is used for giving a name to a channel. Touching this field, the keyboard page opens.

The channel label display in the fader strip can display labels with up to four characters (upper case only). The whole name scrolls by in the display as long as the respective input selector key A or B is pressed, or after a new name has been saved. During scrolling, three dots are displayed between the end of the name and its next start for clarity.

If a name is longer than four characters, only the first two and the last two characters are displayed, so a little care has to be taken when generating the names. The following table illustrates some possibilities:

| Name | Channel display label |
|----------|-----------------------|
| ABC 12 | AB12 |
| ABC 1234 | AB34 |
| MIC1 | MIC1 |
| CD02 | CD02 |

When completed, touch <--: ENTER followed by SAVE to keep the changes, or EXIT to cancel without changes.

SIGN. / TB SOURCESIGN. is used for signaling (red light) and monitoring (cut and dim), the
available options are NONE, CR, and STUDIO.
The TB SOURCE parameter in the INPUT CONFIG. page sets the as-
signment of the input channel to the TB bus (NONE, from CR, or from
STUDIO).
During talkback from the studio, the studio speakers are dimmed only if at

least one input is configured as SIGN. = STUDIO *and* TB SOURCE = STUDIO. *The same applies for talkback from the control room*.

INPUT CONNECTOR(for digital input units only)Each (A and B) input of a digital input unit has three different input connectors (XLR, Cinch/RCA, and optical/TOSlink). This option selects independently for the A and the B inputs which connector is used.

LEVEL *(for analog line input units only)* "Level" is the nominal studio level expressed in dBu.

HEADROOM (for analog line input units only) "Headroom" is the difference between the A/D converter's clipping level and the nominal studio level. Example: An input with a level setting of +6 dBu and a headroom setting of 9 dB will accept a +15 dBu signal before the A/D converter comes into overload.

CTRL. OUT 1/2 Please refer to chapter 8 for details on this subject.

CTRL. IN Please refer to chapter 8 for details on this subject.

BUS ASSIGNMENT When touching the BUS ASSIGNMENT field, the display changes as follows, allowing to select the N–1 and the audition bus assignments:



The TO... fields allow routing the input signal to the corresponding N-1 (A...B) or to the Audition bus. The Audition bus is similar to the N-1 buses, but it is a stereo bus that can be selected as a monitoring source in the control room.

N–1 and Audition bus assignment is related to the input signal and is rerouted with the input signal when the input unit assignment is changed.

| Setting | Meaning |
|------------|--|
| NEVER | The input channel is never routed to N–1 or AUDIT |
| | The input channel is routed to N–1 and/or AUDIT if the channel is either |
| WHEN FOREC | assigned to the program or to the record bus |
| | The input channel is routed to N–1 and/or AUDIT if the channel is |
| WHEN FOW | assigned to the program bus |
| | The input channel is routed to N–1 and/or AUDIT if the channel is |
| | assigned to the record bus |
| | The input channel is always routed to N–1 and/or AUDIT, regardless of |
| ALVIAIS | the bus assignment |

- Notes: The output levels of the N–1 and the Audition bus are controlled on the TEL/N–1/AUDIT page using the rotary encoders. For details, please refer to chapter 5.3.1. When touching BACK TO CTRL OUT/IN CFG, the display switches back from the bus assignment option to the standard INPUT CONFIG. page.
- **TIMER START** If TIMER START is set to YES, the fader stop watch will be started/reset by the corresponding fader, if the input channel is ON and the fader is opened. TIMER START is related to the input signal and will be re-routed when the input unit assignment is changed.
 - **SAVE / EXIT** The Input Configuration must be saved for each input channel. If the user switches to a different input channel, a dialog box will call his attention to the fact that the changes will be cancelled if he does not save the current Input Configuration. The program remains in the Input Configuration page when saving a channel. Once a change has been saved, it will not be cancelled when leaving the Input configuration page with EXIT.

N-1/AUDIT Configuration:

11.2.2 Common Settings



The COMMON SETTINGS page contains the following configuration possibilities:

- **CR DIM WHEN AUDITION SEL** When the Audition bus is selected as a CR monitoring source, and a CR microphone input channel is ON, the CR monitor speaker level will be attenuated (DIM), muted (CUT), or nothing happens at all (DON'T CARE).
 - **PFL CUT ON CH. ACTIVE** If YES is selected, the signal of a channel is taken off the PFL bus when the channel is ON, although the PFL function is active (the PFL function remains active).
 - **ON AIR INDICATION** Three possibilities are available: Σ PROGRAM, Σ RECORD, or Σ PROGRAM AND Σ RECORD.

The ON-AIR signaling is only active if at least one channel is ON, if this channel's fader is open, and if this channel is assigned to the selected bus (Σ PROGRAM, Σ RECORD, or either of them).

In automation operation, ON-AIR is continuously scrolling in the channel label of the input channel selected by the automation system. FADER-STATUS is ON-AIR if the input channel is assigned to the selected bus (Σ PROGRAM, Σ RECORD, or either of them).

KEY FUNCTIONSFunctions of the channel keys labeled ON and OFF.
The ON key either switches the input channel ON or has an ON/OFF tog-
gle function, depending on the function selected for the OFF key.
If the OFF function is selected for the OFF key, the input channel is
switched off by pressing this key; if LOCATE is selected, the ON function
automatically changes to a channel ON/OFF toggle function (also refer to
chapters 8.2 and 8.3).

- **DEFAULT MASTER LEVEL:** The level of the PGM and REC master outputs can be biased in a range of -7 to +5 dB using the two rotary encoders below the corresponding fields. This setting is only valid if no fader strips are assigned to the Σ PGM and/or Σ REC outputs; please note that this setting must be done with care, as it directly affects the level of the main outputs.
 - **Note:** The MASTER FADER ASSIGNMENT function for PGM and REC master outputs that was available on this page in earlier software versions has been moved. Activation and deactivation of the PGM and REC master faders is performed on the CHANNEL ROUTINGS page now; please refer to chapter 5.14.

11.2.3 Output

On the OUTPUT CONFIG. page, the types of the output signals on the AUX 1, AUX 2, and AUDITION buses can be set.

| OUTPUT | CONFIG. | SAVE | EXIT | LOGOUT AUX & INSERT | 14:25:51 |
|----------|---------|------|------|---------------------------|-------------------------|
| AUX 1 | MONO | | | TEL N-1 AUDIT | T. 7.7 |
| AUX 2 | STEREO | | | STUDIO Mon | TH 16.APR.02 |
| AUDITION | MONO | | | MIXER SETUP | 00:00:00 |
| | | | | HOME | 00:00:00 START RESET |

11.2.4 Level Meter

The LEVEL METER configuration page defines the signal sources for the level meters no. 1 and no. 2. The sources for the phase correlators always are the same as the ones selected for the respective level meter.



Available signal sources:

METER 1 SOURCE CR MONITOR, PROGRAM, RECORD, AUX 1, AUX 2, AUDIT, N–1 A.

METER 2 SOURCE CR MONITOR, PROGRAM, RECORD, AUX 1, AUX 2.

11.2.5 Time and Date

The watch gives a time, day-of-week, and date display. The standard time reference is an internal battery-buffered real-time clock (RTC). The RTC continues to run even when the console is switched off. Therefore it is unnecessary to set the watch at power-up.

The RTC may also be synchronized by a time signal on the Time Sync input or by a broadcast automation system (CAB). If time synchronization is established, the top right corner of the center screen shows an "S".

Changes to the time or date settings are made on the TIME page. Access permission to the SYSTEM CONFIG. page is required.

From the HOME page, this page is found by selecting MIXER SETUP, followed by SYSTEM CONFIG., TIME & DATE, and TIME.

An example of the TIME page is given below:

| TIME | | SAVE | EXIT | LOGOUT | 14:28:18 |
|-------|---------|---------|--------|---------------------|-------------------------|
| | | | DATE | AUX & INSERT | |
| | | | FORMAT | TEL N-1 AUDIT | · K- |
| | | | TIME | STUDIO Mon | TH 16.APR.02 |
| HOURS | MINUTES | SECONDS | | MIXER SETUP | 00:00:00 |
| 14 | 28 | 18 | | HOME | 00:00:00 START RESET |

The hours, minutes, and seconds are adjusted with three of the rotary encoders located next to the corresponding fields. By touching "SAVE", the parameters are updated. If they are invalid, the parameter changes are ignored.

The DATE page is shown below. This page is displayed by touching the "DATE" field on either the TIME or the FORMAT page.

| DATE | | SAVE | EXIT | LOGOUT | 14:28:57 |
|------|-------|------|--------|---------------------|---------------------------|
| | | | DATE | AUX & INSERT | |
| | | | FORMAT | TEL N-1 AUDIT | |
| | | | TIME | STUDIO Mon | TH 16.APR.02 |
| DAY | MONTH | YEAR | | MIXER SETUP | 00:00:00 |
| 16 | APR | 2002 | | OFF | 00:00:00 START RESET |

The day, month, and year is adjusted with three of the rotary encoders located next to the corresponding fields. By touching "SAVE", the parameters are updated. If they are invalid, the parameter changes are ignored.
The watch configuration is done in the TIME & DATE FORMAT page. This page is displayed after touching the "FORMAT" field on either the TIME or the DATE page.

| TIME & DATE | | SAVE | EXIT | LOGOUT | 13:45:53 |
|------------------------|-----------|--------|--------|---------------------|---------------------------|
| FORMAT | | | DATE | AUX & INSERT | |
| TIME FORMAT | 24:00 | | FORMAT | TEL N-1 AUDIT | |
| DATE FORMAT | DD.MMM.YY | | TIME | STUDIO Mon | TH 16.APR.02 |
| TIME SYNC. | INTERNAL | OFFSET | | MIXER SETUP | 00:00:00 |
| TIME SYNC. PROTOCOL | Ĥ | -01:00 | | HOME | 00:00:00 START RESET |

Both time and date can be displayed in two formats (select with TIME FORMAT or DATE FORMAT, respectively), as shown below:

| Time Format | Digital Time Indication | |
|--------------------------|-------------------------|--|
| 05:00:00 p.m. | 12 hour time format | |
| 17:00:00 | 24 hour time format | |
| | | |
| Data Farmat | E | |
| Date Format | Example | |
| Date Format DD.MMM.YY | Example MO 20.MAR.02 | |

TIME SYNC. defines the means to set and maintain time and date. The following settings are available:

| Time Sync | Sync Reference | Date Setting | Time Setting |
|-----------------|--|--|---|
| INTERNAL | Internal quartz | By user | By user |
| TIME SYNC. UNIT | Internal quartz, peri- odically updated by the external time reference signal | By user, periodically reset by ext. time reference signal (century never over- written by ext. ref.) | By user, periodically reset by the external time reference signal |
| AUTOMATION | Internal quartz, peri- odically updated by automation time refer- ence | By user, periodically reset by automation time reference | By user, periodically reset by automation time reference |

The Time Sync input can process different time reference signal formats, the current format is selected with a DIP switch on the Time Sync interface. The setting of this switch is displayed in hexadecimal in the TIME SYNC. PROTOCOL field for easy verification. For details please refer to chapter 15.8.

The time zone offset can always be set, regardless whether it makes sense to set an offset in conjunction with the time sync source or not.

| Offset | Watch Function |
|-------------------|--|
| -12:00 to +12:00 | Compensates the sync time by the given offset |
| (resolution: 1 h) | before setting (synchronizing) the internal watch. |

11.2.6 System Test

More information on this subject can be found in chapter 13.3.

11.2.7 Software Update

More information on this subject can be found in chapter 12.2.

11.2.8 Console Dump

A "console dump" consists of configuration data (CONFIG. DATA), snapshots (global and private), mic settings (global and private), and channel routings.

Formatting the PC-Card Before dumping any data, the PC-Card has to be formatted. This is done in the console's PC-Card slot. Touch FORMAT followed by EXECUTE. A dialog box appears where the user can confirm the formatting (i.e. erasing the card completely). While formatting, the EXECUTE field flashes.



The CONSOLE DUMP page allows the user to save mixer setup data on a PC-Card, to send them to the serial port, or to load mixer setup data from a PC-Card. This feature can be used for back-up purposes, for copying data from one console to another, or for diagnostics. A PC-Card can contain one console dump only. It is possible to dump or to load the mixer setup data completely or partially only. The selection is done with the CONFIG. DATA, GLOBAL and PRIVATE SNAPSHOTS, GLOBAL and PRIVATE MIC SETS, and CHANNEL ROUTING fields. The function can then be selected with the fields in the center of the page and will be started by touching the EXECUTE field.

| CONSOLE | | EXIT | LOGOUT | 14:31:27 | |
|------------------------|-------------------------|----------------------|-----------|---------------------|-------------------------|
| CONFIG. DATA | | FORMAT | | AUX & INSERT | |
| GLOBAL SNAPSHOTS | PRIVATE SNAPSHOTS | | | TEL N-1 AUDIT | - K - |
| GLOBAL MIC SETTINGS | PRIVATE MIC SETTINGS | DUMP TO PC-CARD | | STUDIO Mon | TH 16.APR.02 |
| CHANNEL ROUTING | | DUMP TO SERIAL IF | done / ok | MIXER SETUP | 00:00:00 |
| | | LOAD FROM PC-CARD | EXECUTE | HOME | 00:00:00 START RESET |

A checkmark appears in the corresponding field when done, and "done / ok" is displayed after the selected operation(s) have been successfully completed, as shown above (or "done / error" if there is a problem).

When loading data from the PC-Card, the console data will be overwritten. Therefore this procedure has to be confirmed in a dialog box. The EXIT field returns to the SYSTEM CONFIG. page.

- A Configuration is loaded into the console by selecting CONFIG. DATA, LOAD FROM PC-CARD, and EXECUTE. An error message is displayed if the configuration cannot be loaded completely (e.g. Channel Type Mismatch).
 - **Snapshots** are loaded into the console by selecting GLOBAL (or PRIVATE) SNAP-SHOTS, LOAD FROM PC-CARD, and EXECUTE. Snapshots existing in the console will be overwritten.
 - **Mic Settings** are loaded into the console by selecting GLOBAL (or PRIVATE) MIC SETS, LOAD FROM PC-CARD, and EXECUTE. Mic settings already existing in the console will be overwritten.
- **Channel Routings** are loaded into the console by selecting CHANNEL ROUTING, LOAD FROM PC-CARD, and EXECUTE. Channel routings already existing in the console will be overwritten.

11.2.9 System Synchronization

On this page the system synchronization is configured. The synchronization source can be internal or external; for an external source, the sync signal type must be defined.



Possible sync modes:

| Sync source | Sampling rate |
|------------------------------|--|
| | 48 kHz ±100 ppm default, or |
| INTERNAL 48 kHz | 48 kHz, precision adjustable on clock sync IF; |
| | adjustment range approx. ±1000 ppm |
| WORDCLOCK 32/44.1/48 kHz | 32/44.1/48 kHz |
| AES/EBU 32/44.1/48 kHz | 32/44.1/48 kHz |
| VIDEO 25 FRM/S 48 kHz | 48 kHz |
| VIDEO 30 FRM/S 48 kHz | 48 kHz |
| VIDEO 29.97 FRM/S 47.952 kHz | 47.952 kHz |
| VIDEO 29.97 FRM/S 48 kHz | 48 kHz |

Note: The OnAir 1000 is designed to run at a sampling rate of 48 kHz. Due to this fact, filter parameters are accurate at 48 kHz only. If the console is synchronized to 44.1 kHz, the actual center and turnover frequencies of the EQ are lower by 8.125 %; if the clock frequency is 32 kHz, the frequencies will be lower by approx. 33.3 %.

If the console is synchronized to an external signal, a message box is displayed if synchronization is lost; a "Missing External Clock" warning is added to the error list.

If the console was synchronized to either WORDCLOCK or AES/EBU and a "no sync" condition is detected, the console automatically selects the INTERNAL mode; however, the external clock selection in the SYSTEM SYNC page is not changed. As soon as a valid external clock signal is available, the console will be re-synchronized.

If synchronization to a video source is lost, only a message box is displayed. The sync source remains in VIDEO mode.

If the Clock Sync interface is removed, sync source selection changes to INTERNAL mode, and no other sync source can be selected.

11.2.10 Automation

More information on this subject can be found in chapter 9.

11.2.11 Customer Code

| | There is a possibility to activate optional, customer-specific functions. For this purpose, a feature called Customer Code is used. <i>Please note that the</i> <i>customer codes are displayed and entered in hexadecimal.</i> | |
|--------------------------|---|--|
| Code 0x00000000 | Default Setting | |
| Code 0x00000002 | High Shelving Filter Modification When selected, the turnover frequency of the high-shelving filter is re- duced by approx. one octave, resulting in increased filter effect. For more information on EQ and filters, please refer to chapters 4.6 and 4.7. | |
| Code 0x00000004 | External CR DIM Function The spare control input on the Monitoring Module ("SIGN." connector P4, 9-pin D-type) can be used as CR DIM control input. If activated by an external control signal, the CR monitor speakers are attenuated by 20 dB. Please refer to the pin assignment table and connection diagram in chapter 14.11. | |
| Code 0x00000008 | MONITORA: SAS_INPUTROUTING Allows to ask for the SAS_INPUTROUTING, even if no SOURCE was selected for that fader. | |
| Code 0x00000010 | MONITORA: SET_FADER_LEVEL If the broadcast automation system sets a level (SET_FADER_LEVEL) to 0 dB and the current physical position of the corresponding fader is be- tween +4.5 dB and -4.5 dB, then the channel's level is set according to the physical fader position instead of 0 dB. This avoids the console to enter the Auto Takeover mode. <i>Note: Not implemented for the SET_FADER_LEVEL_ONLY command.</i> | |
| Code 0x00000040 | Longer Reverb Time Increases the delay for the switching from CR Cut or CR Dim to CHAN- NEL ON, or from CHANNEL OFF to CR UnCut or CR UnDim from 4050 ms to 160170 ms. This delay is used to avoid acoustical feedback noise from the CR monitor speakers to a microphone channel being opened (CHANNEL ON), when the control room has an unusually long reverb time. | |
| Code 0x00000080 | Faders 0 dB on Top For applications where the 0 dB point of the faders is desired to be at the fader's upper end position, this code can be used. It shifts the DSP gain setting with respect to the mechanical fader position by -10 dB. | |
| Code 0x00000100 Note: | Fader Start and Stop Pulse When CTRL OUT1 mode is set to "ON & FADER" in the INPUT CON- FIG. page (refer to chapter 8.2), 200 ms pulses are generated instead of a static signal at the CTRL OUT1 output when the status is activated (fader open and input channel ON) and when it is de-activated (fader closed and input channel OFF). There is a second possibility to generate start and stop pulses without activating this customer code; using this feature, however, different out- puts are used for the two pulses. For this purpose, set CTRL OUT1 mode to "ON & FADER", and CTRL OUT2 mode to "STOP PULSE" in the IN- PULTC ONTEC | |
| | r 01 CONFIG. page (rejer 10 chapter 8.2). | |

Customer Code Setting:

| Code 0x00000200 | Automatic Channel Routing When activated, the user's own channel routing is automatically activated during log-in. Otherwise, the channel routing has to be manually loaded after having logged-in. |
|------------------|--|
| Code 0x00000400 | Higher Attenuation of Sum Signal During Talkback Allows to attenuate the "N" content of the N–1 signal by a further 10 dB, improving the intelligibility of the TB voice. |
| de Combinations: | If more than one of the Customer Code functions are desired, the corre- |

Customer Code Combinations: If more than one of the Customer Code functions are desired, the corresponding codes can be accumulated by just adding the code numbers. *Please note that the customer codes are displayed and entered in hexadecimal.*

For activating both the 0x00000004 and 0x00000008 codes, enter a customer code sum of 0x0000000C.

After touching CUSTOMER CODE on the SYSTEM CONFIG. page, the keyboard page appears where the new code can be entered.



After confirmation with ENTER, the following dialog box appears. Now, you can either re-boot the system (REBOOT) which is necessary for the new code to become active, or continue (CONT.), then the code modification becomes active at the next power up of the console:



12 SW UPDATE

12.1 Software Structure

The OnAir 1000 software consists of the following packages:

- CPU software
- DSP software
- Peripheral modules and surface control software.

12.1.1 CPU Software Package

| Boot Software | The boot software part is installed in an EPROM and consists of: Hardware initialization Elementary hardware tests Start-up (or loading) of the application. |
|----------------------|--|
| Application Software | The application part of the software package covers the remaining console functions. Program and data (current status, snapshots, configuration) remain stored in memory after power-off, so that neither program nor data have to be loaded during normal operation. This is achieved by using a Flash memory. The application software is loaded at the first start-up of the console (in the factory), and afterwards due to new releases and upgrades only. Pro- gram and data are installed in a Flash memory. The application software is loaded from PC-Cards. For this purpose, read- only cards can be used. |

12.1.2 DSP Software Package

The DSP software package is treated the same way as the CPU software package (application software).

12.1.3 Important Information for Software Update to V4.0

Software V4.0 requires compatible Control Front Board I firmware.

If software V4.0 is loaded without the corresponding firmware, the console will be blocked and cannot be used. In this case, either the former main software has to be re-loaded, the new Control Front Board I firmware must be installed, or the Control Front Board has to be replaced.



Studer will not take any responsibility nor accept warranty claims for not following this procedure.

Please make sure that the console data are backed-up prior to any modification as the internal data structure is different. Note the current customer code setting (refer to chapter 11.2.11).

12.2 SW Update Procedure

After touching the SW UPDATE field in the SYSTEM CONFIG. page, a dialog box appears, where the user can decide whether he wants to load the software (PC-CARD), or whether he wants to stop the software update procedure (CANCEL).



When canceling the software update, the program returns to the SYSTEM CONFIG. page. After touching the PC-CARD button in the dialog box, the next dialog box appears.



As soon as the software has detected a PC-Card in the card slot, the card is checked. If the PC-Card contains valid code, the software download procedure starts. In a first step, the previous program of the console is erased.



Then the new software is copied from the PC-Card into the console's Flash EPROM. This will take several minutes.

| SYSTEM CONFIG. | EXIT | LOGOUT | 14:39:56 |
|-----------------------------|------------------|---------------------|---------------------------|
| Please wait Loading Code | | AUX å INSERT | • \ |
| INPUT Kbytes loaded : 250 | | TEL N-1 AUDIT | |
| | | STUDIO Mon | TH 16.APR.02 |
| TIME & DATE ST | P | MIXER SETUP | 00:00:00 |
| SYSTEM SYNC. AUTOMATION | CUSTOMER CODE | HOME | 00:00:00 START RESET |

Finally the last dialog box appears. The system can now be re-booted (REBOOT) with the new software, or, if required and available, an optional customer code (CCODE) can be entered.



Any customer code (see chapter 11.2.11) will be reset to the default value (0x0000000) when updating the system software. This means that if your system had a customer code before, you need to re-enter it in order to have the particular features again.

After confirmation with ENTER, the following dialog box appears. Now, you can either re-boot the system (REBOOT) or touch CCODE to change the customer code:



12.2.1 Error Handling

If the software update is interrupted by any reason, the system has to be rebooted. Afterwards, a message box appears with the message that the system software must be updated.



Should the PC-Card not contain valid code, the following dialog box appears. Insert a PC-Card with a valid software version; the download will then be restarted. If the download cannot be terminated correctly, the console can be used no more.



13 SYSTEM DIAGNOSTICS AND ERROR HANDLING

The OnAir 1000 error system concept consists of three topics:

- **Diagnostics and Error Handling** The system diagnostics software works completely in the background. The user takes notice of the diagnostics only in case of an irregularity. The system diagnostics consists of finding out the console's hardware configuration and detecting discrepancies to the system configuration. It also effects a hardware performance check during power-up. The error handling describes indications and actions on occurrence of errors and irregular behavior of the console detected by the system diagnostics software.
- **Failure of Restricted Functions** Most of the OnAir 1000's functions do not have any restriction. However, there exist several functions that are restricted in some situations, e.g. loading a snapshot into a console the input channel types of which do not match the ones of the snapshot (Mic/Line input channels). The behavior of the console in case of restrictions is defined at the place where the function is described. The way the user is told about failures is defined globally in the following.
 - **System Tests** They are executed on the service engineer's demand to run some specific performance controls, e.g. to check the function of touch fields and faders. The console is switched to a special test mode for this purpose.

13.1 Error, Warning, and Information Messages

A message field in the HOME page is used by the system diagnostics software to indicate any messages.



There are three types of messages: Errors, Warnings, and Information.

- **Errors** are serious problems detected by the diagnostics software. Error messages are generated if a problem prevents the console from normal operation. In case of an error, the error handler cannot solve the problem.
- **Warnings** are given to the user if the action required by the occurrence of a problem automatically changes any settings of the console so that operation is no longer the same.
- **Information** An information-type message is used by the system diagnostics telling the user that an action required by any conflict automatically changes console settings, but the console can at least be operated as it could before.

As more than one system message can occur at the same time, the diagnostics software manages a Log List containing one entry for each system message, completed by time and date of occurrence. If the Log List contains more than one entry, the PREV and NEXT fields appear on the touch-screen. This allows scrolling through different messages.

The Log List will be lost on power-down.

Warning and information messages can be acknowledged by the user, provided he has got access permission. This causes the diagnostics software to remove the message from the Log List.

Some entries in the Log List need to be indicated to the user immediately on their occurrence. A dialog box does this, regardless of the page currently selected on the touch-screen.



System diagnostics is performed during power-up as well as during operation. For more information see chapters 13.1.1 and 13.2.

13.1.1 System Diagnostics

| Hardware Performance Check | On power-up the diagnostics software executes some hardware device checks which may result in conflicts. |
|----------------------------|--|
| Communication Check | On power-up the diagnostics software tries to establish communication to other microprocessors. If communication fails, an error message is dis- played. |
| Configuration Check | On power-up the diagnostics software finds out the current physical hard- ware configuration. Any difference to the hardware configuration before the last power-down results in a warning or information. |

| Diagnostics and Error Handling – System Configuration Detection | | | | |
|---|-------------|------------|-----------|---|
| Error ID | Туре | Indication | Diag. Box | Error Text (Conflict) |
| 000 | Information | no | no | Module for Channels [n] - [m] installed! |
| 001 | Information | yes | no | Module for Channels [n] - [m] added! |
| 002 | Warning | yes | no | Module for Channels [n] - [m] removed! |
| 003 | Information | no | no | Central Module installed! |
| 004 | Information | no | no | Central Module added! |
| 005 | Error | yes | yes | No communication to Central Module! |
| 006 | Information | no | no | Input Module for Channel [n] installed! |
| 007 | Information | ves | no | Input Module for Channel [n] added! |
| 008 | Warning | ves | no | Input Module for Channel [n] removed! |
| 009 | Information | ves | no | Channel [n] Input Type changed to Mic Input! |
| 010 | Information | ves | no | Channel [n] Input Type changed to Analog Line Input! |
| 011 | Information | ves | no | Channel [n] Input Type changed to Digital Line Input! |
| 012 | Information | ves | no | Channel [n] Input Selector changed to A/B! |
| 013 | Information | ves | no | Channel [n] Input Selector changed to 1/2/3/4/5/6! |
| 014 | Information | no | no | Digital Output Module for [output] installed! |
| 015 | Information | ves | no | Digital Output Module for [output] added! |
| 016 | Warning | ves | no | Digital Output Module for [output] removed! |
| 017 | Information | ves | no | Insert In/m] Input Type changed to Analog Line Input |
| 018 | Information | Ves | no | Insert [n/m] Input Type changed to Digital Line Input! |
| 010 | Information | yc3 | no | Monitoring Module installed |
| 013 | Information | Ves | no | Monitoring Module added |
| 020 | Error | yes | Ves | No communication to Monitoring Module! |
| 021 | Information | yes | yes | Time Syne Medule instelled |
| 022 | Information | 110 | 110 | Time Sync Module installed: |
| 023 | Worping | yes | 110 | Time Sync Module added! |
| 024 | Information | yes | 110 | Inne Sync Module removed! |
| 025 | Information | 110 | 110 | |
| 020 | Information | yes | no | Insert [n/m] Module added! |
| 027 | vvarning | yes | no | Insert [n/m] Module removed! |
| 028 | Information | no | no | Sync Module Installed! |
| 029 | Information | yes | no | |
| 030 | Warning | yes | no | Sync Module removed - Now Running on 48 kHz internal |
| 004 | | | | |
| 031 | Information | no | no | Telephone Hybrid Control Module installed! |
| 032 | Information | yes | no | Telephone Hybrid Control Module added! |
| 033 | Warning | yes | no | Telephone Hybrid Control Module removed! |
| 034 | Information | no | no | Channel DSP for Channel [m/n] installed! |
| 035 | Information | yes | no | Channel DSP for Channel [m/n] added! |
| 036 | Warning | yes | yes | No communication to Channel DSP for Channel [m/n]! |
| 038 | Information | yes | no | Number of Channels increased to [n]! |
| 039 | vvarning | yes | no | Number of Channels decreased to [n]! |
| 043 | Information | no | no | Sum DSP installed! |
| 044 | vvarning | yes | yes | No communication to Sum DSP! |
| 045 | Information | no | no | Insert DSP installed! |
| 046 | warning | yes | yes | No communication to Insert DSP! |
| 047 | Information | no | no | Aux DSP installed! |
| 048 | Warning | yes | yes | No communication to Aux DSP! |
| 049 | Information | yes | no | Channel Front Board type is OnAir 1000! |
| 050 | Information | yes | no | Channel Front Board type is OnAir 1000! |
| 156 | Information | yes | no | Number of Faders increased to [n]! (n in hexadecimal) |
| 157 | Information | yes | no | Number of Faders decreased to [n]! (n in hexadecimal) |
| 158 | Information | yes | no | Channel Routing reset to 1:1 mapping, since the number of |
| L | | | | Channels has been changed! |
| 159 | Information | yes | no | Channel Routing reset to 1:1 mapping, since the number of |
| | | | | Faders has been changed! |

| | Diagnostics and Error Handling – Hardware Performance | | | | | | |
|----------|--|-----|-----------------|---|--|--|--|
| Error ID | Error ID Type Indication Diag. Box Error Text (Conflict) | | | | | | |
| 051 | Warning | yes | yes | Watch RTC Failure! | | | |
| | | | | - RTC Device Missing | | | |
| | - Device Faulty | | - Device Faulty | | | | |
| | | | | - Battery Low | | | |
| 052 | Warning | yes | no | Missing External Clock - Now Running on 48 kHz internal | | | |
| | Clock Reference! | | | | | | |
| 053 | Warning | yes | no | Missing External Video Clock | | | |

| | Diagnostics and Error Handling – Flash Checksum Tests | | | | | | |
|----------|--|-----|-----|--|--|--|--|
| Error ID | Error ID Type Indication Diag. Box Error Text (Conflict) | | | | | | |
| 054 | Warning | yes | yes | Flash verification - All Snapshots, Mic Settings and Channel | | | |
| | | | | Routings lost due to software version mismatch! | | | |
| 055 | Warning | yes | yes | Flash verification - Customer Configuration corrupted! | | | |
| 056 | Warning | yes | yes | Flash Verification - Console Configuration corrupted! | | | |
| 057 | Warning | yes | yes | Flash Verification - Current Console State corrupted! | | | |
| 058 | Warning | yes | yes | Flash Verification - Snapshot Block Checksum Verification | | | |
| | - | - | - | Failure! [n] of [m] Global Snapshots loaded. | | | |
| 059 | Warning | yes | yes | Flash Verification - Mic Setting Block Checksum Verification | | | |
| | | | | Failure! [n] of [m] Global Mic Settings loaded. | | | |
| 072 | Warning | yes | yes | Flash verification - Channel Routing Block Checksum | | | |
| | | | | Verification failure! [n] of [m] channel routings loaded. | | | |

| Diagnostics and Error Handling – PC-Card Battery Tests | | | | | | |
|--|--|-----|-----|---|--|--|
| Error ID | Error ID Type Indication Diag. Box Error Text (Conflict) | | | | | |
| 060 | Information | yes | yes | PC-Card Slot [%] – Card battery low. Replace battery immediately! | | |
| 061 | Warning | yes | yes | PC-Card Slot [%] – Card battery dead. Replace battery immediately! | | |

| | Diagnostics and Error Handling – Flash Checksum Tests | | | | | |
|----------|--|-----|-----|---|--|--|
| Error ID | Error ID Type Indication Diag. Box Error Text (Conflict) | | | | | |
| 062 | Warning | yes | yes | Flash verification – Snapshot Block Checksum Verification | | |
| | | | | failure! [n] of [m] Private Snapshots loaded. | | |
| 063 | Warning | yes | yes | Flash verification – Snapshot Block Checksum Verification | | |
| | | | | failure! [n] of [m] Private Mic Settings loaded. | | |

| Configurator | | | | | | |
|--------------|-------------|------------|-----------|---|--|--|
| Error ID | Туре | Indication | Diag. Box | Error Text (Conflict) | | |
| 064 | Information | yes | yes | Load Configuration Failure due to channel type mismatch! Channel [n] input parameters not reconfigured. | | |
| 065 | Information | yes | yes | Load Configuration Failure due to missing Channel Data in Configuration File! Channel [n] - [m] not reconfigured. | | |
| 066 | Information | yes | yes | Load Configuration Failure due to missing Input Data in Configuration File! Channel [n] Input 3 - 6 not reconfigured. | | |
| 067 | Information | yes | yes | Restore Configuration Failure due to channel type mis- match! Channel [n] not restored during EXIT. | | |
| 068 | Information | yes | yes | Restore Configuration Failure due to number of channels mismatch! Channel [n] - [m] not restored during EXIT. | | |
| 069 | Information | yes | yes | Restore Configuration Failure due to number of inputs mismatch! Channel [n] Input 3 - 6 not restored during EXIT. | | |
| 073 | Information | yes | yes | Input Selection for Channel [n] not activated, since the number of Inputs is 2 instead 6! | | |
| 074 | Information | yes | yes | New Channel Routing not activated, since the number of Channels or the number of Fader strips doesn't match the current configuration! | | |
| 075 | Information | yes | yes | Global Channel Routing not activated, since the number of Channels or the number of Fader strips doesn't match the current configuration! | | |

| | Snapshot Controller | | | | | | |
|----------|--|-----|-----|---|--|--|--|
| Error ID | Error ID Type Indication Diag. Box Error Text (Conflict) | | | | | | |
| 080 | Information | yes | yes | Snapshot Recall not completed due to missing Input Data in Snapshot! Channel [n] Input 3 - 6 not recalled. | | | |
| 081 | Information | yes | yes | Snapshot Recall not possible due to corrupted Snapshot Data! | | | |
| 096 | Information | yes | yes | All Global Channel Routings used Only [n] of [m] Global Channel Routings loaded! | | | |
| 097 | Information | yes | yes | All Global Snapshots used Only [n] of [m] Global Snapshots loaded! | | | |
| 098 | Information | yes | yes | All Private Snapshots used Only [n] of [m] Private Snapshots loaded! | | | |
| 099 | Information | yes | yes | Snapshot Recall Failure due to channel type mismatch! Channel Ini not recalled. | | | |
| 100 | Information | yes | yes | Snapshot Recall not completed due to missing Channel Data in Snapshot! Channel [n] - [m] not recalled. | | | |
| 101 | Information | yes | yes | All Global Mic Settings used Only [n] of [m] Global Mic Settings loaded! | | | |
| 102 | Information | yes | yes | Mic Setting not created - Input must be of type microphone! | | | |
| 103 | Information | yes | yes | Mic Setting not recalled - Input must be of type microphone! | | | |

| | Snapshot Controller (cont.) | | | | | |
|-----|-----------------------------|-----|-----|--|--|--|
| 104 | Information | yes | yes | Mic Setting not recalled due to corrupted Mic Setting Data! | | |
| 105 | Information | yes | yes | Snapshot not loaded because user [%s] (owner of the Snapshot) is not defined on this mixing console! | | |
| 106 | Information | yes | yes | Mic Setting not loaded because user [%s] (owner of the Mic Setting) is not defined on this mixing console! | | |
| 107 | Information | yes | yes | All Private Mic Settings used Only [n] of [m] Private Mic Settings loaded! | | |
| 108 | Information | yes | yes | Recall not possible as selected Snapshot is not defined! | | |
| 109 | Information | yes | yes | Recall not possible as selected Mic Setting is not defined! | | |
| 110 | Information | yes | yes | Snapshot recall failure - channel [n] not available for insert assignment! Insert [m] not recalled. | | |

| | PC-Card Controller | | | | | | |
|----------|--------------------|------------|-----------|--|--|--|--|
| Error ID | Туре | Indication | Diag. Box | Error Text (Conflict) | | | |
| 113 | Information | yes | yes | PC-Card Slot [%] - Empty! | | | |
| 114 | Information | yes | yes | PC-Card Slot [%] - Card not supported! | | | |
| 115 | Information | yes | yes | PC-Card Slot [%] - Card not formatted! | | | |
| 116 | Information | yes | yes | PC-Card Slot [%] - File not found! | | | |
| 117 | Information | yes | yes | PC-Card Slot [%] - Card already formatted! | | | |
| 118 | Information | yes | yes | PC-Card Slot [%] - Card write protected! | | | |
| 119 | Information | yes | yes | PC-Card Slot [%] - Please wait, formatting in progress! | | | |
| 120 | Information | yes | yes | PC-Card Slot [%] - Card formatted! | | | |
| 121 | Information | yes | yes | PC-Card Slot [%] - Card write error, IFX errorcode = [n] | | | |
| 122 | Information | yes | yes | PC-Card Slot [%] - Card read error, IFX errorcode = [n] | | | |
| 123 | Information | yes | yes | PC-Card Slot [%] - Volume not mounted, no IFX workspace. | | | |
| 124 | Information | yes | yes | PC-Card Slot [%] - Volume not mounted, bad BIOS Pa- | | | |
| | | | | rameter Block. | | | |
| 127 | Information | yes | yes | PC-Card Slot [%] - File Empty! | | | |
| 128 | Information | yes | yes | PC-Card Slot [%] - Snapshot Block Checksum Failure! [n] of | | | |
| | | | | [m] Snapshots loaded. | | | |
| 129 | Information | yes | yes | PC-Card Slot [%] - Mic Setting Block Checksum Failure! [n] | | | |
| | | | | of [m] Mic Settings loaded. | | | |
| 130 | Information | yes | yes | PC-Card Slot [%] - Configuration Checksum Failure! Con- | | | |
| | | | | figuration not loaded. | | | |
| 131 | Information | yes | yes | PC-Card Slot [%] - Snapshot Block Software Version | | | |
| | | | | mismatch! | | | |
| 132 | Information | yes | yes | PC-Card Slot [%] - Mic Setting Block Software Version | | | |
| | | | | mismatch! | | | |
| 133 | Information | yes | yes | PC-Card Slot [%] - Configuration Software Version mis- | | | |
| | | | | match! | | | |
| 134 | Information | yes | yes | PC-Card Slot [%] – SW Update Prolib Version mismatch! | | | |
| 400 | | | | SW not loaded. | | | |
| 136 | Information | yes | yes | PC-Card slot [%] - Channel Routing Block Checksum failure! | | | |
| 407 | La Canada d'Anna | | | [n] of [m] Channel Routings loaded. | | | |
| 137 | Information | yes | yes | PC-Card slot [%] - Channel Routing Block software version | | | |
| 400 | Information: | | | mismalch: | | | |
| 138 | information | yes | yes | PU-Uard Slot [%] - Sw update central Module firmware | | | |
| | | | | incompatibility! Software not loaded. | | | |

| | Flash Programming | | | | | | |
|----------|--|-----|---|--|--|--|--|
| Error ID | Error ID Type Indication Diag. Box Error Text (Conflict) | | | | | | |
| 144 | Error | yes | yes | Flash: Sector is protected. AMD error code = [n] | | | |
| 145 | Error | yes | yes Flash: Programming failure. Addr = [adr], prevData = [x], | | | | |
| | | | | newData = [y], AMD error code = [n] | | | |
| 146 | Error | yes | yes | Flash: Erase failure. AMD error code = [n] | | | |
| 147 | Error | yes | yes | Flash: Odd address invalid with word access | | | |
| 148 | Error | yes | yes | Flash: Memory access failed at address = [adr] | | | |
| 149 | Error | yes | yes | Flash: AMD error code = [n] | | | |

| User Interface | | | | | | |
|----------------|--|-----|--|--|--|--|
| Error ID | Error ID Type Indication Diag. Box Error Text (Conflict) | | | | | |
| 176 | Information | yes | no Due to insufficient memory the first 10 messages have bee removed from the loglist | | | |

| Automation Interface | | | | | | |
|----------------------|---|-----|----|---|--|--|
| Error ID | Error ID Type Indication Diag. Box Error Text (Conflict) | | | | | |
| 192 | Warning | yes | no | Communication to Broadcast Automation lost! | | |
| 193 | 193 Information yes no Communication to Broadcast Automation established! | | | | | |

13.2 Indication on Failure of Restricted Functions

Usually, there is no indication of any error or warning if the execution of functions is prohibited in case of restrictions, because the user immediately notices whether the desired function is performed correctly. Nevertheless, some warnings need to be indicated to the user as a feedback, e.g. if a complete console dump fails because of a write-protected memory card. The means to indicate to the user a failure during function execution is a dialog box appearing always at the same position on the touch-screen. It displays a system message and will disappear when the user acknowledges the message by touching the "OK" field in the box.

Each of these system messages is also entered to the Log List. To be removed from the Log List, it has to be acknowledged by the user.

To prevent from annoying the user with lots of warning and error message dialog boxes, they are generated only where it is not possible to notice function execution failure. In these cases an error message is generated if the function execution fails completely. A warning is given to the user if a function might be executed partially only.

Example: The snapshot recall function could not set all channels due to different input channel types:



The user has to acknowledge the dialog box before any other action on the touch-screen will be accepted. Touching configuration keys, e.g. SEL or EQ, which would lead to a different page on the touch-screen, will also be ignored as long as the dialog box has not been acknowledged.

13.3 System Test

The system test can be activated by touching the SYSTEM TEST field on the SYSTEM CONFIG. page. The SYSTEM TEST selection page appears on the touch-screen:

| SYSTEM | TEST | | EXIT | LOGOUT AUX & INSERT | 10:14:12 |
|-------------------|---------|---------|-------------|--------------------------------------|---------------------------|
| | | | | TEL N-1 AUDIT STUDIO MON | TH 17.APR.02 |
| BUTTONS FADERS | DISPLAY | PC-CARD | SW VERSIONS | MIXER SETUP | 00:00:00 |
| | | | | HOME | 00:00:00 START RESET |

13.3.1 Buttons/Faders Test

Selecting the BUTTONS/FADERS test brings the console into a mode where the controls of the console can be checked. To return to the SYS-TEM TEST selection page, the EXIT field has to be touched.

Keys and Lamps Test: If one of the keys OFF, ON, PFL, SEL, EQ, A, or B is pressed, it is illuminated. To check the LEDs next to the fader and the OVL LED, move the fader knob from its lowest position up to the highest position. All LEDs will be on consecutively.

Touch-Screen Test:

If a field is touched on the touch-screen, it is highlighted.



Rotary Encoders Test:

If a rotary encoder is turned clockwise, the bar graph next to it encoder increases. The three narrow bar graphs in the right-hand part of the screen are used for the MON, PFL/TB, and PHONES knobs. The full length of the bar graph corresponds to a complete turn of the rotary encoder.

Fader Calibration:

Touching the CAL FADER field brings up the CAL FADER page which allows checking and calibrating the faders:



For each fader, a bargraph indicates the current fader position. There are two calibration possibilities: The 0 dB point, and the fader start trigger point.

Fader 0 dB Point: (always required)Move all faders not to be calibrated to their -20 dB position (this posi-

- tion is ignored during calibration).Move the fader(s) to be calibrated to their physical 0 dB position.
- Touch FADERS 0dB.
- Fader Start Trigger Point:

The fader start trigger point must be calibrated only in case of problems with the fader start control output signal, i.e. if fader start is active even if the fader is completely closed.

- Move all faders not to be calibrated to their -20 dB position (this position is ignored during calibration).
- Move the fader(s) to be calibrated, starting from the lower end, and set them to a position about 2 to 3 mm above the lower end. This will be the point where the fader start output gets active after calibration.
- Touch FADERS START.
- **Exit:** To return to the system test selection page, the EXIT field has to be touched.

13.3.2 Display Test

Selecting DISPLAY generates a test pattern on the touch-screen allowing to check for faulty LCD pixels. This mode remains on until the screen is touched again.

13.3.3 PC-Card Test



Two types of PC-Cards are used with the console:

- Program card, used for software upgrade
- Data card, used for storing and recalling console configuration, snapshot data, and mic settings

When having touched PC-CARD, the inserted PC-Card is checked for battery voltage and write protection.

13.3.4 SW Versions Display

When having touched SW VERSIONS, the current software versions are displayed for every processor in the system.

| SYSTEM TEST | | EXIT | LOGOUT | 10:27:55 |
|---|---|--|---|-------------------------------------|
| Channel Front Board 1 Input Module Channel 1 Input Module Channel 2 Input Module Channel 3 Input Module Channel 4 Input Module Channel 5 Input Module Channel 6 | Present : YES Present : YES Present : YES Present : YES Present : YES Present : YES Present : YES | SW ID : 0.36 SW ID : 0.9 SW ID : 0.9 SW ID : 0.9 SW ID : 1.1 SW ID : 1.1 SW ID : 0.5 | AUX & INSERT TEL N-1 AUDIT STUDIO MON | TH 17.APR.02 |
| BUTTONS FADERS DISPLAY | PC-CARD | SW VERSIONS PREV NEXT | MIXER SETUP HOME | 00:00:00 00:00:00 start reset |

With the PREV and NEXT fields, all available pages can be browsed.

14 WIRING AND HARDWARE INFORMATION

14.1 **DC** Supply

The power supply is made of a primary switching power supply unit, delivering a 24 V_{DC} voltage to the secondary Power Supply that converts this voltage to the required ± 15 V, ± 5 V, and ± 24 V.

On the Primary Supply Unit (89.20.2013) the mains voltage selection is performed automatically. It is short-circuit proof. The fuse, however, is soldered into the circuit and *must not* be replaced in the field. In case of a failure, the complete power supply unit must be replaced.

The secondary Power Supply (1.942.605.xx) generates all the voltages required, except the +48 V phantom power; this voltage is generated on the Controller Board.

Power consumption of an OnAir 1000 console is about 125 VA.

The OnAir 1000 can be powered by the mains voltage (100...240 V_{AC}) as well as by an external 24 V_{DC} voltage (connector labeled "24V DC"). For redundancy, these two power sources can also be connected to the console simultaneously.



Internal/External Fuses

For DC operation there is a fuse (F1: T 8.0 A H 250 V UL/CSA) located on the PSU sub board (see component layout diagram below). In case of failure, this fuse must be replaced by a spare fuse of exactly the same type and value.



Please note that for DC operation, an additional, external fuse is required for cable and connector protection (T 10 A H 250 V UL/CSA)!

Wiring

If using the 10pin 24 V_{DC} connector, all +24 V pins (no. 1...3) and the GND pins no. 8...10 have to be used in parallel.

Depending on the power supply mode, wire bridges have to be inserted into the 10pin cable socket for correct processing of the power alarm output's voltage thresholds. Refer to the following table:

| Supply mode | Bridge(s) from pin no. to pin no. |
|---|-----------------------------------|
| 100240 V _{AC} only | No bridge required |
| 24 V _{DC} only | 6-7 and 5-7 |
| 100240 V _{AC} and 24 V _{DC} | 6-7 |



Alarm Output Power alarm is triggered if the internal or external supply voltage drops below 23.0 V; then the POWER ALARM LED on the front panel is illuminated, and a power alarm signal is output on pin 4 of the DC supply connector.

The power alarm signal is a relay contact (max. 40 V/200 mA). Its behaviour depends on jumper J1 on the PSU sub board (see component layout diagram on the previous page); either pin 4 is normally floating and pulled to GND if alarm is active (P1 and P2 connected, default factory setting), or pin 4 is normally pulled to GND and becomes floating if alarm is active (P1 and P3 connected).

Should the supply voltage drop below 21.6 V, the console automatically shuts down after having stored all relevant data.

For correct start of the console a supply voltage of min. 22.0 V is required.

Pin Assignment:

t: 24 V DC connector (10pin, male)

Matching female connector: Hirose JR21PK-10S, order no. 54.20.2010



| Pin | Signal |
|-----|---|
| 1 | +2228 VDC |
| 2 | +2228 V _{DC} |
| 3 | +2228 Vpc |
| 4 | Power alarm output (see "Alarm Output" above) |
| 5 | Power supply select 2 (see "Wiring" above) |
| 6 | Power supply select 1 (see "Wiring" above) |
| 7 | GND |
| 8 | GND |
| 9 | GND |
| 10 | GND |

14.2 Redundant Power Supply

Redundant Supply Versions: There exist four basic versions of the OnAir 1000 Digital Mixing Console: Analog- and digitally-biased versions both are available with standard mains and/or 24 V_{DC} supply, as well as with dual, redundant 24 V_{DC} supply

together with a separate, external 19" dual supply unit.

a – Mains voltage operation only, no redundancy:







c - Mains and 24 V_{DC} operation, semi-redundant (only primary power supply doubled):



d - Fully redundant version with ext. 19" unit

| Mains 100240 V., | 19" | OnAir 1000 |
|------------------------------|----------------------|--------------------|
| | Redundancy | (Redundant |
| Mains 100240 V _{AC} | Unit 1.918.225.xx | Supply Version) |

The diagram above shows the different possibilities with the standard versions (a...c) and the dual-supply redundant version (d).

The fully redundant version includes two complete and identical power supply paths, with separate mains inlets (preferably connected to two different phases of the mains), two mains switches, two primary power supplies, two separate DC cables to the console, and two separate secondary DC-DC converters within the console.

The primary AC/DC converters are contained in an external 19" rack mount case. On the front panel two mains switches are fitted, together with LED indicators for both 24 V voltages and an eventual power alarm. On the rear side two connectors are available for the mains inlet, and two connectors where the supply cables to the console can be attached. The 19" case is equipped with a very low noise fan.

The OnAir 1000 versions with fully redundant power supplies use the existing 24 $V_{\rm DC}$ connector at the rear panel for one of the supply cables. In place of the mains inlet connector of the standard versions there is a second 24 $V_{\rm DC}$ connector for the second cable. The cable harness and the decoupling PCB inside the console are different from the ones of the standard versions, too.



The OnAir 1000 consoles with fully redundant power supply are delivered under two different order numbers (see below) and come complete with the 19" Redundancy PSU. However, the cables have to be ordered separately, depending on the required cable length (two cables required per console).

There are *two* fuses (T 8.0 A H 250 V UL/CSA) located on the Sub Board 2 (1.942.607.xx) within the console. In case of failure, these fuses must be replaced by spare fuses of exactly the same type and value.

Power alarm is triggered if one of the external supply voltages drops below 23.0 V or if one of the internal supply voltages fails. The "POWER FAIL" LED on the console is illuminated; in addition, the power alarm signal is

Internal Fuses



Alarm Output

output on pin4 of both DC supply connectors of the console, illuminating the "POWER ALARM" LED on the front panel of the Redundancy PSU. The power alarm output is a relay contact (max. 40 V/200 mA). Its be-

haviour depends on jumper J1 on the PSU sub board.

Pin4 is normally floating, and pulled to GND if alarm is active (P8 and P9 connected, default factory setting), or pin4 is normally pulled to GND and becomes floating if alarm is active (P7 and P8 connected).

Note: It is recommended to leave the jumper setting as it is. If it is changed, the POWER ALARM LED on the front panel of the 19" supply unit will be illuminated if everything is alright, and vice versa.

Should the supply voltage drop below 21.6 V, the console automatically shuts down after having stored all relevant data.

A supply voltage of min. 22.0 V is required for correct start and operation of the console.

"24 V A" and "24 V B" connectors on the supply unit (10pin, female)"24 V A" and "24 V B" connectors on the console (10pin, male).

| Pin | Signal |
|-----|---|
| 1 | +2228 V _{DC} |
| 2 | +2228 VDC |
| 3 | +2228 V _{DC} |
| 4 | Power alarm output (see "Alarm Output" above) |
| 5 | not used |
| 6 | PSS1, Power supply select 1 (see "Single Supply for Redundant Version" below) |
| 7 | GND |
| 8 | GND |
| 9 | GND |
| 10 | GND |

Mating cable connectors: 54.20.2010 (female), 1.918.210.01 (male).

Single Supply for Red. Version: If only <u>one</u> 24 V_{DC} supply should be connected to an OnAir 1000 Redundant Supply Version, this supply <u>must</u> be connected to the console's "24 V A" connector. In such a case, a bridge from pin6 (PSS1) to pin7 (GND) must be inserted in the female 10pin 24 V_{DC} connector. It is recommended to insert this bridge in a second 10pin connector plugged to the console's "24 V B" connector. Even if there is only one 24 VDC supply the secondary switchers are still

Even if there is only one 24 VDC supply, the secondary switchers are still doubled for redundancy.

| Ordering Information | | OnAir 1000 Analog-biased version, for red. supply OnAir 1000 Digitally-biased version, for red. supply | 71.129.441.10 71.129.443.10 |
|----------------------|-------|--|--|
| | Note: | 24 V_{DC} connection cable, 2 m (1 pce.) 24 V_{DC} connection cable, 5 m (1 pce.) 24 V_{DC} connection cable, 7 m (1 pce.) 24 V_{DC} connection cable, 10 m (1 pce.) Cables must be ordered separately (2 pcs. needed) | 1.925.230.00 1.925.231.00 1.925.232.00 1.925.233.00 |
| | | Redundancy PSU, 19" rack-mount external supply unit (included in the OnAir 1000 Red. Supply versions) | 1.918.225.00 |
| | | OnAir 1000 Analog-biased version, standard supply OnAir 1000 Digitally-biased version, standard supply | 71.129.440.10 71.129.442.10 |

Power-Down

Pin Assignment



14.3 Mic Input

| 8888 8888 8888 | | |
|--------------------------|--|--|
| | | |

Each microphone input unit features two inputs and A/B switchover. Inputs A and B are transformer-balanced mono microphone inputs. The maximum input level is such that also mono line level signals can be processed if required.

The analog path has an insert point right after the preamplifier stage. Both the insert send and return signals are electronically balanced. The nominal send and return levels are +6 dBu, with 9 dB headroom for 0 dB_{FS}.

Pin Assignments:



| Pin | Signal |
|-----|---------|
| 1 | Chassis |
| 2 | Input + |
| 3 | Input – |

INSERT connectors (Bantam jacks):

MIC inputs A/B (XLR, 3pin, female):

| Pin | Signal |
|-----|----------|
| Т | Signal + |
| R | Signal – |
| S | Chassis |

CTRL connector (D-type, 9pin, male):



| Pin | Signal | Pin | Signal |
|-----|-------------|-----|-------------|
| 1 | CTRL OUT 1A | 6 | COMMON |
| 2 | +5 V SUPPLY | 7 | CTRL OUT 2B |
| 3 | CTRL OUT 2A | 8 | CTRL IN B |
| 4 | CTRL IN A | 9 | GND |
| 5 | CTRL OUT 1B | | |

Two control outputs (CTRL OUT xA/B) are available for each individual input, which can be used to start and/or cue external devices such as CD players, R-DAT units, or a radio automation system, or for red light signaling. The control signals can be triggered by various functions and depend on the selected fader start mode (INPUT CONFIG. page). For details please refer to chapter 8.

| | For CTRL OUT 1A/B, four operating modes are available: |
|-----------------------|---|
| NOT ACTIVE | Output is always open. |
| ON & FADER | Output is closed (i.e. pulled to GND) if ON key is pressed and fader is |
| | open. |
| PFL/ON & FADER | Output is closed if either PFL is active (independent of the fader position), |
| | or ON key is pressed and fader is open. |
| ON LAMP | Output is closed while the ON lamp is illuminated. |
| | |

For CTRL OUT 2A/B, seven operating modes are available: **NOT ACTIVE** Output is always open. LOCATE KEY Output is closed (i.e. pulled to GND) if the OFF key is pressed and the input channel is active. **PREVIEW** Output is closed if the PFL key is active and the PREVIEW function has been activated; the input channel must be switched off. LOCATE PFL KEY Output produces a pulse if the PFL key is deactivated (LOCATE function is activated). **ATTENTION** Output is closed as long as the OFF key is pressed, independent of whether the input channel is open or closed. **ON LAMP** Output is closed while the ON lamp is illuminated. LOCATE PFL/LOC KEY Combination of the LOCATE KEY and LOCATE PFL KEY functions. For details, please refer to chapter 8. The CTRL IN A/B can be used for muting the input channel, for remote control of the ON/OFF key or for entering ready status. Four operating modes are available:

NOT ACTIVE
READYInput is inactive.READY
EXT. MUTEAcknowledgement of a ready signal.EXT. MUTEChannel is muted (e.g. cough key).EXT. ON/OFFExternal ON/OFF switchover.



Notes: "COMMON" can be wired either to "+5 V SUPPLY" or to an external power supply (max. +15 V_{DC}). An active low control signal is connected between "CTRL IN A" or "CTRL IN B" and "GND". The inputs are opto-coupler inputs with internal 1 k Ω current limiting resistors.

All outputs are open-collector outputs. Small loads, such as LEDs, optocouplers, or relays can be driven directly. Maximum current per opencollector output must *not* exceed 100 mA.

The "+5 V" output can supply up to 50 mA for opto-couplers or relays. *The sum of all "+5 V" outputs for all input CTRL connectors of the whole console must not exceed 300 mA!*

Date printed: 23.10.03

14.4 Analog Line Input



Each analog line input unit has an input selector for two stereo input sources (A and B). Both inputs A and B are electronically balanced and are equivalent.

Pin Assignments:



| Pin | Signal |
|-----|---------|
| 1 | Chassis |
| 2 | Input + |
| 3 | Input – |

CTRL connector (D-type, 9pin, male): Please refer to chapter 14.3 for details.

14.5 Digital Input



Each digital input unit has an input selector for two digital signals A and B. Each of the inputs has all connectors (XLR, Cinch/RCA, TOSlink) required for supporting the AES/EBU and S/PDIF standards.

The most important C-bits (Professional, Audio, Emphasis, Sampling rate, Stereo) are read on the interface and transferred to the host controller. Audio and Emphasis bits are processed. The digital source needs not to be synchronized to the console since all digital input channels are equipped with a 20-bit sampling frequency converter (SFC).

Pin Assignments:



LINE inputs A/B (XLR, 3pin, female):

| Pin | Signal |
|-----|---------|
| 1 | Chassis |
| 2 | Input + |
| 3 | Input – |

S/PDIF connectors A/B (Cinch/RCA):

| | Pin | Signal |
|---|-------|--------|
|) | Inner | Input |
| | Outer | GND |



Optical connectors A/B (TOSlink): For optical-fibre cables.

CTRL connector (D-type, 9pin, male): Please refer to chapter 14.3 for details.

14.6 **TB** Mic Input

| 88 88 88 88 88 88 88 88 88 88 88 88 88 | 000 000 000 | | | | | 8 | | 8 | |
|--|-------------------|------|--|--|--|---|--|---|--|
| | | 0000 | | | | | | | |

The TB Mic input units are used for connecting internal and/or external talkback microphones. In the standard configuration, the OnAir 1000 features an internal electret talkback microphone internally connected to the "CR" TB Mic input unit. The second, "Studio" TB Mic input unit can be used for talkback from the studio. If ever wished, the user can disconnect the internal microphone and connect an external CR talkback microphone to the "CR" TB Mic input unit; input selection is performed by means of an on-board jumper. A transformer-balanced XLR input with jumper-selectable 48 V phantom power is provided. The balanced input is available on a 3pin AMP connector on the PCB as well. The input gain is set with a jumper (LO/HI) and is adjustable with a rear-panel trimmer potentiometer. An on-board limiter protects the output from an accidental overload.

The analog TB outputs are transformer-balanced. They are fed to XLR connectors at the rear panel and to AMP connectors on the PCBs; unbalanced AMP outputs are available on the PCBs. The levels of the balanced outputs are adjusted with rear-panel trimmer potentiometers.

The analog TB outputs must be activated by external control signals or by wire bridges in dummy plugs on the CTRL connectors (connect pins 2-6 and 4-9).

Pin Assignments:



TB MIC IN/OUT (XLR, 3pin, female/male)

| Pin | Signal |
|-----|------------------|
| 1 | Chassis |
| 2 | Input +/Output + |
| 3 | Input –/Output – |

CTRL connector P4 (D-type, 9pin, male), P8 (on PCB AMP, 4pin, male)

| P4, pin | P8, pin | Signal | P4, pin | P8, pin | Signal |
|---------|---------|--------------|---------|---------|--------------|
| 1 | - | n.c. | 6 | 2 | TB CTRL IN + |
| 2 | 1 | +5 V OUT | 7 | - | n.c. |
| 3 | - | n.c. | 8 | - | n.c. |
| 4 | 3 | TB CTRL IN – | 9 | 4 | GND |
| 5 | - | n.c. | | | |

Control Input:

If a control signal is applied to "TB CTRL IN+" and "TB CTRL IN–", the EXT TB MIC OUTPUT is activated.



Notes: "TB CTRL IN +" can be wired either to "+5 V OUT" or to an external supply (max. +15 V_{DC}). An active low control signal is connected between "TB CTRL IN – " and "GND". The input is an opto-coupler input with an

internal 1 k Ω current limiting resistor. The "+5 V OUT" output can supply up to 50 mA. *The sum of all "+5 V" outputs for all input CTRL connectors of the whole console must not exceed 300 mA.*

External TB Mic: The TB Mic input units are equipped with a balanced mic input (XLR, 3pin, female) for the connection of external microphones. Selection of internal/external TB mic is done with a jumper on the PCBs (see chapter 15.2).

14.7 Telephone Hybrid Interface



The telephone hybrid interface is used to control two external telephone hybrid units from the console.

Pin Assignment:

| D-type, | 15pin, | male) | 1 |
|---------|---------|----------------|----------------------|
|] | D-type, | D-type, 15pin, | D-type, 15pin, male) |



| Pin | Signal | Pin | Signal | Pin | Signal |
|-----|----------|-----|------------|-----|-----------|
| 1 | n.c. | 6 | Hold IN | 11 | Spare OUT |
| 2 | n.c. | 7 | Studio OUT | 12 | COMMON |
| 3 | Spare IN | 8 | Tel OUT | 13 | Tel IN |
| 4 | COMMON | 9 | n.c. | 14 | Studio IN |
| 5 | GND | 10 | n.c. | 15 | Hold OUT |



The inputs are opto-coupler inputs with internal 820 Ω current limiting resistors.

All outputs are open-collector outputs. Small loads, such as LEDs, optocouplers, or relays can be driven directly. *Maximum current per opencollector output must not exceed 100 mA*.

14.8 Analog Output

| | 0000 0000 0000 | | 88 ::: 0 | | | |
|--|----------------------|---|----------------|--|--|--|
| | | • | | | | |

The analog output units provide a transformer-balanced, dual stereo or mono output signal (selectable with a jumper). Output signal selection is performed on the PCB by means of jumpers; refer to chapter 15.4.

In mono mode, the output signals of the stereo DAC are added in the analog domain to increase the converters' dynamic range. Therefore, if the DAC input signal is a stereo signal, the resulting mono output level is increased by 3 dB referred to the stereo output, and if the DAC input signal is a mono signal, the resulting mono output level is increased by 6 dB. For compensation of these level differences, the output level can be adjusted with trimmer potentiometers located on the PCB; refer to chapter 15.4 for component location.

Pin Assignment:



| | Pin | Signal |
|---|-----|----------|
| | 1 | Chassis |
| Γ | 2 | Output + |
| | 3 | Output – |

14.9 Digital Output



Each digital output unit delivers two independent AES/EBU output signals. The most important C-bits (Professional, Audio, Emphasis, Sampling rate, Stereo) are set by the host controller. The output sampling rate is that of the console (i.e., usually 48 kHz).

A pair of XLR connectors with separate signal drivers are provided for each of the two AES/EBU output signals.

Output signal selection is performed on the PCB by means of jumpers; refer to chapter 15.5.

Pin Assignment:



| Pin | Signal |
|-----|---------|
| 1 | Chassis |
| 2 | |

C-Bits:

: The abovementioned C-bits are set as follows:

DIGITAL OUT A/B (XLR, 3pin, male):

ANALOG OUT A/B (XLR, 3pin, male):

| Professional: | PRO |
|----------------|--|
| Audio: | AUDIO |
| Emphasis: | NO EMPHASIS |
| Stereo: | depending on output configuration |
| Sampling rate: | According to the internal sampling rate measurement. |

3

Output -

STUDER

14.10 Analog Insert

There are two analog, electronically balanced stereo insert sends and returns (1 and 2) that can be assigned to any of the input channels, or to the program or the record bus.

Selection of the input channel to be assigned to the insert is performed on the AUX MASTER/INSERT page by means of the fourth rotary encoder. Selection is possible only if the corresponding insert is set to OFF (i.e., the return signal is not routed to the output).

The insert send is always active; the return, however, is only active if the ON field of the corresponding insert (on the AUX MASTER/INSERT page) is activated.

The sends are equipped with an internal limiter located before the D/A converter to avoid overloads. Nominal insert level is +6 dBu with 9 dB headroom (i.e. maximum level is +15 dBu for 0 dB_{FS}).

Pin Assignment: INSERT 2 (upper; D-type, 15pin, female):



| Pin | Signal |
|-----|------------------|
| 1 | Return 2 right + |
| 2 | Chassis |
| 3 | Send 2 right – |
| 4 | n.c. |
| 5 | Return 2 left + |
| 6 | Chassis |
| 7 | Send 2 left – |
| 8 | n.c. |
| 9 | Return 2 right – |
| 10 | Chassis |
| 11 | Send 2 right + |
| 12 | n.c. |
| 13 | Return 2 left – |
| 14 | Chassis |
| 15 | Send 2 left + |

INSERT 1 (lower; D-type, 15pin, female):



| P | Pin | Signal |
|---|-----|------------------|
| | 1 | Return 1 right + |
| | 2 | Chassis |
| | 3 | Send 1 right – |
| | 4 | n.c. |
| | 5 | Return 1 left + |
| | 6 | Chassis |
| | 7 | Send 1 left – |
| | 8 | n.c. |
| | 9 | Return 1 right – |
| 1 | 10 | Chassis |
| 1 | 11 | Send 1 right + |
| 1 | 12 | n.c. |
| 1 | 13 | Return 1 left – |
| 1 | 14 | Chassis |
| 1 | 15 | Send 1 left + |

14.11 Monitoring

| | 00 00 00 00 | ତ୍ତ ତ୍ରତ୍ର ତ୍ର | | | 8 | 8 | 8888 | 0000 | 8 • • | 8 • • |
|--|--------------------------|--------------------------|--|--|---|---------------------|------|------|-------------|-------------|
| | 8 8 8 8 8 8 8 8 | © © 0 | | | | 8888 0 0 0 | 8000 | 0000 | 8 • • | 8 |

The monitoring section is a (mainly analog) monitoring unit. It has five external analog inputs in addition to the console's internal buses. It provides analog output signals for headphones and loudspeakers in the control room as well as in the studio.

For details refer to the block diagram in chapter 1, which is an overview over the complete signal processing functionality.

The parallel "STUDIO MON CTRL" interface (25pin D-Type, male) allows the construction of a studio monitoring unit. Signals on this interface:

- $6 \times$ monitor source selector in (opto-coupler inputs)
- 3 × talkback destination in (opto-coupler inputs)
- $6 \times$ source selector feedback out (open collector outputs)

The monitor sources and the talkback destinations are described in chapter 6, "Monitoring".

One of the external analog inputs on the monitoring section is used as a PFL input provided for a CAB (radio automation) system.

For this purpose, the following control signals are available:

• 1 × External PFL IN (opto-coupler input)

CR OUT L/R (XLR, 3pin, male):

• 1 × External PFL OUT (open-collector output)

The "EXT PFL CTRL" D-Type connector (9pin, male) is used for the control signals from and to a CAB system. If "External PFL IN" is activated, the "EXT PFL" audio signal on the "EXTERN MONITOR INPUT" connector is routed to the PFL bus.

When pressing the "EXT PFL" key in the monitoring section, the "External PFL OUT" control signal on the "EXT PFL CTRL" connector is active, and the "EXT PFL" audio signal on the "EXTERN MONITOR IN-PUT" connector is routed to the PFL bus as well.

Pin Assignments:



| Pin | Signal |
|-----|----------|
| 1 | Chassis |
| 2 | Output + |
| 3 | Output – |

EXTERN MONITOR INPUT A/B/C (15pin D-type, female):

| Pin | Connector A (upper) Signal | Connector B (mid) Signal | Connector C (lower) Signal | |
|-----|-------------------------------|-----------------------------|-------------------------------|--|
| 1 | EXT PFL right + | EXT3 right + | EXT2 right + | |
| 2 | Chassis | Chassis | Chassis | |
| 3 | EXT PFL left - | EXT3 left - | EXT2 left – | |
| 4 | n.c. | n.c. | n.c. | |
| 5 | n.c. | OFF AIR right + | EXT1 right + | |
| 6 | Chassis | Chassis | Chassis | |
| 7 | n.c. | OFF AIR left - | EXT1 left - | |
| 8 | n.c. | n.c. | n.c. | |
| 9 | EXT PFL right – | EXT3 right – | EXT2 right – | |
| 10 | Chassis | Chassis | Chassis | |
| 11 | EXT PFL left + | EXT3 left + | EXT2 left + | |
| 12 | n.c. | n.c. | n.c. | |
| 13 | n.c. | OFF AIR right – | EXT1 right – | |
| 14 | Chassis | Chassis | Chassis | |
| 15 | n.c. | OFF AIR left + | EXT1 left + | |



STUDIO MONITOR (AUDIO) A (volume control, 9pin D-type, female):



9

10

15

2

7

| Pin | Signal |
|---------|--|
| 1 | GND |
| 2 | Speakers volume pot, wiper (note 1) |
| 3 | n.c. |
| 4 | Phones volume pot, wiper (note 1) |
| 5 | GND |
| 6 | +5 V (note 2) |
| 7 | Chassis |
| 8 | Chassis |
| 9 | +5 V (note 2) |
| Note 1: | 10 kW lin. volume control potentiometers connected between +5 V and GND. |
| | Volume is maximum if wiper voltage is +5 V. |
| Note 2: | 100 W series resistors provided internally for short-circuit protection. |

STUDIO MONITOR (AUDIO) B (talkback in, 15pin D-type, female):

| Pin | Signal |
|-----|--------------|
| 1 | Ext. TB in + |
| 2 | Chassis |
| 3 | n.c. |
| 4 | n.c. |
| 5 | n.c. |
| 6 | Chassis |
| 7 | n.c. |
| 8 | n.c. |
| 9 | Ext. TB in – |
| 10 | Chassis |
| 11 | n.c. |
| 12 | n.c. |
| 13 | n.c. |
| 14 | Chassis |
| 15 | n.c. |

STUDIO MONITOR (AUDIO) C (15pin D-type, male):

| | Pin | Signal |
|--------|-----|---------------------|
| | 1 | Phones right |
| | 2 | Chassis |
| | 3 | GND |
| | 4 | n.c. |
| 5 | 5 | Loudspeaker right + |
| 4 ? | 6 | Chassis |
| 2 | 7 | Loudspeaker left – |
| 1 | 8 | n.c. |
|) | 9 | GND |
| | 10 | Chassis |
| | 11 | Phones left |
| | 12 | n.c. |
| | 13 | Loudspeaker right – |
| | 14 | Chassis |
| | 15 | Loudspeaker left + |

| \sim | _ | |
|--------|---|----|
| | | 45 |
| | • | 13 |
| | • | 14 |
| • | | 13 |
| • | • | |
| | • | 12 |
| | • | 11 |
| | | |
| | • | 10 |
| | • | 9 |
| • | ノ | |
| | | |



| STUDIO | MON | CTRL | (D-type. | 25 | pin. | male |): |
|---------------|--------|------|------------|----|------|------|-----|
| SICDIC | 111011 | | (D, i) p c | 20 | pm, | mare | , ، |

| 5 | Pin | Signal | Pin | Signal | Pin | Signal |
|--------------|-----|-------------------|-----|-----------------|-----|-------------------|
| 1 | 1 | +5 V SUPPLY | 10 | Lamp EXTERN | 19 | Switch TB TO TEL2 |
| 3 | 2 | COMMON | 11 | Lamp Spare2 | 20 | Lamp PGM |
| <u>2</u> | 3 | Switch AUX1 | 12 | n.c. | 21 | Lamp AUX2 |
|) | 4 | Switch OFF AIR | 13 | GND | 22 | Lamp PFL |
| • | 5 | Switch EXTERN | 14 | +5 V SUPPLY | 23 | Lamp Spare1 |
| 3 | 6 | Switch TB TO TEL1 | 15 | Switch PGM | 24 | Lamp STUDIO MIC |
| | 7 | n.c. | 16 | Switch AUX2 | | ON |
| 5 | 8 | Lamp AUX1 | 17 | Switch PFL | 25 | GND |
| ļ. | 9 | Lamp OFF AIR | 18 | Switch TB TO CR | | |



Notes: "COMMON" can be connected either to "+5 V SUPPLY" or to an external power supply (max. +15 V_{DC}). Active-low control signals are connected between "Switch XXX" and "GND". The Switch inputs are opto-coupler inputs with an internal 1 k Ω current limiting resistor each.

The "Lamp XXX" outputs are open-collector outputs; small loads, such as LEDs, opto-couplers, or relays, can be driven directly. *The current must not exceed 100 mA per output.*

The "+5 V" supply current for LEDs or relays must not exceed 50 mA in total.

| ~ | _ | | Pin | Signal | Pin | Signal |
|-----------------------|---|------------------|-----|----------------------|-----|-------------------------|
| 5 4 3 2 1 | | 9 8 7 6 | 1 | +5 V SUPPLY | 6 | External PFL IN + |
| | | | 2 | External PFL IN – | 7 | External TB to CR – |
| | | | 3 | External PFL OUT | 8 | Spare OUT |
| | | | 4 | External TB COMMON + | 9 | External TB to Studio – |
| | | | 5 | GND | | |

| EXT PF | L CTRL | (D-type | 9 | nin | male | ١. |
|--------|--------|----------|---|-----|------|----|
| | | (D i)pc, | / | pm, | mult | ., |



Notes: "External PFL IN +" and/or "External TB COMMON" can be connected either to "+5 V SUPPLY" or to an external power supply (max. +15 V_{DC}). A pushbutton or an active-low control signal from a radio automation system is connected between the "–" control inputs and "GND". The inputs are opto-coupler inputs with internal 1 k Ω current limiting resistors. The "External PFL OUT" output is an open-collector output; a small load,

such as an LED, an opto-coupler, or a relay can be driven directly. *The current must not exceed 100 mA*.

The "+5 V" supply current for LEDs, opto-couplers, or relays must not exceed 50 mA in total.
| \frown | Pin | Signal | Pin | Signal |
|---|-----|-------------|---------|----------------------------------|
| ⁵ (• •) 9 | 1 | +5 V SUPPLY | 6 | COMMON |
| | 2 | ON AIR IN - | 7 | Spare IN – or Ext. CR DIM IN – * |
| | 3 | CR MIC OUT | 8 | STUDIO MIC OUT |
| $\frac{1}{1}$ $\left(\begin{array}{c} \bullet \\ \bullet \end{array} \right) $ $\left(\begin{array}{c} \bullet \end{array} \right) $ $\left(\begin{array}{c} \bullet \end{array} \right) $ $\left(\begin{array}{c} \bullet \\ \right) $ $\left(\begin{array}{c} \bullet \end{array} $ | 4 | PGM OUT | 9 | Spare OUT |
| \bigcirc | 5 | GND | * depen | ding on Customer Code setting |

SIGN. (D-type, 9 pin, male):



The "ON AIR IN – " signal turns on the "ON AIR" lamp on the console's monitoring section.

The "CR MIC OUT" output is active if any microphone in the control room is on (i.e. fader open, ON pushbutton active, signal routed to the PROGRAM or RECORD bus).

The "STUDIO MIC OUT" output is active if any microphone in the studio is on (i.e. fader open, ON pushbutton active, signal routed to the PRO-GRAM or RECORD bus).

The "PGM OUT" output is active if any input channel is on (i.e. fader open, ON pushbutton active, signal routed to the PROGRAM or RECORD bus).

If set with the Customer Code (refer to chapter 11.2.11), an external control signal at the "Ext. CR DIM IN – " input reduces (dims) the level of the CR monitor speakers by 20 dB.

Notes: "COMMON" can be connected either to "+5 V SUPPLY" or to an external power supply (max. +15 V_{DC}). Active-low control signals are connected between the control inputs and "GND". The inputs are opto-coupler inputs with internal 1 k Ω current limiting resistors.

The outputs are open-collectors; small loads, such as LEDs, opto-couplers, or relays can be driven directly. *The current must not exceed 100 mA per output*.

The "+5 V" supply current must not exceed 50 mA.

CTRL (D-type, 9 pin, male):

This connector provides all the signals required for a guest headphone. The "Phones OUT" signal is the same as the one on the GUEST jack socket on the same PCB.

| \sim | | Pin | Signal | Pin | Signal |
|----------|---|-----|---------------------|-----|------------------|
| • • • | 9 | 1 | Phones OUT left | 6 | Phones OUT right |
| | 8 | 2 | GND | 7 | n.c. |
| • | 7 | 3 | GND | 8 | n.c. |
| ••) | 6 | 4 | Potentiometer wiper | 9 | n.c. |
| \smile | | 5 | +5 V SUPPLY | | |



Notes: A linear 10 k Ω volume control potentiometer can be connected between "+5 V SUPPLY" and "GND". The volume is maximum if the wiper voltage is at +5 V.

The signal on pins 1 and 6 is the same as the one on the GUEST headphones jack socket.

14.12 Serial Interface



The serial interface provides a serial communication link according to either RS232 or RS422 specification (switch-selectable). It is used for the connection to and communication with a computer-assisted broadcasting system (CAB); for details on CAB systems refer to chapter 9.

Pin Assignments:



RS422 (D-type, 9 pin, male):

| | Pin | Signal | Pin | Signal |
|----|-----|----------------------|-----------|----------------------|
| 5 | 1 | GND | 6 | n.c. |
| •) | 2 | RX + (PC to console) | 7 | RX – (PC to console) |
| | 3 | TX – (console to PC) | 8 | TX + (console to PC) |
| | 4 | n.c. | 9 | n.c. |
| | 5 | n.c. | TX: trans | smit; RX: receive |

RS232 (D-type, 9 pin, female):



| Pin | Signal | Pin | Signal | |
|---------|--|-----|-------------------|--|
| 1 | DCD (Note 1) | 6 | DSR (Note 1) | |
| 2 | TX (console to PC) | 7 | RTS (Note 2) | |
| 3 | RX (PC to console) | 8 | CTS (Note 2) | |
| 4 | DTR (Note 1) | 9 | n.c. | |
| 5 | GND TX: transmit; RX: receive | | smit; RX: receive | |
| Note 1: | DCD, DTR, and DSR are connected internally | | | |
| Note 2: | RTS and CTS are connected internally | | | |

14.13 Clock Sync Interface



The clock sync interface allows the console to be synchronized to external clock sources. Synchronizing to the following external signals is provided:

AES/EBU: 32 kHz, 44.1 kHz, 48 kHz;

Word Clock: 32 kHz, 44.1 kHz, 48 kHz;

Video Sync: 25 frames/s, 29.97 frames/s, 30 frames/s.

Termination is jumper-selectable, refer to chapter 15.7.

If synchronized to video sync, the sampling rate is either 48 kHz or, for 29.97 fr/s, 47.952 kHz.

If no valid external clock signal is available, the console runs from its own internal 48 kHz clock reference.

Pin Assignments:

6

AES SYNC INPUT (3pin, female):

| Pin | Signal |
|-----|---------|
| 1 | Chassis |
| 2 | Input + |
| 3 | Input – |

WCLK INPUT, WCLK OUTPUT, VIDEO IN (BNC, 75 Ω):

| 2 | Pin | Signal |
|-----|-------|--------|
|))) | Inner | Input |
| I I | Outer | GND |

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14.14 Time Sync Interface

With the time sync interface, the console's internal clock can be synchronized to an external time reference signal, such as a DCF77 or GPS receiver, or a Mobatime clock providing a serial output signal. The current software supports AFNOR and Seiko protocols, as well as SMPTE biphase timecode (Leitch).

For either of these time reference units (except AFNOR and SMPTE) the connector labeled RS232 is used. For AFNOR and SMPTE signals, a transformer-balanced input is available on the RS422 connector. Please note that the configuration (chapter 11.2.5) and the DIP switch and jumper setting (chapter 15.8) depend on the type of reference signal being used.

Pin Assignments:

| | 2 | 3 | 4 | 5 | | |
|---|---|---|---|-----|---|--|
|) | • | • | • | • |) | |
| - | | | | . / | | |

| | Pin | Signal | Pin | Signal |
|---|-----|------------------------|-----------|------------------------|
| | 1 | GND | 6 | SMPTE/AFNOR - |
|) | 2 | RX + (unit to console) | 7 | RX – (unit to console) |
| / | 3 | TX – (console to unit) | 8 | TX + (console to unit) |
| | 4 | SMPTE/AFNOR + | 9 | GND |
| | 5 | GND | TX: trans | smit; RX: receive |
| | | | | |



RS232 (D-type, 9 pin, male):

RS422 (D-type, 9 pin, male):



| Pin | Signal | Pin | Signal | |
|---|--|-----------|-------------------|--|
| 1 | +12 V (Note 1) | 6 | +12 V (Note 1) | |
| 2 | RX (unit to console) | 7 | -12 ∨ (Note 1) | |
| 3 | TX (console to unit) | 8 | -12 ∨ (Note 1) | |
| 4 | +12 V (Note 1) | 9 | n.c. | |
| 5 | GND | TX: trans | smit; RX: receive | |
| Note 1: | <i>Note 1:</i> These voltages are available if jumpers 12-13 and 10-11 are inserted. These | | | |
| outputs are very high-Z and must not be used, except for the specified DCF77 and MOBA | | | | |
| receivers | s only! | | | |





15 DIP SWITCHES AND JUMPERS

The DIP switch and jumper positions are printed on the PCBs, except for the TB Mic input unit and the Analog Output unit; for these two assemblies, drawings have been included in the following chapters for component location.

15.1 Input Units





Correct assignment of the input unit position relative to the input channel is mandatory. For this purpose, every input unit needs to be addressed. On the Mic, Line, and Digital input units, this address is set with a DIP switch. On the Talkback Mic input units, however, the address is set with a jumper (refer to chapter 15.2).

When replacing one of the input units, please make sure that the address setting corresponds to the following table:

| | | | | DIP Switch no.: | | | | | |
|--------------------------|---------------------------|-----|-----|-----------------|----|----|----|----|-----|
| Unit | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | |
| 1 (Mic In 1) | 7 (Line In 4/Dig. In 2)* | OFF | ON | ON | ON | ON | ON | ON | OFF |
| 2 (Mic In 2) | 8 (Line In 5/Dig. In 3)* | ON | OFF | ON | ON | ON | ON | ON | OFF |
| 3 (Mic In 3) | 9 (Dig. In 1/Dig. In 4)* | OFF | OFF | ON | ON | ON | ON | ON | OFF |
| 4 (Line In 1) | 10 (Dig. In 2/Dig. In 5)* | ON | ON | OFF | ON | ON | ON | ON | OFF |
| 5 (Line In 2) | | OFF | ON | OFF | ON | ON | ON | ON | OFF |
| 6 (Line In 3/Dig. In 1)* | | ON | OFF | OFF | ON | ON | ON | ON | OFF |
| * depending on h | hasic console version | | | | | | | | |

* depending on basic console version

15.2 TB Mic Input Units

Input Unit Position:

On the Talkback Mic input units, the position is set with a jumper:





Jumper Setting:

MIC INT/EXT

PHANTOM PWR ON/OFF

GAIN HI/LO

Input selection – either internal, unbalanced microphone (i.e. the built-in electret TB mic with fixed supply voltage), connected to P6, or external, balanced microphone with switchable phantom power, connected to P2 or to P7. *Factory setting for pos. 11: INT MIC; for pos. 12: EXT MIC.*

Selection of 48 V phantom power for the balanced mic input (P2 or P7). *Factory setting: PHANTOM PWR OFF.*

For the internal standard electret TB mic, the LO position (nominal input level: -60 dBu) is used. In HI position, the mic input gain is increased by 20 dB, i.e. nominal input level -80 dBu. *Factory setting: GAIN LO*.

| Jumper Setting (cont.): | | | | |
|-----------------------------|-----------|--|--|--|
| EXT. TB CTRL | ON/OFF | Activates or deactivates the external TB control input on P4 or P8. <i>Factory setting: EXT. TB CTRL ON.</i> | | |
| | IN18 | Address selection. Factory setting for position 11, TB Mic Input "CR" (i.e., the built-in electret TB mic): IN5; for position 12, TB Mic Input "Studio": IN6. These jumper positions must not be modified. | | |
| On-Board Connectors: | P6 | Internal electret TB mic input (unbalanced, with fixed supply voltage) | | |
| | P7 | External, transformer-balanced mic input (parallel to XLR connector P2) | | |
| | P5 | Unbalanced TB output | | |
| | P9 | Transformer-balanced TB output (parallel to XLR connector P3) | | |
| | P8 | External TB control input (parallel to D-type connector P4). | | |
| Adjustments: | RA1 | MIC GAIN; the factory setting is performed for the built-in electret TB mic. The integrated limiter just starts limiting at input levels above –60 dBu (GAIN jumper in LO position). | | |
| | RA2 | OUTPUT LEVEL; the factory setting corresponds to the customer- specified line level (adjustment range $-10+14$ dBu). | | |

15.3 Telephone Hybrid Interface

The address on the Telephone Hybrid interface must *always* be set according to the following table:

| ſ | DIP Switch no.: | | | | | | | | |
|---|-----------------|-----|----|----|----|----|----|-----|--|
| | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | |
| l | OFF | OFF | ON | ON | ON | ON | ON | OFF | |

STUDER

15.4 Analog Output Units

| 11 10 10 | |
|---|--|
| ■ © © © © © © © © © © © © © © © © © © | |
| 17 16 15 | |
| | |

The Analog Output units have jumpers for mono/stereo selection and for selecting the desired output signal, individually for each of the outputs A and B.

The output level is set with trimmer potentiometers, separate by outputs A/B and by left/right channels.



15.5 **Digital Output Units**



The Digital Output units have two DIP switches (marked A and B on the PCB) for selection of the desired output signal for each of the two independent outputs. Selection is done according to the following table:

| Output Signal and | DIP Switch no.: | | | | | | | | | |
|---|-----------------|-----|-----|-----|----|----|----|-----|--|--|
| Unit Position | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | | |
| PROGRAM (PGM, ON-AIR) (pos. 13, upper – factory set) | ON | OFF | OFF | ON | ON | ON | ON | OFF | | |
| RECORD (REC) (pos. 13, lower – factory set) | OFF | OFF | OFF | ON | ON | ON | ON | OFF | | |
| AUDITION (AUD) (not assigned) | ON | ON | ON | OFF | ON | ON | ON | OFF | | |
| N–1 A (not assigned) | OFF | ON | ON | OFF | ON | ON | ON | OFF | | |
| N–1 B (not assigned) | ON | OFF | ON | OFF | ON | ON | ON | OFF | | |
| AUX 1 (pos. 14, upper – factory set) | OFF | OFF | ON | OFF | ON | ON | ON | OFF | | |
| AUX 2 (pos. 14, lower – factory set) | ON | ON | OFF | OFF | ON | ON | ON | OFF | | |

Insert Unit 15.6

The address on the Insert unit must *always* be set according to the following table:

| DIP Switch no.: | | | | | | | | |
|-----------------|----|-----|----|----|----|----|-----|--|
| 1 2 3 4 5 6 7 8 | | | | | | | | |
| ON | ON | OFF | ON | ON | ON | ON | OFF | |

15.7 **Clock Sync Interface**

If P7 and P8 are connected with a jumper, the 75 Ω termination of the VIDEO IN input is active. If this jumper is removed, the input is terminated with 110 Ω .

15.8 Time Sync Interface

DIP Switch The external time reference signal for the Time Sync interface is selected with a DIP switch. The DIP switch setting is displayed in hexadecimal on the TIME & DATE FORMAT page (TIME SYNC. PROTOCOL field) for easy verification.

| Time course | Hex | | | D | IP swi | tch no |).: | | |
|---|--|---|---------|----------|---------|---------|---------|----------|-----|
| Time source | Display | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| NONE | NONE | ON | ON | ON | ON | ON | ON | ON | ON* |
| AFNOR NFS87-500 | 1 | OFF | ON | ON | ON | ON | ON | ON | ON* |
| DCF77 | 2 | ON | OFF | ON | ON | ON | ON | ON | ON* |
| (expert mouse clock) | | | | | | | | | |
| (IF482; 9600 baud) | 3 | OFF | OFF | ON | ON | ON | ON | ON | ON* |
| GPS (NMEA 0183 V1.5, V2.0; 4800 baud) | 4 | ON | ON | OFF | ON | ON | ON | ON | ON* |
| SEIKO serial clock (RS485; 2400 baud) | 5 | OFF | ON | OFF | ON | ON | ON | ON | ON* |
| RCC 8000A (9600 baud) | 6 | ON | OFF | OFF | ON | ON | ON | ON | ON* |
| HOPF 6021 telegram (9600 baud) | 7 | OFF | OFF | OFF | ON | ON | ON | ON | ON* |
| VORTEX 482D (9600 baud), opt. "0" | 8 | ON | ON | ON | OFF | ON | ON | ON | ON* |
| LEITCH CSD-5300 (300 baud) (SMPTE bi-phase TC) | 9 | OFF | ON | ON | OFF | ON | ON | ON | ON* |
| SMPTE bi-phase input (LEITCH format) | Α | ON | OFF | ON | OFF | ON | ON | ON | ON* |
| SEIKO QC-7CF2 | В | OFF | OFF | ON | OFF | ON | ON | ON | ON* |
| SIPRONIKA SAT520 protocol (9600, 7E2) | С | ON | ON | OFF | OFF | ON | ON | ON | ON* |
| GORGY ASCII format (2400 baud) | D | OFF | ON | OFF | OFF | ON | ON | ON | ON* |
| Reserved for future use | | | a | all rema | aining | combi | nation | s | |
| | | | | * Sta | ndard | setting | j: ON | | |
| | | This switch can be set to OFF in order to mask out the validity bits in some data formats (e.g. it is possible to | | | | | | | |
| | use Mobatime data from a Mobatime clock that has r | | | nas no | | | | | |
| | | DCF re | eceptio | n and tl | hus ser | nds "M" | instead | d of "A" |) |

| Jumper Setting | IF type | Jumper setting | Used for: |
|----------------|-----------------|----------------|------------------------------------|
| | RS232 | 8-9, 5-6 | DCF77, Hopf, MOBA, RCC8000A, Seiko |
| | RS422/RS485 | 7-8, 4-5 | GPS, Leitch (SMPTE TC), AFNOR |
| | Supply (high-Z) | 10-11, 12-13 | DCF77 or MOBA only! |

Note: For DIP switch and jumper locations refer to the drawing on the next page.





15.9 Monitoring Controller PCB

On the Monitoring Controller PCB, *all* DIP switches must be set to their OFF position.

15.10 Level Meter PCB

For information on jumper setting and level meter calibration, please refer to the OnAir 1000 Service Manual.

15.11 PSU Sub Board

Jumper Setting: The power alarm signal (pin4 of the 10pin 24 V_{DC} connector) is a relay contact (max. 40 V/200 mA). Its behaviour depends on jumper J1 on the PSU sub board (see component layout diagram in chapter 14.1); either the output pin is normally floating and pulled to GND if alarm is active (P1 and P2 connected, default factory setting), or it is normally pulled to GND and becomes floating if alarm is active (P1 and P3 connected). Please note that wire bridges have to be inserted into the 10pin cable socket for correct processing of the power alarm output's voltage thresholds. Refer to chapter 14.1.

15.12 **DSP PCB**

Jumper Setting: On the DSP PCB, only the "CFG1" jumper must be inserted. *Do not change this setting.*

15.13 Control Front Board I

DIP Switches: On the Control Front Board I, *all* DIP switches must be set to their OFF position.

16 INDEX

A

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|---------------------------|-----------------------|
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| AFNOR | |
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1 FEATURES

- Fully digital mixing console
- Touch-screen based user interface ("Touch'n'action")
- 10 input + 2 master channels
- Integrated input router (for input unit-to-fader strip assignment)
- Flexible configuration
- Snapshots
- PC-Cards for snapshots, configuration, and software update
- Modular interfacing
- Compact, no external racks required
- Low power consumption
- Excellent price/performance ratio

1.1 System Architecture



1.2 Signal Processing



Signal processing is based on 24-bit DSPs (Texas Instruments TMS57070). The input channels are processed by six DSP chips (i.e. two stereo channels per DSP). The summing bus is a PLD (programmable logic device) design and can handle up to 32 bit. The resulting sums are further processed by three additional DSPs.

A serial communication link between the host controller and the DSPs downloads the algorithms at the startup and sets new parameters during operation.

1.3 Audio Buses

| PGM: | Main output bus, stereo or mono, analog and/or digital |
|---------------|--|
| REC: | Main output bus, stereo or mono, analog and/or digital |
| AUX 1: | Auxiliary output, stereo or mono, analog and/or digital |
| AUX 2: | Auxiliary output, stereo or mono, analog and/or digital |
| N-1 A: | Clean-feed output, mono, analog and/or digital |
| N-1 B: | Clean-feed output, mono, analog and/or digital |
| AUDITION: | Audition output (clean-feed), stereo or mono, analog and/or digital. |
| | Internal huses |

PFL: Pre-fader listening bus, stereo

TB: Talkback from control room, talkback from studio.

1.4 Host Controller

The host controller is a Motorola MC68360 running at 25 MHz. The tasks performed by the controller are:

- SW download to the DSPs
- SW download to the graphics controller
- Communication with the user surface
- Communication with DSPs
- Communication with modules
- Logic functions
- Graphics
- System test and error handling
- Flash EPROM handling

The software is based on a VRTX real-time operating system. VRTX was originally designed for industrial applications and is very reliable. The complete application is written in the "C" programming language.

2 HARDWARE



2.1 Input Units



★ Please note that the input channel-to-fader strip assignment as illustrated above applies only for SW versions before V4.0; for SW V4.0 and later, this applies as well, as long as no fader re-routing has been performed (i.e. for the factory-set default 1:1 routing).

The two basic versions of the Studer OnAir 1000 console are equipped with the following input units:

| Channel No. | Digitally-biased version: | Analog-biased version: |
|-------------|---------------------------------|---------------------------------|
| 1 | Mic Input Unit 1.942.720.xx | Mic Input Unit 1.942.720.xx |
| 2 | Mic Input Unit 1.942.720.xx | Mic Input Unit 1.942.720.xx |
| 3 | Mic Input Unit 1.942.720.xx | Mic Input Unit 1.942.720.xx |
| 4 | Line Input Unit 1.942.732.xx | Line Input Unit 1.942.732.xx |
| 5 | Line Input Unit 1.942.732.xx | Line Input Unit 1.942.732.xx |
| 6 | Digital Input Unit 1.942.740.xx | Line Input Unit 1.942.732.xx |
| 7 | Digital Input Unit 1.942.740.xx | Line Input Unit 1.942.732.xx |
| 8 | Digital Input Unit 1.942.740.xx | Line Input Unit 1.942.732.xx |
| 9 | Digital Input Unit 1.942.740.xx | Digital Input Unit 1.942.740.xx |
| 10 | Digital Input Unit 1.942.740.xx | Digital Input Unit 1.942.740.xx |

All inputs have A/B switchover.

For details on input units, please refer to the OnAir 1000 Operating Manual, chapters 14.3 to 14.6.

2.2 Output Units

All Studer OnAir 1000 consoles are equipped with the following output units:

2 × Digital Output Units 1.942.624.xx, and

3 × Dual Analog Output Units 1.942.621.xx (transformer-balanced).

For details on output units, please refer to the OnAir 1000 Operating Manual, chapters 14.8 and 14.9.

2.3 Monitoring

The monitoring section includes monitoring functions for control room (CR) and studio. Audio and control signals are handled by this section. The CR monitoring functions are controlled from the user surface. The studio monitoring functions can be controlled either from the user surface (source selection) or from an optional, simple studio monitor unit ("TB box", 1.924.555.xx).

For details on the monitoring section, please refer to the block diagram on the following page and to the OnAir 1000 Operating Manual, chapter 14.11.



2.4 Touch Screen

The LC display has a resolution of 640×200 pixels. A cold-cathode fluorescent lamp (CFL) provides the back light, having a typical lifetime (50% brightness) of 10'000 to 15'000 hours (meaning about 13 to 20 months of continuous operation). CFL replacement is very easy, and spare parts are available.

Caution!



The CFL supply voltage is about 400...600 V. Switch the console OFF before opening it!

A touch matrix with 12×6 active fields is mounted on the LCD. The touch switches are pressure sensitive and can only switch if a certain force is applied. Therefore the touch matrix function is not influenced by contamination (as fingerprints etc.).



The touch-screen surface consists of a PET foil. *Never use any solvents such as acetone to clean the surface.* Most glass or PC monitor cleaners do a good job.

2.5 User Surface

On the channel sections there is only one PCB (Front Board 1.942.710.xx) which reads all the keys and faders.

The center section consists of three PCBs:

- Control Front Board I (1.942.610.xx) is the controller for the center section. It is mounted behind the LCD and reads the touch matrix, the rotary encoders, as well as all keys and the potentiometers of the monitoring control section.
- Control Front Board II (1.942.111.xx) is the PCB with the control elements for the monitoring (volume controls, keys).
- Control Front Board III (1.942.612.xx) carries the rotary encoders.

2.6 Signal Processing and System Control

The DSP Board (1.942.102.xx) performs the audio functions for the console. All input units are connected to this board via the ribbon cable connectors labeled IMB-1 and IMB-2 (IMB = Input Module Bus). Each IMB consists of an entire six-channel section (6 input units).

The output units are connected to the Output Module Bus (OMB). All output units, the monitoring section, and the Control Front Board I are connected to the OMB.

For more details refer to the block diagram in the Operating Manual, chapter 1.

2.7 Standard Power Supply Versions

The power supply is made of a primary switching power supply unit, delivering a 24 V_{DC} voltage to the secondary Power Supply that converts this voltage to the required ±15 V, +5 V, and +24 V.

On the Primary Supply unit (89.20.2013) the mains voltage selection is performed automatically. It is short-circuit proof. The fuse, however, is soldered into the circuit and *must not* be replaced in the field. In case of a failure, the complete power supply unit must be replaced.

The secondary Power Supply (1.942.605.xx) generates all the voltages required, except the +48 V phantom power; this voltage is generated on the Controller Board.

Power consumption of an OnAir 1000 console is about 125 VA.

The OnAir 1000 can be powered by the mains voltage (100...240 V_{AC}) as well as by an external 24 V_{DC} voltage (connector labeled "24V DC"). For redundancy, these two power sources can also be connected to the console simultaneously.



Internal/External Fuses

For DC operation there is a fuse (F1: T 8.0 A H 250 V UL/CSA) located on the PSU sub board (see component layout diagram on the next page). In case of failure, this fuse must be replaced by a spare fuse of exactly the same type and value.



Please note that for DC operation, an additional, external fuse is required for cable and connector protection (T 10 A H 250 V UL/CSA)!

Wiring

ng If using the 10pin 24 V_{DC} connector, all +24 V pins (no. 1...3) and the GND pins no. 8...10 have to be used in parallel.

Depending on the power supply mode, wire bridges have to be inserted into the 10pin cable socket for correct processing of the power alarm output's voltage thresholds. Refer to the following table:

| Supply mode | Bridge(s) from pin no. to pin no. |
|---|-----------------------------------|
| 100240 V _{AC} only | No bridge required |
| 24 V _{DC} only | 6-7 and 5-7 |
| 100240 V _{AC} and 24 V _{DC} | 6-7 |

Earthing



This unit must be earthed, due to the mains input filter network being connected to the mains earth. Also in case of DC operation, earthing of the unit is mandatory. Refer to chapter 2.2.2 of the OnAir 1000 Operating Manual for details.



Alarm Output Power alarm is triggered if the internal or external supply voltage drops below 23.0 V; then the POWER ALARM LED on the front panel is illuminated, and a power alarm signal is output on pin 4 of the DC supply connector.

The power alarm signal is a relay contact (max. 40 V/200 mA). Its behaviour depends on jumper J1 on the PSU sub board (see component layout diagram above); either pin 4 is normally floating and pulled to GND if alarm is active (P1 and P2 connected, default factory setting), or pin 4 is normally pulled to GND and becomes floating if alarm is active (P1 and P3 connected).

Should the supply voltage drop below 21.6 V, the console automatically shuts down after having stored all relevant data.

For correct start of the console a supply voltage of min. 22.0 V is required.

Pin Assignment 24 V DC connector (10pin, male)

Matching female connector: Hirose JR21PK-10S, order no. 54.20.2010

| Pin | Signal |
|-----|---|
| 1 | +2228 VDC |
| 2 | +2228 V _{DC} |
| 3 | +2228 V _{DC} |
| 4 | Power alarm output (see "Alarm Output" above) |
| 5 | Power supply select 2 (see "Wiring" above) |
| 6 | Power supply select 1 (see "Wiring" above) |
| 7 | GND |
| 8 | GND |
| 9 | GND |
| 10 | GND |



2.8 Redundant (Dual) Power Supply Versions

There exist four basic versions of the OnAir 1000 Digital Mixing Console: Analog- and digitally-biased versions both are available with standard mains and/or 24 V_{DC} supply, as well as with dual, redundant 24 V_{DC} supply together with a separate, external 19" dual supply unit.



a - Mains voltage operation only, no redundancy:



 $b - 24 V_{DC}$ operation, *no redundancy*:



c - Mains and 24 V_{DC} operation, semi-redundant (only primary power supply doubled):



d-Fully redundant version with ext. 19" unit



The diagram above shows the different possibilities with the standard versions (a...c) and the dual-supply redundant version (d).

The fully redundant version includes two complete and identical power supply paths, with separate mains inlets (preferably connected to two different phases of the mains), two mains switches, two primary power supplies, two separate DC cables to the console, and two separate secondary DC-DC converters within the console.

The primary AC/DC converters are contained in an external 19" rack mount case. On the front panel two mains switches are fitted, together with LED indicators for both 24 V voltages and an eventual power alarm. On the rear side two connectors are available for the mains inlet, and two connectors where the supply cables to the console can be attached. The 19" case is equipped with a very low noise fan.

The OnAir 1000 versions with fully redundant power supplies use the existing 24 V_{DC} connector at the rear panel for one of the supply cables. In place of the mains inlet connector of the standard versions there is a second 24 V_{DC} connector for the second cable. The cable harness and the decoupling PCB inside the console are different from the ones of the standard versions, too.



The OnAir 1000 consoles with fully redundant power supply are delivered under two different order numbers (see below) and come complete with the 19" Redundancy PSU. However, the cables have to be ordered separately, depending on the required cable length (two cables required per console).



There are *two* fuses (T 8.0 A H 250 V UL/CSA) located on the Sub Board 2 (1.942.607.xx) within the console. In case of failure, these fuses must be replaced by spare fuses of exactly the same type and value.

Alarm Output

Power alarm is triggered if one of the external supply voltages drops below 23.0 V or if one of the internal supply voltages fails. The "POWER FAIL" LED on the console is illuminated; in addition, the power alarm signal is

output on pin4 of both DC supply connectors of the console, illuminating the "POWER ALARM" LED on the front panel of the Redundancy PSU. The power alarm output is a relay contact (max. 40 V/200 mA). Its

behaviour depends on jumper J1 on the PSU sub board (see component layout diagram in the circuit diagrams chapter).

Pin4 is normally floating, and pulled to GND if alarm is active (P8 and P9 connected, default factory setting), or pin4 is normally pulled to GND and becomes floating if alarm is active (P7 and P8 connected).

Note: It is recommended to leave the jumper setting as it is. If it is changed, the POWER ALARM LED on the front panel of the 19" supply unit will be illuminated if everything is alright, and vice versa.

Power-Down Should the supply voltage drop below 21.6 V, the console automatically shuts down after having stored all relevant data. A supply voltage of min. 22.0 V is required for correct start and operation of the console.

Pin Assignment "24 V A" and "24 V B" connectors on the supply unit (10pin, female) "24 V A" and "24 V B" connectors on the console (10pin, male).







| Pin | Signal |
|-----|---|
| 1 | +2228 V _{DC} |
| 2 | +2228 V _{DC} |
| 3 | +2228 V _{DC} |
| 4 | Power alarm output (see "Alarm Output" above) |
| 5 | not used |
| 6 | PSS1, Power supply select 1 (see "Single Supply for Redundant Version" below) |
| 7 | GND |
| 8 | GND |
| 9 | GND |
| 10 | GND |

Mating cable connectors: 54.20.2010 (female), 1.918.210.01 (male).

Single Supply for Red. Version If only one 24 V_{DC} supply should be connected to an OnAir 1000 Redundant Supply Version, this supply <u>must</u> be connected to the console's "24 V A" connector. In such a case, a bridge from pin6 (PSS1) to pin7 (GND) must be inserted in the female 10pin 24 V_{DC} connector. It is recommended to insert this bridge in a second 10pin connector plugged to the console's "24 V B" connector. Even if there is only one 24 VDC supply, the secondary switchers are still doubled for redundancy. OnAir 1000 Analog-biased version, for redundant supply 71.129.441.10 **Ordering Information** OnAir 1000 Digitally-biased version, for redundant supply...71.129.443.10 Note: Cables must be ordered separately (2 pcs. needed) Redundancy PSU, 19" rack-mount external supply unit (included in the OnAir 1000 Redundant Supply versions)...... 1.918.225.00 OnAir 1000 Digitally-biased version, standard supply............71.129.442.10
2.9 Level Meters

Since the audio signals delivered from the DSP Board have an internal, socalled "left-justified" format, an interface is required for connecting the meters. This interface is integrated on the Level Meter Board (1.942.613.xx) and provides conversion to analog format for two stereo channels. The Level Meter Board consists of two parts arranged as a "sandwich".

Note: Within the OnAir 1000 System, it is not possible to install level meters other than the standard Level Meter Board 1.942.613.xx mentioned above. If required, external level meters can of course be connected using the signals at the rear-panel outputs.



For meter alignment and PPM/VU switchover, please refer to chapter 4.3.3

3 CONFIGURATION

3.1 Software Configuration

The software configuration defines the functionality of the console. SW configuration is described in chapter 11 of the OnAir 1000 Operating Manual.

3.1.1 N–1 Configuration

Unlike in analog mixing consoles (where the N–1 signal is derived from the output sum by subtracting a channel signal), the N–1 are separate buses in the OnAir 1000.



The OnAir 1000 has two N–1 buses (N–1 A, N–1 B), both are mono buses. If a certain channel shall be routed to an N–1 bus, the corresponding field in the INPUT CONFIG. page must be defined (refer to chapter 11.2.1 of the OnAir 1000 Operating Manual). Talkback to both N–1 buses is possible from the control room and from the studio.

3.1.2 Audition Configuration

The audition bus is similar to the N–1 buses. Configuration is identical. Talkback to the audition bus is not allowed, but it can be selected as a monitoring source in the control room. Therefore it can be used like an N–1 bus where all CR microphones are inhibited. If Audition is selected as the monitoring source, the microphones can be open while the loudspeakers are on, allowing to work without headphones.

The audition bus is a stereo bus, and it can be used as a stereo or mono N-1 bus if talkback is not required.

3.2 Hardware Configuration

The hardware needs correct addressing to allow communication between the software and the installed PCBs. At power-on the software checks if the hardware configuration has changed since the last power-off. In case of a failure (or a configuration mismatch), an information message is displayed to inform the user on the failure (or changes).

For details on hardware configuration, also refer to chapter 15 of the OnAir 1000 Operating Manual.

3.2.1 Input Unit Configuration



All input units are software-controlled and need an individual address setting. On the Mic, Line, and Digital Input Units (i.e., positions 1...10), a DIP switch is used for the PCB addressing. In case of the Talkback Mic Input Units (on positions 11 and 12), the address is set with a jumper. *These addresses are correctly set in the factory. Address setting is required only when replacing a PCB – in this case, just set the DIP switch(es) and/or jumper(s) on the new PCB exactly as on the removed PCB*.

Address setting reference:

| DCP Desition | | DIP Switch no.: | | | | | | | | | |
|----------------------|--------------------------------------|-----------------|-----|-----|----|----|----|----|-----|--|--|
| FUD | Position. | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | | |
| 1 (Mic In 1) | 7 (Line In 4/Dig. In 2)* | OFF | ON | ON | ON | ON | ON | ON | OFF | | |
| 2 (Mic In 2) | 8 (Line In 5/Dig. In 3)* | ON | OFF | ON | ON | ON | ON | ON | OFF | | |
| 3 (Mic In 3) | 9 (Dig. In 1/Dig. In 4)* | OFF | OFF | ON | ON | ON | ON | ON | OFF | | |
| 4 (Line In 1) | 10 (Dig. In 2/Dig. In 5)* | ON | ON | OFF | ON | ON | ON | ON | OFF | | |
| 5 (L | ine In 2) | OFF | ON | OFF | ON | ON | ON | ON | OFF | | |
| 6 (Line li | n 3/Dig. In 1)* | ON | OFF | OFF | ON | ON | ON | ON | OFF | | |
| | * depending on basic console version | | | | | | | | | | |

Talkback Mic Input:

| PCB Position: | Jumper Position: |
|--------------------------|------------------|
| 11 (TB Mic Input CR) | IN 5 |
| 12 (TB Mic Input Studio) | IN 6 |

For details, refer to chapters 15.1 and 15.2 of the OnAir 1000 Operating Manual. For a drawing of the TB Mic Input Unit, please refer to chapter 4.3.4.

3.2.2 Output Unit Configuration

Digital Output Units

| 14 13 | | | | | | | | | | | | | |
|--------------------|---|---|----|----|---|---|---|---|---|---|----|---|---|
| 80000 00000 | | 0 | 0 | 0 | 0 | 0 | Θ | 0 | 0 | 0 | 0 | 0 | Θ |
| | 2 | 0 | 00 | 0 | 0 | 8 | 8 | 8 | 8 | 2 | 0 | 9 | 0 |
| I∎∎ ◎ □ ඊඊඊඊඊඊ | | • | • | I. | H | ŏ | ŏ | ŏ | ŏ | ă | Н | • | • |
| | | | | | | | | | | | | | |
| | _ | ¢ |). | ٠ | | • | | | | | νC | X | D |

The Digital Output Units (on positions 13 and 14) have two independent digital outputs conforming to the AES3-1992 standard. Each of these has two parallel output drivers. Since the digital output units are software-controlled, two DIP switches (marked A and B on the PCB) are used for selection of the desired output signal. For details, please refer to the table below.

| Output Signal and | DIP Switch no.: | | | | | | | |
|---|-----------------|-----|-----|-----|----|----|----|-----|
| PCB Position | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| PROGRAM (PGM, ON-AIR) (pos. 13, upper) | ON | OFF | OFF | ON | ON | ON | ON | OFF |
| RECORD (REC) (pos. 13, lower) | OFF | OFF | OFF | ON | ON | ON | ON | OFF |
| AUDITION (AUD) (not assigned) | ON | ON | ON | OFF | ON | ON | ON | OFF |
| N–1 A (not assigned) | OFF | ON | ON | OFF | ON | ON | ON | OFF |
| N–1 B (not assigned) | ON | OFF | ON | OFF | ON | ON | ON | OFF |
| AUX 1 (pos. 14, upper) | OFF | OFF | ON | OFF | ON | ON | ON | OFF |
| AUX 2 (pos. 14, lower) | ON | ON | OFF | OFF | ON | ON | ON | OFF |

Output selection according to the table is correctly set in the factory. It is required only when replacing a PCB – in this case, just set the DIP switch(es) on the new PCB exactly as they are set on the removed PCB.

Analog Output Units

| 17 16 15 | | | | | | | | | | | | |
|----------|----|-----|---|---|---|---|---|---|---|---|---|---|
| | | 00 | 0 | Q | Q | 0 | Q | Q | 0 | Q | Q | 8 |
| | ī. | 00 | | | ö | ö | ö | ö | 8 | 8 | Y | 9 |
| 000000 | • | ••• | | | ð | Ō | ð | ē | Ø | | • | - |
| | | 0 0 | | | | | | 0 | | | | |
| | | ۰0 | • | | | | | | | R | X | D |

The Analog Output Units (on positions 15...17) have a row of jumper pins for mono/stereo selection and for selecting the desired output signal, individually for each of the outputs A and B. Due to the high component count on the PCB, it was impossible to label the jumper pins; for details, please refer to the illustration below.



Output selection according to the illustration is correctly set in the factory. It is required only when replacing a PCB – in this case, just set the jumpers on the new PCB exactly as they are set on the removed PCB.

3.2.3 DSP Board

On the DSP PCB, the jumper labeled CFG1 must *always*, the jumper labeled CFG0 must *never* be inserted. *Do not change this setting*.

3.2.4 Level Meter Board

Changeover between VU, PPM, and fast PPM operation is done directly on the Level Meter Board; the default factory setting is PPM. For this changeover, several jumpers must be changed simultaneously, together with adjusting trimmer potentiometers. Please refer to chapter 4.3.3, Level Meters, for adjustment and changeover.

4 SERVICE



Required Tools:

- **Important:** Before opening the console for service purposes, make sure to back-up all *snapshots, mic settings,* and *configuration data* on a PC-Card. Please refer to chapter 11.2.8 of the Operating Instructions, "Console Dump".
 - Allen screwdrivers no. 2 and 2.5, Torx screwdriver no. 7, Hex socket key no. 5.5
 - Digital audio signal generator, or CD player with digital output (AES/EBU, or S/PDIF coax/optical) and test CD (sine-wave, 400 Hz, with following levels: -3 dB_{FS}, -5.5 dB_{FS}, -9 dB_{FS} [in-phase and out-of-phase, i.e., one channel inverted], -32 dB_{FS}, -33 dB_{FS}, -42 dB_{FS}; and 1 kHz/-9 dB_{FS} sine-wave bursts [10 ms on, 1.7 s off]).

If neither a digital audio signal generator nor a test CD should be available, contact Studer Product Support (<u>www.studer.ch</u>).

- Analog audio level meter with balanced input (e.g. AudioPrecision System One, System Two, or Portable One; Neutrik A2)
 - Digital voltmeter (e.g. Fluke 23, order no. 10.337.077.00)
- 2-channel oscilloscope (with aligned 10:1 probes)
- Front cover pivot adapters

- order no. 1.942.500.49 order no. 1.942.500.26 order no. 1.942.500.27
- Extension flat cable for OMB (64 pin)
 Extension flat cable for IMB (40 pin)

4.1 **Opening the Console**



- Switch the console off.
- Unscrew the screws of the front cover (2 each at the left and right edges, 3 at the upper edge of the front panel; Allen screwdriver no. 2.5).
- Unscrew the two oval-head screws on top of the rear cover (Allen screwdriver no. 2.5); install the two pivot adapters (1.942.500.49) and fix them with the oval-head screws, as shown above. The pivot adapters protect your unit from scratches.
- For the following step, please ask a second person for help in order to avoid scratches on your console.
- Carefully lift the front cover and set it up in a vertical position; the pivot adapters protect the cover from slipping away towards the rear.

4.2 Removing and Inserting Assemblies



4.2.1 Input and Output Assemblies

Removal:

- Open the console, as described in chapter 4.1.
- Unplug all connectors to the assemblies mounted on the front cover and put the front cover aside.
- Now the input and output assemblies can be accessed. These assemblies are mounted to the rear panel of the console and interconnected via multipin flat cables (Input Module Bus, IMB / Output Module Bus, OMB).
- Unlock and unplug the multipin connector of the PCB to be removed as well as the ones of the assemblies at the right and at the left of the PCB concerned.
- Unscrew the screws of the PCB to be removed at the rear panel depending on the type of PCB, Torx screwdriver no. 7 and/or Hex Socket key no. 5.5 and/or Allen screwdriver no. 2.
- Now the PCB can be carefully removed.

Installation:

- *Note:* Spare assemblies come pre-aligned, so usually no alignment is necessary unless the levels at the analog outputs of your console are different from the standard setting. In such a case, set the output levels according to your requirements. Setting the output levels must be done with the output assemblies removed from the console, so this is now a good moment to perform this setting refer to chapter 4.3.2.
 - Correct setting of jumpers and/or DIP switches is important for correct function of the console. For details, refer to chapter 15 of the Operating Instructions. When replacing an PCB, copy the jumper and DIP switch positions from the old PCB to the new one.

- Installation of a PCB is performed in reverse order of its removal: Insert the PCB into the console, fasten it with the corresponding screws, connect the daisy-chain cable, and lock the multipin connectors.
- After having re-installed the front cover with all the connections, perform a function check of your console. If your console must be operated with an input or output unit missing, a message appears on the center touch-screen. Acknowledge by touching the ACCEPT field.

4.2.2 Interface Assemblies

| | This paragraph deals with the removal and installation of the interface assemblies, i.e. the ones mounted horizontally at the bottom of the rear panel (Serial IF, Telephone Hybrid IF, Clock Sync IF, and Time Sync IF). Yes, you're perfectly right, this is a kind of gournet menu, for advanced service engineers with steady hands and ultra-stable nerves only. All others please keep out for their own health and happiness! |
|---------------|--|
| Removal: | Open the console, as described in chapter 4.1. Unplug all connectors to the assemblies mounted on the front cover and put the front cover aside. Remove the rear panel: Unscrew 2 Allen screws no. 2.5 on its top, 2 on its upper rear side, and 4 on its lower rear side. Now the complete rear panel can be removed. Do this very carefully, possibly with the help of a second person, in order to avoid scratches on your console. The length of the internal wiring is sufficient to put down the rear panel on the table with the connectors facing downwards. Unlock and unplug the multipin connector of the PCB to be removed. Unscrew the screws of the PCB to be removed at the rear panel – depending on the type of PCB, Torx screwdriver no. 7 and/or Hex socket key no. 5.5 and/or Allen screwdriver no. 2. Now the PCB can be removed. |
| Installation: | Correct setting of jumpers and/or DIP switches is important for correct function of the console. For details, refer to chapter 15 of the Operating Instructions. When replacing an PCB, copy the jumper and DIP switch positions from the old PCB to the new one. Installation of an PCB is performed in reverse order of its removal. Insert the PCB into the console, fasten it with the corresponding screws, connect the flat cable, lock the multipin connector, and reinstall the rear panel. After having re-installed the front cover with all the connections, perform a function check of your console. If your console must be operated with an input or output unit missing, a message appears on the center touch-screen. Acknowledge by touching the ACCEPT field. |

4.3.1 Power Supply

Procedure:

The +5 V_D (digital) is the only voltage that can be aligned.

Measure the +5 V_D supply between pins 1 and 3 of P19 on the DSP board.



Set the measured voltage to exactly **+5.2** V with RA1 on the Power Supply Board 1.942.605.xx, as shown below:



STUDER

1.942.605.xx

4.3.2 Analog Outputs

1.942.621.xx

| Initial Setup: | Perform th 13.3.1 of t Touch the SYSTEM Adjust all headroom | ne fader 0 d he OnAir 1 e MIXER CONFIG a analog lir (default fac | B point alig 000 Operat SETUP f and INPUT he level inj ctory values | gnment for ing Manua field on th The INPU puts to you s: nominal | all faders, l. he touch-s UT CONFI ur specified level +6 dE | according creen, foll G. page w d nominal Bu, headroo | to chapter lowed by ill appear. level and m 9 dB). |
|----------------------------------|---|--|---|--|---|---|---|
| Input Signal: | If using an your speci put unit (pressing the If using a nal of 400 vide a dig room setti correct inp TOS). Sele B key in the If using a with an ap job as wel amples in | n analog su fied nomin e.g. CH4). he A or B ka digital sign Hz to a di ital output ng is 11 dE put connec ect the corru- he channel su test CD: I popropriate t 1. If your te the followin | <i>ignal gener</i> al level to Select the ey in the ch <i>ial generato</i> igital input level of "– 3, the gene tor on the responding strip. f no test ge est CD and est CD shou ng table for | <i>rator:</i> Feed both (L and c correspon- nannel strip <i>or:</i> Feed a unit (e.g. 4 (your head rator must INPUT C input of th enerator sh d connected ild provide setting the | l an analog l R) inputs iding input digital 400 CH9). The room) dB _{FS} be set to - ONFIG. pa is channel ould be av l to a digit different le | g 400 Hz si of an analo t of this cl Hz stereo generator 5" (i.e., if y -11 dB _{FS}). age (XLR, by pressing ailable, a (al input do evels, refer el: | gnal with og line in- hannel by audio sig- must pro- rour head- Select the RCA, or g the A or CD player es a good to the ex- |
| Test CD level (1 kHz sine-wave): | 0 dB _{FS} | –3 dB _{FS} | -6 dB _{FS} | –9 dB _{FS} | –10 dB _{FS} | –12 dB _{FS} | -20 dB _{FS} |
| Output level @ 9 dB headroom: | +15 dBu | +12 dBu | +9 dBu | +6 dBu | +5 dBu | +3 dBu | –5 dBu |

For Consoles with Serial No. 1282 and above:



For the most recent console versions, no removal of the Analog Output Units is required for the output level adjustment. The trimmer potentiometers are accessible through the air vent holes in the upper and rear covers, as shown in the illustrations below. *Please note that some of the air vent holes are not perfectly in-line with the trimmer potentiometers, so it is recommended to watch through the other holes when inserting the alignment tool. It is absolutely necessary to use an insulated alignment tool in order to avoid damage by short-circuits.*

- First, perform the steps "Initial Setup" and "Input Signal" at the beginning of this chapter.
- Select the Channel Control page (e.g. "I 9A"): Touch HOME and press the SEL key of the corresponding channel. Perform the following settings on the Channel Control page:

MODE: ST (stereo), LR (normal stereo mode)

PHASE: NORM

CAL: 0 dB

Routing:

- To the PGM and REC outputs; the ΣPROGRAM and ΣRECORD touchscreen fields must be highlighted.
- To the AUX outputs: Activate the ON and PF touch-screen fields for both AUX 1 and AUX 2 outputs. Set the AUX 1 and AUX 2 gain to 0 dB. This position is marked with a dash.

BAL: center position.

- AUX master setting:
 - Select the AUX MASTER/INSERT page by touching the AUX & INSERT field. Activate the ON touch-screen fields for both AUX 1 and AUX 2 masters. Set the AUX 1 and AUX 2 gain to 0 dB. This position is marked with a dash.
 - Select the OUTPUT CONFIG. page by touching MIXER SETUP followed by OUTPUT. Select STEREO for both AUX 1 and AUX 2.
- N–1 setting:
 - Select the INPUT CONFIG. page of the corresponding input channel by touching MIXER SETUP followed by INPUT. Then touch the BUS ASSIGNMENT field. Select ALWAYS for both N–1 A and B.
 - Touch TEL/N–1/AUDIT. On the appearing page, set the N–1 A and B gain to 0 dB. This position is marked with a dash.
- Switch the equalizer off by pressing the EQ key if it is illuminated.
- Switch the corresponding channel ON, and set its channel fader as well as the PGM and REC faders to their 0 dB position.
- Connect an analog audio level meter to one of the output connectors.
- Align for a reading of your desired *nominal level (default factory setting:* +6 dBu) on the audio level meter; refer to the illustrations. Due to the mono mixing matrix of the N-1 outputs, the level measured on these outputs is 1.5 dB above nominal level.





- Repeat this procedure for each of the four output connectors of each of the three Analog Output Units.
- **Notes:** Please consider that maximum undistorted level of the analog outputs is +23 dBu (@ 0 dB_{FS}) into a 300 Ω load. When setting the output levels, do not exceed a value of +14 dBu (with an input signal of -9 dB_{FS}), considering the standard headroom margin of 9 dB. Once finished, don't forget to re-establish the console settings as they were before the alignment procedure.

For Consoles with Serial No. 1281 and below:

• Select the Channel Control page (e.g. "I 9A"): Touch HOME and press the SEL key of the corresponding channel. Perform the following settings on the Channel Control page:

MODE: ST (stereo), LR (normal stereo mode) PHASE: NORM

CAL: 0 dB

Routing: to the PGM and REC outputs; the Σ PROGRAM and Σ RECORD touch-screen fields must be highlighted.

- BAL: center position.
- Switch the equalizer off by pressing the EQ key if it is illuminated.
- Switch the console off.
- Open the console according to chapter 4.1, but leave the front cover connected so that the console remains operative.
- Remove the Analog Output Unit to be aligned (refer to chapter 4.2.1) and re-connect it to connector P71 of the DSP board using the 64pin extension cable 1.942.500.26. Reconnect the daisy-chain flat cable of the Output Module Bus to the extension cable. Now the Analog Output Unit can be placed in front of the console on an insulating sheet, e.g. a piece of cardboard. Put down the console front cover, but don't fix it with its screws.
- Set the jumpers of the Analog Output Unit temporarily to RECORD (for the lower/"B" output connectors) and to PGM (for the upper/"A" output connectors), select STEREO for both outputs A and B; refer to the illustration on the following page.
- Switch the console on again.
- Switch the corresponding channel ON, and set its channel fader as well as the PGM and REC faders to their 0 dB position.
- Connect an analog audio level meter to one of the PCB's output connectors.
- Align for a reading of your desired *nominal level (default factory setting:* $+6 \, dBu$) on the audio level meter; select the corresponding trimmer potentiometer according to the table below.

| Output: | Align with: |
|--------------|-------------|
| PGM/A, right | RA1 |
| PGM/A, left | RA2 |
| REC/B, right | RA3 |
| REC/B, left | RA4 |

- Repeat this procedure for each of the four output connectors of each of the three Analog Output Units.
- **Note:** Please consider that maximum undistorted level of the analog outputs is +23 dBu (@ 0 dB_{FS}) into a 300 Ω load. When setting the output levels, do not exceed a value of +14 dBu (with an input signal of -9 dB_{FS}), considering the standard headroom margin of 9 dB.



Notes: The two other default factory jumper positions (marked in gray) are valid for the AUX1/2 outputs (PCB in location 16) and for the N–1 A/B outputs (PCB in location 17).

Once finished, don't forget to set the jumpers back to their original positions and to re-establish the console settings as they were before the alignment procedure.

4.3.3 Level Meter

1.942.613.xx

Prerequisite: The console is set to a nominal level of +6 dBu and to 9 dB headroom (adjustment in the INPUT CONFIG. page of the analog line inputs).

Notes: Changeover between VU, PPM, and PPM fast operation is done directly on the Level Meter PCB; PPM is the default factory setting. For this changeover, several jumpers must be changed simultaneously, together with aligning trimmer potentiometers. Please refer to the drawing below. Please consider that a re-calibration of the trimmer potentiometers RA101 to RA401 must always be performed after a change of the display characteristics (PPM to VU, or vice versa).



Jumper Setting:

J101...J401: *PPM: PPM fast: VU:* Meter characteristics selection Peak Program Meter (default factory setting) Peak Program Meter with reduced fall time Standard VU meter.

| (cont.) J102, J103, J302, J303: <i>log:</i> <i>lin:</i> | It is recommended to set all four jumpers to the same position – having different meter characteristics in one single console doesn't make sense. Logarithmic/linear selection for PPM and PPM fast for VU only. It is recommended to set all four jumpers to the same position – having different meter characteristics in one console doesn't make sense. |
|---|--|
| J1: | LED switchover Set this jumper according to the PPM/VU setting with jumpers J101J401. |
| Procedure: | Perform the fader 0 dB point alignment for all faders, according to chapter 13.3.1 of the OnAir 1000 Operating Manual. Touch the MIXER SETUP field on the center touch-screen, followed by SYSTEM CONFIG and INPUT. The INPUT CONFIG. page will appear. Press the SEL key in the strip of the input channel where your CD player or generator is connected (e.g. Ch9). Select the corresponding input of this channel by pressing the A or B key in the channel strip. Select the correct input connector (XLR, RCA, or TOS). Select the Channel Control page (e.g. "I 9A") by touching HOME and pressing the SEL key of the corresponding channel again. Perform the following settings on the Channel Control page: MODE: ST (stereo), LR (normal stereo mode) PHASE: NORM CAL: 0 dB Routing: to the PGM and REC outputs; Σ PROGRAM and Σ RECORD touch-screen fields must be highlighted. BAL: center position. Switch the EQ off by pressing the EQ key if it is illuminated. Select the LEVEL METER page (touch MIXER SETUP field on the cen- ter touch-screen, followed by SYSTEM CONFIG and LEVEL METER). Switch the corresponding channel ON, and set its channel fader as well as the PGM and REC faders to their 0 dB position. Switch the console off. Connect a digital signal generator (400 Hz) to one of the digital inputs (e.g. Channel 9); a CD player with an appropriate test CD does a good job as well. Open the console according to chapter 4.1, but leave the front cover con- nected so that the console remains operative. Switch the console according to chapter 4.1, but leave the front cover con- nected so that the console remains operative. |
| • | Set the jumpers on the Level Meter PCB to the desired positions according to the diagram above. |
| Meter in PPM Mode: 1. | Make sure that all parameters are correctly set according to the "Proce- dure" paragraph above. |
| 2. 3. | quired), and jumpers J102/103/302/303 to the <i>LOG</i> positions. Trimmer potentiometer preset: |
| 4. | Set RA102/202/302/402 to center Set RA103/203/303/403 to position "2 o'clock" Feed a 400 Hz, -3 dB _{FS} sine-wave signal to the console. |
| | |

5. Align for a reading of $-1.00 \pm 0.02 V_{DC}$, using the test points and trimmer potentiometers according to the table below.

| Meter no.: | Test point: | Align with: |
|---------------|-------------|-------------|
| Meter 1 left | TP102 | RA101 |
| Meter 1 right | TP202 | RA201 |
| Meter 2 left | TP302 | RA301 |
| Meter 2 right | TP402 | RA401 |

6. Align for a reading of $+3.06 \pm 0.10 V_{DC}$, using the test points and trimmer potentiometers according to the table below.

| Meter no.: | Test point: | Align with: |
|---------------|-------------|-------------|
| Meter 1 left | TP103 | RA102 |
| Meter 1 right | TP203 | RA202 |
| Meter 2 left | TP303 | RA302 |
| Meter 2 right | TP403 | RA402 |

Check: All green LEDs plus 4 red LEDs must be illuminated.

- 7. Feed a 400 Hz, -33 dB_{FS} sine-wave signal to the console.
- 8. Align for a reading of $+560 \pm 20 \text{ mV}_{DC}$, using the test points and trimmer potentiometers according to the table below.

| Meter no.: | Test point: | Align with: |
|---------------|-------------|-------------|
| Meter 1 left | TP103 | RA103 |
| Meter 1 right | TP203 | RA203 |
| Meter 2 left | TP303 | RA303 |
| Meter 2 right | TP403 | RA403 |

Check: 4 green LEDs must be illuminated.

- 9. Repeat steps no. 4 to 8 above until optimum alignment is reached.
- 10. Feed a 400 Hz, $-9 \text{ dB}_{\text{FS}}$ sine-wave signal to the console. *All green LEDs must be illuminated.* A slight correction with RA101/201/301/401 may be necessary.
- 11. For checking the integrator stage, feed a 1 kHz, -9 dB_{FS} sine-wave burst signal (10 ms ON, 1.7 s OFF) to the console. *All green LEDs must be illuminated*. Please note that the top green LED is illuminated for a very short time only. To improve visibility during this check, the other green LEDs can be covered, e.g., with a sheet of paper.
- 12. If required, proceed with the "Correlator Meter Setting" paragraph below.
- *Note:* Once finished, make sure to re-establish the console settings as they were before the alignment.

Meter in VU Mode:

- **1.** Make sure that all parameters are correctly set according to the "Procedure" paragraph above.
- **2.** Set jumpers J101/201/301/401 to the *VU* positions, and jumpers J102/103/302/303 to the *LIN* positions.
- 3. Feed a 400 Hz, -9 dB_{FS} sine-wave signal to the console.
- **4.** Align for 0 dB indication on the meters, using the trimmer potentiometers according to the table below.

| Meter no.: | Align with: |
|----------------|-------------|
| Meter 1, left | RA101 |
| Meter 1, right | RA201 |
| Meter 2, left | RA301 |
| Meter 2, right | RA401 |

Check: All green LEDs must be illuminated.

- 5. Check the voltages at the following test points: TP101/201/301/401: **285 mV**_{AC} TP102/202/302/402: $-370 \pm 20 \text{ mV}_{DC}$ TP103/203/303/403: $\pm 2.55 \pm 0.10 \text{ V}_{DC}$
- 6. Feed a 400 Hz, $-5.5 \text{ dB}_{\text{FS}}$ sine-wave signal to the console. *Check: All LEDs of the bargraph must be illuminated.* Check the voltages at the following test points: TP103/203/303/403: +3.8 V_{DC} ±0.1 V
- Feed a 400 Hz, -32 dB_{FS} sine-wave signal to the console. Check: Only the lowest green LED of the bargraph must be illuminated.
- 8. Check the voltages at the following test points: TP103/203/303/403: +170 mV_{DC} \pm 30 mV
- 9. If required, proceed with the "Correlator Meter Setting" paragraph below.
- **10.** Once finished, make sure to re-establish the console settings as they were before the alignment.

Correlator Meter Setting:

- **1.** The correlator meter can be aligned regardless of the VU or PPM meter setting.
- **2.** Make sure that all parameters are correctly set according to the "Procedure" paragraph above.
- 3. For the following adjustment, an oscilloscope with a 10:1 probe is required. Set the Oscilloscope to: x = 0.5 or 1 ms/div.; y = 0.2 V/div., select AC coupling, and proceed with step no. 4. If no oscilloscope should be available, setting the trimmer potentiometers

RA204 and RA404 to their center positions is a tolerable makeshift solution. In such a case, proceed with step no. 8.

- 4. Feed a 400 Hz, -42 dB_{FS} sine-wave signal to the console.
- 5. Connect the scope probe to pin 6 of IC109.
- **6.** Measured wave form, typical:



Adjust the pulse duration ratio with trimmer potentiometer RA204 to 1:1; the pulse amplitude becomes then minimal.

The measured wave form is depending on the input signal level.

- 7. Connect the scope probe to pin 6 of IC309 and perform the same adjustment with RA404.
- 8. Feed a 400 Hz, -9 dB_{FS} sine-wave signal, L/R out of phase, to the console.
- 9. Align for a reading of $+1.35 \pm 0.20 V_{DC}$, using the test points TP107/307 and trimmer potentiometers RA105/305. *Check: Only one LED at the far left of the bargraph scales is illuminated.*
- **10.** Feed a 400 Hz, $-9 \, dB_{FS}$ sine-wave signal, L/R in phase, to the console.
- 11. Align for a reading of $\pm 200 \pm 5 \text{ mV}_{DC}$, using the test points TP107/307 and trimmer potentiometers RA106/306. Check: Only one LED at the far right of the bargraph scales is illuminated.
- 12. Repeat steps no. 8 to 11 above until optimum alignment is reached.
- **13.** Once finished, make sure to re-establish the console settings as they were before the alignment.

4.3.4 **TB** Mic Input

1.942.719.xx



| "Studio": EXT MIC. | 7). |
|--|------------|
| PHANTOM PWR ON/OFF Selection of 48 V phantom power for the balanced mic input (P2 or P <i>Factory setting: PHANTOM PWR OFF.</i> | |
| GAIN HI/LO For the internal standard electret TB mic, the LO position (nominal inplevel: -60 dBu) is used. In HI position, the mic input gain is increased 20 dB, i.e. nominal input level -80 dBu. <i>Factory setting: GAIN LO</i> . | out by |
| EXT. TB CTRL ON/OFF Activates or deactivates the external TB control input on P4 or P8. <i>Facto setting: EXT. TB CTRL ON.</i> | ry |
| IN18 Address selection. Factory setting for TB Mic Input "CR" (i.e., the built electret TB mic): IN5; for TB Mic Input "Studio": IN6. These jumper p sitions must not be changed. | ·in 10- |
| On-Board Connectors: P6 Internal electret TB mic input (unbalanced, with fixed supply voltage) | |
| P7 External, transformer-balanced mic input (parallel to XLR connector P2) | |
| P9 Transformer-balanced TB output (parallel to XLR connector P3) | |

P8 External TB control input (parallel to D-type connector P4).

| Alignmen | t: MIC GAIN OUTPUT LEVEL | Depending on the output level of the microphone used, the limiter thresh- old is aligned with the MIC GAIN trimmer potentiometer (RA1). For the internal standard electret TB mic (nominal output level –60 dBu), the MIC INT/EXT jumper is set to the INT position, the GAIN jumper is set to the LO position. Connect an AF voltmeter to the unbalanced TB OUT con- nector (P5 on the PCB). Then a sine-wave signal (approx. 1 kHz with a fixed level of –60 dBu is fed to the INT ELECTRET MIC connector (P6 on the PCB), and the MIC GAIN trimmer potentiometer is aligned to a level just below the point where no more output level increase is meas- ured. If an other microphone is used, the appropriate input connector, jumper settings and input signal level must be used. After the MIC GAIN alignment, the level of the balanced TB mic output – available at the XLR connector OUT (P3), or at the AMP connector TB OUT SYM (P9) on the PCB – is aligned with the OUTPUT LEVEL trim- mer potentiometer (RA2) to the desired nominal level. |
|----------|--------------------------|---|
| 4.3.5 | Insert | 1.942.660.xx |
| | | The analog insert assembly needs to be sent to the factory for alignment. Replacement assemblies come pre-aligned. |
| 4.3.6 | Monitoring | 1.942.182.xx, 1.942.133.xx, 1.942.139.xx, 1.942.129.xx |
| | | |

Monitoring assemblies need to be sent to the factory for alignment. Replacement assemblies come pre-aligned.

5 SERVICE TERMINAL

A service terminal connector is provided for servicing the OnAir 1000.

The Service Terminal Task enables the user to bypass the "normal" user interface and approach the system in a more direct way. This is used in two specific cases:

Service and Maintenance (no

- e (normal user mode)
- Enable/disable the output of errors, warnings, and information on the service terminal. (Filter for errors, warnings, and information).
- System data (current console status, console configuration, etc.) can be dumped to the service terminal.
- For integration test use, parts of the system data can be transferred to the service terminal.
- Shutdown or reset the console.

Debugging and Testing (extended user mode, for factory tests and experts only)

There are some software debugging functions that are hidden to the normal user. In addition to the normal user mode, these functions can also read and write memory contents, and communicate with other tasks directly (i.e. not through the command interpreter).

- System data (current console status, console configuration, etc.) can be displayed on the service terminal.
- Any message can be generated and sent to any task in the system.
- Task message communication can be displayed individually on the service terminal.
- Telegrams to any bus can be generated and posted.
- Telegram flow can be displayed on the service terminal for each bus (input/output, DSP bus...) individually.
- Telegram polling period can be determined, or telegram polling can be suppressed by time-out 0.
- Any memory position can be displayed and written.
- Any flash-memory position (bit range, section no., address) can be displayed and written.



It is important to notice that the service terminal must not be used during the normal operation of the console. Functions and modes activated from the service terminal can strongly affect the console's performance. Some debug and test functions can even corrupt the system data and cause a system crash.

The service terminal command structure is similar to the DOS directory level structure; if a service terminal command is not finished, the header of the next command line indicates the level reached. Extended user mode (i.e. debug) is a level, too, in this context. The command for leaving a level is similar to the DOS command ("..").

5.1 Connecting Cable

The terminal is connected to the 10-pin header connector (P13) labeled "SERVICE TERMINAL" on the Controller Board. The connecting cable is a straight 10-pin header to 9-pin D-type cable, where wire no. 10 is not used at the D-type connector side; a standard ribbon cable works fine, provided that its length does not exceed 2...3 m.



Almost any ASCII terminal capable of handling 38.4 kBaud will work; the Windows95 "HyperTerminal" or "Procomm" have been tested.

| ort Settings | | 1 |
|-----------------------|-----------------|--------------------------|
| <u>B</u> its per seco | nd: 38400 | |
| <u>D</u> ata b | its: 8 | |
| <u>P</u> ar | ity: None | |
| <u>S</u> top b | its: 1 | T |
| <u>F</u> low cont | rol: Xon / Xoff | |
| | | <u>R</u> estore Defaults |
| | - | |

The following terminal settings must be used:

5.2 SW Update via the Service Terminal

Procedure

- 1. Dump all snapshots, mic settings, and configuration data to a PC-Card or to the service terminal.
- 2. Connect the cable between the PC (COM1 or COM2) and the OnAir 1000 ("service terminal" connector on the controller board). To do this, you have to open the console, refer to chapter 4.1.
- **3.** After pressing ENTER or switching the console on, you will see the message

```
ServIf:\
```

on the terminal.

If you enter ? followed by ENTER, you will see a list of all possible debug commands.

- 4. To download the new software you need to type **startdownload serviceterminal**, followed by ENTER at the prompt.
- 5. Then send the file OA1000.ABS as a text file. This procedure will take about 10...15 min to complete the download.
- 6. Reload all snapshots, mic settings, and configuration data from the PC-Card or the service terminal.

5.3 Service Terminal Commands and Examples



Below, a series of Service Terminal commands is listed; for each command, a possible reaction of the console is given as well. Most commands are self-explaining. *However, please be aware of the fact that for using some of the commands, additional knowledge and skills are required.* See page 5-9, ServIf:\debug\disp\uc:userID> 0

```
ServIf:\>?
   Orthography :
    _____
   Service-Monitor operation is modelled on DOS-Command
   and -Directory Structure.
    - command-line-header indicates the state(-level)
    - several commands (separated by ' ' or '\') are possible
    - if a command is not finished, then the next level is achieved
    - '..' is used to get one level back
   ENTER
                     exec command
    SPACE or \
                    separate commands
                     backspace
   BS
   ESC
                     delete line
                    insert mode on/off
   CTRL V
   CTRL_X
                   repeat(exec) last command
                    restore last line
cursor one character left
    CTRL N
   CTRL L
    CTRL R
                    cursor one character right
    CTRL_B
                     cursor one word left
                     cursor one word right
    CTRL F
    Commands :
    _____
    DOWN
                      simulates a PowerDown : puts a PowerFail-
                     Telegram in the Inp-Queue of Diagnostics-And-
                     Error-Handling-Task
    FILTER
                     change to FILTER-mode : suppress/display
                     ERROR-, INFO- and WARNING- messages
    CAL
                     change to CALIBRATE-mode : send a FaderCal-
                     Message to Surface Controller 1/2/3/4/all
    IFXWS
                      Examines IFX Workspace and displays WorkspaceSize,
                       atual Haadwarkanaa
                                          and NumberWerkerscome
```

| | Actualosedworkspace and Numberworkspaceroosmall |
|---------------|--|
| STARTDOWNLOAD | change to STARTDOWNLOAD-mode (Start SW Update) |
| | SW from SERVICE TERMINAL or PC CARD |
| STOPDOWNLOAD | Cancel SW Update |
| PGMASKSINIT | Init PGMasks |
| DUMP | change to DUMP-mode (dump ConsoleConfiguration, |
| | GlobalSnapshots, PrivateSnapshots to Service Terminal) |
| CHECK | change to CHECK-mode (for Integration use) |
| CCODE | 0xFFFFFFFF Enter your customer code |
| RQS | resets the Queue statistics to its initial values |

change to DISPLAY-mode

ServIf:\debug>?

DISP <par>

| | disp Conf- and Par-Tables |
|---|---|
| MEM <addr> <val></val></addr> | diap and change memory |
| FLASH <bitrng> <secno> <addr> <val></val></addr></secno></bitrng> | change to FLASH-ERASE-MODE erase a section of FlashMemory |
| TDEB <busid> <on off=""></on></busid> | change to TDEB-mode |
| TSEND <busid> <tel0></tel0></busid> | debug incoming Telegrams change to TSEND-mode |
| TPOLL <busid> <val></val></busid> | send a Tel. to a xx-bus change to TPOLL-mode |
| MDEB <mq> <on off=""></on></mq> | timeout to suppress polling change to MDEB-mode |
| MSEND <mq></mq> | debug incoming Messages change to MSEND-mode |
| DSP <lqstart lqstop=""></lqstart> | send a msg to a xx-queue change to DSP-mode used for LOSTART/LOSTOP |
| plus all Commands of ROOT-mode | |

ServIf:\debug\disp>?

| CCH | display | Console Configuration Header |
|-----------------|---------|-------------------------------|
| CSH | display | Current Console State Header |
| GCC | display | Global Console Configuration |
| SC | display | Surface Configuration |
| SI(:module-no>) | display | System Identification |
| UC(:userID) | display | User Configuration |
| CC | display | Channel Configuration |
| IC(:input-no>) | display | Input Configuration |
| CCP | display | Channel Common Parameters |
| CIP(:input-no>) | display | Channel Input Parameters |
| MP | display | Monitoring Parameters |
| PDP | display | Profanity Delay Parameters |
| ASP | display | Aux Send Parameters |
| IAP | display | Insert Assign Parameters |
| CRP | display | Channel Routing Parameters |
| SCP | display | Snapshot Control Parameters |
| DP | display | Diagnostics Parameters |
| CUC | display | Customer Code Parameters |
| USER | display | User Parameters |
| TIP | display | Timer Parameters |
| AP | display | Automation Parameters |
| ACP | display | Automation Channel Parameters |
| | | |

ServIf:\debug\tdeb>?

| Tel-Debug | autob | is | off. |
|-----------|-------|----|------|
| Tel-Debug | cmbus | is | off. |
| Tel-Debug | iomb | is | off. |
| Tel-Debug | spi | is | off. |
| | | | |

ServIf:\debug\tdeb>iomb on ServIf:\debug\tdeb>

| vir. /der | ug (tuer | ~ | | | | |
|-----------|----------|---|-----|-----|-----|-----|
| Tdeb(s) | iomb | : | 20 | 80 | 01 | |
| Tdeb(r) | iomb | : | 20 | | FF | |
| Tdeb(s) | iomb | : | 20 | 81 | 01 | |
| Tdeb(r) | iomb | : | 20 | | FF | |
| Tdeb(s) | iomb | : | 20 | 82 | 01 | |
| Tdeb(r) | iomb | : | 20 | | FF | |
| Tdeb(s) | iomb | : | 20 | 83 | 01 | |
| Tdeb(r) | iomb | : | 20 | | FF | |
| Tdeb(s) | iomb | : | 20 | 84 | 01 | |
| Tdeb(r) | iomb | : | 20 | | FF | |
| •••• | | | | | | |
| | | | | | | |
| Tdeb(s) | iomb | : | 24 | 8C | 05 | iom |
| Tdeb(s) | iomb | : | 20 | 80 | 01 | |
| Tdeb(r) | iomb | : | 20 | | FF | |
| Tdeb(s) | iomb | : | 20 | 81 | 01 | |
| Tdeb(r) | iomb | : | 20 | | FF | |
| • • • • | | | | | | |
| •••• | | | ~ 4 | | | |
| Tdeb(r) | iomb | : | 24 | | FF | |
| Tdeb(s) | iomb | : | 24 | 8C | 01 | |
| Tdeb(r) | iomb | : | 24 | | FF | b |
| Tdeb(s) | iomb | : | 20 | 80 | 05 | |
| Tdeb(s) | iomb | : | 20 | 81 | 05 | |
| • • • • | | | | | | |
| •••• | | | ~ ~ | 0.5 | 0.5 | |
| 'I'deb(s) | lomb | : | 24 | 8B | 05 | |
| Tdeb(s) | iomb | : | 24 | 8C | 05 | off |

ServIf:\debug\tsend>?

| AUTOB | to send Telegrams to Automation | |
|-------|---|---|
| IOMB | to send Telegrams to Input/Output Modul | е |
| CMBUS | to send Telegrams to Control Module | |

ServIf:\debug\tpoll>?

| IOMB | to | set | polling | rate | on | Input/Ou | itput N | Module | Bus |
|-------|----|-----|---------|------|----|----------|---------|--------|-----|
| CMBUS | to | set | polling | rate | on | Control | Module | e Bus | |

ServIf:\debug\mdeb>?

| vIf:\debug\ | \mdeb>? | | |
|-------------|---------|----|------|
| Msg-Debug | iomb | is | off. |
| Msg-Debug | cmbu | is | off. |
| Msg-Debug | diag | is | off. |
| Msg-Debug | chan | is | off. |
| Msg-Debug | hwaudio | is | off. |
| Msg-Debug | dspb | is | off. |
| Msg-Debug | hwui | is | off. |
| Msg-Debug | monc | is | off. |
| Msg-Debug | cpgc | is | off. |
| Msg-Debug | flpg | is | off. |
| Msg-Debug | frpg | is | off. |
| Msg-Debug | lpgc | is | off. |
| Msg-Debug | rpgc | is | off. |
| Msg-Debug | conf | is | off. |
| Msg-Debug | time | is | off. |
| Msg-Debug | hwic | is | off. |
| Msg-Debug | serv | is | off. |
| Msg-Debug | card | is | off. |
| Msg-Debug | snap | is | off. |
| Msg-Debug | auto | is | off. |
| Msg-Debug | auif | is | off. |

ServIf:\debug\mdeb> Mdeb : time : 37

| Maeb | • | LTINE | • | 57 | |
|------|---|-------|---|----|-----|
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | |
| Mdeb | : | time | : | 37 | tim |
| Mdeb | : | time | : | 37 | е о |
| Mdeb | : | time | : | 37 | ff |
| | | | | | |

ServIf:\debug\msend>?

<queue-name> determine a <queue-name>-Queue for sending a Message Queues : iomb, cmbu, diag, chan, hwaudio, dspb, hwui monc, cpgc, flpg, frpg, lpgc, rpgc, conf time, hwic, serv, card, snap, auto, auif

ServIf:\debug\dsp>?

| LQSTART | |
|---------|--|
| LQSTOP | |

start asking overloads stop asking overloads

ServIf:\debug\disp>dp DIAGNOSTICS PARAMETERS : _____

| Main | PLL | State |
|-------|-------|-------|
| Video | DT.T | 9+2+0 |
| VIUEO | , בחד | JUALE |

Queue Statistics:

| Queue Name | Buffer full | Max. message | Buffer free | Queue Name | Buffer full | Max. message | Buffer free |
|------------|----------------|-----------------|----------------|------------|----------------|-----------------|----------------|
| iomb | 0 | 0 | 93.1 | cmbu | 0 | 0 | 100.0 |
| diag | 0 | 0 | 100.0 | chan | 0 | 0 | 93.1 |
| hwaudio | 0 | 0 | 76.8 | dspb | 0 | 0 | 99.0 |
| hwui | 0 | 0 | 95.2 | monc | 0 | 0 | 100.0 |
| cpgc | 0 | 0 | 99.6 | flpg | 0 | 0 | 96.4 |
| frpg | 0 | 0 | 100.0 | lpgc | 0 | 0 | 97.6 |
| rpgc | 0 | 0 | 100.0 | conf | 0 | 0 | 100.0 |
| time | 0 | 0 | 100.0 | hwic | 0 | 0 | 96.4 |
| serv | 0 | 0 | 100.0 | card | 0 | 0 | 100.0 |
| snap | 0 | 0 | 100.0 | auto | 0 | 0 | 82.0 |
| auif | 0 | 0 | 99.3 | | | | |

unlocked

undefined

ON-AIR 1000 MODULE SETUP : _____

| Module | Туре | Number of Inputs | Present |
|---|--|---|---|
| CPU Mainboard Channel Front Board 1 Input Module Channel 1 Input Module Channel 2 Input Module Channel 3 Input Module Channel 4 Input Module Channel 5 Channel Front Board 2 Input Module Channel 7 Input Module Channel 7 | mic mic ana line ana line dig line dig line dig line | c inp 2 c inp 2 c inp 2 e inp 2 e inp 2 e inp 2 e inp 2 e inp 2 e inp 2 | yes yes yes yes yes yes yes yes yes yes |
| Control Front Board Pgm Digital Output Record Digital Output Aux 1 Digital Output Aux 2 Digital Output N-1 A Digital Output N-1 B Digital Output Audition Digital Output Monitor Module Insert Module 1/2 Insert Module 3/4 Time Sync Module Clock Sync Module Clock Sync Module General Purpose Module Channel DSP 1 Channel DSP 5 | | | yes yes yes yes no no yes yes yes yes yes yes yes yes yes |
| Channel DSP 19 Channel DSP 21 Channel DSP 23 Sum DSP Insert DSP Aux DSP Number of channels Insert 1/2 Type Insert 3/4 Type | 12 analog analog | | no no yes yes yes |

| | : |
|--|--|
| SW-Version | V4 0 23 01 2002 |
| Number of Channels | 24 |
| Checksum | 0x294b |
| | |
| If:\debug\disp>csh CURRENT CONSOLE STATE HEADER : | |
| | : |
| Block in Use | In Use |
| Checksum | 0x2971 |
| Tf·\debug\disp>gcc | |
| GLOBAL CONSOLE CONFIGURATION : | |
| | |
| PFL Bus Source PF | (PFL) |
| Insert 1/2 Type | yes on Diff when Audicion don't can upalog Insert 3/4 Type anal |
| Meter 1 Source or | nalog inselt 374 type analog analog monit |
| Service Term baudrate 38400 | baud SerModuleBus2 baudrate 38400 ba |
| Program digital standard as | es/ebu Record digital standard aes/eb |
| Aux 1 digital standard ac | $\frac{1}{2}$ s/ebu Aux 1 output type ster |
| Aux 2 digital standard ac | es/ebu Aux 2 output type stere |
| N-1 A digital standard ac | s/ebu N-1 A output type mor |
| N-1 B digital standard as | s/ebu N-1 B output type mor |
| Audition digital std a | es/ebu Audition output type stere |
| Svnc source int | ernal |
| Sampling rate 4 Davlight saving change | 8 kHz Watch sync source time sync modul ves MEST Offset |
| Profanity delay time | 0 s Profanity Delay Option |
| ON-AIR Indication | pgm Billing Enable r |
| DCF 77 Option | no LAURA Mode |
| Number of Faders | 24 |
| Default Pgm Master Level 0.0 | Default Rec Master Level 0.0 |
| Time Synch Protocol 2 | |
| If · \debug\disp>sc | |
| ACE CONFIGURATION : | |
| | |
| Function of channel KEY 3 | off-func |
| Operationg Mode | normal mode |
| Display Mode | advanced |
| Time Format | europe |
| Date Format | DD.MMM.YY |
| Europhian of VEV E1 | mon out 1 |

| Disping noa | 0 | | aav | ance | . 01 |
|-------------|-----|----|-------|------|------|
| Time Format | | | e | urop |)e |
| Date Format | | | DD.MI | MM.Y | Ϋ́ |
| Function of | KEY | F1 | mon | ext | 4 |
| Function of | KEY | F2 | mon | ext | 5 |
| Function of | KEY | FЗ | mon | ext | 6 |
| Function of | KEY | F4 | mon | ext | 7 |
| Function of | KEY | F5 | mon | ext | 8 |
| | | | | | |

ServIf:\debug\disp\si:module>2

| SYSTEM IDENTIFICATION : | Module 2 | 2 |
|---|-----------------------|--------|
| | | |
| Module : Channel Front Board 1 Software ID | Present : yes 0.30 | 5) |

ServIf:\debug\disp\uc:userID>1

| USER CONFIGURATION : userID | 1 |
|--|--|
| | |
| User Name User Password | DEFAULT USER |
| Accept Error allowed Insert allowed Aux Send allowed Audition Level allowed Aux 1 (chn) allowed Aux 2 (chn) allowed Phase allowed Phantom allowed Stereo allowed Gain/Cal allowed Sum allowed Eq allowed Input Selection allowed | yes yes yes yes yes yes yes yes yes yes |
| Default channel routing init | no |

| Chn | assigned | sel. | protect | Chn | assigned | l sel. | protect |
|-----|----------|-------|---------|-----------|----------|--------|---------|
| | Fader | Input | | | Fader | Input | |
| 1 | 1 | A | no | 14 | 14 | A | no |
| 2 | 2 | A | no | 15 | 15 | A | no |
| 3 | 3 | A | no | 16 | 16 | A | no |
| 4 | 4 | A | no | 17 | 17 | A | no |
| 5 | 5 | A | no | 18 | 18 | A | no |
| 6 | 6 | A | no | 19 | 19 | A | no |
| 7 | 7 | A | no | 20 | 20 | A | no |
| 8 | 8 | A | no | 21 | 21 | A | no |
| 9 | 9 | A | no | 22 | 22 | A | no |
| 10 | 10 | A | no | 23 | 23 | A | no |
| 11 | 11 | A | no | 24 | 24 | A | no |
| 12 | 12 | A | no | PGMMASTER | NONE | | no |
| 13 | 13 | A | no | RECMASTER | NONE | | no |

ServIf:\debug\disp\uc:userID> 0

| USER | CONFIGURATION : | userID | 0 |
|------|-----------------|--------|---|
| | | | |

| User Name | ADMIN |
|------------------------------|---------|
| User Password | 'ADMIN' |
| Accept Error allowed | yes |
| Insert allowed | yes |
| Aux Send allowed | yes |
| Audition Level allowed | yes |
| N-1 Level allowed | yes |
| Aux 1 (chn) allowed | yes |
| Aux 2 (chn) allowed | yes |
| Phase allowed | yes |
| Phantom allowed | yes |
| Stereo allowed | yes |
| Gain/Cal allowed | yes |
| Pan/Bal allowed | yes |
| Sum allowed | yes |
| Eq allowed | yes |
| Input Selection allowed | yes |
| System Configuration allowed | yes |
| Channel Routing allowed | yes |
| Default channel routing init | no |

ServIf:\debug\disp>cc CHANNEL CONFIGURATION : _____

| Chn | Input Type | Number of Inputs | Arr ID1 | ay of ID2 | Input ID3 | Ident ID4 | ificati ID5 | lon ID6 | Auto Ctrl |
|-----|--------------|---------------------|------------|--------------|--------------|--------------|----------------|------------|--------------|
| | | - | | | | | | | |
| 1 | mic inp | 2 | 1 | 2 | no | no | no | no | no |
| 2 | mic inp | 2 | 3 | 4 | no | no | no | no | no |
| 3 | mic inp | 2 | 5 | 6 | no | no | no | no | no |
| 4 | mic inp | 2 | 7 | 8 | no | no | no | no | no |
| 5 | mic inp | 2 | 9 | 10 | no | no | no | no | no |
| 6 | mic inp | 2 | 11 | 12 | no | no | no | no | no |
| 7 | ana line inp | 2 | 13 | 14 | no | no | no | no | no |
| 8 | ana line inp | 2 | 15 | 16 | no | no | no | no | no |
| 9 | ana line inp | 2 | 17 | 18 | no | no | no | no | no |
| 10 | ana line inp | 2 | 19 | 20 | no | no | no | no | no |
| 11 | ana line inp | 2 | 21 | 22 | no | no | no | no | no |
| 12 | ana line inp | 2 | 23 | 24 | no | no | no | no | no |
| 13 | ana line inp | 2 | 25 | 26 | no | no | no | no | no |
| 14 | ana line inp | 2 | 27 | 28 | no | no | no | no | no |
| 15 | ana line inp | 2 | 29 | 30 | no | no | no | no | no |
| 16 | ana line inp | 2 | 31 | 32 | no | no | no | no | no |
| 17 | ana line inp | 2 | 33 | 34 | no | no | no | no | no |
| 18 | ana line inp | 2 | 35 | 36 | no | no | no | no | no |
| 19 | ana line inp | 2 | 37 | 38 | no | no | no | no | no |
| 20 | ana line inp | 2 | 39 | 40 | no | no | no | no | no |
| 21 | dig line inp | 2 | 41 | 42 | no | no | no | no | no |
| 22 | dig line inp | 2 | 43 | 44 | no | no | no | no | no |
| 23 | dig line inp | 2 | 45 | 46 | no | no | no | no | no |
| 24 | dig line inp | 2 | 47 | 48 | no | no | no | no | no |

ServIf:\debug\disp\ic:inp>1

INPUT CONFIGURATION : _____ input 1

| Label-String | I 1A | Input level (Line only) | 60 |
|-----------------|------------|-------------------------|------------|
| Headroom Level | 9 | To Audition Bus | never |
| To N-1 A Bus | never | To N-1 B Bus | never |
| To N-1 C Bus | never | To N-1 D Bus | never |
| To N-1 E Bus | never | To N-1 F Bus | never |
| Timer Start | no | Ctrl. Out 1 | on & fader |
| Ctrl. Out 2 | locate-pfl | Ctrl. In | ext. mute |
| Talkback source | ST | | |
| Signaling | ST | | |
| Input Connector | XLR | | |

ServIf:\debug\disp>ccp

CHANNEL COMMON PARAMETERS :

| Chn | Inp Sel | Main | Main Fadlev | Chn on/off | PFL on/off | Loc | Next |
|-------|------------|--------|----------------|---------------|---------------|-------|-------|
| 01111 | 501 | Ter | I ddiev | 011/011 | 011/011 | Too | nene |
| 1 | А | -12800 | -12800 | off | off | inact | inact |
| 2 | A | -12800 | -12800 | off | off | inact | inact |
| 3 | A | -12800 | -12800 | off | off | inact | inact |
| 4 | А | -12800 | -12800 | off | off | inact | inact |
| 5 | А | -12800 | -12800 | off | off | inact | inact |
| 6 | А | -12800 | -12800 | off | off | inact | inact |
| 7 | А | -12800 | -12800 | on | off | inact | inact |
| 8 | А | -12800 | -12800 | on | off | inact | inact |
| 9 | А | -12800 | -12800 | on | off | inact | inact |
| 10 | А | -12800 | -12800 | on | off | inact | inact |
| 11 | А | -12800 | -12800 | on | off | inact | inact |
| 12 | А | -12800 | -12800 | on | off | inact | inact |
| 13 | А | -12800 | -12800 | off | off | inact | inact |
| 14 | А | 0 | 0 | off | off | inact | inact |
| 15 | A | -12800 | -12800 | on | off | inact | inact |
| 16 | А | -12800 | -12800 | on | off | inact | inact |
| 17 | A | -12800 | -12800 | on | off | inact | inact |
| 18 | A | -12800 | -12800 | on | off | inact | inact |
| 19 | A | -12800 | -12800 | on | off | inact | inact |
| 20 | A | -12800 | -12800 | on | off | inact | inact |
| 21 | A | -12800 | -12800 | on | off | inact | inact |
| 22 | A | -12800 | -12800 | on | off | inact | inact |
| 23 | A | -12800 | -12800 | on | off | inact | inact |
| 24 | А | -12800 | -12800 | on | off | inact | inact |

ServIf:\debug\disp\cip:ch>1

CHANNEL INPUT PARAMETERS :

Phantom (Mic Inputs)noPhasenormalGain / Cal35Stereo / MonostereoStereo mode (Left-Right)LRTo ProgramyesTo RecordyesPan / Balance0Aux 1 On / OffnoAux 1 Source AF / PFAF (CUE)Aux 1 Level-700Aux 2 On / OffnoAux 2 Source AF / PFAF (CUE)Aux 2 Level-700Equalizer On / OffoffHPF On / OffoffLF Gain0LF Frequency200MF Gain0MF Frequency1000HF Gain0HF Frequency10000Ready active / inactiveinactCough active / inactiveinact

input 1

ServIf:\debug\disp>mp

| MONITORING PARAMETERS : | |
|---|---|
| Studio Monitoring Source Control Room Monitoring Source Control Room Volume CR DIM CR CUT ST DIM ST CUT | program program -4403 off off off off |
| PFL/TB Volume | -4613 |
| Headphone Source | cr stereo |
| DJ Headphone Volume | -3526 |
| PFL to Monitor | yes |
| TB CR to Studio | no |
| TB CR LO AUX I | 110 |
| TB CR to Aux 2 TR CR to N-1 A | 110 |
| TB CR to N-1 B | no |
| TB CR to $N-1$ C | no |
| TB CR to N-1 D | no |
| TB CR to N-1 E | no |
| TB CR to N-1 F | no |
| TB Studio to CR | no |
| TB Studio to N-1 A | no |
| TB Studio to N-1 B | no |
| TB Extern to CR | no |
| TB Extern to Studio | no |
| Extern to PFL | no |
| ON-AIR Signal Input | no |
| Monitor Expander Output Fl | not active |
| Monitor Expander Output F2 | not active |
| Monitor Expander Output F3 | not active |
| Monitor Expander Output F5 | not active |
| Monitor Expander Output F3 Monitor Expander Output F4 Monitor Expander Output F5 | not active not active not active |

ServIf:\debug\disp>asp

| AUX SEND PARAMETERS : |
|-------------------------|
| Aux Master 1 Level |
| Aux Master 1 On |
| Aux Master 1 PFL |
| Aux Master 2 Level |
| Aux Master 2 On |
| Aux Master 2 PFL |
| N-1 A Level |
| N-1 A On |
| N-1 A PFL |
| N-1 B Level |
| N-1 B On |
| N-I B PFL |
| N-I C Level |
| N-I C OR |
| N-I C FFL |
| $N = 1 D \Omega \alpha$ |
| N-1 D PFT. |
| N-1 E Level |
| N-1 = On |
| N-1 E PFL |
| N-1 F Level |
| N-1 F On |
| N-1 F PFL |
| Audition Level |
| Audition On |
| Audition PFL |
| Program Master Level |
| Program Master On |
| Program Master PFL |
| Record Master Level |
| Record Master On |
| Record Master PFL |

ServIf:\debug\disp>iap INSERT ASSIGN PARAMETERS : _____

| Insert | 1 | assign | NC |
|--------|---|--------|-----|
| Insert | 1 | on/off | off |
| Insert | 2 | assign | NC |
| Insert | 2 | on/off | off |
| Insert | 3 | assign | NC |
| Insert | 3 | on/off | off |
| Insert | 4 | assign | NC |
| Insert | 4 | on/off | off |
| | | | |

ServIf:\debug\disp>crp CHANNEL ROUTING PARAMETERS : _____

Channel to Fader Assignment

| Chn | assigned | protect | Chn | assigned | protect |
|-----|----------|---------|-----------|----------|---------|
| | Fader | | | Fader | |
| 1 | 1 | no | 14 | NONE | no |
| 2 | 2 | no | 15 | 15 | no |
| 3 | 3 | no | 16 | 16 | no |
| 4 | 4 | no | 17 | 17 | no |
| 5 | 5 | no | 18 | 18 | no |
| 6 | 6 | no | 19 | 19 | no |
| 7 | 7 | no | 20 | 20 | no |
| 8 | 8 | no | 21 | 21 | no |
| 9 | 9 | no | 22 | 22 | no |
| 10 | 10 | no | 23 | 23 | no |
| 11 | 11 | no | 24 | 24 | no |
| 12 | 12 | no | PGMMASTER | 13 | no |
| 13 | NONE | no | RECMASTER | 14 | no |

Fader to Channel Assignment

| Fader | assigned | Fader | assigned |
|-------|----------|-------|-----------|
| | Chn | | Chn |
| 1 | 1 | 13 | PGMMASTER |
| 2 | 2 | 14 | RECMASTER |
| 3 | 3 | 15 | 14 |
| 4 | 4 | 16 | 15 |
| 5 | 5 | 17 | 16 |
| 6 | 6 | 18 | 17 |
| 7 | 7 | 19 | 18 |
| 8 | 8 | 20 | 19 |
| 9 | 9 | 21 | 20 |
| 10 | 10 | 22 | 21 |
| 11 | 11 | 23 | 22 |
| 12 | 12 | 24 | 23 |

ServIf:\debug\disp>scp SNAPSHOT CONTROL PARAMETERS : _____ SnapShot Section active Section 0 SnapShot Section Write Pointer 0x00181F21 Array of 5 Global SnapShots : GRUNDEINST. NIL NIL NIL NIL Array of 10 pointers to Global Mic Settings : NIL Array of 5 Global Channel Routings : NIL NIL NIL NTT. NIL For each user an array of 4 Private SnapShots : Private SnapShots of user DEFAULT USER: NIL NIL NIL NIL Private SnapShots of user with index 2: NTT. NIL NIL NIL Private SnapShots of user with index 20: NIL NIL NIL NIL Private SnapShots of user with index 21: NIL NIL NIL NTT. For each user an array of 4 Private Mic Settings : Private Mic Settings of user DEFAULT USER: NIL NIL NIL NIL Private Mic Settings of user with index 2: NIL NIL NIL NIL



ServIf:\debug\disp>cuc

CUSTOMER CODE :

| previous version | 0x0000000 |
|------------------|-----------|
| current version | 0x0000000 |

ServIf:\debug\disp>user

USER PARAMETERS :

| Currently | logged | in | user | (name) |
|-----------|--------|----|------|--------|
| Currently | logged | in | user | (ID) |

ServIf:\debug\disp>tip

TIMER PARAMETERS :

| User | Stopwatch | State | stopped |
|-------|-----------|-------|---------|
| Fader | Stopwatch | State | stopped |

ServIf:\debug\disp>ap

AUTOMATION PARAMETERS :

BCA Present

no

ADMIN

0

ServIf:\debug\disp>acp

AUTOMATION CHANNEL PARAMETERS :

| Chn | Source Selection | Display Text |
|-----|---------------------|-----------------|
| 1 | NO | |
| 2 | NO | |
| 3 | NO | |
| 4 | NO | |
| 5 | NO | |
| 6 | NO | |
| 7 | NO | |
| 8 | NO | |
| 9 | NO | |
| 10 | NO | |
| 11 | NO | |
| 12 | NO | |
| 13 | NO | |
| 14 | NO | |
| 15 | NO | |
| 16 | NO | |
| 17 | NO | |
| 18 | NO | |
| 19 | NO | |
| 20 | NO | |
| 21 | NO | |
| 22 | NO | |
| 23 | NO | |
| 24 | NO | |
STUDER



6 SPARE PARTS, ACCESSORIES

Spare Parts:

| Item | Order No.: | Designation |
|------|---------------|--|
| 1 | 1.942.500.08 | Side panel, lower, left |
| 2 | 1.942.500.06 | Side panel, upper, left |
| 3 | 1.942.500.09 | Side panel, lower, right |
| 4 | 1.942.500.07 | Side panel, upper, right |
| F | 1.942.500.02 | Rear panel, for analog-biased version |
| 5 | 1.942.500.03 | Rear panel, for digitally-biased version |
| 6 | 1.942.500.04 | Top cover |
| 7 | 1.942.500.01 | Bottom cover |
| 0 | 58.30.0101 | Fader 100 mm, 10 k Ω lin. |
| 0 | 1.911.000.48 | Fader knob, light grey (12 pcs. shipped with the unit as standard) |
| 0 | 42.01.0414 | Rotary knob large, dark grey |
| 9 | 42.01.0464 | Knob cover large, light grey |
| 10 | 42.01.0209 | Rotary knob small, dark grey |
| 10 | 42.01.0257 | Knob cover small, light grey, with dash |
| 11 | 1.942.082.00 | Touch-screen module, complete |
| | 10.942.161.00 | CFL backlight lamp |
| 12 | 1.942.500.15 | Glass cover for correlator display (2 used) |
| 13 | 1.942.500.16 | Glass cover for level meter display (4 used) |
| 14 | 1.942.500.05 | Glass cover for channel label display (10 used) |
| | | Keyboard and lamp rubber mat set (w. conductive-plastic contacts), |
| 15 | 1 0/2 710 01 | for two fader strips (6 sets used), consisting of: |
| 15 | 1.342.710.01 | Keyboard mat (A/B/SEL/EQ); keyboard mat (OVL/PFL); 2 x lamp |
| | | mat (arrow up/down/PGM/REC/AUX/INS); keyboard mat ON/OFF) |
| 16 | 1.942.010.08 | Keyboard rubber mat (START-STOP/RESET) for Stopwatch |
| 17 | 1.942.010.07 | Keyboard and lamp rubber mat for Monitoring section |
| 18 | 71.01.0174 | Loudspeaker, 15 $\Omega/1$ W |
| 10 | 89.20.0302 | PC-Card, 4 MB |
| 19 | 89.20.0301 | PC-Card, 2 MB |
| | | |

Accessory Set:

| 1.942.596.00 | (shipped with the unit): |
|----------------|--|
| consisting of: | Mains cable socket IEC320 |
| | DC supply connector |
| | Mains cable strain relief |
| | Fader knobs: 2 each - red, orange, yellow, green, blue |
| | 1.942.596.00 consisting of: |

CONTENTS PART 3 – DIAGRAMS

Software Survey (Which software component is located on which assembly? - please turn page)

Assembly Survey (In the same order as given in the table of contents below)

System Wiring Diagram, Standard Versions

System Wiring Diagram, Redundant Supply Versions

| Supply | Assembly No. | Diagram | Component Layout | Parts List |
|---|--------------|---------|------------------|------------|
| Subboard 1 for PSU (for standard versions only) | 1.942.606.81 | .81 | .81 | .81 |
| Subboard 2 for PSU (for redundant supply versions only) | 1.942.607.00 | .00 | .00 | .00 |
| Power Supply | 1.942.605.81 | .00 | .00 | .81 |

| Control/DSP | Assembly No. | Diagram | Component Layout | Parts List |
|-------------------------|--------------|--------------|------------------|--------------|
| Controller | 1.942.601.20 | 1.942.100.20 | 1.942.600.20 | 1.942.600.21 |
| Control Front Board I | 1.942.610.20 | 1.942.110.20 | 1.942.110.20 | 1.942.110.22 |
| Control Front Board II | 1.942.111.00 | .00 | .00 | .00 |
| Control Front Board III | 1.942.612.00 | .00 | .00 | .00 |
| Front Board | 1.942.710.20 | .20 | .20 | .20 |
| DSP Board | 1 942 602 20 | 1 942 102 22 | 1 942 102 22 | 1 942 602 20 |

| Input Units | Assembly No. | Diagram | Component Layout | Parts List |
|-------------------------------|--------------|--------------|------------------|--------------|
| Mic Input Unit | 1.942.720.21 | 1.942.220.21 | 1.942.720.20 | 1.942.720.21 |
| to above: Insert Send | 1.942.221.00 | .00 | .00 | .00 |
| Line Input Unit | 1.942.732.20 | 1.942.230.21 | 1.942.732.20 | 1.942.732.20 |
| Digital Input Unit | 1.942.740.20 | 1.942.240.23 | 1.942.740.20 | 1.942.740.20 |
| TB Mic Input Unit | 1.942.719.00 | 1.942.219.81 | 1.942.719.00 | 1.942.719.00 |
| Analog Insert Unit | 1.942.660.20 | - | .660.20 | - |
| to above: Insert Controller | 1.942.161.00 | .161.00 | .161.00 | .161.00 |
| to above: Analog Insert D-Sub | 1.942.163.00 | .163.00 | .163.00 | .163.00 |

| Outputs | Assembly No. | Diagram | Component Layout | Parts List |
|---------------------------|--------------|--------------|------------------|--------------|
| Digital Output Unit | 1.942.624.20 | 1.942.124.20 | 1.942.624.20 | 1.942.624.20 |
| (Dual) Analog Output Unit | 1.942.621.00 | 1.942.121.82 | 1.942.621.00 | 1.942.621.00 |

| Monitoring | Assembly No. | Diagram | Component Layout | Parts List |
|-----------------------|--------------|---------|------------------|------------|
| Monitoring Controller | 1.942.182.21 | .21 | .21 | .21 |
| CR Monitor Out | 1.942.133.00 | .00 | .00 | .00 |
| Studio Monitor D-Sub | 1.942.139.00 | .00 | .00 | .00 |
| CR Monitor D-Sub | 1.942.129.00 | .00 | .00 | .00 |

| Metering | Assembly No. | Diagram | Component Layout | Parts List |
|-------------|--------------|---------|------------------|------------|
| Level Meter | 1.942.613.00 | .00 | .00 | .00 |
| | | | | |
| Interfaces | Assembly No | Diagram | Component Lavout | Parts List |

| Interfaces | Assembly No. | Diagram | Component Layout | Parts List |
|----------------------------|--------------|--------------|------------------|--------------|
| Clock Sync Interface | 1.942.635.20 | 1.942.135.20 | 1.942.635.20 | 1.942.635.20 |
| Telephone Hybrid Interface | 1.942.640.20 | 1.942.140.20 | 1.942.640.20 | 1.942.640.20 |
| (Serial) RS232 Interface | 1.942.645.00 | 1.942.145.00 | 1.942.645.00 | 1.942.645.00 |
| Time Sync Interface | 1.942.650.22 | 1.942.150.20 | 1.942.650.20 | 1.942.650.22 |

| Optional Interface | Assembly No. | Diagram | Component Layout | Parts List |
|--|--------------|---------|------------------|------------|
| Control Interface Box, Application Information | 1.942.803.00 | .00 | - | - |
| to above: Control Interface | 1.942.802.00 | .00 | .00 | .00 |

Software Survey

| Assembly | | Software Label | Position |
|------------------------------------|---------------|-------------------|----------|
| Controller | 1.942.600.xx | 1.942.904.xx | IC30 |
| | | 1.942.905.xx | IC31 |
| | | 1.942.926.xx | IC18 |
| | ſ | 1.942.951.28 IC28 | IC28 |
| SIM Set cordior version | 1 042 000 20 | 1.942.951.28 IC26 | IC26 |
| Sw Set, earlier version. | 1.942.900.20 | 1.942.951.28 IC24 | IC24 |
| | l | 1.942.951.28 IC23 | IC23 |
| | ſ | 1.942.900.xx IC28 | IC28 |
| CW Cat from 1/2 09 and un | 1 042 000 var | 1.942.900.xx IC26 | IC26 |
| | 1.942.900.XX | 1.942.900.xx IC24 | IC24 |
| | l | 1.942.900.xx IC23 | IC23 |
| | | Snapshot | IC32 |
| | | Flash Memory | |
| DSP Board | 1.942.102.xx | 1.942.906.xx | IC03 |
| | | 1.942.907.xx | IC04 |
| | | 1.942.908.xx | IC28 |
| | | 1.942.908.xx | IC27 |
| | | 1.942.908.xx | IC26 |
| | | 1.942.909.xx | IC38 |
| | | 1.942.909.xx | IC39 |
| | | 1.942.909.xx | IC40 |
| Control Front Board I | 1.942.110.xx | 1.942.910.xx | IC01 |
| Front Board | 1.942.710.xx | 1.942.950.xx | IC36 |
| Mic Input | 1.942.720.xx | 1.942.921.xx | IC06 |
| Line Input | 1.942.732.xx | 1.942.922.xx | IC08 |
| Digital Input | 1.942.740.xx | 1.942.923.xx | IC07 |
| Digital Output | 1.942.624.xx | 1.942.927.xx | IC10 |
| | | 1.942.912.xx | IC07 |
| | | 1.942.912.xx | IC11 |
| Monitoring Controller | 1.942.182.xx | 1.942.913.xx | IC08 |
| Time Sync IF | 1.942.650.xx | 1.942.928.xx | IC01 |
| Clock Sync IF | 1.942.635.xx | 1.866.914.xx | IC01 |
| | | 1.866.910.xx | IC09 |
| Telephone Hybrid IF | 1.942.640.xx | 1.942.915.xx | IC03 |
| Analog Insert | 1.942.660.xx | 1.942.918.xx | IC05 |
| (SW installed on Insert Controller | 1.942.161.xx) | | |

Assembly Survey



| Index | Designation | Order No. | Index | Designation | Order No. |
|----------|--|--------------|----------|----------------------------|--------------|
| [1] | Primary Power Supply (Standard Versions) | | [13] | TB Mic Input | 1.942.719.xx |
| [1] | Power Supply (Redundant Supply Versions) | 1.942.605.xx | [14] | Analog Insert | 1.942.660.xx |
| [2] | Subboard 1 for PSU (Standard Versions) | 1.942.606.xx | to above | Insert Controller | 1.942.161.xx |
| [2] | Subboard 2 for PSU (Redundant Supply V.) | 1.942.607.xx | to above | Analog Insert D-Sub | 1.942.163.xx |
| [3] | Power Supply (both Versions) | 1.942.605.xx | [15] | Digital Output | 1.942.624.xx |
| [4] | Controller | 1.942.600.xx | [16] | (Dual) Analog Output | 1.942.621.xx |
| [5] | Control Front Board I | 1.942.110.xx | [17] | Monitoring Controller | 1.942.182.xx |
| [6] | Control Front Board II | 1.942.111.xx | [18] | CR Monitor Out | 1.942.133.xx |
| [7] | Control Front Board III | 1.942.612.xx | [19] | Studio Monitor D-Sub | 1.942.139.xx |
| [8] | Front Board | 1.942.710.xx | [20] | CR Monitor D-Sub | 1.942.129.xx |
| [9] | DSP Board | 1.942.102.xx | [21] | Level Meter | 1.942.613.xx |
| [10] | Mic Input | 1.942.720.xx | [22] | Clock Sync Interface | 1.942.635.xx |
| to above | Insert Send | 1.942.221.xx | [23] | Telephone Hybrid Interface | 1.942.640.xx |
| [11] | Line Input | 1.942.732.xx | [24] | (Serial) RS232 Interface | 1.942.645.xx |
| [12] | Digital Input | 1.942.740.xx | [25] | Time Sync Interface | 1.942.650.xx |



System Wiring Diagram – Standard Versions



System Wiring Diagram – Redundant Supply Versions











| idx | Pos. | Part No. | Qty. | Type/Val. | Description | lcbx | Pos. | Part No. | Qty. | Type/Val. | Description |
|-----|----------|------------|-------|------------------|--|------|--------------|--------------------------|---------|--------------|--|
| | 0.1 | 59 60 3337 | 1 pce | 100n | CEB 50V. 10%, X7B. 0805 | 0 | MP 1 | 1.942.607.11 | 1 pce | | SUBBOARD 2 FOR PSU PCB |
| 0 | C 2 | 59.60.3337 | 1 pce | 100n | CER 50V. 10%, X7R. 0805 | ō | MP 2 | 1.942.607.10 | 1 pce | | NR.ETIKETTE |
| 0 | C 3 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | MP 3 | 43.01.0108 | 1 pce | Label | ESE-WARNSCHILD |
| 0 | C 4 | 59.29.0422 | 1 pce | 22m | EL 35V RM10 radial | 0 | MP 4 | 50.20.3011 | 5 pcs | | Kühlkörper, TO 220, vertikal |
| 0 | C 5 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | MP 6 | 50.20.3012 | 2 pcs | | Kühikörper, TO 220, vertical |
| 0 | C 6 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | P 1 | 54.12.0506 | 1 pce | 6p - | P Stecker 6p Power-Pin |
| 0 | C 8 | 59.68.0111 | 1 pce | 22u | EL 35V. 6.3*5.7 | 0 | P 2 | 54.12.0510 | 1 pce | 10p | P Stecker 10p Power-Pin |
| ő | C 9 | 59.68.0111 | 1 pce | 22u | EL 35V, 6.3*5.7 | 0 | P 3 | 54.12.0505 | 1 pce | 5p | P Stecker 5p Power-Pin |
| 0 | C 10 | 59.68.0111 | 1 pce | 22u | EL 35V, 6.3*5.7 | 0 | P 4 D 6 | 54.12.0000 | 1 pce | 6p 40 | P Stecker op Power-Pill P Stecker op Power-Pill |
| 0 | C 11 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | ő | P6 | 54.12.0702 | 1 000 | 20 | Stecker gerade PCB |
| 0 | C 12 | 59.80.3337 | 1 pce | 100n | CER 50V, 10%, X/H, 0805 | 0 | P 7 | 54.01.0020 | 1 pce | 1p | Pin, 1reihig, gerade |
| 0 | C 14 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | P 8 | 54.01.0020 | 1 pce | 1p | Pin, 1reihig, gerade |
| ŏ | C 15 | 59.60.3337 | 1 poe | 100n | CER 50V, 10%, X7R, 0605 | 0 | P 9 | 54.01.0020 | 1 poe | 1p | Pin, 1reihig, gerade |
| 0 | C 16 | 59.60.3337 | 1 poe | 100n | CER 50V, 10%, X7R, 0805 | 0 | P 10 | 54.12.0510 | 1 pce | 10p | P Stacker 10p Power-Pin |
| 0 | C 17 | 59.60.3337 | 1 poe | 100n | CER 50V, 10%, X7R, 0605 | ŏ | P 12 | 54.12.0505 | 1 pce | 50 | P Stecker 5p Power-Pin |
| 0 | C 18 | 59.60.3337 | 1 poe | 100n | CER 50V, 10%, X7R, 0805 | 0 | P 13 | 54.12.0504 | 1 pce | 4p | P Stecker 4p Power-Pit |
| 0 | 0.19 | 59.50.3337 | 1 pce | 22m | El 35V RM10 radial | 0 | P 14 | 54.12.0503 | 1 pce | 3p | P Stecker 3p Power-Pin |
| ŏ | C 21 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | P 15 | 54.12.0510 | 1 pce | 10p | P Stecker 10p Power-Pin |
| 0 | C 22 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | P 16 | 54.12.0506 | 1 pce | 5p 10p | P Stecker op Power-Pit |
| 0 | C 23 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | ő | P 18 | 54.12.0505 | 1 pce | 5p | P Stecker 5p Power-Pin |
| 0 | C 24 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7H, 0805 | 0 | P 19 | 54.12.0506 | 1 pce | 6p | P Stecker 6p Power-Pin |
| C | D 1 | 50.04.0516 | 1 pce | USD945 | Schottky Rect 16A, 45V | 0 | P 20 | 54.12.0504 | 1 pce | 4p | P Stecker 4p Power-Pin |
| 0 | D 2 | 50.60.8001 | 1 pce | 4448 | 200nA 75V 4ns SOD 80 | 0 | Q 1 | 50.60.0001 | 1 pce | BC847B | NPN 45V 100mA SOT 23 |
| 0 | D 3 | 50.60.8003 | 1 pce | S18 | 1000mA 100V DO 214AG | ċ | Q 2 | 50.60.0001 | 1 pce | BCB47B | NPN 45V 100mA SOT 23 |
| 0 | 04 | 50.60.8003 | : pce | 318 1N5822 | Tabletar | 0 | Q 3 | 50.60.0001 | 1 pce | BC847B | NPN 45V 100mA SOT 23 |
| ő | D 6 | 50.04.0519 | 1 000 | 1N5822 | 3A. Schottky | 0 | Q 4 | 50.60.0001 | 1 pce | BC847B | NPN 45V 100mA SOT 23 |
| ő | D7 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 1 | 57.60.1102 | 1 pce | 1 k0 | MF, 1%, 0204, E24 |
| 0 | D 8 | 50.04.0516 | 1 pce | USD945 | Schottky Rect 16A, 45V | 0 | R 2 | 57.60.1102 | 1 pce | 1 k0 | MF, 1%, 0204, E24 |
| 0 | D 9 | 50.04.0529 | pce | 19TQ015 | 19A 15V Schottky, TO 220 | 0 | R 3 | 57.60.1102 | 1 pce | 1k0 | MF, 1%, 0204, E24 |
| 0 | D 10 | 50,60,8001 | pce | 4448 S1B | 2001A 75V 40S SUD 80 1000mA 100V DO 214AC | | R 4 | 57.60,1102 | 1 pce | 1k0 1k0 | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | D 12 | 50.60.8003 | . pce | S1B | 100)mA 100V DO 214AG | . 0 | RA | 57.60.1102 | 1 pce | 1k0 | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| ő | D 13 | 50.60.8003 | · pce | S1B | 1000mA 100V DO 214AC | ŏ | 87 | 57.60.1331 | 1 pce | 330R | MF, 1%, 0204, E24 |
| 0 | D 14 | 50.60.8001 | - pce | 4448 | 200mA 75V 4ns SOD 80 | 0 | R B | 57.60.1562 | 1 pce | 5k8 | MF, 1%, 0204, E24 |
| 0 | D 15 | 50.60.8001 | . bce | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 9 | 57.60.1151 | 1 pce | 150R | MF, 1%, 0204, E24 |
| 0 | D 16 | 50.60.8001 | , bce | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 10 | 57.60.1101 | 1 pce | 100R | MF, 1%, 0204, E24 |
| 0 | D 17 | 50.60.8003 | , bce | 1518 | D 1N 5818 1N 5810 | 0 | R 11 | 57.60.1393 | 1 pce | 39k | MF, 1%, 0204, E24 |
| 0 | D 19 | 50.04.0519 | , 00e | 1N5822 | 3A.Schottky | | B 13 | 57.60.1332 | 1 000 | 390B | MF, 1%, 0204, E24 MF 1%, 0204 E24 |
| ő | D 20 | 50.60.8003 | · pce | S1B | 1000mA 100V DO 214AC | ŏ | B 14 | 57.60.1391 | 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 | D 21 | 50.04.0519 | . bce | 1N5822 | 3A, Schottky | ò | R 15 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | D 22 | 50.04.0519 | , bce | 1N5822 | 3A, Schottky | 0 | R 16 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | D 23 | 50.60.8001 | , bce | 4448 | 200nA 75V 4ns SOD 80 | 0 | R 17 | 57.60.1102 | 1 poe | 1 k0 | MF, 1%, 0204, E24 |
| 0 | D 24 | 50.04.0512 | : pce | 1N5818 1N5822 | D IN 5818, IN 5819, 34 Schottky | 0 | R 18 | 57.60.1101 | 1 pce | 100R | MF, 1%, 0204, E24 |
| 0 | D 26 | 50.04.0519 | 1 pce | 1N5822 | 3A. Schottky | 0 | H 19 B 20 | 57.60.1393 | 1 pos | 39K 10k | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| ő | D 27 | 50.60.8001 | pce : | 4448 | 200mA 75V 4ns SOD 80 | ŏ | R 21 | 57.60.1332 | 1 000 | 3k3 | MF, 1%, 0204, E24 |
| 0 | D 28 | 50.04.0529 | 1 pce | 19TQ015 | 19A 15V Schottky, TO 220 | 0 | R 22 | 57.60.1333 | 1 pce | 33k | MF, 1%, 0204, E24 |
| 0 | D 29 | 50.60.8001 | 1 pce | 4448 | 200nA 75V 4ns SOD 80 | 0 | R 23 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | D 30 | 50.04.0519 | 1 pce | 1N5822 | 3A, Schottky 200mA 75V dne SOD 80 | 0 | R 24 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | D 32 | 50.60.8001 | , DOB | 4448 | 200mA 75V 4ns SOD 80 | 0 | H 20 | 57.60.1332 | 1 nce | 300B | MF, 1%, 0204, E24 |
| ō | D 33 | 50.60.8001 | , bce | 4448 | 200mA 75V 4ns SOD 80 | ő | R 27 | 57.60.1102 | 1 pce | 1k0 | MF, 1%, 0204, E24 |
| 0 | D 34 | 50.60.8001 | ; boe | 4448 | 200mA 75V 4ns SOD 80 | ò | R 28 | 57.60.1121 | 1 pce | 120R | MF, 1%, 0204, E24 |
| 0 | D 35 | 50.60.8001 | t poe | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 29 | 57.60,1332 | 1 pce | 3k3 | MF, 1%, 0204, E24 |
| 0 | D 36 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOLI An | 0 | R 30 | 57.80,1333 | 1 pce | 33k | MF, 1%, 0204, E24 |
| 0 | D 38 | 50.04.0519 | poe | 1N5822 | 3A, Schotky | 0 | H 31 | 57.60,1103 | 1 pce | 10K 10k | MF, 1%, 0204, 524 MF, 1%, 0204, 524 |
| ő | D 39 | 50.04.0516 | poe | USD945 | Schottky Rect 16A, 45V | ő | R 33 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | D 40 | 50.04.0516 | poe 1 | USD945 | Schottky Rect 16A, 45V | ō | FI 34 | 57.60.1333 | 1 pce | 33k | MF, 1%, 0204, E24 |
| 0 | D 41 | 50.04.0516 | , bce | USD945 | Schottky Rect 16A, 45V | 0 | R 35 | 57.92.7012 | 1 pce | 0.3A | PTC 60V |
| 0 | DL 1 | 50.60.9414 | , bce | grn | SMD LED 2.7*3.5 | 0 | R 36 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | DL 2 | 50.60.9414 | , bce | grn | SMD LED 2.7*3.5 | 0 | H 37 | 57.60.1103 | 1 pce | 1 UK | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | DL 3 | 50.60.9414 | , boe | grn | SMD LED 2.7*3.5 | ő | R 39 | 57.60.1102 | 1 pce | 1 k0 | MF, 1%, 0204, E24 |
| 0 | DL4 | 50,60.9414 | pce | grn | SMULEU 2.73.5 SMD LED 2.795 | ó | R 40 | 57,60,1102 | 1 pce | 1 k0 | MF, 1%, 0204, E24 |
| 0 | DL6 | 50.80.9414 | : poe | arn | SMD LED 2.7'3.5 | ٥ | R 41 | 57.60,1333 | 1 pce 1 | 33k | MF, 1%, 0204, E24 |
| 0 | DL 7 | 50.80.9414 | . boe | grn | SMD LED 2.7*3.5 | 0 | R 42 | 57,60,1333 | 1 pce | 33k | MF, 1%, 0204, E24 |
| ó | DL 8 | 50.60.9414 | pce | grn | SMD LED 2.7*3.5 | 0 | H 43 | 57.60.1332 | 1 pce | 3K3 10k | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | DL 9 | 50.60.9414 | , boe | grn | SMD LED 2.7*3.5 | 0 | H 44 R 46 | 57.60.1103 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 MF, 1%, 0204, F24 |
| 0 | DL 10 | 50.60.9414 | , bce | grn | SMD LED 2.7*3.5 | ŏ | R 46 | 57.60,1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | DL 11 | 50.60.9414 | : poe | grn | SMULED 2.7'3.5 | 0 | R 47 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | DL 13 | 50.60.9414 | : poe | grn | SMD LED 2.7*3.5 | ٥ | R 48 | 57.60.1332 | 1 poe | 3k3 | MF, 1%, 0204, E24 |
| | | | | 0.04 | T FIGS & STOLE | 0 | R 49 | 57.60,1101 | 1 poe | 100R | MF, 1%, 0204, E24 |
| 0 | F1 F2 | 51.01.0128 | poe | A0.8 | 1 5'20 L 250V | 0 | H 50 B 61 | 57.60,1103 57.60 1202 | 1 pce | i UK aka | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | F 2 | 51.01.0126 | pce | D.UA | 1 0 20 L 200V | 0 | R 52 | 57.60.1392 | 1 pce | 39k | MF, 1%, 0204, E24 |
| 0 | IC 1 | 50.61.9001 | ' poe | LM393 | Dual voltage comp. SO 8 | ő | R 53 | 57.60,1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | IC 2 | 50.61.9001 | : pce | LM393 | Dual voltage comp. SO 8 | 0 | R 54 | 57.60.1332 | 1 pce | 3k3 | MF, 1%, 0204, E24 |
| 0 | IC 4 | 50.10.0104 | : pce | LM393 | Series regulator 1.5A +37V | 0 | R 55 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | IC 5 | 50.10.0104 | 1 pce | TL431 | Shunt regulator | 0 | R 56 | 57.56.5220 | 1 pce | 22R | WW, 10%, 4 W |
| ő | IC 6 | 50.61.9001 | 1 pce | LM393 | Dual voltage comp. SO 8 | 0 | H 5/ B 58 | 57.60.1391 | 1 pce | 390 H 10k | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | IC 7 | 50.61.9001 | t pce | LM393 | Dual voltage comp. SO 8 | ŏ | R 59 | 57.60.1121 | 1 pce | 120R | MF, 1%, 0204, E24 |
| 0 | IC 8 | 50.61.9001 | t pce | LM393 | Dual voltage comp. SO 8 | o | R 60 | 57.60.1151 | 1 pce | 150R | MF, 1%, 0204, E24 |
| 0 | IC 9 | 50.61.9001 | t pce | LM393 | Dua voitage comp. SO 8 | 0 | R 61 | 57.60.1331 | 1 pce | 330 R | MF, 1%, 0204, E24 |
| 0 | J 1 | 54.01.0021 | 1 pce | Jumper | 0.65*0.63mm, Au | 0 | R 62 | 57.60.103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 | К1 | 55.04.0198 | 1 pce | 2*u | 5V 125V 2A Ag/Au | 0 | R 63 | 57.60.1562 | 1 pce | 5k6 | MF, 1%, 0204, E24 |
| 0 | К 2 | 55.04.0198 | 1 poe | 2*u | 5V 125V 2A Ag/Au | J | A 04 | 07.00. 393 | , poe | JON | ···· ; · ··· , 050*, 54* |

| ldx | Pos. | Part No. | Qty. | Type/Val. | Description |
|-----|------|------------|-------|-----------|--------------------------|
| 0 | B 65 | 57 60 1332 | 1 000 | aka | MF 1% 0204 E24 |
| ő | R 66 | 57.60.1103 | 1 000 | 10k | MF, 1%, 0204, E24 |
| ò | B 67 | 57.60.1102 | 1 008 | 1 K0 | MF. 1%, 0204, E24 |
| ō | B 68 | 57.60.1102 | 1 pce | 1 K0 | MF. 1%, 0204, E24 |
| ō | R 69 | 57.60.1102 | 1 pce | 1 k0 | MF, 1%, 0204, E24 |
| 0 | R 70 | 57.60.1102 | 1 pce | 1 k0 | MF, 1%, 0204, E24 |
| 0 | R 71 | 57.60.1102 | 1 pce | 1 k0 | MF, 1%, 0204, E24 |
| 0 | R 72 | 57.60.1102 | 1 pce | 1 k0 | MF, 1%, 0204, E24 |
| 0 | R 73 | 57.60.1102 | 1 pce | 1 K0 | MF, 1%, 0204, E24 |
| 0 | TP 1 | 54.33.6010 | 1 pce | 2.8*0.8 | PCB-Flachstecker, gerade |
| 0 | XF 1 | 53.03.0401 | 1 pce | | XF 5 * 20, -SOCKEL |
| 0 | XF 2 | 53.03.0401 | 1 pce | | XF 5*20, -SOCKEL |
| | | | | | |

---- End of List

Comments:

Power Supply 1.942.605.81



STUDER

Power Supply 1.942.605.81



| Accompanying documents: Zugehoerige Unterlogen: | | General talerance: Freinasstoleranz: | Scole Wosssto: | ti on | 01.11.20 |)0 ZT | PG | R_ | 0 |
|--|-------|---|-------------------|---------------|------------------|--------------|-------------------|------------|-------|
| PL, BV640 | | | | Auso | Date Datum | Viso Gez. | Checked Gepr . | Sen Gel | Index |
| Substitute for: Ersatz fuer: | | | | Poge Seite | a; | 1 | / 1 | | |
| REGENSDORF | FOWER | SUPPLY, | E SE | Z | Number Numer: | . 942 | . 60 |)5. | 00 |

POWER SUPPLY 1.942.605.81 (1)

Page: 1 of 2

| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx | . Pos. | Part No. | Qty. | Type/Val. | Description |
|----------|----------|------------|------|----------------|------------------------------|--------|--------------|--------------|--------|---------------|--------------------------------|
| | | | | | | | | | | | |
| 0 | C 1 | 59.06.5223 | | 22n | PETP, 63V, 5%, RM5 | 0 | DV 2 | 50.04.1112 | | 5V1 | Zener, 5%, 0.5W, DO-35 |
| 0 | <u> </u> | 50 06 5222 | | 303 | DETD 631/ 5% DM5 | 0 | DV 3 | 50.04.1112 | | 5V1 | Zener, 5%, 0.5W, DO-35 |
| | 02 | 39.00.3332 | | 5115 | FEIF, 00V, 376, KW3 | 0 | DV 4 | 50.04.1112 | | 5V1 | Zener. 5%. 0.5W. DO-35 |
| 0 | C 3 | 59.06.0474 | | 470n | PETP, 63V, 10%, RM5 | - | DVE | not used | | 111/ | Zopor 5% 0.5% DO 35 |
| 0 | C 4 | 59.05.2222 | | 2n2 | PP, 2.5%, 160V | 0 | DV 5 | not used | | 11V | Zener, 5%, 0.5W, DO-35 |
| 0 | C.5 | 59 22 3470 | | 4711 | EI 10V 20% RM5 | 0 | IC 1 | 50.10.0125 | | L4970A | Switching Regulator 10A |
| Ň | 00 | 50.00.0000 | | 00 | | 0 | IC 2 | 50.10.0106 | | TL431 | Shunt regulator |
| ~ | | 50.22.8220 | | 4.7 | EL 500 200 RMB | 0 | IC 3 | 50.10.0106 | | TL431 | Shunt regulator |
| 0 | 67 | 59.22.8479 | | 407 | EL 50V 20% RM5 | 0 | IC 4 | 50 10 0106 | | TI 431 | Shunt regulator |
| 0 | C 8 | 59.22.8229 | | 2u2 | EL 50V 20% RM5 | 0 | 10 4 | 50.10.0100 | | 12401 | |
| 0 | C 9 | 59.34.5391 | | 390p | CER 63V, 5%, N1500 | 0 | 10.5 | 50.10.0122 | | L4960 | L 4960, |
| ٥ | C 10 | 59 06 5332 | | 303 | PETP 63V 5% RM5 | 0 | IC 6 | 50.10.0122 | | L4960 | L 4960, |
| | 0 10 | 33.00.3002 | | | | 0 | IC 7 | 50 10 0122 | | L4960 | L 4960. |
| 0 | C 11 | 59.29.0422 | | 22m | EL 35V RM10 radial | | 10.0 | 50 10 0122 | | 14900 | 1 4900 |
| 0 | C 12 | 59.29.4472 | | 4m7 | EL 35V RM10 radial 105 | • | 100 | 00.00.0045 | | 00.00 | 404 Tarrid Obasha |
| 0 | C 13 | 59.06.0103 | | 10n | PETP, 63V, 10%, RM5 | 0 | LI | 62.03.0045 | | 800H | TUA Torold Chocke |
| ٥ | C 14 | 59 34 4221 | | 220n | CER 63V 5% N750 | 0 | L 2 | 62.03.0015 | | 72uH | 2A Toroid Chocke |
| Š | 0.14 | 50.04.4221 | | 10. | | 0 | L 3 | 62.03.0035 | | 180uH | 3A Toroid Chocke |
| U | C 15 | 59.06.0103 | | 100 | PETP, 63V, 10%, RM5 | 0 | 1.4 | 62 03 0035 | | 180uH | 34 Toroid Chocke |
| 0 | C 16 | 59.29.0422 | | 22m | EL 35V RM10 radial | 0 | | 02.00.0000 | | 100011 | |
| Ū | G 17 | 39.22.0470 | | 47u | EL 40V 20% RM3 | 0 | L 5 | 62.03.0035 | | 1800H | 3A Toroid Chocke |
| 0 | C 18 | 59.22.4102 | | 1m0 | EL 16V 20% RM5 | 0 | L 6 | 62.03.0035 | | 180uH | 3A Toroid Chocke |
| 0 | C 10 | 50 22 4102 | | 1m0 | EI 16V 20% PM5 | 0 | L7 | 62.03.0015 | | 72uH | 2A Toroid Chocke |
| 0 | 0.13 | 50.00.4400 | | 10 | | 0 | L 8 | 62.03.0015 | | 72uH | 2A Toroid Chocke |
| 0 | C 20 | 59.22.4102 | | 1m0 | EL 16V 20% RM5 | 0 | 1.0 | 62 03 0015 | | 7204 | 2A Toroid Chocke |
| 0 | C 21 | 59.22.6470 | | 47u | EL 40V 20% RM5 | 0 | L 3 | 02.03.0013 | | 72011 | |
| 0 | C 22 | 59.06.0104 | | 100n | PETP. 63V. 10%. RM5 | 0 | L 10 | 62.03.0015 | | 72uH | 2A Toroid Chocke |
| 0 | C 23 | 59.06.0104 | | 100n | PETP, 63V, 10%, RM5 | 0 | MP 1 | 1.942.105.12 | | | POWER SUPPLY PCB |
| 0 | C 24 | 50.06.0333 | | 330 | DETD 631/ 10% PM5 | 0 | MP 2 | 1.942.105.01 | | | Kühlkörper |
| | 0.24 | 38.00.0333 | | | FETF, 03V, 10%, KWS | 0 | MP 3 | 1.942.105.02 | | | Kühlkörper |
| 0 | C 25 | 59.06.5152 | | 105 | PETP, 63V, 5%, RM5 | 0 | | 21 38 0354 | a nee | M3*6 | Z - Schraube KS A2 blank |
| 0 | C 26 | 59.06.0333 | | 33n | PETP, 63V, 10%, RM5 | 0 | 1011 4 | 21.30.0334 | | 1415 0 | |
| 0 | C 27 | 59.06.5152 | | 1n5 | PETP, 63V, 5%, RM5 | 0 | MP 5 | 21.53.9354 | 4 pcs | M3*6 | Z-Schraube Inbus-Ripp Zn gb ch |
| 0 | C 28 | 59.06.0333 | | 33n | PETP 63V 10% RM5 | ٥ | MP 6 | 50.20.2003 | 8 pec | | Montageelip zu TO 220, N/ISOL. |
| Å | 0 20 | E0.08.E4E2 | | 105 | DETD 63V EN DME | 0 | MP 7 | 50.20.2005 | | | Montageclip zu SOT 93 |
| 0 | C 29 | 59.00.5152 | | CITI | PETP, 03V, 5%, RM5 | 0 | MP 8 | 50 20 0318 | 5 nce | TO220 | Glimmerscheibe zu Clin |
| 0 | C 30 | 59.06.0333 | | 33n | PETP, 63V, 10%, RM5 | 0 | 100 | 10120.0010 | p p co | 10220 | |
| 0 | C 31 | 59.06.5152 | | 1n5 | PETP, 63V, 5%, RM5 | 0 | MP 9 | 1.942.105.10 | | | NR.ETIKETTE 5X20 |
| ٥ | C 32 | 50 34 4221 | | 220n | CER 63V 5% N750 | 0 | MP 10 | 43.01.0108 | | Label | ESE-WARNSCHILD |
| | 0.52 | 39.34.4221 | | 2200 | CER 030, 5%, 14750 | 1 | MP 11 | 43.10.0110 | | Α | Revisions-Etikette 5mm h'blau |
| 0 | C 33 | 60.22.6100 | | 100 | EL 36V 20% RM6 | 0 | P 1 | 54 12 0506 | | 6n | Power-Pin Stecker |
| 0 | C 34 | 59.34.4221 | | 220p | CER 63V, 5%, N750 | ů o | | 54.40.0540 | | 40- | Bauras Bia Stanling |
| 0 | C 35 | 59.34.4221 | | 220p | CER 63V, 5%, N750 | 0 | P 2 | 54.12.0510 | | тор | Power-Pin Stecker |
| 1 | C 36 | 59 22 6220 | | 2211 | EI 35V 20% BM5 | 0 | P 3 | 54.12.0505 | | 5p | Power-Pin Stecker |
| | 0.00 | 50 34 4224 | | 220n | | 0 | P 4 | 54.12.0506 | | 6p | Power-Pin Stecker |
| 0 | 0.37 | 39.34.4221 | | 220p | CER 03V, 5%, 11750 | 0 | P 5 | 54 12 0504 | | 4n | Power-Pin Stecker |
| 0 | C 38 | 59.34.4221 | | 220p | CER 63V, 5%, N750 | ň | 80 | 54.12.0503 | | 30 | Rower-Bin Stecker |
| 0 | C 39 | 59.22.6100 | | 10u | EL 35V 20% RM5 | | | 54.12.0000 | | op DT (oo | |
| 0 | C 40 | 59.34.4221 | | 220p | CER 63V, 5%, N750 | 0 | Q1 | 50.99.0106 | | B1138 | TRIAC 400V, 8A |
| ٥ | C 41 | 59 34 4221 | | 220n | CER 63V 5% N750 | 0 | Q 2 | 50.03.0340 | | BC337-25 | 800mA, 45V, NPN |
| ž | 0.40 | 50.00.6000 | | 200 | | 0 | Q 3 | 50.03.0515 | | BC307B | BC 307 B , BC 557 B , PNP |
| 1 | C 42 | 59.22.6220 | | 220 | EL 35V 20% RM5 | 0 | 04 | 50.03.0436 | | BC237B | BC 237 B 547 B 550 B |
| 0 | C 43 | 59.34.4221 | | 220p | CER 63V, 5%, N750 | 0 | 0.4 | 50.05.0450 | | DC237D | DC 237 D, 347 D, 330 D, |
| v | C 44 | 39.99.1700 | | 1110 | EL 33V 20% RM3 | 0 | 0.5 | 50.03.0436 | | BC237B | BC 237 B. 547 B. 550 B. |
| 0 | C 45 | 59 99 1708 | | 1m0 | EL 35V 20% RM5 | 0 | Q 6 | 50.03.0436 | | BC237B | BC 237 B, 547 B, 550 B, |
| ~ | 0.40 | 50.00 1709 | | 10 | | 0 | Q 7 | 50.03.0436 | | BC237B | BC 237 B, 547 B, 550 B, |
| 0 | C 40 | 59.99.1706 | | mo | EL 35V 20% RIVIS | 0 | 0.8 | 50.03.0436 | | BC237B | BC 237 B 547 B 550 B |
| 0 | C 47 | 59.99.1708 | | 1m0 | EL 35V 20% RM5 | | 00 | 50.00.0400 | | DOLOTO | DO 207 D, 047 D, 000 D, |
| 0 | C 48 | 59.99.1708 | | 1m0 | EL 35V 20% RM5 | 0 | 0.8 | 50.03.0436 | | BC237B | BC 237 B, 547 B, 550 B, |
| 0 | C 49 | 59 22 5101 | | 1000 | EI 25V 20% BM5 | 0 | Q 10 | 50.03.0436 | | BC237B | BC 237 B, 547 B, 550 B, |
| õ | C 50 | 50 00 1709 | | 1m0 | EL 251/ 2004 PM5 | 0 | Q 11 | 50.03.0436 | | BC237B | BC 237 B, 547 B, 550 B, |
| | 0.50 | 38.88.1700 | | 1110 | | 0 | R 1 | 57 56 5220 | | 22R | WW. 10% 4 W |
| 0 | C 51 | 59.22.5101 | | 100u | EL 25V 20% RM5 | 0 | D 2 | 67 11 2160 | | 150 | ME 18/ 0207 |
| 0 | C 52 | 59.99.1708 | | 1m0 | EL 35V 20% RM5 | 0 | R 2 | 57.11.3150 | | 105 | WF, 1%, 0207 |
| 0 | C 53 | 59.22.5101 | | 100u | EL 25V 20% RM5 | 0 | R 3 | 57.92.7013 | | 0.5A | PTC 60V |
| ٥ | C 54 | 59 99 1708 | | 1m0 | EL 351/ 20% RM5 | 0 | R 4 | 57.11.3222 | | 2k2 | MF, 1%, 0207 |
| õ | 0.55 | 50 00 5404 | | 1000 | | 0 | R 5 | 57.11.3333 | | 33k | MF, 1%, 0207 |
| | 0.00 | 38.22.3101 | | 1000 | | 0 | R 6 | 57.11.3682 | | 6k8 | MF. 1% 0207 |
| 0 | C 56 | 59.22.5101 | | 100u | EL 25V 20% RM5 | 0 | D 7 | 57 11 2152 | | 154 | ME 19/ 0207 |
| 0 | C 57 | 59.22.5101 | | 100u | EL 25V 20% RM5 | 0 | R/ | 57.11.3155 | | 106 | MF, 1%, 0207 |
| 0 | C 58 | 59.22.5101 | | 100u | EL 25V 20% RM5 | 0 | ĸв | 57.11.3221 | | 220R | MF, 1%, 0207 |
| 0 | C 59 | 59 22 5101 | | 1000 | EI 251/ 20% RM5 | 0 | R 9 | 57.11.3332 | | 3k3 | MF, 1%, 0207 |
| č | C 60 | 50.06.0104 | | 1000 | DETD 631/ 10% DM5 | 0 | R 10 | 57.11.3222 | | 2k2 | MF, 1%, 0207 |
| ~ | 0.00 | 50.00.0104 | | 1000 | DETD 631/ 409/ DMS | 0 | R 11 | 57.11.3472 | | 4k7 | MF, 1%, 0207 |
| U | 001 | J9.00.0104 | | 1000 | FEIF, 03V, 10%, RM5 | ^ | R 12 | 57 11 3473 | | 4k7 | ME 1% 0207 |
| 0 | C 62 | 59.06.0104 | | 100n | PETP, 63V, 10%, RM5 | | D 40 | 67.11.04/Z | | | ME 48/ 0007 |
| 0 | C 63 | 59.06.0102 | | 1n0 | PETP, 63V, 10%, RM5 | 0 | rt 13 | 57.11.3333 | | JJK | IVIF, 1%, U2U/ |
| 0 | D 1 | 50 04 0516 | | USD945 | Schottky Rect 16A 45V | 0 | R 14 | 57.11.3102 | | 1k0 | MF, 1%, 0207 |
| n n | D 2 | 50.04.0127 | | BAT85 | 200mA Schottky | 0 | R 15 | 57.11.3222 | | 2k2 | MF, 1%, 0207 |
| 0 | 52 | 50.04.012/ | | DATOS | 200mA, Ochulky | 0 | R 16 | 57,11.3682 | | 6k8 | MF. 1%.0207 |
| 0 | 0.3 | 50.04.0127 | | BA185 | ZuomA, Schottky | ~ | P 17 | 57 11 2222 | | 332 | ME 1% 0207 |
| 0 | D 4 | 50.04.0125 | | 1N4448 | 75V, 150mA, 4ns, DO-35 | U . | 0.17 D.42 | 57.11.3333 | | 60N | AN , 1/0, 0207 |
| 0 | D 5 | 50.04.0519 | | 1N5822 | 3A, Schottky | 0 | K 18 | 57.11.3472 | | 4K/ | MF, 1%, 0207 |
| 0 | DB | 50 04 0519 | | 1N5822 | 34 Schottky | 0 | R 19 | 57.11.3682 | | 6k8 | MF, 1%, 0207 |
| Š | 57 | 50.04.0010 | | 1140022 | ZEV (ASO A A A BO AS | 0 | R 20 | 57.11.3154 | | 150k | MF. 1%. 0207 |
| U | 07 | 50.04.0125 | | 1N4448 | 75V, 150MA, 4NS, DO-35 | 0 | P 21 | 57 11 3471 | | 470P | ME 194 0207 |
| 0 | D 8 | 50.04.0125 | | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | D 22 | 57 44 0400 | | 140 | ME 19/ 0207 |
| 0 | D 9 | 50.04.0125 | | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | r 22 | 57.11.3102 | | IKU | IVIF, 1%, U2U7 |
| 0 | D 10 | 50.04.0125 | | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | R 23 | 57.11.3153 | | 15k | MF, 1%, 0207 |
| õ | D 11 | 50.04.0125 | | 1N4448 | 75V 150mA 4ne DO 35 | 0 | R 24 | 57.11.3123 | | 12k | MF, 1%, 0207 |
| | 5 11 | 00.04.0120 | | | 75V, 15011A, 418, DU-35 | n | R 25 | 57 11 3332 | | 3k3 | MF. 1%. 0207 |
| 0 | D 12 | 50.04.0125 | | 1 N4448 | 75V, 150mA, 4ns, DO-35 | ~ | R 26 | 57 14 90002 | | 668 | ME 1% 0207 |
| 0 | D 13 | 50.04.0125 | | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | 11 20 | 57.11.3062 | | UND | NI , 176, 0207 |
| 0 | D 14 | 50.04.0125 | | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | R 27 | 57.11.3153 | | 15K | MF, 1%, 0207 |
| 0 | D 15 | 50 04 0125 | | 1N4448 | 75V 150mA 4ne DO-35 | 0 | R 28 | 57.11.3473 | | 47k | MF, 1%, 0207 |
| ~ | D 10 | 50.04.0125 | | 414440 | 75V 450mA 4m DO 05 | 0 | R 29 | 57.11.3104 | | 100k | MF, 1%, 0207 |
| 0 | 010 | 50.04.0125 | | 1114448 | 10V, 150MA, 4NS, DO-35 | 0 | R 30 | 57 11 9109 | | 12k | ME 1% 0207 |
| 0 | D 17 | 50.04.0125 | | 1N4448 | 75V, 150mA, 4ns, DO-35 | - | N 00 | 57.11.3123 | | 141 | 170,0207 |
| 0 | D 18 | 50.04.0125 | | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | R 31 | 57.11.3332 | | 3K3 | M⊨, 1%,0207 |
| 0 | D 19 | 50.04.0519 | | 1N5822 | 3A, Schottky | 0 | R 32 | 57.11.3682 | | 6k8 | MF, 1%, 0207 |
| 0 | D 20 | 50 04 0526 | | MBR 360 | D MBR 360 SB 360 31 DO 06 | 0 | R 33 | 57.11.3153 | | 15k | MF, 1%, 0207 |
| <u>,</u> | 5 20 | 50.04.0520 | | | | n | R 34 | 57.11 3473 | | 47k | MF. 1%. 0207 |
| 0 | U 21 | 50.04.0519 | | 1115822 | JA, SCHOTTKY | 0 | R 35 | 57 11 9104 | | 100k | ME 1% 0207 |
| 0 | D 22 | 50.04.0526 | | MBR360 | D MBR 360, SB 360, 31 DQ 06, | - | N 00 | 57.11.3104 | | 100 | NII, 170, 0207 |
| 0 | D 23 | not used | | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | R 36 | 57.11.3123 | | 12K | MF, 1%, 0207 |
| 0 | DV 1 | 50 04 1112 | | 5\/1 | Zener 5% 0.5W DO-35 | 0 | R 37 | 57.11.3332 | | 3k3 | MF, 1%, 0207 |

Description

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POWER SUPPLY 1.942.605.81 (1)

| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|------|------------|------|-----------|-------------------------------|
| 0 | R 38 | 57.11.3682 | | 6k8 | MF, 1%, 0207 |
| 0 | R 39 | 57.11.3153 | | 15k | MF, 1%, 0207 |
| 0 | R 40 | 57.11.3473 | | 47k | MF, 1%, 0207 |
| 0 | R 41 | 57.11.3104 | | 100k | MF, 1%, 0207 |
| 0 | R 42 | 57.11.3123 | | 12k | MF, 1%, 0207 |
| 0 | R 43 | 57.11.3332 | | 3k3 | MF, 1%, 0207 |
| 0 | R 44 | 57.11.3682 | | 6k8 | MF, 1%, 0207 |
| 0 | R 45 | 57.11.3473 | | 47k | MF, 1%, 0207 |
| 0 | R 46 | 57.11.3104 | | 100k | MF, 1%, 0207 |
| 0 | R 47 | 57.11.3101 | | 100R | MF, 1%, 0207 |
| 0 | R 48 | 57.11.3103 | | 10k | MF, 1%, 0207 |
| Ū | R 49 | 37.11.3104 | | IOOK | MF, 1%, 0207 |
| 0 | R 50 | 57.11.3101 | | 100R | MF, 1%, 0207 |
| 0 | R 51 | 57.11.3103 | | 10k | MF, 1%, 0207 |
| 0 | R 52 | 57.11.3104 | | 100k | MF, 1%, 0207 |
| 0 | R 53 | 57.11.3101 | | 100R | MF, 1%, 0207 |
| 0 | R 54 | 57.11.3103 | | 10k | MF. 1%.0207 |
| 0 | R 55 | 57.11.3104 | | 100k | MF, 1%, 0207 |
| 0 | R 56 | 57.11.3101 | | 100R | MF, 1%, 0207 |
| 0 | R 57 | 57.11.3103 | | 10k | MF, 1%, 0207 |
| 0 | R 58 | 57.11.3104 | | 100k | MF, 1%, 0207 |
| 0 | R 59 | not used | | 3k3 | MF, 1%, 0207 |
| 0 | RA 1 | 58.01.8102 | | 1k0 | Cermet, 10%, 0.5W, horizontal |

End of List

(01) C36, C42: 10uF->22uF; Revision label->"A" added

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1.942.100.20

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PAGE 2/4 SC 1.942.100.20 STUDER Controller Board



PAGE

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STUDER

Controller Board 1.942.601.20

Dete



|) 19.12.96/LC | 0 0 | 0 | 0 | |
|---------------|------------|---------|----------|--------|
| | (n-A | ir | PAGE | 4/4 |
| STUDER | Controller | Board S | C 1.942. | 100.20 |



CONTROLLER BOARD 1.942.600.21 (0)

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| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx. | F | 'os. | Part No. | Qty. | Type/Val. | Description |
|------|------------|------------|------|--------------|--|------|----|---------------------|---------------|------|---------------------|--------------------------------|
| | | | | | | 0 | 10 | 3.1 | 50 62 1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 1 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | ~ 2 | 50.63.4203 | | FPI D8282 | EPI D 2500 PI CC84 |
| 0 | C 2 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | 23 | 50 63 1503 | | 62256 | SRAM 32K*8 100ns |
| 0 | C 3 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | C 4 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 4 | 59.60.2369 | | 680p | CER 50V, 5%, C0G, 0805 | 0 | 10 | 2.5 | 50.63.4203 | | EPLD8282 | EPLD 2500 PLCC84 |
| 0 | C 5 | 59.60.2369 | | 680p | CER 50V, 5%, C0G, 0805 | 0 | 10 | C 6 | 50 63 1503 | | 62256 | SBAM 32K*8, 100ns |
| 0 | C 6 C 7 | 50 60 3537 | | 100n | CER 50V, 10%, X7R, 1210 | ō | 10 | 27 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | 01 | 59.00.3537 | | 1000 | CER 50V, 10%, X7R, 1210 | 0 | 10 | C 8 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| 0 | 0 | 59.00.3537 | | 1000 | CER 50V, 10%, X7R, 1210 | 0 | 10 | C 9 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 40 | 59.00.3537 | | 1000 | CER 50V, 10%, X/R, 1210 | 0 | 10 | C 10 | 50.63.4203 | | EPLD8282 | EPLD 2500 PLCC84 |
| 0 | 0.11 | 59.00.3537 | | 1001 | CER 50V, 10%, X/R, 1210 | 0 | 10 | C 11 | 50.63.1503 | | 62256 | SRAM 32K*8, 100ns |
| 0 | C 12 | 59 22 6100 | | 1001 | EL 35V 20% RM5 | 0 | ĸ | C 12 | 50.11.0157 | | TL7705D | IC TL 7705 DCP, |
| ő | C 13 | 59 60 2473 | | 10 | CER 50V 5% C0G 1206 | 0 | 10 | C 13 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 14 | 59 22 3470 | | 470 | EI 10V 20% BM5 | 0 | I | C 14 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| õ | C 15 | 59.60.3537 | | 100n | CER 50V. 10%, X7R, 1210 | 0 | I | C 15 | 50.62.1273 | | 74HC273 | Octal D-FF with reset |
| 0 | C 16 | 59 60 3537 | | 100n | CER 50V 10% X7R 1210 | 0 | 10 | C 16 | 50.62.1273 | | 74HC273 | Octal D-FF with reset |
| 0 | 0 17 | 50.22.0470 | | 4u7 | EL 50V 20% RM5 | 0 | 10 | C 17 | 50.62.1273 | | 74HC273 | Octal D-FF with reset |
| 0 | C 18 | 59.22.8479 | | 4u7 | EL 50V 20% RM5 | 0 | 10 | C _, 18 1 | 1.942.926.20 | | | SW.100 CONTROLLER BOARD |
| 0 | C 19 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | | - 10 | 50 15 0120 | | 50634201 | IC MAX 222 CDE |
| 0 | C 20 | 59.22.8479 | | 4u7 | EL 50V 20% RM5 | 0 | 10 | 2 20 | 50.15.0120 | | MAX232 | IC MAX 232 CPE |
| 0 | C 21 | 59.22.8479 | | 4u7 | EL 50V 20% RM5 | 0 | 10 | 20 | 50.02.1004 | | 7410 04 | Hex High to Low Lovel Shifter |
| 0 | C 22 | 59.22.8479 | | 4u7 | EL 50V 20% RM5 | 0 | | 221 | 50.02.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| 0 | C 23 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | Ő | 10 | 223 1 | 942 900 29 | | 111 02 | SW 100 CONTROLLER BOARD |
| 0 | C 24 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | • | | 520 | .042.000.20 | | 50631301 | SHITTO CONTROLLER BOARD |
| 0 | C 25 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | C 24 1 | .942.900.29 | | 00007007 | SW.100 CONTROLLER BOARD |
| 0 | C 26 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | | | | | | 50631301 | |
| 0 | C 27 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | C 25 | 50.63.0201 | | 68EN360 | Communication Controller |
| υ | C 28 | 59.60.3537 | | 100n | CER 50V, 10%, X/R, 1210 | 0 | к | 5 20 1 | .942.900.29 | | | 3W.100 CONTROLLER BOARD |
| 0 | C 29 | 59.60.2473 | | 1n | CER 50V, 5%, C0G, 1206 | 0 | 10 | - 27 | 50 16 0901 | | 50631301 DS12887 | Real Time Clock |
| 0 | C 30 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | 227 | 942 900 29 | | 0312007 | SW 100 CONTROLLER BOARD |
| 0 | C 31 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | v | | 120 | .042.000.20 | | 50631301 | SWITTO CONTROLLER BOARD |
| 0 | C 32 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | 29 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| 0 | C 33 | 59 60 3743 | | 330n | CFR 50V, 10%, X7R, 1812 | 0 | ю | 5.30 1 | .942.904.20 | | | SW. 100 CONTROLLER BOARD |
| 0 | C 34 | 59.60.3325 | | 10n | CER 50V, 10%, X7R, 0805 | | | | | | 50180103 | |
| 0 | C 35 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | 231 1 | .942.905.20 | | | SW.100 CONTROLLER BOARD |
| 0 | C 36 | 59.60.3537 | | 100n | CER 50V, 10%, X/R, 1210 | 0 | 10 | - 90 | 50 62 1201 | | 50180103 | Flash Momony 512K*9 |
| 0 | C 37 | 59.60.2329 | | 15p | CER 50V, 5%, COG, 0805 | 0 | 10 | - 33 | 50.62.1050 | | 29F04050 | Hex High-to-I ow Level Shifter |
| 0 | C 38 | 59.60.2329 | | 15p 100p | CER 50V, 5%, COG, 0805 CER 50V 10% X7R 1210 | υ | 10 | J 34 | 50.62.12/3 | | /4HC2/3 | Octal D-FF with reset |
| 0 | C 40 | 50 60 3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | 2 35 | 50.62.1245 | | 74HC245 | Octal bus transceiver |
| 0 | C 40 | 50 60 3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | 2 36 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 41 | 50.60.2527 | | 1000 | CER 50V, 10%, X/R, 1210 | 0 | 10 | 37 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 42 | 50 60 3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 | 2 38 | 50.62.1245 | | 74HC245 | Octal bus transceiver |
| 0 | C 44 | 59.00.0507 | | 1000 | CER 50V, 10%, X7R, 1210 | Ó | 10 | 30 | 50.62.1273 | | 74HC273 | Octal D-FF with reset |
| 0 | C 45 | 59.60.3325 | | 10n | CER 50V. 10%, X7R, 0805 | 0 | 10 | C 40 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| 0 | C 46 | 59.60.3325 | | 10n | CER 50V. 10%, X7R, 0805 | 0 | IC | C 41 | 50.62.1273 | | 74HC273 | Octal D-FF with reset |
| 0 | C 47 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | ю | 2 42 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 48 | 59.22.8101 | | 100u | EL 63V 20% RM5 | 0 | IC | 2 4 3 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| 0 | C 49 | 59.22.8221 | | 220u | EL 63V 20% RM5 | 0 | IC | 2 4 4 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 50 | 59.22.6221 | | 220u | EL 40V 20% RM5 | 0 | ю | 2 45 | 50.62.1273 | | 74HC273 | Octal D-FF with reset |
| 0 | C 51 | 59.22.6101 | | 100u | EL 40V 20% RM5 | 0 | ю | 246 | 50.62.1032 | | 74HC 32 | Quad 2input OR |
| 0 | C 52 | 59.22.3471 | | 470u | EL 10V 20% RM5 | 0 | ю | C 47 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 53 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | ю | 2 48 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 54 | 59.22.4471 | | 470u | EL 16V 20% RM5 | 0 | ю | 2 49 | 50.10.0118 | | L4962 | IC L 4962 E, |
| 0 | C 55 | 59.60.3531 | | 33n | CER 50V, 10%, X7R, 1210 | 0 | L | 1 | 62.02.3100 | | 10uH | 10%, radial RM 5 |
| 0 | C 56 | 59.22.6471 | | 470u | EL 40V 20% RM5 | 0 | L | 2 | 62.02.3100 | | 10uH | 10%, radial RM 5 |
| 0 | C 57 | 59.60.3315 | | 1n5 | CER 50V, 10%, X7R, 0805 | 0 | M | P1 1 | .942.100.11 | | | CONTROLLER BOARD PCB |
| 0 | C 58 | 59.60.2357 | | 220p | CER 50V, 5%, C0G, 0805 | 0 | M | IP2 1 | .942.600.10 | | | NR.ETIKETTE 5X20 |
| 0 | C 59 | 59.60.2357 | | 220p | CER 50V, 5%, C0G, 0805 | 0 | M | IP 3 | 43.01.0108 | | Label | ESE-WARNSCHILD |
| 0 | C 60 | 59.22.5221 | | 220u | EL 25V 20% RM5 | 0 | M | IP 4 | 21.53.0354 | | M3*6 | Z-Schraube Inbus Zn gb chr |
| 0 | C 61 | 59.60.2357 | | 220p | CER 50V, 5%, C0G, 0805 | 0 | | | 24.10.1030 | | 3.2/5.5 | Rippenscheibe |
| 0 | C 62 | 59.22.3470 | | 47u | EL 10V 20% RM5 | 0 | M | 1P0 1D7 1 | 22.01.0030 | | NI3 | UNITERIECH INCK SOCKET |
| 0 | 0.63 | 59.22.5221 | | 2200 | EL 25V 20% RM5 | 0 | M | 17 1 12 8 | 80 01 1400 | | | |
| 0 | C 64 | 59.00.3537 | | 100n | CER 50V, 10%, X/R, 1210 | 0 | M | IP9 1 | 010 002 61 | | | UNTERLAGE ZU 61 01 0281 |
| 0 | C 66 | 50 22 3101 | | 1000 | EL 10V 20% RM5 | ů | м | IP 10 | 21.01.0205 2 | DCS | M2*8 | Z - Schraube Zn gb chr |
| 0 | C 67 | 59 60 3537 | | 1000 100n | CER 50V 10% X7R 1210 | 0 | м | IP 11 | 24.16.2020 2 | DCS | 2.2/4.5 | Fächerscheibe Form A |
| õ | C 68 | 59 60 3537 | | 100n | CER 50V 10% X7R 1210 | 0 | м | IP 12 | 50.03.9934 | • | TO220 | Kühlkörper |
| õ | D 1 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | Μ | IP 13 | 22.01.8020 2 | pcs | M2 | 6kt-Mutter 0.8d St gb |
| 0 | D2 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | м | IP 14 | not used 2 | pcs | M2*8 | Z - Schraube Zn gb chr |
| 0 | D 3 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | м | IP 15 | not used 2 | pcs | 2.2/4.5 | Fächerscheibe Form A |
| 0 | D 4 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | Μ | IP 16 | not used 2 | pcs | M2 | 6kt-Mutter 0.8d St gb |
| 0 | D 5 | 50.04.0138 | | UF4004 | D BYT 01-400, UF 4004 | 0 | Μ | IP 17 1 | .101.001.20 | | Label | TEXT-ETIK. 5*20 HARDWARE -20 |
| 0 | D 6 | 50.04.0512 | | 1N5818 | D 1N 5818, 1N 5819, | 0 | Ρ | 1 | 54.12.0704 | | 4p | Stecker gerade PCB |
| 0 | D 7 | 50.04.0527 | | MBR160 | MBR 160, SB 160, 11 DQ 06, | 0 | Ρ | 2 | 54.24.0123 | | | J JACK-SOCKET, 6.3MM, PCB |
| 0 | D 8 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | Ρ | 3 | 54.14.2102 | | 16p | 1/20" Au, gerade, Verrieg |
| 0 | D 9 | 50.60.8101 | | BAS85 | 200mA 30V Schottky SOD 80 | 0 | Ρ | 4 | 54.14.2102 | | 16p | 1/20" Au, gerade, Verrieg |
| 0 | D 10 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | Ρ | 5 | 54.14.2102 | | 16p | 1/20" Au, gerade, Verrieg |
| 0 | DL 1 | 50.04.2133 | | TLUY 2401 | DL TLUY 2401 GB MATT | 0 | P | 6 | not used | | 10p | 1/20" Au, gerade, Verrieg |
| 0 | DL 2 | 50.04.2133 | | TLUY 2401 | DL TLUY 2401 GB MATT | 0 | P | 7 | 54.14.2103 | | 20p | 1/20" Au, gerade, Verrieg |
| 0 | DL 3 | 50.04.2132 | | TLUG 2401 | DL TLUG 2401 GN MATT | 0 | P | 8 | 54.14.2102 | | 16p | 1/20" Au, gerade, Verrieg |
| 0 | DL 4 | 50.04.2133 | | TLUY 2401 | DL TLUY 2401 GB MATT | 0 | P | 9 | 54.14.2102 | | 16p | 1/20" Au, gerade, Verrieg |
| 0 | DL 5 | 50.04.2133 | | TLUY 2401 | DL TLUY 2401 GB MATT | U | 2 | 10 | 54.14.2102 | | 100 | 1/20 Au, gerade, Verrieg |
| 0 | DL 6 | 50.04.2133 | | ILUY 2401 | DL ILUY 2401 GB MATT | 0 | 2 | 11 | 54.14.2102 | | 10p | 1/20" Au, gerade, Verrieg |
| 0 | | 50.04.2750 | | rea | LED mit Halter | 0 | P | 12 | 54 14 2101 | | 100 | 1/20 Au, gerade, verneg |
| 0 | | 50.04.2750 | | rea EV/4 | LED mit Halter | 0 | P | 13 | 54 14 2101 | | 10p | 1/20" Au, gerade, verrieg |
| U | UV 1 | JU.04.1112 | | UV I | Zener, 5%, 0.5W, DU-35 | 0 | | | - T. 17.4 IVI | | · ~ P | www.ww.gorado, vollicy |

CONTROLLER BOARD 1.942.600.21 (0)

Page: 2 of 2

| ldx. Pos. | Part No. | Qty. | Type/Val. | Description | ldx. | . P | 08. | Part No. | Qty. | Type/Val. | Description |
|-----------|------------|------|--------------|-----------------------------|--------|-----|---------|-------------|------|-------------|--------------------------------------|
| 0 P 15 | 54.14.2101 | | 10p | 1/20" Au, gerade, Verrieg | 0 | R | 79 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 P 16 | 54.14.2056 | | 64p | Stecker gerade Au | 0 | R | 80 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 |
| 0 P 17 | 54.99.0347 | | 68p | 1 slot PCMCIA-III connector | 0 | R | 81 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 P 18 | 54.14.2102 | | 16p | 1/20" Au, gerade, Verrieg | 0 | R | 82 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 P 19 | not used | | 10p | Power-Pin Stecker | 0 | R | 83 | 57.60.1153 | | 15k | MF, 1%, 0204, E24 |
| 0 Q 1 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R | 84 | 57.60.1123 | | 12k | MF, 1%, 0204, E24 |
| 0 Q 2 | 50.60.0001 | | BC847B | NPN 45V 100mA SOT 23 | 0 | R | 85 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 Q 3 | 50.60.0001 | | BC847B | NPN 45V 100mA SOT 23 | 0 | R | 86 | 57.60.1681 | | 680R | MF, 1%, 0204, E24 |
| 0 Q 4 | 50.60.1001 | | BC857B | PNP 45V 100mA SOT 23 | 0 | R | 87 | 57.60.1202 | | 2k0 | MF, 1%, 0204, E24 |
| 0 R1 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | 0 | R | 88 | 57.60.1473 | | 47k | MF, 1%, 0204, E24 |
| 0 82 | 57.60.1470 | | 4/R | MF, 1%, 0204, E24 | 0 | R | 89 | 57.60.1472 | | 4K/ | MF, 1%, 0204, E24 |
| 0 R4 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | ő | R | 91 | 57.60.1102 | | 1k0 | MF. 1%, 0204, E24 |
| 0 R5 | 57.60.1101 | | 100R | MF. 1%, 0204, E24 | 0 | R | 92 | 57.60.1104 | | 100k | MF. 1%, 0204, E24 |
| 0 R6 | 57.60.1101 | | 100R | MF. 1%, 0204, E24 | 0 | R | 93 | 57.60.1103 | | 10k | MF. 1%, 0204, E24 |
| 0 R7 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | 94 | 57.60.1473 | | 47k | MF, 1%, 0204, E24 |
| 0 R 8 | 57.60.1101 | | 100R | MF. 1%. 0204. E24 | 0 | R | 95 | 57.60.1101 | | 100R | MF. 1%. 0204. E24 |
| 0 R 9 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | 96 | 57.60.1473 | | 47k | MF, 1%, 0204, E24 |
| 0 R 10 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | 97 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 R 11 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | 98 | 57.92.7015 | | 1.1A | PTC 50V |
| 0 R 12 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | 99 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 |
| 0 R 13 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | 100 | 57.92.7015 | | 1.1A | PIC 50V 8*P Perinter Netw 2% SIP9 |
| 0 R 15 | 57 60 1101 | | 100R | MF 1% 0204 E24 | 0 | R | 72 | 57 88 4472 | | 4k7 | 8*R Resistor-Netw 2% SIP9 |
| 0 R 16 | 57 60 1101 | | 100R | MF. 1%, 0204, E24 | 0 | R | Z 3 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 R 17 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | Z 4 | 57.88.4472 | | 4k7 | 8*R Resistor-Netw 2% SIP9 |
| 0 R 18 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | ů 0 | R | Z 5 | 57.88.4223 | | 22k | 8*R Resistor-Netw 2% SIP9 |
| 0 R 10 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | Z 6 | 57.99.4103 | | 10k | 8*P. Resistor-Netw 2% SIP0 |
| 0 R 20 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | Z 7 | 57.88.4223 | | 22k | 8*R Resistor-Netw 2% SIP9 |
| 0 R 21 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | Z 8 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 R 22 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 | 0 | R | Z 9 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 R 23 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | Z 10 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 R 24 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R | Z 11 | 57.88.4223 | | 22k | 8*R Resistor-Netw 2% SIP9 |
| 0 R 25 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R4 | 2 12 | 57.88.4223 | | 22K | 8"R Resistor-Netw 2% SIP9 |
| 0 R 26 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R. | 2 13 | 57.88.4103 | | 10k | 8"R Resistor-Netw 2% SIP9 |
| 0 8 27 | 57.00.1101 | | 100R | MF, 1%, 0204, E24 | 0 | R4 | 2 14 | 57.88.4103 | | 10K | 8"R Resistor-Netw 2% SIP9 |
| 0 8 29 | 57 60 1471 | | 470R | MF 1% 0204 E24 | 0 | R | 7 16 | 57 88 4103 | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 11 20 | 37.00.1471 | | 470 | MF, 176, 0204, E24 | 0 | 3 | 1 | 33.13.0130 | | I'A | S I TASTE, I'A,IMPULS, I.O N |
| 0 R 31 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | т | 1 1 | .022.655.00 | | | 48V ON AIR 2000 TRAFO |
| 0 R 32 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | TF | P 1 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 R 33 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | TF | 2 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 R 34 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | TF | ° 3 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 R 35 | 57.60.1330 | | 33R | MF. 1%. 0204. E24 | 0 | TF | 94 | 54.02.0320 | | 1D | PCB-Flachst 2.8*0.8. gerade |
| 0 R 36 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | XI | C 18 | 53.03.2244 | | 44p | PLCC-Socket |
| 0 R 37 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | XI | C 19 | 53.03.0168 | | 16р 70р | DIL 0.3", IOT, gerade |
| 0 8 30 | 57.60.1103 | | 1000 | MF, 1%, 0204, E24 | 0 | | C 22 | 52 02 2222 | | 72p | Silvivi-Socket /2p |
| 0 R 40 | 57 60 1101 | | 100R | MF 1% 0204 E24 | 0 | XI | C 24 | 53 03 2232 | | 32p | PLCC-Socket |
| 0 R 41 | 57.60.1101 | | 100R | MF. 1%, 0204, E24 | 0 0 | XI | C 26 | 53.03.2232 | | 32p | PLCC-Socket |
| 0 R 42 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | XI | C 27 | 53.03.0169 | | 24p | DIL 0.6", löt, gerade |
| 0 R 43 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | XI | C 28 | 53.03.2232 | | 32p | PLCC-Socket |
| 0 R 44 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | XI | C 30 | 53.03.0165 | | 20p | DIL 0.3", löt, gerade |
| 0 R 45 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | XI | C 31 | 53.03.0165 | | 20p | DIL 0.3", löt, gerade |
| 0 R 46 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | XI | C 32 | 53.03.2232 | | 32p | PLCC-Socket |
| 0 R 47 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | Y | 1 | 89.01.0560 | | 4.9152MHz | XTAL |
| 0 R 48 | 57.60.1472 | | 4K/ | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 49 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | End of List | |
| 0 R 51 | 57.60.1182 | | 1k8 | MF. 1%, 0204, E24 | | | | | | | |
| 0 R 52 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 53 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 54 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 55 | 57.60.1100 | | 10R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 56 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 57 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 58 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 59 | 57.60.1102 | | 1KU | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 60 | 57 60 1471 | | 470R 470R | MF, 1%, 0204, E24 | | | | | | | |
| 0 8 62 | 57 60 1330 | | 33R | MF 1% 0204 E24 | | | | | | | |
| 0 R 63 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 64 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 65 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 66 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 67 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 68 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 | | | | | | | |
| U R 69 | 57.60.1182 | | 168 | MF, 1%, 0204, E24 | | | | | | | |
| 0 R70 | 57 60 1102 | | 104 | MF, 1%, U2U4, E24 | | | | | | | |
| 0 872 | 57 60 1182 | | 168 | ME 1% 0204 E24 | | | | | | | |
| 0 R 73 | 57.60 1101 | | 100R | MF. 1%, 0204, E24 | | | | | | | |
| 0 R 74 | 57.60.1106 | | 10M | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 75 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 76 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 77 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | |
| 0 R 78 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | | | |



STUDER





STUDER



| ldx | Pos. | Part No. Qty. | Type/Val. | Description | ldx | Pos. | Part No. Qtv | . Type/Val. | Description |
|-----|----------|--------------------------|--------------|------------------------------|------|-----------|--------------|-------------|----------------------------|
| | C 1 | 50.06.0104 | 100p | DETD 621/ 109/ DM6 | | P 13 | 57 11 3102 | 140 | ME 1% 0007 |
| | 01 | 59.00.0104 | 100- | PETP, 03V, 10%, RMS | 0 | | 57.11.3102 | 1000 | MF, 176, 0207 |
| | 02 | 59.00.0104 | 10011 | PETP, 63V, 10%, RM5 | 0 | R 14 | 57.11.3101 | 100R | MF, 1%, 0207 |
| 0 | 03 | 59.22.5101 | 1000 | EL 25V 20% RM5 | U | R 15 | 57.11.3101 | 100R | MF, 1%, 0207 |
| 0 | 64 | 59.22.5220 | 22U | EL 25V 20% RM5 | U | R 16 | 57.11.3101 | 100R | MF, 1%, 0207 |
| 0 | C 5 | 59.06.0104 | 100n | PETP, 63V, 10%, RM5 | U | R 17 | 57.11.3103 | 10k | MF, 1%, 0207 |
| | | 59.06.0104 | 1000 | PETP, 63V, 10%, RM5 | 0 | R 18 | 57.11.3472 | 4K7 | MF, 1%, 0207 |
| | 67 | 59.06.0104 | 1000 | PETP, 63V, 10%, RM5 | 0 | R 19 | 57.11.3103 | 10K | MF, 1%, 0207 |
| 0 | 08 | 59.06.0104 | 100n | PEIP, 63V, 10%, RM5 | 0 | R 20 | 57.11.3103 | 10k | MF, 1%, 0207 |
| 0 | 0.9 | 59.06.0102 | 100 | PETP, 63V, 10%, RM5 | 0 | R 21 | 57.11.3103 | 10k | MF, 1%, 0207 |
| 0 | C 10 | 59.06.0102 | 100 | PETP, 63V, 10%, RM5 | 0 | R 22 | 57.11.3103 | 10k | MF, 1%, 0207 |
| | C 11 | 59 06 0104 | 100n 100u | PETP, R3V, 10%, RM5 | 0 | P. 23 | 67.02.7011 | 0.2. | PTC 60V |
| | C 13 | 59.06.0102 | 1000 | DETD 631/ 109/ DM6 | 0 | R 24 | 57.92.7011 | 0.2A | |
| | C 14 | 59.06.0102 | 100 | DETD 63V, 1076, RMIS | 0 | R 20 | 57.11.3182 | 168 | MF, 1%, 0207 |
| 0 | C 15 | 59.00.0102 | 100 | PETP, 03V, 10%, RIVIS | 0 | R 20 | 57.11.3101 | 100R | MF, 1%, 0207 |
| | 0.10 | 59.00.0102 | 1=0 | PETP, 03V, 10%, RWS | 0 | R 27 | 57.11.3101 | 100R | MF, 1%, 0207 |
| 0 | C 10 | 59.34.2220 | 22n | CER 63V 5% N150 | 0 | R 20 | 57.11.3103 | 10K | MF, 1%, 0207 |
| 0 | C 18 | 59 34 2220 | 22n | CER 63V 5% N150 | ů. | R 30 | 57 11 3103 | 104 | ME 19(0207 |
| 0 | C 19 | 59.06.0102 | 1n0 | PETP 63V 10% RM5 | 0 | R 31 | 57 11 3103 | 10k | ME 19(0207 |
| n n | C 20 | 59.06.0102 | 1n0 | PETP 63V 10% PM5 | 0 | R 32 | 57 92 7011 | 0.24 | NF, 1%, 0207 |
| 0 | C 21 | 59 22 6220 | 220 | EI 35V 20% PM5 | 0 | R 33 | 57 11 3472 | 447 | FIC 80V |
| 0 | 0 22 | 59.00.0104 | 1000 | PETP. 03V. 10%. BMD | 0 | R 04 | 57.11.3104 | 1006 | ME 1% 0207 |
| 0 | C 23 | 59.06.0104 | 100n | PETP. 63V. 10% RM5 | 0 | R 35 | 57.11.3104 | 100k | ME 1% 0207 |
| 0 | C 24 | 59.06.0104 | 100n | PETP, 63V, 10%, RM5 | 0 | R 36 | 57.11.3104 | 100k | ME, 1% 0207 |
| 0 | C 25 | 59.06.0104 | 100n | PETP, 63V, 10%, RM5 | 0 | R 37 | 57,11,3104 | 100k | MF 1% 0207 |
| 0 | C 26 | 59.06.0104 | 100n | PETP, 63V, 10%, RM5 | 0 | R 38 | 57.11.3151 | 150R | MF. 1% 0207 |
| | | | / | , | 0 | R 39 | 57,11.3104 | 100k | MF 1% 0207 |
| 0 | 01 | 50.04.0122 | 1N4001 | 1A, DO 41 | Ő | R 40 | 57.11.3470 | 47R | MF. 1%, 0207 |
| 0 | D 2 | 50.04.0122 | 1N4001 | 1A, DO 41 | 0 | R 41 | 57.11.3472 | 4k7 | ME 1% 0207 |
| 0 | D 3 | 50.04.0105 | 1N4004 | 1A, DO 41 | 0 | R 42 | 57.11.3104 | 100k | ME 1% 0207 |
| 0 | D 4 | 50.04.0125 | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | R 43 | 57.11.3104 | 100k | MF. 1% 0207 |
| 0 | D 5 | 50.04.0105 | 1N4004 | 1A, DO 41 | 0 | R 44 | 57.11.3104 | 100k | ME, 1%, 0207 |
| 0 | | 50 04 0105 | 1N/00/ | 1A, DO 11 | U | K 45 | 57.11.3104 | 100k | MF, 1%, 0207 |
| U | U7 | 50.04.0125 | 1N4448 | 75V, 150mA, 4ns, DO-35 | 0 | R 46 | 57.11.3104 | 100k | MF. 1%.0207 |
| 0 | DL 1 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | R 47 | 57.11.3104 | 100k | MF, 1%, 0207 |
| | | | | | 0 | R 48 | 57.11.3104 | 100k | MF, 1%, 0207 |
| 0 | DS 1 | 55.01.0164 | 4*a | SZ ,4*A, DIL | 0 | R 49 | 57.11.3104 | 100k | MF, 1%, 0207 |
| U | IG 1 | 1.942.910.22 | | SW.110 CONTROL FRONT BOARD 1 | n | R 50 | 57.11.3151 | 160R | MF, 196, 0207 |
| | | | (50160314. | DS87C520) | 0 | R 51 | 57.11.3470 | 47R | MF, 1%, 0207 |
| 0 | IC 2 | 50.17.1244 | 74HC244 | IC 74 HC 244 A | 0 | R 52 | 57.11.3470 | 47R | MF, 1%, 0207 |
| 0 | IC 3 | 50,17,1014 | 74HC14 | IC 74 HC 14 A | 0 | R 53 | 57.11.3151 | 150R | MF, 1%, 0207 |
| 0 | IC 4 | 50.10.0108 | LM317L | Series regulator 100mA+37V | 0 | R 54 | 57.11.3101 | 100R | MF, 1%, 0207 |
| 0 | IC 5 | 50 17 1014 | 74HC14 | IC 71 HC 11 ., ,A | 0 | R 55 | 57.11.3101 | 100R | MF, 1%, 0207 |
| 0 | IC 6 | 50.17.1595 | 74HC595 | IC 74 HC 595 ,A | U | R 56 | 57.11.3101 | 100R | MF, 1%, 0207 |
| 0 | IC 7 | 50.17.1541 | 74HC541 | IC 74 HC 541 ., ,A | 0 | R 57 | 57.11.3104 | 100k | MF, 1%, 0207 |
| 0 | IC 8 | 50.17.4051 | | IC 74 HC 4051 ., ,A | 0 | R 58 | 57.11.3104 | 100k | MF, 1%, 0207 |
| 0 | IC 9 | 50.17.1244 | 74HC244 | IC 74 HC 244 ., ,A | 0 | R 59 | 57.11.3104 | 100k | MF, 1%, 0207 |
| 0 | IC 10 | 50.17.1244 | 74HC244 | IC 74 HC 244 ., ,A | 0 | R DU | 57.11.3104 | 100k | MF, 1%, 0207 |
| 0 | IC 11 | 89.20.2201 | 600VAC | DC / AC Converter | 0 | R 61 | 57.11.3151 | 150R | MF, 1%, 0207 |
| 0 | MD 1 | 1 042 110 11 1 00 | 2 | CONTOL EPONT POARD 1 DCR | 0 | R 02 | 57.11.3272 | 287 | MF, 1%, 0207 |
| | MP 2 | 43.01.0108 1 pc | e lahol | ESE WARNSCHILD | 0 | R 03 | 57.11.3470 | 47R | MF, 1%, 0207 |
| n n | MP 3 | 1942 110 10 1 pc | | NR ETIKETTE 5Y20 | 0 | R 65 | 57.11.3163 | 186 | MF, 1%, 0207 |
| ľ | 1411 0 | 1.042.110.10 1 pc | olus Hardw | are-Etikette 1 101 001 21) | 0 | R 66 | 57.11.3101 | 100R | MF, 1%, 0207 |
| 0 | MP 4 | 43.01.0104 1 pc | e | WARNSCHILD (BLITZ) | ő | R 67 | 57 11 3101 | 1008 | MF, 1%, 0207 |
| | | | | (| n | Res | 57 11 3101 | 100R | MF, 1%, 0207 ME 1% 0207 |
| 0 | P 1 | 54.14.2056 | 64p | Stecker gerade Au | 0 | R 69 | 57 11 3101 | 100R | MF 1% 0207 |
| 0 | P 2 | 54.14.2103 | 20p | P STECKER 20 P,AU,VR,GERADE | | | 07.11.0101 | TOUL | 111, 170, 0201 |
| | 23 D4 | p4.14.2102 | 16p | P STECKER 16 P,AU,VR,GERADE | 0 | RZ 1 | 57.88.4472 | 4k7 | 8*R Resistor-Netw 2% SIP9 |
| | F 4 | 54.99.0337 | P4p | H 4p Pin-Row vertical | 0 | RZ 2 | 57.88.4103 | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 | PE | 54.10.4010 54.14.2402 | 180 | Flex-ZiF gerade, PUB | 0 | RZ 3 | 57.88.4104 | 100k | 8"R Resistor-Netw 2% SIP9 |
| 0 | P7 | 54.14.2103 | 20p | F STEUNER ZUP,AU,VR,GERADE | 0 | RZ 4 | 57.88.4103 | 10k | 8"R Resistor-Netw 2% SIP9 |
| U U | r 1 | JH. 10.4012 | ı∠p | Flex-ZIF gerade, PGB | . 0 | RZ 5 | 57.88.4104 | 100k | STR Resistor-Netw 2% SIP9 |
| 0 | Q 1 | 50.03.0631 | ZTX750 | ZTX 750 | 0 | KZ 6 | 57.88.4103 | 10K | 8"R Resistor-Netw 2% SIP9 |
| 0 | Q 2 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | 0 | XIC 1 | 53.03.0172 | 40p | DIL 0.6", löt, gerade |
| 0 | Q 3 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | | | | • | |
| 0 | Q 4 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | 0 | XY 1 | 89.01.1499 | | QUARZ - ISOLIERPLATTE |
| 0 | Q 5 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | n | Y 1 | 89.01.1016 | 22.1184MH+ | XTAL HC 49/U |
| 0 | Q 6 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | | | | | |
| 0 | Q 7 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | | | | End of Lie | |
| 0 | Q 8 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | _ | | | and of Lis | <u> </u> |
| 0 | Q 9 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | Cor | nments | | | |
| 0 | Q 10 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | (22) | UT Softwa | ire change | | |
| 0 | R 1 | 57.11.3000 | 080 | ME 0207 | | | | | |
| Ó | R2 | 57.11.3102 | 1k0 | ME 1% 0207 | | | | | |
| Ó | R 3 | 57.11.3102 | 1k0 | MF. 1%,0207 | | | | | |
| 0 | R 4 | 57.11.3121 | 120R | MF. 1%, 0207 | | | | | |
| 0 | R 5 | 57.11.3331 | 330R | MF, 1%, 0207 | | | | | |
| 0 | R 6 | 57.11.3101 | 100R | MF. 1%. 0207 | | | | | |
| 0 | R 7 | 57.11.3103 | 10k | MF, 1%, 0207 | | | | | |
| 0 | R 8 | 57.11.3331 | 330R | MF, 1%, 0207 | | | | | |
| 0 | R 9 | 57.11.3101 | 100R | MF, 1%, 0207 | | | | | |
| 0 | R 10 | 57.92.7015 | 1.1A | PTC 50V | | | | | |
| 0 | R 11 | 57.92.7015 | 1.1A | PTC 50V | | | | | |
| 0 | R 12 | 57.11.3391 | 390R | MF, 1%, 0207 | | | | | |

STUDER



STUDER





On-Air 1000 Digital Mixing Console

| ponent side | l (al anno 1997) | n sins ny afis a mananana amin'ny sara-amin'ny sara-amin'ny sara-amin'ny sara-amin'ny sara-amin'ny sara-amin'ny sara- | | Solder side | |
|--------------------------------------|---|---|---|---|---|
| Obli Obli Obli Obli Obli | | Opr 10 | 0 0 0 0 1 1 | | C1 |
|)0L 15 |)0L 18 | |)pr.20 | | (R) (R)3) |
| DL22 DL23 | 0L24 DL25 | | 01.26 01.27 | | ID4] (R17) (R16) (R23) |
| ⁷³ 78 | 00 8 5 | | 5 E | | 17.1 5 10.100 (R29) (R28) (R36) |
| | | | | | |
| | | | ODL3 | Image: Research in the second secon | (R18) ISU 221 R53 (R52) |
| 0bl 40 | 00L42 | | ODL 45 | Image: Constraint of the second sec | C4 [R58] Z50 [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] [] |
| ODL 46 | 00L48 | Opl.50 | 00L52 | | 1 1 1 |
| Obl54 | Oble6 | 00158 | Opreo Opreo | | [R82] 3391 (2) 10243 (R87) (R86) |
| | | Ople2 | DL64 | R927 R936 R935 D380 P380 P380 P380 P380 P380 P380 MD50 R100 MD53 R930 P380 P3000 P3000 P3000 P3000 P3000 P3000 <th< td=""><td><u>₹</u></td></th<> | <u>₹</u> |
| | | | Oble6 | | + |
| RA1 | RA2 | (RA3) | DLL68 | CIUJ [13] [13] [13] [13] [13] [13] [13] [13] | EIOS |
| | bouent side bouent side 00154 00153 00140 00134 00135 00140 00141 00135 00141 00135 00141 00135 00141 00135 00141 00135 00141 | Douest side Out Out <th< td=""><td>ponent side 100 1</td><td>boundet side Outsi Outsi</td><td></td></th<> | ponent side 100 1 | boundet side Outsi | |

| | ldx. | Pos. | Part No. Q | ty. Type/Val. | Description | idx. | Pos. | Part No. Qty. | Type/Val. | Description |
|---|----------|----------------|------------|---------------|---------------------|----------------|-------|--------------------|------------|--------------------------------|
| | o | C 1 | 59.60.1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | DL 21 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | C 2 | 59.60.1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | DL 22 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | C 3 | 59.60.0680 | 68p | CER 63V, 5%, COG, 0 | 805 0 | DL 23 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | C 4 | 59.60.1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | DL 24 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | C 5 | 59.60.1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | DL 25 | 50.04,2152 | HLMP1440 | LE D 3mm, gelb klar |
| | 0 | C 6 | 59.60.1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | DL 26 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | C 7 | 59.60,1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | UL 27 | 50,04.2152 | HLMP 1440 | LED Smm, geib klar |
| | 0 | C 8 | 59.60.1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | DL 28 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | C 9 | 59.60.1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | DL 29 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | C 10 | 59.60.1104 | 100n | CER 63V, 10%, X7R, | 1210 0 | DL 30 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | C 11 | 59.60.1104 | 100 n | CER 63V, 10%, X7R, | 1210 0 | DL 31 | 50.04.2152 | HLMP1440 | LED 3mm, gelo kiar |
| | 0 | C 12 | 59.60.1104 | 100 n | CER 63V 10% X7R | 1210 0 | DL 32 | 50.04.2152 | HLMP1440 | LED 3mm, delo klar |
| | 0 | D 1 | 50 60 9001 | 4449 | D 11 4449 800 | 0 | DL 33 | 50.04.2152 | HI MP1440 | LED 3mm, geib klar |
| | 0 | 07 | 50.60.8001 | 4440 | D 11 4448 SOD | 80 0 | DL 35 | 50.04.2152 | HI MP1440 | LED 3mm geib klar |
| | <u>,</u> | D 3 | 50 60 8001 | 4448 | D 11 4448 SOD | 80 0 | DL 36 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 4 | 50.60.8001 | 4448 | D 11 4448 SOD | 80 0 | DL 37 | 50 04 2152 | HLMP1440 | LED 3mm, gelb klar |
| | Ŭ | 05 | 50.50.8001 | 4448 | U LL 4448 500 | - UU U | DL 38 | 00.04.2152 | HLMP 1440 | LED Junn, gelb klar |
| | 0 | D 6 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | DL 39 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 7 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | DL 40 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 8 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | DL 41 | 50,04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 9 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | DL 42 | 50.04.2152 | HLMP1440 | LED 3mm, geib klar |
| | Ω | D 10 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | DL 43 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 11 | 50.60,8001 | 4448 | D LL 4448 SOD | 80 0 | DL 44 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 12 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | DL 45 | 50.04.2152 | | LED 3mm, gelb klar |
| . | 0 | D 13 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | DL 46 | 50.04.2152 | HEMP1440 | LED 3mm, gelb klar |
| 1 | 0 | D 14 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | UL 47 | 50.04.2152 | HI MO1440 | LED 3mm, gelb klar |
| | 0 | D 15 | 50.60,8001 | 4448 | D LL 4448 SOD | 80 0 | UL 48 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klor |
| | 0 | D 10 | 50.00.8001 | 4440 | D LL 4448 SOU | 100 0 100 A | DL 50 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 12 | 50.00.0001 | 4448 | D LL 4448 SOL | 00 0 | DL 51 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 10 | 50.00.0001 | 4448 | D 11 4448 SOL | 180 0 | DL 52 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 20 | 50.60.8001 | 4448 | D 4448 SOL | 80 0 | DL 53 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 21 | 50.60.8001 | 4448 | D 11 4448 SOC | 80 0 | DL 54 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 22 | 50,60.8001 | 4448 | D LL 4448 SOC | 80 0 | DL 55 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 23 | 50.60.8001 | 4448 | D LL 4448 SOE | 80 0 | DL 56 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 24 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | DL 57 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 25 | 50.60.8001 | 4448 | D LL 4448 SOC | 080 0 | DL 58 | 50,04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | σ | D 26 | 50.60.8001 | 4448 | D LL 4448 SOC | 080 0 | DL 59 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 27 | 30 00 0001 | 4440 | D LL 4446 300 | 0 00 | DL 00 | 50.04.2152 | HLMP1440 | LED 3mm, golb klar |
| | 0 | D 28 | 50.60.8001 | 4448 | D LL 4448 SOE | 0 080 | DL 61 | 50.04.2152 | HLMP1440 | LED 3mm, geib klar |
| | 0 | D 29 | 50.60 8001 | 4448 | D LL 4448 SOL | 080 | DL 62 | 50.04.2152 | HLIMP 1440 | LED 3mm, gelb klar |
| | 0 | D 30 | 50.60.8001 | 4448 | D LL 4448 SOL | 080 | DL 63 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | U 31 | 50.60.8001 | 4448 | D LL 4448 SOL | 180 0 | DL 64 | 50.04.2152 | HIMP1440 | 1 ED 3mm celb klar |
| | 0 | D 32 | 50.60.8001 | 4448 | D LL 4448 SOL | 0 80 0 | DL 66 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 34 | 50 60.8001 | 4448 | D LL 4448 SOF | 0 80 0 | DL 67 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 35 | 50.60.8001 | 4448 | D LL 4448 SOC | 80 0 | DL 68 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 36 | 50.60.8001 | 4448 | D LL 4448 SOC | 80 0 | DL 69 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar |
| | 0 | D 37 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 | | | | |
| | 0 | D 38 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | IC 1 | 50.61.8101 | | IC CDP 68 HC 68 A2M ,A |
| | 0 | D 39 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | IC 2 | 50.62.1595 | 74HC595 | IC 74 HC 595 . ,A |
| | 0 | D 40 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | IC 3 | 50.62,1595 | 74HC595 | IC 74 HC 595 |
| | 0 | D 41 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | 10.4 | 50,62,1595 | 74HC595 | IC 74 HC 595 A |
| | 0 | D 42 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | 10.5 | 50.62,1595 | 7400555 | 10 14 HC 385 . ,A |
| | 0 | D 43 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 | MD 1 | 1942 111 11 1 000 | | CONTOL FRONT BOARD 7 PCB |
| | 0 | D 44 | 50.60.8001 | 4448 | D LL 4446 SOL | 180 0 | MP 2 | 43.01.0108 1 pce | Label | ESE-WARNSCHILD |
| | 0 | D 46 | 50.60.8001 | 4448 | D LL 4446 SOD | an 0 | MP 3 | 1,942,111.10 1 pce | | NR.ETIKETTE 5X20 |
| | 0 | D 47 | 50.60.8001 | 4448 | D 11 4448 SOD | 80 | | - | | |
| | 0 | D 48 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | P 1 | 54.14.2101 | 10p | P STECKER 10 P,AU,VR,GERADE |
| | 0 | D 49 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | P 2 | 54,14,2103 | 20p | P STECKER 20 P.AU.VR.GERADE |
| | 0 | D 50 | 50.60,8001 | 4448 | D LL 4448 SOD | 80 | | | | |
| | 0 | D 51 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | Q 1 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | D 52 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | Q 2 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | D 53 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | Q3 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | D 54 | 50.60.8001 | 4448 | D LL 4448 SOD | 80 0 | Q 4 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | | 60.04-2460 | UI 1104040 | ED 2mm matrice | U | 0.5 | 50.60.0050 | BC817-25 | Q BC 817-25 NPN SOT 23 |
| | 0 | | 50.04.2159 | | LED 3mm, rot klar | 0 | 07 | 50,60,0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | 013 | 50.04.2159 | HLMP1340 | LED 3mm rot klar | 0 | Q 8 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | õ | DL 4 | 50.04 2159 | HLMP1340 | LED 3mm rot klar | 0 | Q 9 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | DL 5 | 50.04,2159 | HLMP1340 | LED 3mm, rot klar | 0 | Q 10 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | DL 6 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | Q 11 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | DL 7 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | Q 12 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | DL 8 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | Q 13 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | DL 9 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | Q 14 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | DL 10 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | Q 15 | 50.60.0050 | BC817-25 | U BC 817-25, NPN SOT 23 |
| 1 | 0 | DL 11 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | Q 16 | 50.60.0050 | BC817-25 | Q BU 817-25, NPN SOT 23 |
| | 0 | DL 12 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | 0.10 | 50.60.0050 | BC817-25 | O BC 817-25 NDN COT 23 |
| | 0 | DL 13 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | 0.10 | 50.60.0050 | BC817-25 | Q BC 817-25 NPN SOT 23 |
| 1 | U | UL 14 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | 0.20 | 50,60,0050 | BC817-25 | Q BC 817-25 NPN SOT 23 |
| | 0 | UL 13 DI 16 | 50.04.2159 | HLMP1340 | LED 3mm, rot klar | 0 | Q 21 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | õ | DL 17 | 50.04.2152 | | LED 3mm, geib klar | õ | Q 22 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | õ | DL 18 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar | 0 | Q 23 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | DL 19 | 50.04.2152 | HLMP1440 | LED 3mm. gelb klar | 0 | Q 24 | 50.60.0050 | BC817-25 | Q BC 817-25, NPN SOT 23 |
| | 0 | DL 20 | 50.04.2152 | HLMP1440 | LED 3mm, gelb klar | 0 | Q 25 | 50.60.0050 | BC817-25 | Q. BC 817-25, NPN SOT 23 |
| 1 | | | | - | | | | | | |

| <u>ldx</u> . | P | 'os. | Part No. | Qty. | Type/Val. | Description | | ldx. | Pos | s. Pa | rt No. | Qty. | Type/Val. | Description |
|--------------|--------|------|------------|------|--------------|---------------------------------------|--------|------|------|--------------|------------------------|------|-------------|--|
| 0 | Q | 26 | 50.60,0050 | | BC817-25 | Q BC 817-25, NPN | SOT 23 | 0 | R 81 | 1 5 | 7.60.1471 | | 470R | MF, 1%, 0204, E24 |
| 0 | Q | 27 | 50.60.0050 | | BC817-25 | Q BC 817-25, NPN | SOT 23 | 0 | R 82 | 2 5 | 7.60.1472 | | 4K7 | MF, 1%, 0204, E24 |
| 0 | Q | 28 | 50.60.0050 | | BC817-25 | Q BC 817-25, NPN | SOT 23 | 0 | R 83 | 3 5 | 7.60.1472 | | 4K7 | MF, 1%, 0204, E24 |
| 0 | Q | 29 | 50.60.0050 | | BC817-25 | Q BC 817-25, NPN | SOT 23 | 0 | R 84 | 4 5 | 7.60.1472 | | 4K7 | MF, 1%, 0204, E24 |
| 0 | Q | 30 | 50.60.0050 | | BC817-25 | Q BC 817-25, NPN | SOT 23 | 0 | R 85 | 5 5 | 7.60.1472 | | 4K7 | MF, 1%, 0204, E24 |
| - 65 | 6.) | 1.31 | 50,60,0050 | | RC817-25 | O RC 817-25, NPN | SOT 23 | 0 | R 87 | n 5 7 5 | 7.60.1471 | | 470R | MF. 1%, 0204, F24 MF. 1%, 0204, F24 |
| 0 | R | 1 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | 0 | R 88 | 8 5 | 7.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | R | 2 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | 0 | R 89 | 9 5 | 7.60.1471 | | 470R | MF, 1%, 0204, E24 |
| 0 | R | 3 | 57.60.1271 | | 270R | MF, 1%, 0204, E24 | | 0 | R 90 | 0 5 | 7.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | R | 4 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | 0 | R 91 | 1 5 | 7.60.1471 | | 470R | MF, 1%, 0204, E24 |
| 0 | R | 5 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | 0 | R 94 | ∠ ⊃ 3 5 | 7.60 1101 | | 4708 | MF, 1%, 0204, E24 MF 1%, 0204, E24 |
| 0 | P | 7 | 57 60 1101 | | 2/UR 1008 | MF, 1%, 0204, E24 | | ő | R 94 | 4 5 | 7.60.1102 | | 1K | MF. 1%, 0204, E24 |
| 0 | R | 8 | 57 60 1101 | | 100R | MF 1% 0204 E24 | | 0 | R 95 | 5 5 | 7.60.1102 | | 1K | MF, 1%, 0204, E24 |
| 0 | R | 9 | 57.60.1271 | | 270R | MF, 1%, 0204, E24 | | 0 | R 96 | 6 5 | 7.60.1472 | | 4K7 | MF, 1%, 0204, E24 |
| n | R | 10 | 57 60 1472 | | 4K7 | MF, 1%, 0204, F24 | | 0 | R 97 | 7 5 | 7.60.1472 | | 4K7 | MF. 1%. 0204. E24 |
| 0 | R | 11 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | 0 | R 96 | 6 5 0 5 | 7.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | R | 12 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | 0 | R 10 | 9 5 00 57 | 7.60.1471 | | 100R | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | л р | 13 | 57.60.1472 | | 4N7 | MF, 1%, 0204, E24 | | ō | R 10 | 01 57 | 7.60.1471 | | 470R | MF, 1%, 0204, E24 |
| õ | R | 15 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | 0 | R 10 | 02 57 | 7.60.1472 | | 4K7 | MF, 1%, 0204, E24 |
| 0 | R | 16 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | 0 | R 10 | 50 60 | 7.60.1101 | | 100R | MF, 196, 0204, E24 |
| 0 | Ŗ | 17 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | 0 | R 10 | 04 57 | 7.60.14/1 | | 470K | MF, 1%, 0204, E24 ME 1%, 0204 E24 |
| 0 | R | 18 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | 0 | R 10 | 06 57 | 7.60.1102 | | 1K | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | R | 19 | 57.60.14/1 | | 470R | MF, 1%, 0204, E24 | | õ | R 10 | 07 57 | 7.60.1102 | | 1K | MF, 1%, 0204, E24 |
| 0. | R | 20 | 57.60.1101 | | 470R | MF, 1%, 0204, E24 MF 1% 0204 E24 | | 0 | R 10 | 08 57 | 7.60.1472 | | 4K7 | MF, 1%, 0204, E24 |
| 0 | R | 22 | 57.60.1102 | | 1K | MF, 1%, 0204, E24 | | 0 | R 10 | 09 57 | 7.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | R | 23 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | 0 | R 11 | 10 57 | 7.60.1471 | | 470R | MF, 1%, 0204, E24 |
| 0 | R | 24 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | 0 | | 4 50 | 0 2402 | | 104 | 1*P lin |
| 0 | R | 25 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | 0 | RA | 1 50 | 5.20.7102 8.20.7102 | | 10k | 1*B lin |
| 0 | R | 26 | 57.60.1102 | | 1K 1K | MF, 1%, 0204, E24 | | o | RAS | 3 68 | 3.20.7102 | | 10% | 1*B, lin |
| 10 | R | 28 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 29 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | 0 | S 1 | 1.94 | 42.010.07 | | | KONTAKTMATTE,27 TASTEN |
| 0 | R | 30 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 31 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | - | | | | (| End of List | |
| 0 | R | 32 | 57.60.1102 | | 1K 1008 | MF. 1%, 0204, E24 MF 1%, 0204, E24 | | Çor | nmen | 115 | | | | |
| õ | R | 34 | 57.60.1471 | | 470R | MF. 1%, 0204, E24 | | | | | | | | |
| 0 | R | 35 | 57.60.1102 | | 1K | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 36 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 37 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 30 | 57.00.1472 | | 4157 | MF, 1%, 0204, E24 | | | | | | | | |
| õ | R | 40 | 57.60.1103 | | 10K | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 41 | 57.60.1103 | | 10K | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 42 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 43 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 44 | 57.60,1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| ő | R | 45 | 57.60.1101 | | 1008 | MF 1% 0204 E24 | | | | | | | | |
| 0 | R | 47 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 48 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 49 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 50 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 51 | 57.60.1101 | | 1008 | MF, 1%, 0204, E24 | | | | | | | | |
| ō | R | 53 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 54 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 55 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 56 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | к р | 58 | 57 60 1471 | | 4K7 | MF 1% 0204, E24 | | | | | | | | |
| .0 | R | 59 | 57,60,1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 60 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 61 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 62 | 57.60,1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 63 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 65 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| õ | R | 66 | 57.60,1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 67 | 57.60.1102 | | 1K | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 68 | 57.60,1102 | | 1K | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 69 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 70 | 57.60,1472 | | 4K/ | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 72 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 MF, 1%, 0204 F24 | | | | | | | | |
| 0 | R | 73 | 57.60.1472 | | 4K7 | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 74 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 75 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 76 | 57.60,1101 | | 100R | MF, 1%, 0204; E24 | | | | | | | | |
| 0 | R | 78 | 57 60 1101 | | 470R 100R | MF 1% 0204, E24 | | | | | | | | |
| 0 | R | 79 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 | | | | | | | | |
| 0 | R | 80 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | | | | | | | | |


Control Front Board III 1.942.612.00



| ldx | Pos. | Part No. | Qty. | Type/Val. | Description |
|------------------|---------------------------------|--|----------------|--|--|
| 0 0 0 | C 1 C 2 C 3 | 59.06.0104 59.06.0104 59.06.0104 | | 100n 100n 100n | PETP, 63V, 10%, RM5 PETP, 63V, 10%, RM5 PETP, 63V, 10%, RM5 |
| 0 0 | D 5 D 6 | 50.04.0125 50.04.0125 | | 1N4448 1N4448 | 75V, 150mA, 4ns, DO-35 75V, 150mA, 4ns, DO-35 |
| 0 0 0 | DL 1 DL 2 DL 3 DL 4 | 50.04.2152 50.04.2152 50.04.2152 50.04.2152 | | HLMP1440 HLMP1440 HLMP1440 HLMP1440 | LED 3mm, gelb klar LED 3mm, gelb klar LED 3mm, gelb klar LED 3mm, gelb klar |
| 0 0 | MP 1 MP 3 | 1.942.112.11 1.942.112.10 | 1 pce 1 pce | | CONTOL FRONT BOARD 3 PCB NR.ETIKETTE 5X20 |
| 0 | P 1 | 54.14.2103 | | 20p | P STECKER 20 P,AU,VR,GERADE |
| 0 0 | R 1 R 2 | 57.11.3101 57.11.3101 | | 100R 100R | MF, 1%,0207 MF, 1%,0207 |
| 0 | RA 1 | 58.20.7101 | | 5k | 1*R, lin |
| 0 0 0 0 | S 1 S 2 S 3 S 4 S 5 | 55.12.1302 55.12.1302 55.12.1302 55.12.1302 1.942.010.08 | | N=24 N=24 N=24 N=24 | Drehgeber Drehgeber Drehgeber Drehgeber KONTAKTMATTE,2 TASTEN |
| | | | | | |

- End of List -

| | | e. | | | | | 3 |
|------------|-----------------------|---------------------|---------------------|--------------|-----------------|--------------|-------|
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| | | tion | 29 10.96 | ΡZ | YΑ | Ga | 0 |
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| REGENSDORF | CONTROL FRONT BOARD 3 | Number : Numer : | 1.9 | 42. | 612 | 2.00 |) |

Comments





On-Air 1000 Digital Mixing Console

STUDER











On-Air 1000 Digital Mixing Console



On-Air 1000 Digital Mixing Console

STUDER



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| REGENSDORF | ¥# 1.94Z./10.20 | |

| ldx Pos. | Part No. Qty. | Type/Val. | Description | ldx Pos. | Part No. Q | y. Type/Val. | Description | ldx Pos. | Part No. G | Qty. | Type/Val. | Description | ldx Pos. | Part No. | Qty. | Type/Va. | Description |
|----------|------------------|-----------|--|----------|----------------|--------------|-------------------------|----------|--------------|----------|------------|----------------------|-----------|------------|-------|----------|------------------|
| 0.01 | 50 00 2027 4 | 400- | 055 50V 40% X25 0005 | 0 6 80 | 50 60 2227 4 | 400- | 050 501 402 170 0005 | | 50.00.0004 4 | | 1110 | 200-1 751/ 4== 200.0 | 0 74 | 50 60 9402 | 1 nco | vel | SMD LED volion |
| | 59.60.3337 T pce | 1000 | CER 50V 10%, X7R, 0805 | 0 0 0 | 59.60.3337 1 p | ce 100n | CER 50V, 10%, X7R 0805 | 0 D 69 | 50.60.6001 1 | pce | 4440 | 200mA 75V 4hs SOD 80 | 0 2175 | 50.60.9402 | 1 nce | vei | SMD LED yellow |
| 0 02 | 59.60.3337 1 pce | 1000 | CER 50V 10%, X7R, 0805 | 0 0 007 | 59.00.3337 1 | 1000 | CER 50V, 10%, X7R 0805 | 0 0 70 | 50.00.0001 1 | pue | GI 34A | 500mA 50V DO 213 | 0 21 76 | 50 60 940? | 1 nce | vel | SMD LED yellow |
| 0 C4 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R 0805 | 0 C 83 | 59.60.2233 1 r | ce 22n | CER 50V, 15%, COG 0603 | 0 011 | 00.00.0002 | puu | GLOWI | 00000 000 00000 | 0 DL 77 | 50.60.9402 | 1 pce | vel | SMD LED yellow |
| 0 C 5 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 C 84 | 59.60.2233 1 r | ce 22o | CER 50V, 5%, C0G, 0603 | 0 DL 1 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 78 | 50.60.940? | 1 pce | yei | SMD LED yellow |
| 0 C 6 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 C 85 | 59.60.3337 1 | ce 100n | CER 50V. 10%, X7R. 0805 | 0 DL 2 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 DL 79 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C7 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 C 86 | 59.60.3337 1 r | ce 100n | CER 50V. 10%, X7R 0805 | 0 DL 3 | 50.60.9401 1 | pce | red | SM0 LED superred | 0 DL 80 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 8 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 C 87 | 59.60.3337 1 | ce 100n | CER 50V, 10%, X7R. 0805 | 0 DL4 | 50.60.9402 1 | pce | yei | SM0 LED yellow | 0 DL 81 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 9 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 C 88 | 59.60.3337 1 p | ce 100n | CER 50V, 10%, X7R, 0805 | 0 DL5 | 50.60.9401 1 | pce | red | SM0 LED superred | 0 DL 82 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 10 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 C 89 | 59.60.3337 1 p | ce 100n | CER 50V, 10%, X7R, 0805 | 0 DL6 | 50.60.9402 1 | pce | yei | SM0 LED yellow | 0 DL 83 | 50.60.940 | 1 pce | red | SMD LED superred |
| 0 C 11 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 01 | 50 60 8001 17 | 0 4448 | 200m4 75)/ 4nc SOD 80 | | 50.60.9402 1 | nce | vei | SM0 LED yellow | 0. JL 84 | 50.60.9402 | 1 pce | Yei | SMD LED yellow |
| 0 C 12 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 02 | 50.60.8001 1 | ne 4448 | 200mA 75V 4ns SOD 80 | 0 DL9 | 50.60.9402 1 | DCe | vel | SM0 LED vellow | 0 0 0 85 | 50.60.940 | 1 pce | rea | SMD LED superred |
| 0 C 13 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 03 | 50.60.8001 1 r | ne 4440 | 200mA 75V 4ns SOD 80 | 0 DL 10 | 50.60.9402 1 | oce | vei | SM0 LED vellow | 0 21.85 | 50.60.940 | 1 pce | yei | SMD LED yellow |
| 0 0 14 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 4 | 50.60.8001 1 | ce 4448 | 200mA 75V 4ns SOD 80 | 0 DL 11 | 50.60.9402 1 | pce | vel | SM0 LED yellow | 0 31.88 | 50.60.940: | 1 pce | yei | SMD LED yellow |
| 0 C 15 | 59.60.3337 1 pce | 1000 | CER 50V 10%, X7R, 0005 | 0 D 5 | 50.60.8001 1 m | a 4448 | 200mA 75V 4ns SOD 80 | 0 DL 12 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 21.89 | 50.60.940? | 1 nce | vel | SMD LED yellow |
| 0 C 17 | 59.60.3337 1 pce | 100n | CER 50V 10% X7B 0805 | 0 D 6 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 13 | 50.60.9402 1 | pce | yei | SMD LED yellow | 0 01.90 | 50,60,9402 | 1 pce | vel | SMD LED vellow |
| 0 C 18 | 59.60.3337 1 pce | 100n | CER 50V 10% X7B 0805 | 0 D 7 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 14 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 91 | 50.60.940? | 1 pce | vel | SMD LED vellow |
| 0 C 19 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 8 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 15 | 50.60.9402 1 | рсе | yei | SM) LED yellow | 0 DL 92 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 20 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 9 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 16 | 50.60.9402 1 | pce | yei | SM) LED yellow | 0 DL 93 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 21 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 10 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 17 | 50.60.9402 1 | pce | yei | SM) LED yellow | 0 DL 94 | 50.60.940? | 1 pce | yei | SMD LED yellow |
| 0 C 22 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 11 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 18 | 50.60.9402 1 | pce | yel | SM) LED yellow | 0 DL 95 | 50.60.940? | 1 pce | yei | SMD LED yellow |
| 0 C 23 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 12 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 19 | 50.60.9401 1 | pce | red | SM) LED superred | 0 DL 96 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 24 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 13 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 20 | 50.60.9402 1 | pce | yei | SMD LED yellow | 0 DL 97 | 50.60.940? | 1 pce | yei | SMD LED yellow |
| 0 C 25 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 14 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL21 | 50.60.9401 1 | pce | red | SMD LED superied | 0 0L.98 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 26 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 0 16 | 50.60.8001 1p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 22 | 50.60.9402 1 | nce | vol | SM) LED yellow | 0 01.99 | 50.60.940 | 1 pce | red | SMD LED superred |
| 0 0 27 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X/R, 0805 | 0 0 17 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL24 | 50 60 9402 1 | nce | vel | SM) I ED vellow | 0 0 1 101 | 50.80.940 | 1 pce | yei | SMD LED yellow |
| 0 0 28 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 18 | 50 60 8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 25 | 50.60.9402 1 | DCe | vel | SM) LED vellow | 0 0 107 | 50 60 9407 | 1 nce | vel | SMD LED vollow |
| 0 C 29 | 59.60.3337 T pce | 1000 | CER 50V. 10%, X7R, 0805 | 0 D 19 | 50 60 8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 26 | 50.60.9402 1 | DCB | vel | SM) LED vellow | 0 0 102 | 50.60.9402 | 1 000 | yei | SMD LED yellow |
| 0 0 30 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 20 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 27 | 50.60.9402 1 | DCB | vel | SM) LED vellow | 0 0 104 | 50.60.940? | 1 pce | vel | SMD LED yellow |
| 0 C 32 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R 0805 | 0 D 21 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 28 | 50,60,9402 1 | DCe | vel | SM) LED vellow | 0 DL 105 | 50.60.9402 | 1 pce | vel | SMD LED vellow |
| 0 C 33 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 22 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 29 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 106 | 50.60.940? | 1 pce | yei | SMD LED yellow |
| 0 C 34 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 23 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 30 | 50.60.9402 1 | pce | yeł | SM) LED yellow | 0 DL 107 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 35 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 24 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 31 | 50.60.9402 1 | рсе | yel | SMD LED yellow | 0 DL 108 | 50.60.940? | 1 pce | yei | SMD LED yellow |
| 0 C 36 | 59 60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 25 | 50.60.8001 1 p | ce 4448 | 200mA 75V 4ns SOD 80 | 0 DL 32 | 50.60.9402 1 | pce | yeł | SM) LED yellow | 0 DL 109 | 50.60.9402 | 1 pce | yel | SMD LED yellow |
| 0 C 37 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 26 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 33 | 50.60.9402 1 | pce | yel | SM) LED yellow | 0 DL 110 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 38 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 27 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 34 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 111 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 39 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 28 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 35 | 50.60.9401 1 | bce | red | SMD LED superred | 0 DL 112 | 50.60.940? | 1 pce | yei | SMD LED yellow |
| 0 C 40 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 29 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL36 | 50.60.9402 1 | pce | yei | SMJ LED yellow | 0 DL 113 | 50.60.940 | 1 pce | yel | SMD LED yellow |
| 0 C 41 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 0 31 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL3/ | 50.60.9401 1 | pce | rea | SMD LED superred | 0 JL114 | 50.80.9402 | 1 pce | yei | SMD LED yellow |
| 0 C 42 | 5960.3337 1 pce | 100n | CER 50V, 10%, X/R, 0805 | 0 D 32 | 50.60.8001 1 m | e 4448 | 200mA 75V 4ns SOD 80 | 0 0139 | 50.60.9402 1 | nce | vel | SM0 LED yellow | 0 0 1115 | 50.60.940 | 1 pce | rea | SMD LED superred |
| 0 C 43 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 33 | 50.60.8001 1 0 | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 40 | 50.60.9402 1 | nce | vel | SMD LED yellow | 0 0 117 | 50.60.940 | 1 pce | red | SMD LED yellow |
| 0 C 44 | 59.60.3337 1 pce | 1000 | CER 50V, 10%, X7R, 0805 | 0 D 34 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 41 | 50.60.9402 1 | pce | vel | SM0 LED vellow | 0 0 1118 | 50 60 9402 | 1 nce | vei | SMD LED superior |
| 0 C 46 | 59.60.3337 1 nce | 100n | CER 50V 10% X7R 0805 | 0 D 35 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 42 | 50.60.9402 1 | , pce | yel | SM0 LED yellow | 0 DL 119 | 50.60.9402 | 1 pce | vel | SMD I ED vellow |
| 0 C 47 | 59 60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 36 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 43 | 50.60.9402 1 | рсе | yel | SM0 LED yellow | 0 DL 120 | 50.60.940? | 1 pce | yel | SMD LED yellow |
| 0 C 48 | 59.60.3337 1 pce | 100n | CER 50V 10% X7R 0805 | 0 D 37 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 44 | 50.60.9402 1 | pce | yei | SM0 LED yellow | 0 DL 121 | 50.60.9402 | 1 pce | vel | SMD LED vellow |
| 0 C 49 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 38 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 45 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 DL 122 | 50.60.940 | 1 pce | yei | SMD LED yellow |
| 0 C 50 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 39 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 46 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 DL 123 | 50.60.940 | 1 pce | yel | SMD LED yellow |
| 0 C 51 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 40 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 47 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 DL 124 | 50.60.9402 | 1 pce | yel | SMD LED yellow |
| 0 C 52 | 59.68.0029 1 pce | 100u | EL 6V 6.3*5.7 | 0 D 41 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 48 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 DL 125 | 50.60.940? | 1 pce | yei | SMD LED yellow |
| 0 C 53 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 42 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 49 | 50.60.9402 1 | pce | yei | SMD LED yellow | 0 DL 126 | 50.60.9402 | 1 pce | yei | SMD LED yellow |
| 0 C 54 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 43 | 50.60.8001 1 p | e 4446 | 200mA 75V 4ns SOD 80 | 0 DL 50 | 50.60.9402 1 | pce | yer rod | SM0 LED yeared | 0 DL 127 | 50.60.9402 | 1 pce | yei | SMD LED yellow |
| 0 C 55 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 44 | 50.60.8001 1 p | e 4440 | 200mA 75V 4ns SOD 80 | 0 DL52 | 50.60.9402 1 | nce | vel | SM0 LED superiod | 0 JL 128 | 50.80.9402 | 1 pce | yei | SMD LED yellow |
| 0 C 56 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 46 | 50.60.8001 1 c | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 53 | 50.60.9401 1 | pce | red | SM0 LED superred | 0 0 1130 | 50.60.940 | 1 000 | yei | SMD LED yellow |
| 0 057 | 59.60.3337 1 pce | 1000 | CER 50V. 10%, X7R, 0805 | 0 D 47 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 54 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 0 1131 | 50 60 940 | 1 nce | red | SMD LED yellow |
| 0 0 58 | 59.60.3337 T pce | 1000 | EL 6V 63*57 | 0 D 48 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 55 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 132 | 50.60.9402 | 1 pce | vel | SMD LED vellow |
| 0 0.00 | 59.60.3337 1 pce | 100a | CER 50V 10% X7R 0805 | 0 D 49 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 56 | 50.60.9402 1 | pce | yei | SM0 LED yellow | 0 DL 133 | 50.60.940 | 1 pce | red | SMD LED superred |
| 0 C 61 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 50 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 57 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 134 | 50.60.940 | 1 pce | yel | SMD LED yellow |
| 0 C 62 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 51 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 58 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 135 | 50.60.940 | 1 pce | yel | SMD LED yellow |
| 0 C 63 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 52 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 59 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 136 | 50.60.940 | 1 pce | yel | SMD LED yellow |
| 0 C 64 | 59.60.3337 1 pce | 100n | CER 50V 10%, X7R, 0805 | 0 D 53 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 60 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 DL 137 | 50.60.940 | 1 pce | yel | SMD LED yellow |
| 0 C 65 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 54 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 61 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 DL 138 | 50.60.940 | 1 pce | yel | SMD LED yellow |
| 0 C 66 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 55 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 62 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 DL 139 | 50.60.940 | 1 pce | yel | SMD LED yellow |
| 0 C 67 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 D 56 | 50.60.8001 1p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL63 | 50.60.9402 1 | pce | yei | SMD LED yellow | 0 0L.140 | 50.60.9401 | 1 pce | yel | SMD LED yellow |
| 0 C 68 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 0 5/ | 50.60.8001 1p | a 4448 | 200mA 75V 4ns SOD 80 | 0 DL65 | 50.60.9402 1 | pce | yol | SM0 LED yellow | U JL141 | 50.60.940 | 1 pce | yei | SMD LED yellow |
| 0 C 69 | 59.60.3337 1 pce | 100n | CER 50V. 10%, X7R, 0805 | 0 0 59 | 50.60.8001 1p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 66 | 50.60.9402 1 | DCB | vel | SM0 LED yellow | 0 31442 | 50.60.9402 | 1 pce | yei | SMD LED yellow |
| 0 C 70 | 59.60.3337 1 pce | 100n | GER 50V. 10%, X7R, 0805 | 0 0 60 | 50.60.8001 1p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 67 | 50.60.9401 1 | pce | red | SM0 LED superred | 0 1143 | 50.60.9402 | 1 pce | yei | SMD LED VEROW |
| 0 0 71 | 59.50.3337 1 pce | 100n | CER DUV. 10%, X/R, U800 CER DUV. 10%, X7P, 0805 | 0 D 61 | 50.60.8001 1 n | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 68 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 01 144 | 50.60.940 | 1 nce | vel | SMD LED yellow |
| 0 0.73 | 59.60.3337 1 pce | 1000 | CER 50V 10% X7R 0805 | 0 D 62 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 69 | 50.60.9401 1 | pce | red | SM0 LED superred | 0 DL 146 | 50.60.940! | 1 pce | vel | SMD LED vellow |
| 0 C 74 | 59.68.0069 1 nre | 470 | EL 16V. 6.3*5.7 | 0 D 63 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 70 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 147 | 50.60.940 | 1 pce | red | SMD LED superred |
| 0 C 75 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 64 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL71 | 50.60.9402 1 | pce | yel | SM0 LED yellow | 0 0L 148 | 50.60.9401 | 1 pce | yel | SMD LED yellow |
| 0 C 76 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 65 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 72 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 0L 149 | 50.60.940 | 1 pce | red | SMD LED superred |
| 0 C 77 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 D 66 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | 0 DL 73 | 50.60.9402 1 | pce | yel | SMD LED yellow | 0 DL 150 | 50.60.940 | 1 pce | yei | SMD LED yellow |
| 0 C 78 | 59.68.0029 1 pce | 100u | EL 6V 6.3*5.7 | 0 D 67 | 50.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | | | | | | 0 DL 151 | 50.60.940! | 1 pce | yel | SMD LED yellow |
| 0 C 79 | 59.60.3337 1 pce | 100n | CER 50V, 10%, X7R, 0805 | U D 68 | а0.60.8001 1 p | e 4448 | 200mA 75V 4ns SOD 80 | | | | | | 0 DL 152 | 50.60.9401 | 1 pce | yel | SMD LED yellow |
| | | | | | | | | | | | | | | | | | |

| - | | | | - | | | | | _ | | T | Description | P | D | | De la dise |
|----------|--------------------|-------------------|---|----------|------------------|-----------|---------------------|-------------|---------------------|----------------|-----------|-------------------|----------|------------------|-----------|-------------------|
| ldx Pos. | Part No. Qty. | Type/Val. | Description | ldx Pos. | Part No. Qty. | Type/Val. | Description | idx Pos. | Part | tNo. Qty. | Type/Val. | Description | ldx Pos. | Part No. Qty. | Type/Val. | Description |
| 0 01 450 | 50.00.0400 4 | | OMB / ED valler | 0 0 0 | 51 14 2054 1 pco | 40 n | Stecker gerade Au | 0 R 76 | 57.6 | 30.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 155 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 DL 153 | 50.60.0402 1 pce | yei | SMD LED yellow | 0 P2 | 54.14.2054 1 pce | 400 | Olabital gerade 7th | 0 R 77 | 57.6 | 30.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 156 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 DL 154 | 50.60.9402 1 pce | yei | SMD LED yellow | 0 R 1 | 57.60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 78 | 57.6 | 50.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 157 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 DL 155 | 50.60.9402 1 pce | yei | SMD LED yellow | 0 R 2 | 57.60.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 79 | 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 158 | 57.60.1391 1 pce | 390R | MF,1%, 0204, E24 |
| 0 DL 150 | 50.60.9402 1 pce | vel | SMD LED yellow | 0 R 3 | 57.60.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 80 | 57.6 | 30.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 159 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 DL 158 | 50.60.9402 1 pce | vel | SMD LED yellow | 0 R 4 | 57.60.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 81 | 57.6 | 30.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 160 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 DL 159 | 50 60 9402 1 pce | vel | SMD I ED vellow | 0 R 5 | 57.60.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 82 | 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 161 | 57.60.1391 1 pce | 390R | MF,1%, 0204, E24 |
| 0 DL 160 | 50 60 9402 1 pce | vel | SMD LED yellow | 0 R 6 | 57.60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 83 | 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 162 | 57.60.1391 1 pce | 390R | MF,1%, 0204, E24 |
| 0 DI 161 | 50 60 9401 1 pce | red | SMD LED superred | 0 R7 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 84 | 57.6 | 30.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 163 | 57.60.1271 1 pce | 270R | MF, 1%, 0204, E24 |
| 0 DL 162 | 50.60.9402 1 pce | vel | SMD LED vellow | 0 R 8 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 85 | 57.6 | 30.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 164 | 57.60.1221 1 pce | 220R | MF, 1%, 0204, E24 |
| 0 DL 163 | 50.60.9402 1 pce | yel | SMD LED yellow | 0 89 | 57.80.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 86 | 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 165 | 57.60.1103 1 pce | 10k | MF, 1%, 0204, E24 |
| 0 DL 164 | 50.60.9402 1 pce | yel | SMD LED yellow | 0 R 10 | 57.60.1391 1 pce | 390R | ME 1% 0204 E24 | U R87 | 57.5 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | U R 166 | 57.60.1103 1 pce | 10k | MF,1%,0204,E24 |
| 0 DL 165 | 50.60.9402 1 pce | yel | SMD LED yellow | 0 8 11 | 57.60.1391 1 µ09 | 3900 | ME 1% 0204 E24 | U R 88 | 57.5 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 167 | 57.60.1221 1 pce | 220R | MF,1%, 0204, E24 |
| 0 DL 166 | 50.60.9401 1 pce | red | SMD LED superred | 0 8 12 | 57.60.1391 1 pca | 3900 | MF 1% 0204 E24 | 0 8 69 | 57.0 | 20.1391 1 pce | 390R | MF, 1%, 0204, 524 | 0 R 168 | 57.60.1271 1 pce | 270R | MF,1%, 0204, E24 |
| 0 DL 167 | 50.60.9402 1 pce | yei | SMD LED yellow | 0 8 14 | 57.60.1391 1 pce | 3908 | MF 1% 0204 E24 | 0 1 1 2 0 1 | 57.6 | 30.1271 1 pce | 270P | ME 1% 0204, C24 | 0 R 169 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 DL 168 | 50.60.9402 1 pce | yel | SMD LED yellow | 0 12 15 | 57.60.1391 1 nce | 3908 | ME 1% 0204 E24 | 0 8 93 | 57.6 | 30.1271 1 pce | 2208 | ME 1% 0204 E24 | 0 0 170 | 57.00.1391 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 DL 169 | 50.60.9402 1 pce | yel | SMD LED yellow | 0 1016 | 57.60.1391 1 pce | 390R | MF 1% 0204 E24 | 0 8 93 | 57.6 | 30 1103 1 pce | 10k | ME 1% 0204 E24 | 0 8 171 | 67.60.1391 1 pce | 3908 | MF, 1%, 0204, E24 |
| 0 DL 170 | 50.60.9402 1 pce | yel | SMD LED yellow | 0 8 17 | 57.60.1391 1 pce | 390R | MF. 1%, 0204, E24 | 0 894 | 57.6 | 30 1103 1 pce | 10k | ME 1% 0204 E24 | 0 8 173 | 57.60.1391 1 pce | 390R | ME 1% 0204 E24 |
| 0 DL 171 | not used 1 pce | red | SMD LED superred | 0 R 18 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 95 | 57.6 | 30.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 174 | 57.60.1391 1 pce | 390R | MF 1% 0204 E24 |
| 0 10 4 | 72.01.0409 1.000 | Addate | 517 Det matrix display groop | 0 R 19 | 57.60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 96 | 57.6 | 30.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 175 | 57.60.1391 1 pce | 390R | MF 1% 0204 E24 |
| 0 101 | 73.01.0406 Tpce | 40/git | 9hit shift/output register | 0 R 20 | 57.60.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 97 | 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 176 | 57.60.1391 1 pce | 390B | ME 1%, 0204, E24 |
| 0 102 | 50.62.1595 1 pce | 74HC595 | 8bit shift/output register | 0 R 21 | 57.60.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 98 | 57.6 | 30.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 177 | 57.60.1391 1 pce | 390R | MF.1%, 0204, E24 |
| 0 103 | 73.01.0408 1 pce | 4digit | 5*7 Dot matrix display green | 0 R 22 | 57.60.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 99 | 57.6 | 30.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 178 | 57.60.1391 1 pce | 390R | MF,1%, 0204, E24 |
| 0 104 | 60.62.1505 1 pce | 74HC595 | 8hit shift/output register | 0 R 23 | 57.60.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 10 | 0 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 179 | 57.60.1391 1 pce | 390R | MF.1%, 0204, E24 |
| 0 10 5 | 50.62.1595 1 pce | 74HC595 | Shit shift/output register | 0 R 24 | 57.60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 10 | 1 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 180 | 57.60.1391 1 pce | 390R | MF.1%, 0204, E24 |
| 0 10 7 | 72.01.0409 1 pce | Adiait | 5*7 Dot matrix display green | 0 R 25 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 10 | 2 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 181 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 107 | F0.62.1505 1 pce | 74UC505 | Bhit chiff/output register | 0 R 26 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 10 | 3 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 182 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 8 | 50.62.1595 1 pce | 74HC595 | 8bit shift/output register | 0 R 27 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 10 | 4 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 183 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 3 | 73.01.0408 1 pce | Adinit | 5*7 Dot matrix display green | 0 R 28 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 10 | 5 57.6 | 50.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 184 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 15 | 50.62.1595 1 pce | 74HC505 | 8bit shift/output register | 0 R 29 | 57.60.1391 1 pce | 390R | MF. 1%, 0204, E24 | 0 R 10 | 6 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 185 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 11 | 50.62.1595 1 pce | 74HC595 | 8bit shift/output register | 0 R 30 | 57.60.1391 1 pce | 390R | MF. 1%, 0204, E24 | 0 R 10 | 7 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 186 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 12 | 73.01.0409 1 pce | Adiait | 5*7 Dot matrix display green | 0 8 31 | 57 60 1391 1 pce | 390B | MF. 1% 0204, E24 | 0 R 10 | 8 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 187 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 13 | F0 62 1505 1 pce | 40/git | Shit ebiff/output register | 0 8 32 | 57 60 1391 1 oce | 390R | MF. 1%, 0204, E24 | 0 R 10 | 9 57.6 | 60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 188 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 14 | 50.02.1595 1 pce | 74110535 | Bhit shift/output register | 0 R 33 | 57.60.1391 1 pce | 390R | MF. 1%, 0204, E24 | 0 R 11 | 0 57.6 | 60.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 189 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 15 | 50.62.1595 1 pce | 74HG585 | 517 Dot motify display green | 0 8 34 | 57 60 1391 1 pce | 390B | ME 1% 0204 E24 | 0 B 11 | 1 57.6 | 60.1103 1 pce | 10k | MF. 1%, 0204, E24 | 0 R 190 | 57 69 1097 1 pce | 10k | CE 5% 0603 |
| 0 10 10 | 50.60.1505 1 pce | 74UCEDE | 9bit chift/output register | 0 835 | 57 60 1391 1 nce | 390R | MF 1% 0204 E24 | 0 R 11 | 2 57.6 | 60.1103 1 pce | 10k | MF. 1%, 0204, E24 | 0 R 191 | 57.60.1103 1 pce | 10k | MF.1% 0204 E24 |
| 0 10 17 | 50.02.1050 pce | 74110050 | Obit shift/output register | 0 8 36 | 57.60.1391 1 nce | 390R | ME 1% 0204 E24 | 0 R 11 | 3 57.6 | 60 1221 1 pce | 220R | ME 1% 0204 F24 | 0 R 192 | 57.60.1102 1 pce | 1k0 | ME 1% 0204 E24 |
| 0 10 18 | 50.62.1595 pce | /480595 | 517 Det metrix dioploy groop | 0 8 37 | 57.60.1271 1 nce | 2708 | ME 1% 0204 E24 | 0 8 11 | 4 57.6 | 60.1271 1 pce | 270R | ME 1% 0204 E24 | 0 R 193 | 57.60.1102 1 pce | 1k0 | ME 1% 0204 E24 |
| 0 10 19 | 73.01.0406 pce | 401git | S 7 Dut matrix display green | 0 8 38 | 57.60.1221 1 pce | 220B | MF 1% 0204 E24 | 0 8 11 | 5 57.6 | 60.1391 1 pce | 390R | ME 1% 0204 E24 | 0 R 194 | 57.60.1102 1 pce | 1k0 | ME 1% 0204 E24 |
| 0 10 20 | 50.62.1595 pee | 74HC595 | Shit shift/output register | 0 8 39 | 57.60.1103 1 pce | 10k | MF 1% 0204 E24 | 0 8 11 | 6 57.6 | 60 1391 1 nce | 390R | MF 1% 0204 F24 | 0 R 195 | 57.60.1102 1 pce | 1k0 | ME 1% 0204 E24 |
| 0 10 21 | 72.04.0409 : pee | /4HC050 | 5*7 Dot matrix display groop | 0 R 40 | 57.60.1103 1 pce | 10k | MF. 1%. 0204. E24 | 0 8 11 | 7 57.6 | 60 1391 1 pce | 390R | ME 1% 0204 E24 | 0 R 196 | 57.60.1102 1 pce | 1k0 | MF.1%, 0204, E24 |
| 0 10 22 | 73.01.0406 pce | 40igit 74UCE0E | Shit shift/output register | 0 R41 | 57 60 1221 1 pce | 220B | MF. 1%. 0204. E24 | 0 8 11 | 8 57.6 | 60 1391 1 pce | 390R | ME 1% 0204 E24 | 0 R 197 | 57.60.1102 1 pce | 1k0 | MF.1%, 0204, E24 |
| 0 10 23 | 50.62.1595 pce | 74HC595 | Obit shift(eutput register | 0 8 42 | 57.60.1271 1 pce | 270R | ME 1% 0204 E24 | 0 8 11 | 9 57 6 | 60 1391 1 pce | 390R | ME 1% 0204 E24 | 0 R 198 | 57 60 1103 1 pce | 10k | ME 1% 0204 E24 |
| 0 10 24 | 30.02.1095 pce | 74RG595 | 517 Det metrix display groop | 0 843 | 57.60.1391 1 pce | 390R | MF 1% 0204 E24 | 0 8 12 | 0 57.6 | 60.1391 1 pce | 390R | MF 1% 0204 F24 | 0 R 199 | 57.60.1102 1 pce | 1k0 | ME 1% 0204 E24 |
| 0 10 25 | 73.01.0408 pce | 40/git | S 7 Dot matrix display green | 0 8 44 | 57.60.1391 1 pce | 390B | MF. 1% 0204, E24 | 0 8 12 | 1 57.6 | 80 1391 1 pce | 3908 | ME 1% 0204 E24 | 0 B 200 | 57.60 1102 1 pce | 1k0 | ME 1% 0204 E24 |
| 0 10 20 | 50.62.1595 i pue | 74HC595 | Abit shift/output register | 0 R45 | 57.60.1391 1 pce | 390B | MF. 1%. 0204. E24 | 0 8 12 | 2 57.6 | 80 1391 1 pre- | 390R | ME 1% 0204 E24 | 0 B 201 | 57.60.1102 1 pce | 1k0 | ME 1% 0204 E24 |
| 0 10 27 | 72.01.0409 i pee | /4HG555 | E*7 Dot motify display green | 0 R 46 | 57 60 1391 1 pce | 390B | MF. 1%, 0204, E24 | 0 12 12 | 2 576 | 60.1391 1.nce | 3008 | ME 1% 0204 E24 | 0 B 202 | 57.60.1102 1 pce | 1k0 | ME 1% 0204 E24 |
| 0 10 20 | 50.60.1505 1 pce | 7400606 | 9bit shift/output register | 0 R 47 | 57.60.1391 1 pce | 390B | MF, 1%, 0204, E24 | 0 8 12 | 0 57.6 M 57.6 | 60.1391 1 pce | 3908 | ME 1% 0204 F24 | 0 B 203 | 57.60.1102 1 oce | 1k0 | ME 1% 0204 E24 |
| 0 10 29 | 50.62.1595 1 pce | 74HC595 | Shit shift/output register | 0 R 48 | 57.60.1391 1 pce | 390R | MF. 1%. 0204, E24 | 0 8 12 | - 07.0 5 57.6 | 60 1391 1 pce | 3908 | MF 1% 0204 F24 | 0 B 204 | 57.60.1102 1 pce | 1k0 | MF.1%, 0204, E24 |
| 0 10 30 | 50.62.1595 T poe | 740033 | 2 to 9 line deceder | 0 R 49 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 8 12 | 6 57.6 | 60.1391 1.nce | 390R | MF 1% 0204 F24 | 0 B 205 | 57.69.1097 1 pce | 10k | CE 5% 0603 |
| 0 10 33 | 50.62.1135 1 pce | 74HC136 | Octal huffer line drive/recei | 0 R 50 | 57.60.1391 1 pce | 390R | MF, 1% 0204, E24 | 0 8 12 | 7 576 | 60.1271 1 nce | 270R | MF 1% 0204 F24 | 0 R 206 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 32 | 50.62.1541 1 pce | 74HC541 | Octal Dutier line drive necel | 0 R 51 | 57.60.1391 1 pce | 390R | MF, 1% 0204, E24 | 0 R12 | 28 57 F | 60.1221 1 pce | 220R | MF 1% 0204 E24 | 0 R 207 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 33 | 50.62.1573 Tpce | 74HC573 | Octal b-type laton | 0 8 52 | 57.60.1391 1 pce | 390R | MF. 1% 0204, E24 | 0 8 12 | 0 57 A | 60 1103 1 pce | 10k | MF 1% 0204 F24 | 0 8 208 | 57 69 1097 1 pcc | 10k | CF 5% 0603 |
| 0 10 34 | 10/02/1041 1 PCB | 14/16041 | SW Eronthoard (50.18.0314) | 0 R 53 | 57.60.1391 1 pce | 390R | MF, 1% 0204 E24 | 0 12 19 | 0 57 P | 60.1103 1 nce | 10k | MF. 1%, 0204, E24 | 0 R 209 | 57.69.1097 1 nos | 10k | CF 5% 0603 |
| 0 10 36 | 50.10.0104 1 pce | 1 M2179D | Series regulator 1.54 +37V | 0 R 54 | 57.60.1391 1 pce | 390R | MF, 1% 0204, E24 | 0 R 13 | 57.6 | 60.1221 1 pce | 220R | MF. 1%, 0204, E24 | 0 R 210 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 10 37 | 50.62.1573 1 pce | 74HC572 | Octal D-type latch | 0 R 55 | 57.60.1271 1 pce | 270R | MF, 1%. 0204, E24 | 0 R 13 | 2 57.6 | 60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 211 | 57.69.1097 1 DCF | 10k | CF 5% 0603 |
| 0 10 37 | 50.62 1573 1 pce | 74HC573 | Octal D-type latch | 0 R 56 | 57.60.1221 1 pce | 220R | MF, 1%. 0204, E24 | 0 R 13 | 3 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 212 | 57.69.1097 1 DCF | 10k | CF 5% 0603 |
| 0 10 30 | 50.62.1595 1 pce | 74HC595 | 8bit shift/output register | 0 R 57 | 57.60.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 13 | 4 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 213 | 57.92.7011 1 DCE | 0.2A | PTC 60V |
| 0 10.40 | 50.62.1138 1 pre | 74HC138 | 3 to 8 line decoder | 0 R 58 | 57.60.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 13 | 5 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 214 | 57.60.1103 1 pce | 10k | MF,1%, 0204, E24 |
| 0 10 41 | 50.61.8101 1 pce | 68HC68 | A/D Converter 10bit 8Ch SO 20 | 0 R 59 | 57.60.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 13 | 36 57. 6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 215 | 57.60.1103 1 pce | 10k | MF,1%, 0204, E24 |
| 0 10 42 | 50.61.8101 1 pce | 68HC68 | A/D Converter 10bit 8Ch SO 20 | 0 R 60 | 57.60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 13 | 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 216 | 57.60.1331 1 pce | 330R | MF,1%, 0204, E24 |
| 0 10 42 | 50.60.1100 i pee | 74110128 | 2 to 9 line decodes | 0 R 61 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 13 | 8 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 217 | 57.60.1102 1 pce | 1k0 | MF.1%, 0204, E24 |
| 0 10 43 | 50.02.1138 i pce | 74HC138 | 3 to 8 line decoder | 0 R 62 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 13 | 9 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 218 | 57.60.1103 1 pce | 10k | MF 1%, 0204, E24 |
| 0 10 44 | 50.02.1136 i pce | 74HC138 | 3 to 8 line decoder | 0 R 63 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 14 | 0 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 219 | 57.60.1271 1 pce | 270R | MF.1%, 0204, E24 |
| 0 10 40 | 50.63.1503 1 PCB | 7440462 | Showing 32N-0, 10018 Synchr proport 4bit counter bin | 0 R 64 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 14 | 1 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 220 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 10 40 | not used 1 see | 20E010 | Synow preset 40tt counter bitt | 0 R 65 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 14 | 2 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 221 | 57.60.1391 1 pce | 390R | MF,1%, 0204, E24 |
| 0 10 40 | 60.63.1109 1 pce | 295010 | EEDDOM 64*16 SO 9 | 0 R 66 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 14 | 3 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 222 | 57.60.1391 1 nce | 390R | MF,1%,0204, E24 |
| 0 10 48 | 00.03.1100 1 PC0 | 74UC120 | 2 to R line decoder | 0 R 67 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 14 | 4 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 223 | 57.60.1391 1 nos | 390R | MF.1%, 0204, E24 |
| 0 10 49 | 00.02.1138 1 pce | 74HG138 | s to a line decoder | 0 R 68 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 14 | 5 57.6 | 60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 224 | 57.69.1097 1 nce | 10k | CF 5% 0603 |
| 0 10 50 | 50.62.1014 1 pce | 74HU 14 | nex adminit trigger inverter | 0 R 69 | 57.60.1391 1 pcs | 390R | MF, 1%, 0204. E24 | 0 R 14 | 6 57 P | 60.1221 1 DCR | 220R | MF, 1%, 0204, E24 | 0 R 225 | 57.69,1097 1 009 | 10k | CF 5% 0603 |
| 0 10 51 | 50.52.1595 1 pce | 74HC595 | oox shirt/output register | 0 R 70 | 57.60.1391 1 pca | 390R | MF, 1%, 0204. E24 | 0 R 14 | 7 57 P | 60.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 226 | 57.69.1097 1 nce | 10k | CF 5% 0603 |
| 0 10 52 | 00.02.1138 1 PCP | 74HG138 | o to a line decoder Quad Sinput AND | 0 R71 | 57.60.1391 1 pce | 390R | MF, 1%, 0204. E24 | 0 R 14 | 8 57.6 | 60.1103 1 pce | 10k | MF, 1%, 0204, E24 | 0 R 227 | 57,69,1097 1 noe | 10k | CF 5% 0603 |
| u 10.53 | 00.02.1008 1 pce | 74HC 08 | Quad Zinput AND | 0 R 72 | 57.60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 14 | 9 57.6 | 60.1221 1 pce | 220R | MF, 1%, 0204, E24 | 0 R 228 | 57.69.1097 1 noe | 10k | CF 5% 0603 |
| 0 MP 1 | 1.942.710.12 1 pce | | FRONTBOARD PCB | 0 R 73 | 57.60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 15 | 50 57.6 | 60.1271 1 pce | 270R | MF, 1%, 0204, E24 | 0 R 229 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 MP 2 | 43.01.0108 1 pce | Labei | ESE-WARNSCHILD | 0 R 74 | 57.60.1221 1 pce | 220R | MF, 1%, 0204. E24 | 0 R 15 | 51 57 P | 60.1391 1 DCA | 390R | MF, 1%, 0204, E24 | 0 R 230 | 57.69.1097 1 pce | 10k | CF 5% 0603 |
| 0 MP 3 | 1.942.710.10 1 pce | | NR.ETIKETTE | 0 R 75 | 57.60.1103 1 pce | 10k | MF, 1%, 0204. E24 | 0 R 15 | 52 57 6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 231 | 57,69,1097 1 nos | 10k | CF 5% 0603 |
| 0 MP 4 | 1.942.710.01 6 pcs | | KONTAKTMATTEN SET | | | | | 0 R 15 | 53 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, E24 | 0 R 232 | 57.60.1101 1 pce | 100R | MF, 1%, 0204, E24 |
| | | | | | | | | 0 R 15 | 54 57.6 | 60.1391 1 pce | 390R | MF, 1%, 0204, £24 | 0 R 233 | 57.60.1101 1 nce | 100R | MF.1%, 0204, E24 |
| U P1 | 54.14.2054 1 pce | 40p | Stecker gerade Au | | | | | | | | | | | | | |

Front Board 1.942.710.20

| 0 R 234 57.60.1000 1 pce 0 R0 MF, 0 204 0 R 235 57.92.7653 1 pce 1 6A PTC 30V 0 R 237 57.60.1121 1 pce 1 20R MF, 1%, 0204, E24 0 R 240 not used 1 pce 1 k0 MF, 1%, 0204, E24 0 R 241 57.60.1102 1 pce 1 k0 MF, 1%, 0204, E24 0 R 242 57.60.1102 1 pce 1 k0 MF, 1%, 0204, E24 0 R 243 not used 1 pce 330R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 1 k0 MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 1 k0 MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 1 00R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 1 00R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 100R MF, 1%, 0204, E24 | ldx | Pos. | Part No. | Qty. | Type/Val. | Description |
|--|-----|--------|-------------|-------|------------|--------------------------------------|
| 0 R 235 57.92.7053 1 pce 1.6A PTC 30V 0 R 236 57.60.1121 1 pce 120R MF, 1%, 0204, E24 0 R 240 not used 1 pce 330R MF, 1%, 0204, E24 0 R 241 57.60.1102 1 pce 1k0 MF, 1%, 0204, E24 0 R 241 57.60.1102 1 pce 1k0 MF, 1%, 0204, E24 0 R 243 not used 1 pce 1k0 MF, 1%, 0204, E24 0 R 244 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 270R MF, 1%, 0204, E24 0 | 0 | R 234 | 57.60.1000 | 1 pce | 0R0 | MF, 0204 |
| 0 R 236 57.60.1121 1 pce 120R MF, 1%, 0204, E24 0 R 237 57.60.1102 1 pce 330R MF, 1%, 0204, E24 0 R 241 57.60.1102 1 pce 180 MF, 1%, 0204, E24 0 R 241 57.60.1102 1 pce 180 MF, 1%, 0204, E24 0 R 243 not used 1 pce 330R MF, 1%, 0204, E24 0 R 244 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 247 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 253 57.60.1103 1 pce 270R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 390R MF, 1%, 0204, E24 <td< td=""><td>0</td><td>R 235</td><td>57.92.7053</td><td>1 pce</td><td>1.6A</td><td>PTC 30V</td></td<> | 0 | R 235 | 57.92.7053 | 1 pce | 1.6A | PTC 30V |
| 0 R 237 57.60.1121 1 pce 120R MF, 1%, 0204, E24 0 R 240 not used 1 pce 140 MF, 1%, 0204, E24 0 R 241 57.60.1102 1 pce 1k0 MF, 1%, 0204, E24 0 R 242 57.60.1102 1 pce 1k0 MF, 1%, 0204, E24 0 R 244 57.60.1101 1 pce 330R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 250 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 20R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 300R MF, 1%, 0204, E24 0 | 0 | R 236 | 57.60.1121 | 1 pce | 120R | MF, 1%, 0204, E24 |
| 0 R 240 not used 1 pce 330R MF, 1%, 0204, E24 0 R 241 57.60.1102 1 pce 1k0 MF, 1%, 0204, E24 0 R 242 57.60.1102 1 pce 1k0 MF, 1%, 0204, E24 0 R 243 not used 1 pce 330R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 1k0 MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 1k0 MF, 1%, 0204, E24 0 R 247 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 254 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 </td <td>0</td> <td>R 237</td> <td>57.60.1121</td> <td>1 pce</td> <td>120R</td> <td>MF, 1%, 0204, E24</td> | 0 | R 237 | 57.60.1121 | 1 pce | 120R | MF, 1%, 0204, E24 |
| 0 R 240 not used 1 pce 30R MF, 1%, 0204, E24 0 R 241 57.80.1102 1 pce 1k0 MF, 1%, 0204, E24 0 R 243 not used 1 pce 30R MF, 1%, 0204, E24 0 R 245 57.80.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.80.1101 1 pce 100R MF, 1%, 0204, E24 0 R 247 57.80.1101 1 pce 100R MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0< | ٥ | P. 230 | 57.60.1102 | 1 peo | 1k0 | MF, 1%, 0204, E24 |
| 0 R 241 57.60.1102 1 pce 1k0 MF, 1%, 0204, E24 0 R 242 57.60.1102 1 pce 330R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 330R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 1k0 MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 1k0 MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 250 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 10k CF 5% 0603 0 | 0 | R 240 | not used | 1 pce | 330R | MF, 1%, 0204, E24 |
| 0 R 242 57.60.1102 1 pce 330R MF, 1%, 0204, E24 0 R 243 not used 1 pce 330R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 247 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 250 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 10k CF 5% 0603 0 <td>0</td> <td>R 241</td> <td>57.60.1102</td> <td>1 pce</td> <td>1k0</td> <td>MF, 1%, 0204, E24</td> | 0 | R 241 | 57.60.1102 | 1 pce | 1k0 | MF, 1%, 0204, E24 |
| 0 R 243 not used 1 pce 330R MF, 1%, 0204, E24 0 R 245 57,60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 246 57,60.1331 1 pce 100R MF, 1%, 0204, E24 0 R 247 57,60.1331 1 pce 100R MF, 1%, 0204, E24 0 R 248 57,60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 248 57,60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57,60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 253 57,60.1271 1 pce 270R MF, 1%, 0204, E24 0 R 256 57,60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57,60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57,60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57,60.1391 1 pce 100k CF 5% 0603 0 <td>0</td> <td>R 242</td> <td>57.60.1102</td> <td>1 pce</td> <td>1k0</td> <td>MF, 1%, 0204, E24</td> | 0 | R 242 | 57.60.1102 | 1 pce | 1k0 | MF, 1%, 0204, E24 |
| 0 R 244 57 60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 245 57.60.1101 1 pce 1k0 MF, 1%, 0204, E24 0 R 247 57.60.1331 1 pce 1k0 MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 249 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 253 57.60.1103 1 pce 270R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 254 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.69.1097 1 pce 10k CF 5% 0603 0 <td>0</td> <td>R 243</td> <td>not used</td> <td>1 pce</td> <td>330R</td> <td>MF, 1%, 0204, E24</td> | 0 | R 243 | not used | 1 pce | 330R | MF, 1%, 0204, E24 |
| 0 R 245 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 247 57.60.1331 1 pce 330R MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 250 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 253 57.60.1271 1 pce 270R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 10k CF 5% 0603 0 R 256 57.69.1097 1 pce 10k CF 5% 0603 | 0 | R 244 | 57 60 1331 | 1 pce | 330R | MF, 1%, 0204, F24 |
| 0 R 246 57.60.1102 1 pce 1 k0 MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 250 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 252 57.60.1103 1 pce 270R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 10k CF 5% 0603 0 R 256 57.69.1097 1 pce 10k CF 5% 0603 0 R 256 57.69.1097 1 pce 10k CF 5% 0603 <t< td=""><td>0</td><td>R 245</td><td>57.60.1101</td><td>1 pce</td><td>100R</td><td>MF, 1%, 0204, E24</td></t<> | 0 | R 245 | 57.60.1101 | 1 pce | 100R | MF, 1%, 0204, E24 |
| 0 R 247 57.60.1331 1 pce 330R MF, 1%, 0204, E24 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 250 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 270R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 | 0 | R 246 | 57.60.1102 | 1 pce | 1k0 | MF, 1%, 0204, E24 |
| 0 R 248 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 250 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 252 57.60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 252 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.69.1097 1 pce 10k CF 5% 0603 0 R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 < | 0 | R 247 | 57.60.1331 | 1 pce | 330R | MF, 1%, 0204, E24 |
| 0 R 249 57 60 1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57,60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 253 57,60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 253 57,60.1103 1 pce 270R MF, 1%, 0204, E24 0 R 255 57,60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57,60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57,60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57,60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57,60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57,60.1097 1 pce 10k CF 5% 0603 0 R 261 57,69.1097 1 pce 10k CF 5% 0603 0 R 263 57,69.1097 1 pce 10k CF 5% 0603 0 R 264 57,69.1097 1 pce 10k CF 5% 0603 0 | 0 | R 248 | 57.60.1101 | 1 pce | 100R | MF, 1%, 0204, E24 |
| 0 R 250 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 251 57.60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 253 57.60.1271 1 pce 270R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 390R MF 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 | ٥ | R 249 | 57 60 1101 | 1 pce | 100R | MF, 1%, 0204, E24 |
| 0 R 251 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 253 57.60.1271 1 pce 270R MF, 1%, 0204, E24 0 R 253 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 260 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.89.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 | 0 | R 250 | 57.60.1101 | 1 pce | 100R | MF, 1%, 0204, E24 |
| 0 R 252 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 253 57.60.1271 1 pce 270R MF, 1%, 0204, E24 0 R 254 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 257 57.60.1391 1 pce 10k CF 5% 0603 0 R 259 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k </td <td>0</td> <td>R 251</td> <td>57.60.1101</td> <td>1 pce</td> <td>100R</td> <td>MF, 1%, 0204, E24</td> | 0 | R 251 | 57.60.1101 | 1 pce | 100R | MF, 1%, 0204, E24 |
| 0 R 253 57.60.1271 1 pce 270R MF, 1%, 0204, E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 258 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 258 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 260 57.69.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF <td>0</td> <td>R 252</td> <td>57.60.1103</td> <td>1 pce</td> <td>10k</td> <td>MF, 1%, 0204, E24</td> | 0 | R 252 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 |
| R 254 57.60.1391 1 pce 390R MF. 1%. 0204. E24 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 257 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 267 not used 1 pce 10k CF | 0 | R 253 | 57.60.1271 | 1 pce | 270R | MF, 1%, 0204, E24 |
| 0 R 255 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 257 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 260 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 267 not used 1 pce | 0 | R 254 | 57 60 1391 | 1 pce | 390R | MF 1% 0204 F24 |
| 0 R 256 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 257 57.60.1391 1 pce 10k CF 5% 0603 0 R 259 57.69.1097 1 pce 10k CF 5% 0603 0 R 259 57.69.1097 1 pce 10k CF 5% 0603 0 R 260 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k MF, 1%, 0204, E24 0 R 268 57.60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce | 0 | R 255 | 57.60.1391 | 1 pce | 390R | MF, 1%, 0204, E24 |
| 0 R 257 57.60.1391 1 pce 390R MF, 1%, 0204, E24 0 R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 250 57.69.1097 1 pce 10k CF 5% 0603 0 R 260 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 <td< td=""><td>0</td><td>R 256</td><td>57.60.1391</td><td>1 pce</td><td>390R</td><td>MF, 1%, 0204, E24</td></td<> | 0 | R 256 | 57.60.1391 | 1 pce | 390R | MF, 1%, 0204, E24 |
| R 258 57.69.1097 1 pce 10k CF 5% 0603 0 R 259 57.69.1097 1 pce 10k CF 5% 0603 0 R 260 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 | 0 | R 257 | 57 60 1391 | 1 nce | 390R | ME 1% 0204 E24 |
| 0 R 259 57.68.1097 1 pce 10k CF 5% 0603 0 R 260 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce </td <td>õ</td> <td>R 258</td> <td>57 69 1097</td> <td>1 pce</td> <td>10k</td> <td>CE 5% 0603</td> | õ | R 258 | 57 69 1097 | 1 pce | 10k | CE 5% 0603 |
| 0 R 260 57.69.1097 1 pce 10k CF 5% 0603 0 R 261 57.69.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 | 0 | R 250 | 57 69 1097 | 1 nce | 10k | CE 5% 0603 |
| 0 R 261 57.68.1097 1 pce 10k CF 5% 0603 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.68.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 269 not used 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1101 1 pce 10k MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce | 0 | R 260 | 57 69 1097 | 1 nce | 10k | CF 5% 0603 |
| 0 R 2261 57.68.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 264 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 100R MF, 1%, 0204, E24 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1103 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 | ñ | R 261 | 57 69 1097 | 1 nce | 10k | CE 5% 0603 |
| 0 R 262 57.69.1097 1 pce 10k CF 5% 0603 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 100R MF, 1%, 0204, E24 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 272 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 </td <td>ñ</td> <td>R 262</td> <td>57 69 1097</td> <td>1 nce</td> <td>10k</td> <td>CE 5% 0603</td> | ñ | R 262 | 57 69 1097 | 1 nce | 10k | CE 5% 0603 |
| 0 R 263 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 100R MF, 1%, 0204, E24 0 R 268 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 272 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R <td< td=""><td>0</td><td>D 263</td><td>57 69 1097</td><td>1 pco</td><td>10k</td><td>CE 5% 0603</td></td<> | 0 | D 263 | 57 69 1097 | 1 pco | 10k | CE 5% 0603 |
| 0 R 204 57.05.1097 1 pce 10k CF 57.6003 0 R 265 57.69.1097 1 pce 10k CF 5% 0603 0 R 266 57.69.1097 1 pce 10k CF 5% 0603 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 268 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 272 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 41 58.30.0101 1 pce 10k lin Fader 100m 0 R A2 58.30.0101 1 pce 10k | 0 | N 200 | 57.03.1037 | 1 pce | 10k | |
| 0 R 266 57.68.1097 1 pce 10k CF 5% 0603 0 R 267 not used 1 pce 100R MF, 1%, 0204, E24 0 R 268 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 269 not used 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 272 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 10k lin Fader 1 | ñ | R 265 | 57 69 1097 | 1 nce | 10k | CF 5% 0603 |
| 0 R 267 not used 1 pce 100 R MF, 1%, 0204, E24 0 R 268 57.60.1101 1 pce 100 R MF, 1%, 0204, E24 0 R 269 not used 1 pce 100 R MF, 1%, 0204, E24 0 R 269 not used 1 pce 10k MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 272 57.60.1101 1 pce 100 R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100 R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100 R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100 R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100 R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100 R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 10k lin Fader 100 m 0 R 4 </td <td>ñ</td> <td>P 266</td> <td>57 69 1097</td> <td>1 000</td> <td>104</td> <td>CE 5% 0603</td> | ñ | P 266 | 57 69 1097 | 1 000 | 104 | CE 5% 0603 |
| 0 R 268 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 269 not used 1 pce 100R MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 272 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m | 0 | P 267 | notused | 1 pce | 1000 | ME 1% 0204 E24 |
| 0 R 269 not used 1 pce 160 K MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 270 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 271 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 272 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 <t< td=""><td>0</td><td>D 260</td><td>57 60 1101</td><td>1 000</td><td>1000</td><td>ME 1% 0204 E24</td></t<> | 0 | D 260 | 57 60 1101 | 1 000 | 1000 | ME 1% 0204 E24 |
| 0 R 203 Hot Used Hot | 0 | R 200 | 57.00.1101 | 1 000 | 1000 | MF, 1%, 0204, E24 |
| 0 R 271 57.60.1103 1 pce 10k MF, 1%, 0204, E24 0 R 272 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m < | 0 | R 209 | 57 60 1103 | 1 pce | 10k | MF, 1%, 0204, E24 MF 1%, 0204 E24 |
| 0 R 211 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 58.30.0101 1 pce 10k lin Fader 100m 0 R 4 | ň | R 271 | 57 60 1103 | 1 nca | 10k | ME 1% 0204 E24 |
| 0 R 272 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, E24 0 R A 1 58.30.0101 1 pce 10k lin Fader 100m 0 RA 2 58.30.0101 1 pce 10k lin Fader 100m 0 RA 3 58.30.0101 1 pce 10k lin Fader 100m 0 RA 4 58.30.0101 1 pce 10k lin Fader 100m 0 RA 5 58.30.0101 1 pce 10k lin Fader 100m 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10< | 0 | D 272 | 57 60 1101 | 1 000 | 1008 | ME 1% 0204 E24 |
| 0 R 273 57.60.1101 1 pce 100R MF, 1%, 0204, 224 0 R 274 57.60.1101 1 pce 100R MF, 1%, 0204, 224 0 R A 1 58.30.0101 1 pce 10k lin Fader 100m 0 R A 2 58.30.0101 1 pce 10k lin Fader 100m 0 R A 3 58.30.0101 1 pce 10k lin Fader 100m 0 R A 3 58.30.0101 1 pce 10k lin Fader 100m 0 R A 4 58.30.0101 1 pce 10k lin Fader 100m 0 R A 5 58.30.0101 1 pce 10k lin Fader 100m 0 R A 5 58.30.0101 1 pce 10k lin Fader 100m 0 R A 5 58.30.0101 1 pce 10k lin Fader 100m 0 R A 8 58.30.0101 1 pce 10k lin Fader 100m 0 R A 9 58.30.0101 1 pce 10k lin Fader 100m 0 R A 10 58.30.0101 1 pce 10k lin Fader 100m 0 | 0 | D 272 | 57 60 1101 | 1 pce | 1000 | ME 1% 0204 E24 |
| 0 R 274 57.50.1101 1 pce 100 R MP, 178, 0204, E24 0 RA 1 58.30.0101 1 pce 10k lin Fader 100m 0 RA 2 58.30.0101 1 pce 10k lin Fader 100m 0 RA 3 58.30.0101 1 pce 10k lin Fader 100m 0 RA 3 58.30.0101 1 pce 10k lin Fader 100m 0 RA 4 58.30.0101 1 pce 10k lin Fader 100m 0 RA 5 58.30.0101 1 pce 10k lin Fader 100m 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 | 0 | D 074 | 57.00.1101 | 1 000 | 1000 | MF, 1%, 0204, E24 |
| 0 RA 1 58.30.0101 1 pce 10k lin Fader 100m 0 RA 2 58.30.0101 1 pce 10k lin Fader 100m 0 RA 3 58.30.0101 1 pce 10k lin Fader 100m 0 RA 4 58.30.0101 1 pce 10k lin Fader 100m 0 RA 4 58.30.0101 1 pce 10k lin Fader 100m 0 RA 5 58.30.0101 1 pce 10k lin Fader 100m 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 </td <td>J</td> <td>17 2/4</td> <td>07,00.1101</td> <td>i hea</td> <td>TUUK</td> <td>WI , 170, UZU4, EZ4</td> | J | 17 2/4 | 07,00.1101 | i hea | TUUK | WI , 170, UZU4, EZ4 |
| 0 RA 2 58.30.0101 1 pce 10k lin Fader 100m 0 RA 3 58.30.0101 1 pce 10k lin Fader 100m 0 RA 4 58.30.0101 1 pce 10k lin Fader 100m 0 RA 5 58.30.0101 1 pce 10k lin Fader 100m 0 RA 5 58.30.0101 1 pce 10k lin Fader 100m 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 <t< td=""><td>0</td><td>RA 1</td><td>58.30.0101</td><td>1 pce</td><td>10k lin</td><td>Fader 100m</td></t<> | 0 | RA 1 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA3 58.30.0101 1 pce 10k lin Fader 100m 0 RA4 58.30.0101 1 pce 10k lin Fader 100m 0 RA5 58.30.0101 1 pce 10k lin Fader 100m 0 RA6 58.30.0101 1 pce 10k lin Fader 100m 0 RA6 58.30.0101 1 pce 10k lin Fader 100m 0 RA7 58.30.0101 1 pce 10k lin Fader 100m 0 RA7 58.30.0101 1 pce 10k lin Fader 100m 0 RA8 58.30.0101 1 pce 10k lin Fader 100m 0 RA9 58.30.0101 1 pce 10k lin Fader 100m 0 RA10 53.30.0101 1 pce 10k lin Fader 100m 0 RA11 58.30.0101 1 pce 10k lin Fader 100m 0 RA11 58.30.0101 1 pce 10k lin Fader 100m 0 RA12 58.30.0101 1 pce 10k lin Fader 100m 0 RA12 58.30.0101< | 0 | RA 2 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 4 58.30.0101 1 pce 10k lin Fader 100m 0 RA 5 58.30.0101 1 pce 10k lin Fader 100m 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0 | 0 | RA 3 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 5 56.30.0101 1 pce 10k lin Fader 100m 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", löt, gerade 0 XIC 47 <t< td=""><td>0</td><td>RA 4</td><td>58.30.0101</td><td>1 pce</td><td>10k lin</td><td>Fader 100m</td></t<> | 0 | RA 4 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 6 58.30.0101 1 pce 10k lin Fader 100m 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", löt, gerade 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 | RA 5 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 7 58.30.0101 1 pce 10k lin Fader 100m 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", löt, gerade 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 | RA 6 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 8 58.30.0101 1 pce 10k lin Fader 100m 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", löt, gerade 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 | RA 7 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 9 58.30.0101 1 pce 10k lin Fader 100m 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", iöt, gerade 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 | RA 8 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 10 58.30.0101 1 pce 10k lin Fader 100m 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", löt, gerade 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 | RA 9 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 11 58.30.0101 1 pce 10k lin Fader 100m 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", löt, gerade 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 · | RA 10 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 RA 12 58.30.0101 1 pce 10k lin Fader 100m 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", löt, gerade 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 | RA 11 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 XIC 35 53.03.0172 1 pce 40p DIL 0.6", iöt, gerade 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 | RA 12 | 58.30.0101 | 1 pce | 10k lin | Fader 100m |
| 0 XIC 47 not used 1 pce 32p PLCC-Socket | 0 | XIC 35 | 53 03 0172 | 1 nce | 40n | DII 0.6" löt derade |
| | 0 | XIC 47 | not used | 1 pce | 32p | PLCC-Socket |
| | - | ×. | 00 00 400 4 | 1 | | |
| י די אין אין אין אין אין אין אין אין אין אי | U | 11 | 89.60.1004 | 1 pce | 22.1184MHz | |

Comments:

DSP Board 1.942.602.20



P1/11, (1) 19.7.99/SG 1.942.102.22

DSP Board 1.942.602.20



DSP Board 1.942.602.20



P3/11, (1) 19.7.99/SG 1.942.102.22

On-Air 1000 Digital Mixing Console

STUDER

DSP Board 1.942.602.20



DSP Board 1.942.602.20



P5/11, (1) 19.7.99/SG 1.942.102.22

On-Air 1000 Digital Mixing Console

STUDER





DSP Board 1.942.602.20



On-Air 1000 Digital Mixing Console

STUDER







P9/11, (1) 19:7.99/SG 1.942.102.22

P73 12

P73 13

P73 14

P73 15

P73 16

+24V

VERR



P10/11, (1) 19.7:99/SG 1.942.102.22

STUDER

On-Air 1000 Digital Mixing Console

DSP Board 1.942.602.20



P11/11, (1) 19.7.99/SG 1.942.102.22

On-Air 1000 Digital Mixing Console

STUDER

DSP Board 1.942.602.20

* For DSP Board On-Air 1000 (1.942.602.xx), diagram 1.942.102.xx is valid. The following components are not used for 1.942.602.xx: IC15, IC16, IC19, IC20, IC23, IC24 ICs: P3. P4 Connectors: Software 1.942.908.20 is replaced by 1.942.958.20 (IC26, IC27, IC28)



DETAL A

MP5

REGENSCORF

7& R197 *x*= / C108

677 R198

Page: 1 of 4

| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | | ldx. | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|------|------------|------|-----------|---------------|-----------|--------|---------|--------------|------|-------------------|--------------------------------|
| | | | | | | | ٥ | C 88 | 59 60 3337 | | 100n | CER 50V 10% X7R 0805 |
| 0 | C 1 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | ő | C 89 | 59 60 3337 | | 100n | CER 50V 10% X7R 0805 |
| 0 | C 2 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 90 | 59 60 3337 | | 100n | CER 50V 10% X7R 0805 |
| 0 | C 3 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | ő | C 91 | 50.60.3337 | | 100n | CER 50V 10% X7R 0805 |
| 0 | C 4 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | 0 91 | 50.00.3337 | | 1000 | CER 50V, 10%, X7R, 0805 |
| 0 | C 5 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 92 | 50 60 2227 | | 1000 | CER 50V, 10%, X7R, 0005 |
| ٥ | Ce | 50.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 93 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 7 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | n n | C 95 | 59 60 3337 | | 100n | CER 50V 10% X7R 0805 |
| 0 | C 8 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | Ň | C 06 | 50 60 3337 | | 100n | CER 50V 10% X7R 0805 |
| 0 | C 9 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 90 | 50 60 2227 | | 1000 | CER 50V, 10%, X7R, 0005 |
| 0 | C 10 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 97 | 59.00.3337 | | 1000 | CER 50V, 10%, X7R, 0005 |
| 0 | C 11 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | 0.98 | 59.00.3337 | | 1000 | CER 30V, 10%, X/R, 0003 |
| 0 | C 12 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 100 | 50.60.3337 | | 1000 | CER 50V, 10%, X7R, 0000 |
| 0 | C 13 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 100 | 50.60.0470 | | 1001 | CER 63V 5% COG 0805 |
| 0 | C 14 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | 0 101 | 59.00.0470 | | 47p | CER 63V, 5%, COG, 0805 |
| 0 | C 15 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 102 | 59.00.0470 | | 47p | CER 63V, 5%, COG, 0805 |
| 0 | C 16 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | 0 103 | 59.60.3337 | | 1000 | CER 50V, 10%, X/R, 0805 |
| U | C 17 | 59.00.3337 | | 1000 | CER 50V, 10% | X7R, 0000 | 0 | C 104 | 59 60 3337 | | 100n | CER 50V. 10%. X7R. 0805 |
| 0 | C 18 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 105 | 59.00.3337 | | 1000 | CER 50V, 10%, X/R, 0805 |
| 0 | C 19 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | 0 100 | 59.60.3337 | | 1001 | CER 50V, 10%, X/R, 0805 |
| 0 | C 20 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 107 | 59.22.3470 | | 4/0 | EL 10V 20% RM5 |
| 0 | C 21 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 108 | 59.60.2237 | | 33p | CER 50V, 5%, COG, 0603 |
| 0 | C 22 | 59.60.3337 | | 100n | CER 50V. 10% | X7R. 0805 | 0 | C 109 | 59.60.1472 | | 4n7 | CER 63V, 10%, X7R, 0805 |
| 0 | C 23 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 110 | 59.00.3337 | | 1000 | CER 50V, 10%, X/R, 0805 |
| 0 | C 24 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | 0 111 | 59.60.3337 | | 1000 | CER 50V, 10%, X/R, 0805 |
| 0 | C 25 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 112 | 59.60.2237 | | 33p | CER 50V, 5%, COG, 0603 |
| 0 | C 26 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 113 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 27 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 114 | 59.60.2237 | | 33p | CER 50V, 5%, C0G, 0603 |
| 0 | C 28 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 115 | 50.60.3337 | | 100n | CER 50V, 10%, X7R, 0905 |
| 0 | C 29 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 116 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 30 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 117 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 31 | 59.60.3337 | | 100n | CER 50V. 10% | X7R. 0805 | 0 | C 118 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 32 | 59.60.3337 | | 100n | CER 50V. 10% | X7R. 0805 | 0 | C 119 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 33 | 60.60.3337 | | 100n | CER 60V, 10% | X7R, 0905 | 0 | C 120 | 59.60.0560 | | 56p | CER 63V, 5%, C0G, 0805 |
| 0 | C 34 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | C 121 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| Ó | C 35 | 59.60.3337 | | 100n | CER 50V. 10% | X7R. 0805 | 0 | C 122 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| ő | C 36 | 59 60 3337 | | 100n | CER 50V 10% | X7B. 0805 | 0 | C 123 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 |
| ő | C 37 | 59 22 6221 | | 2200 | EL 40V 20% | RM5 | 0 | D 1 | 50.04.0105 | | 1N4004 | 1A, DO 41 |
| 0 | C 38 | 59 60 3337 | | 1000 | CER 50V 10% | X7R 0805 | 0 | D 2 | 50.04.0105 | | 1N4004 | 1A, DO 41 |
| ŏ | C 39 | 59.60.3337 | | 100n | CER 50V, 10% | X7R, 0805 | U | D 3 | 1008.00.00 | | 4448 | 200MA 75V 4NS 50D 80 |
| 0 | C 40 | 59.60.3337 | | 100n | CER 50V. 10% | X7R. 0805 | 0 | D 4 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 |
| ő | C 41 | 59 60 3337 | | 100n | CER 50V 10% | X7R 0805 | 0 | IC 1 | 50.14.1009 | | 7C128A | SRAM 2K*8 35ns |
| ő | C 42 | 50 60 3337 | | 100n | CER 50V 10% | X7R 0805 | 0 | IC 2 | 50.14.1009 | | 7C128A | SRAM 2K*8 35ns |
| 0 | C 43 | 50 60 3337 | | 100n | CER 50V, 10% | X7R, 0805 | 0 | IC 3 | 1.942.906.20 | | | SW.102 DSP BOARD, OCTOPUS1 |
| 0 | C 44 | 38.00.3337 | | 10011 | CER 30V, 10% | X78,0000 | | | | | 50634202. EPLD 7 | 064 |
| õ | C 45 | 59 60 3337 | | 100n | CER 50V 10% | X78 0805 | 0 | IC 4 | 1.942.907.20 | | | SW.102 DSP BOARD, OCTOPUS2 |
| ñ | C 46 | 50 60 3337 | | 100n | CER 50V 10% | X7R 0805 | • | | E0 1E 010E | | 50634202, EPLD 7 |)64 |
| õ | C 47 | 50 60 3337 | | 1000 | CER 50V 10% | X7R 0805 | 0 | | 50.15.0105 | | 3407 | IC MC 3467 P, DS 3467 N, |
| 0 | 0.47 | 50.00.0007 | | 1000 | CER 50V, 10% | X7R, 0005 | 0 | 10.6 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| 0 | C 48 | 59.00.3337 | | 1000 | CER 50V, 10% | X7R, 0805 | 0 | IC 7 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| 0 | C 49 | 59.00.3337 | | 1000 | CER 50V, 10% | X7R, 0805 | 0 | IC 8 | 50.62.1157 | | 74HC157 | Quad 2ch multiplexer |
| 0 | 0.54 | 59.00.3337 | | 1000 | CER 50V, 10% | X7R, 0805 | 0 | 10.9 | 50.62.1004 | | 74HC 04 | Hex inverter |
| 0 | 0.51 | 59.00.3337 | | 1000 | CER 50V, 10% | X7R, 0805 | 0 | IC 10 | 50.62.1157 | | 74HC157 | Quad 2ch multiplexer |
| 0 | 0.52 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 11 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | 0.53 | 59.00.3337 | | 1000 | CER 50V, 10%, | X7R, 0805 | 0 | IC 12 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | 0.54 | 59.60.3337 | | 100n | CER 50V, 10%, | X/R, 0805 | 0 | IC 13 | 50.63.0407 | | TMS57070 | DSP 24 bit |
| 0 | 0.55 | 59.22.34/1 | | 470u | EL 10V 20% | | 0 | IC 14 | 50.63.0407 | | TMS57070 | DSP 24 bit |
| 0 | 0.55 | 59.60.3337 | | 1000 | CER 50V, 10%, | X7R, 0805 | 0 | IC 15 | not used | | TMS57070 | DSP 24 bit |
| 0 | 0.57 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 16 | not used | | TMS57070 | DSP 24 bit |
| 0 | 0.58 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 17 | 50.63.0407 | | TMS57070 | DSP 24 bit |
| 0 | C 59 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 18 | 50.63.0407 | | TMS57070 | DSP 24 bit |
| 0 | C 60 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 19 | not used | | TMS57070 | DSP 24 bit |
| 0 | 0.01 | 59.00.3337 | | 1000 | CER 50V, 10%, | A/R, 0800 | 0 | IC 20 | not used | | 1MS57070 | DSP 24 bit |
| U | 0.02 | 59.60.3337 | | 1000 | CER 50V, 10%, | A/K, U800 | 0 | IC 21 | 50.63.0407 | | IMS57070 | DSP 24 bit |
| 0 | 0.63 | 59.60.3337 | | 1000 | CER 50V, 10%, | X/K, 0805 | 0 | IC 22 | 50.63.0407 | | TMS57070 | DSP 24 bit |
| 0 | C 64 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 23 | not used | | TMS57070 | DSP 24 bit |
| 0 | C 65 | 59.60.3337 | | 1000 | CER 50V, 10%, | X/R, 0805 | 0 | IC 24 | not used | | TMS57070 | DSP 24 bit |
| 0 | C 66 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 25 | 50.62.1245 | | 74HC245 | Octal bus transceiver |
| 0 | C 67 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 26 | 1.942.958.20 | | | SW.102 DSP BOARD, LIMONAD |
| 0 | C 68 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 27 | 1.942.958.20 | | | SW.102 DSP BOARD, LIMONAD |
| 0 | C 69 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 28 | 1.942.958.20 | | | SW.102 DSP BOARD, LIMONAD |
| 0 | C 70 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 29 | 50.62.1245 | | 74HC245 | Octal bus transceiver |
| 0 | C 71 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 30 | 50.62.1154 | | 74HC154 | 1-of-16 decoder/demux |
| 0 | C 72 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 31 | not used | | not used | not used |
| 0 | C 73 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 32 | 50.62.1541 | | 74HC541 | Octal buffer line driver/recei |
| 0 | C 74 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 33 | 50.63.0407 | | TMS57070 | DSP 24 bit |
| 0 | C 75 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 34 | 50.63.0407 | | TMS57070 | DSP 24 bit |
| 0 | C 76 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 35 | 50.63.0407 | | TMS57070 | DSP 24 bit |
| 0 | C 77 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 36 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter |
| 0 | C 78 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 37 | 50.10.0104 | | LM317SP | Series regulator 1.5A+37V |
| 0 | C 79 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 38 1 | 1.942.909.20 | | | SW.102 DSP BOARD, OCTDEMUX |
| 0 | C 80 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | | | | | 50634202. EPLD 70 | 264 |
| 0 | C 81 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 39 1 | 1.942.909.20 | | | SW.102 DSP BOARD, OCTDEMUX |
| 0 | C 82 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | ^ | IC 40 | 1 042 000 20 | | 50634202, EPLD 70 | |
| 0 | C 83 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | | | | 50634202 5010 7 | 064 |
| 0 | C 84 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | 0 | IC 41 | 50.62.1138 | | 74HC138 | 3 to 8 line decoder |
| 0 | C 85 | 59.22.8100 | | 10u | EL 63V 20% | RM5 | 0 | IC 42 | 50.14.1009 | | 7C128A | SRAM 2K*8 35ns |
| 0 | C 86 | 59.22.8100 | | 10u | EL 63V 20% | RM5 | 0 | IC 43 | 50.14.1009 | | 7C128A | SRAM 2K*8 35ns |
| 0 | C 87 | 59.60.3337 | | 100n | CER 50V, 10%, | X7R, 0805 | | | | | | |

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| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx | . 1 | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|--------------|-------------|-------|-----------|--|--------|--------|---------------|-----------|--------|------------|-------------------------------------|
| 0 | IC 44 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter | 0 | ş | 23 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 45 | 50.62.1151 | | 74HC151 | 8 channel multiplexer | 0 | ł | P 24 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 46 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter | 0 | ł | P 25 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 47 | 50.62.1151 | | 74HC151 | 8 channel multiplexer | 0 | ł | P 26 | 54.12.050 | 5 | 5p | Power-Pin Stecker |
| 0 | IC 48 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter | 0 | ł | P 27 | 54.12.050 | 4 | 4p | Power-Pin Stecker |
| 0 | IC 49 | 50.62.1151 | | 74HC151 | 8 channel multiplexer | 0 | F | 28 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 50 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter | 0 | 1 | 29 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | 10 51 | 50.62.1950 | | 74HC4050 | Hex High to Low Level Shifter | 0 | | - 30 | not use | . D | 1p | Pin, Treinig, gerade |
| 0 | 10.52 | 50.62.1950 | | 74HC4050 | Hex High-to-Low Level Shifter | 0 | 1 | - 31 | not use | u d | 1p 1p | Pin, freinig, gerade |
| 0 | 10 53 | 50.62.1163 | | 74HC103 | Synchr preset 4bit counter bin | 0 | | - 32 | not use | u d | 1p 1p | Pin, freinig, gerade |
| 0 | 10 00 | 50.02.1105 | | 74HC4030 | Hex High-to-Low Level Shifter | 0 | | ° 00 ° 04 | 101 430 | u | וף 1ע | Pin, freihig, gerade |
| 0 | IC 56 | 50.62.1000 | | 74HC 00 | Quad 2input NAND | 0 | F | 3 5 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 57 | 50.62.1423 | | 74HC423 | Dual multivibr monost retrigg | 0 | F | 3 6 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 58 | 50.62.1074 | | 74HC 74 | Dual D-type FF, preset clear | 0 | F | P 37 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 59 | 50.62.1904 | | 74HCU04 | Hex inverter unbuffered | 0 | F | 3 8 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 60 | 50.15.0105 | | 3487 | IC MC 3487 P. DS 3487 N. | 0 | F | 3 9 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 61 | 50.61.0202 | | LM358 | Op-Amp single supply | 0 | 1 | 9 40 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | IC 62 | 50.62.4946 | | 74HC19046 | PLL with bandgap contr VCO | 0 | 1 | - 41 - 42 | not use | 0 | 1p | Pin, Treinig, gerade |
| 0 | 10 63 | 50.02.1103 | | 7400103 | Synchr preset 4bit counter bin | 0 | | - 42 | not use | u d | 1p 1p | Pin, freihig, gerade |
| 0 | IC 65 | 50.62.1163 | | 7400103 | Dual 4ch multiplexer | 0 | | - 43 D 44 | notuse | 4 | 1p 1p | Pin, freihig, gerade |
| ő | J1 | 53.03.0218 | | 1p | single-in-line | 0 | F | - 45 - 45 | not use | d | 1p | Pin, freihig, gerade |
| 0 | J 2 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 46 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 3 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 47 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 4 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 48 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 5 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 49 | not use | d | 1p | Pin, 1reihig, gerade |
| ٥ | Je | 53.03.0218 | | 1p | einglo-in-lino | 0 | F | 5 0 | not ueo | 9 | 1p | Pin, 1reihig, gerade |
| 0 | J 7 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 51 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 8 | 53.03.0218 | | 1p | single-in-line | 0 | F | 52 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 9 | 53.03.0218 | | 1p | single-in-line | 0 | 1 | 53 | not use | | 1p | Pin, 1reihig, gerade |
| 0 | J 10 | 53.03.0218 | | 1p 1p | single-in-line | 0 | - | - 54 2 55 | not use | - | 1p 1p | Pin, Treinig, gerade |
| 0 | J 12 | 53.03.0218 | | 10 | single-in-line | 0 | F | - 55 - 56 | not use | u d | 1p | Pin, freihig, gerade |
| 0 | J 13 | 53.03.0218 | | 10 | single-in-line | 0 | F | P 57 | not use | - - | 1p | Pin, 1reihig, gerade |
| 0 | J 14 | 53.03.0218 | | 1p | single-in-line | 0 | F | 9 58 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 15 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 59 | not use | t | 1p | Pin, 1reihig, gerade |
| 0 | J 16 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 60 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 17 | 03.03.02 IO | | 1p | skiyle-lin-line | 0 | , | • 01 | not use | L | Ιμ | Fin, freihig, gerade |
| 0 | J 18 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 62 | not use | b | 1p | Pin, 1reihig, gerade |
| 0 | J 19 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 63 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 20 | 53.03.0218 | | 1p | single-in-line | 0 | P | P 64 | not use | | 1p | Pin, 1reihig, gerade |
| 0 | J 21 | 53.03.0218 | | 1p 1p | single-in-line | 0 | , r | - 00 0.66 | not use | | 1p 1p | Pin, freinig, gerade |
| ő | J 23 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 67 | not use | , , | 10 | Pin, 1reihig, gerade |
| 0 | J 24 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 68 | not use | d | 1p | Pin, 1reihig, gerade |
| 0 | J 25 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 69 | not use | t | 1p | Pin, 1reihig, gerade |
| 0 | J 26 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 70 | 54.14.205 | 3 | 64p | Stecker gerade Au |
| 0 | J 27 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 71 | 54.14.205 | 3 | 64p | Stecker gerade Au |
| 0 | J 28 | 53.03.0218 | | 1p | single-in-line | 0 | F | • 72 | 54.14.205 | 2 | 16p | Stecker gerade Au |
| 0 | J 29 | 53.03.0218 | | 1p | single-in-line | 0 | F | P 73 | 54.14.205 | 2 | 16p | Stecker gerade Au |
| 0 | J 30 | 53.03.0218 | | 1p | single-in-line | 0 | | 21 24 | 50.60.000 | | BC84/B | NPN 45V 100mA SOI 23 |
| 0 | 132 | 53.03.0218 | | 1p 1n | single-in-line | 0 | F | 22 | 57 60 147 | י ר | 47R | MF, 1%, 0204, 224 MF 1% 0204 F24 |
| ō | J 33 | 53.03.0218 | | 1p | single-in-line | 0 | F | 23 | 57.60.147 | 5 | 47R | MF, 1%, 0204, E24 |
| 0 | J 34 | 53.03.0218 | | 1p | single-in-line | 0 | F | R 4 | 57.60.147 | D | 47R | MF, 1%, 0204, E24 |
| 0 | J 35 | 53.03.0218 | | 1p | single-in-line | 0 | F | ۲5 | 57.60.147 | C | 47R | MF, 1%, 0204, E24 |
| 0 | J 36 | 53.03.0218 | | 1p | single-in-line | 0 | F | R 6 | 57.60.147 | C | 47R | MF, 1%, 0204, E24 |
| 0 | JP 1 | 54.01.0021 | | Jumper | 0.63*0.63mm, Au | 0 | F | R 7 | 57.60.147 | 2 | 47R | MF, 1%, 0204, E24 |
| 0 | JP 2 | 54.01.0021 | | Jumper | 0.63*0.63mm, Au | 0 | F | 88 | 57.60.147 | 0 | 47R | MF, 1%, 0204, E24 |
| 0 | MP1 1 | .942.102.11 | 1 pce | Labal | DSP BOARD PCB | 0 | F | 29 | 57.60.110 | | 10R | MF, 1%, 0204, E24 |
| 0 | MP 2 | 43.01.0108 | i pce | Laber | ESE-WARNSCHILD | 0 | r c | K 10 | 57.00.110 | , , | 10R | MF, 1%, 0204, E24 |
| 0 | MP4 1 | 101 001 20 | | Label | TEXT-ETIK 5*20 HARDWARE -20 | 0 | F | 2 12 | 57 60 110 | י ר | 10R | MF, 1%, 0204, E24 MF 1% 0204 E24 |
| 0 | MP 5 1 | 010 108.64 | | Laber | WIRE WRAP DRAHT D 255 L= 80 | 0 | F | 2 13 | 57.60.110 | 5 | 10R | MF, 1%, 0204, E24 |
| 0 | P1 | 54.14.2054 | | 40p | Stecker gerade Au | 0 | F | R 14 | 57.60.110 |) | 10R | MF, 1%, 0204, E24 |
| 0 | P 2 | 54.14.2054 | | 40p | Stecker gerade Au | 0 | F | R 15 | 57.60.110 |) | 10R | MF, 1%, 0204, E24 |
| 0 | P 3 | not used | | 40p | Stecker gerade Au | 0 | F | र 16 | 57.60.110 |) | 10R | MF, 1%, 0204, E24 |
| 0 | P 4 | not used | | 40p | Stecker gerade Au | 0 | F | R 17 | 57.60.147 |) | 47R | MF, 1%, 0204, E24 |
| 0 | P 5 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | R 18 | 57.60.147 |) | 47R | MF, 1%, 0204, E24 |
| 0 | P6 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | R 19 | 57.60.147 | 0 | 47R | MF, 1%, 0204, E24 |
| 0 | P7 D9 | not used | | 1p 1p | Pin, Treinig, gerade | 0 | r 5 | K 20 | 57.60.147 | , , | 4/K | MF, 1%, 0204, E24 |
| 0 | PG | not used | | 1p 1n | Pin, freihig, gerade | 0 | 5 | 2 22 | 57 60 147 | , ר | 4/R 47R | MF, 1%, 0204, E24 |
| ō | P 10 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | 23 | 57.60.147 | - | 47R | MF. 1%, 0204, E24 |
| 0 | P 11 | not used | | 1p | Pin, 1reihig, gerade | 0 0 | F | R 24 | 57.60.147 |) | 47R | MF, 1%, 0204, E24 |
| 0 | P 12 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | R 25 | 57.60.147 |) | 47R | MF, 1%, 0204, E24 |
| 0 | P 13 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | R 26 | 57.60.147 |) | 47R | MF, 1%, 0204, E24 |
| 0 | P 14 | not used | | 1р | Pin, 1reihig, gerade | 0 | F | R 27 | 57.60.147 |) | 47R | MF, 1%, 0204, E24 |
| 0 | P 15 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | R 28 | 57.60.147 |) | 47R | MF, 1%, 0204, E24 |
| 0 | P 16 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | R 29 | 57.60.147 |) | 47R | MF, 1%, 0204, E24 |
| 0 | P 1/ P 18 | not used | | тр 10 | Pin, Treihig, gerade Pin, Treihig, gerade | 0 | F | (30) 9 9 1 | 57.60.110 | 3 | 10k 10k | MF, 1%, 0204, E24 |
| 0 | P 19 | 54.12 0510 | | 10p | Power-Pin Stecker | 0 | r F | 32 | not use | , 1 | 10k | MF 1% 0204, E24 |
| 0 | P 20 | 54.11.0136 | | 2*3p | Pin 0.63*0.63, RM2.54 | 0 | F | 33 | not use | t | 10k | MF, 1%, 0204, E24 |
| 0 | P 21 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | R 34 | 57.60.110 | 1 | 100R | MF, 1%, 0204, E24 |
| 0 | P 22 | not used | | 1p | Pin, 1reihig, gerade | 0 | F | R 35 | 57.60.133 |) | 33R | MF, 1%, 0204, E24 |

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| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx | . I | Pos. | Part No. | Qty. | Type/Val. | Description |
|--------|----------------|------------|------|------------|--|-----|--------|----------------|------------|------|--------------|---|
| 0 | P 36 | 57 60 1330 | | 33P | ME 1% 0204 E24 | | , | 2 123 | 57 60 1103 | | 10k | ME 1% 0204 E24 |
| 0 | R 30 R 37 | 57 60 1330 | | 33R | MF, 1%, 0204, E24 MF 1% 0204 E24 | 0 | i | R 123 | 57 60 1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 38 | 57 60 1103 | | 10k | MF 1% 0204 E24 | 0 | ì | R 125 | 57 60 1103 | | 10k | MF 1% 0204 F24 |
| 0 | R 39 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | Ì | R 126 | 57.60.1103 | | 10k | MF. 1%, 0204, E24 |
| 0 | R 40 | not used | | 10k | MF. 1%, 0204, E24 | 0 | Î | R 127 | not used | | 10k | MF. 1%, 0204, E24 |
| 0 | R 41 | not used | | 10k | MF, 1%, 0204, E24 | 0 | 1 | R 128 | not used | | 10k | MF, 1%, 0204, E24 |
| 0 | R 42 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | ł | R 129 | 57.60.1000 | | 0R0 | MF, 0204 |
| 0 | R 43 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | 1 | R 130 | 57.60.1000 | | 0R0 | MF, 0204 |
| 0 | R 44 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | 1 | २ 131 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 45 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | 1 | R 132 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 46 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | | R 133 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 4/ | not used | | 10k | MF, 1%, 0204, E24 | 0 | | ₹ 134 | 57 60 1330 | | 33R | MF, 1%, 0204, E24 |
| 0 | R 49 | not used | | 10k | MF, 1%, 0204, E24 | 0 | | R 136 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 |
| 0 | R 50 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | i | R 137 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 |
| 0 | R 51 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | I | R 138 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 52 | 57.60.1330 | | 33R | MF. 1%. 0204. E24 | 0 | ł | २ 139 | 57.60.1103 | | 10k | MF. 1%. 0204. E24 |
| 0 | R 53 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | ł | R 140 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | R 54 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | ł | R 141 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 |
| 0 | R 55 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | 1 | R 142 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 |
| 0 | R 56 | not used | | 10k | MF, 1%, 0204, E24 | 0 | 1 | R 143 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 57 R 58 | 57.60.1101 | | 10K | MF, 1%, 0204, E24 MF, 1%, 0204, E24 | 0 | F | ₹ 144 ₹ 145 | 57.60.1103 | | 10k 100R | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | R 59 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | ł | R 146 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 |
| 0 | R 60 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 147 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 |
| 0 | R 61 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 148 | 57.60.1331 | | 330R | MF, 1%, 0204, E24 |
| 0 | R 62 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | २ १४९ | 57.60.1243 | | 24k | MF, 1%, 0204, E24 |
| ٥ | R 63 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | ٥ | 5 | ₹ 150 | 57.60.1243 | | 24k | MF, 1%, 0204, E24 |
| 0 | R 64 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 151 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 65 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | | R 152 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 66 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | | R 153 | 57.60.1470 | | 4/R 47D | MF, 1%, 0204, E24 |
| 0 | R 0/ | 57.60.1330 | | 330 | MF, 1%, 0204, E24 | 0 | | 104 0 155 | 57 60 1470 | | 47 R 47 P | MF, 1%, 0204, E24 ME 1% 0204 E24 |
| 0 | R 69 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 MF, 1%, 0204, E24 | 0 | | R 155 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 MF, 1%, 0204, E24 |
| 0 | R 70 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 157 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 71 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 158 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 72 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 159 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 73 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | र 160 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| U | R 74 | 57.00.1101 | | 1005 | MF, 176, 0204, E24 | 0 | ľ | 101 | not used | | ono | MF, 0204 |
| 0 | R 75 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 162 | not used | | 0R0 | MF, 0204 |
| 0 | R /6 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | | K 163 | 57.60.1470 | | 4/R | MF, 1%, 0204, E24 |
| 0 | R / / | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | | 104 2 165 | not used | | | MF, 0204 |
| 0 | R 79 | 57 60 1103 | | 10k | MF, 1%, 0204, E24 MF 1% 0204 F24 | 0 | F | R 166 | 57 60 1470 | | 47R | MF 1% 0204 F24 |
| Ő | R 80 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 167 | 57.60.1000 | | 0R0 | MF, 0204 |
| 0 | R 81 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 168 | 57.60.1000 | | 0R0 | MF, 0204 |
| 0 | R 82 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | F | R 169 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 83 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 170 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 84 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | ł | R 171 | 57.60.1470 | | 47R 47R | MF, 1%, 0204, E24 |
| 0 | R 86 | 57 60 1103 | | 10k | MF, 1%, 0204, E24 MF 1%, 0204 E24 | 0 | f | 2 173 | 57 60 1470 | | 47R | MF 1% 0204, E24 |
| õ | R 87 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 174 | 57.60.1270 | | 27R | MF, 1%, 0204, E24 |
| 0 | R 88 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 175 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 89 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 176 | 57.60.1270 | | 27R | MF, 1%, 0204, E24 |
| 0 | R 90 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | F | R 177 | 57.60.1270 | | 27R | MF, 1%, 0204, E24 |
| 0 | R 91 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 178 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 92 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 179 | 57.60.1105 | | 1M | MF, 1%, 0204, E24 |
| 0 | R 93 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | 1 | < 180 5 191 | 57.60.1103 | | 10K | MF, 1%, 0204, E24 |
| 0 | R 94 | 57 60 1103 | | 10k | MF, 1%, 0204, E24 MF 1% 0204 F24 | 0 | F | 2 182 | 57 60 1470 | | 47R | MF, 1%, 0204, E24 MF 1% 0204 E24 |
| ō | R 96 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | F | R 183 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 97 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 184 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 98 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 185 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 99 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 186 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 100 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 187 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 101 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 188 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 102 R 103 | 57.60.1330 | | 100K | MF, 1%, 0204, E24 | 0 | r 5 | 2 100 | 57 60 1470 | | 47R 47D | MF, 1%, 0204, E24 |
| 0 | R 104 | 57 60 1330 | | 33R | MF 1% 0204 E24 | 0 | F | 2 191 | 57 60 1470 | | 47R | MF 1% 0204 E24 |
| o | R 105 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 192 | 57.60.1100 | | 10R | MF, 1%, 0204, E24 |
| 0 | R 106 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 193 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 107 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | P | R 194 | 57.60.1100 | | 10R | MF, 1%, 0204, E24 |
| 0 | R 108 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | F | R 195 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 109 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 196 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | R 110 P 111 | 57 60 1330 | | 33R 33D | MF, 1%, U2U4, E24 ME 1%, 0204 E24 | 0 | F , | K 19/ 2 108 | 57.60.1562 | | 1KU 546 | MF, 1%, U2U4, E24 |
| 0 | R 112 | 57.60 1103 | | 10k | MF. 1%, 0204, E24 MF. 1%, 0204, E24 | 0 | r F | 190 199 | 57.60.1153 | | 15k | MF. 1%, 0204, E24 MF. 1%, 0204, E24 |
| o | R 113 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 200 | 57.60.1473 | | 47k | MF, 1%, 0204, E24 |
| Ó | R 114 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | F | R 201 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 115 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 202 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | R 116 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 203 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | R 117 | 57.60.1330 | | 33R | MF, 1%, 0204, E24 | 0 | F | R 204 | 57.60.1271 | | 270R | MF, 1%, 0204, E24 |
| 0 | R 118 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 | 0 | F | R 205 | 57.60.1681 | | 680R | MF, 1%, 0204, E24 |
| U A | R 120 | 57 60 1000 | | 0R0 | MF 0204 | 0 | F F | 200 | 57 60 1470 | | 100K | IVIF, 176, U2U4, E24 ME 1%, 0204 E24 |
| 0 | R 121 | 57.60.1103 | | 10k | MF, 1%, 0204, F24 | 0 | r F | 208 | 57.60 1100 | | 10R | MF. 1%, 0204, E24 |
| õ | R 122 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | 0 | F | R 209 | 57.60.1100 | | 10R | MF, 1%, 0204, E24 |

Pos. Part No. Qty. Type/Val. Description

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| ldx | . Pos. | Part No. | Qty. | Type/Val. | Description | ldx. Pos. | Part No. | Qty. | Type/Val. | Description |
|-----|--------|------------|------|-------------|-----------------------------|-----------|----------|------|-----------|-------------|
| 0 | R 210 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 | | | | | |
| 0 | R 211 | 57.60.1000 | | 0R0 | MF, 0204 | | | | | |
| 0 | RZ 1 | 57.88.4331 | | 330R | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 2 | 57.88.4331 | | 330R | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 3 | 57.88.4331 | | 330R | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 4 | 57.88.4331 | | 330R | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 5 | 57.88.4222 | | 2k2 | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 6 | 57.88.4222 | | 2k2 | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 7 | 57.88.4222 | | 2k2 | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 8 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 9 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | RZ 10 | 57.00.4103 | | 10K | O'R Resistur-Netw 2% 31P9 | | | | | |
| 0 | RZ 11 | 57.88.4471 | | 470R | 8*R Resistor-Netw 2% SIP9 | | | | | |
| 0 | TP 1 | not used | | 1p | PCB-Flachst 2.8*0.8, gerade | | | | | |
| 0 | TP 2 | not used | | 1p | PCB-Flachst 2.8*0.8, gerade | | | | | |
| 0 | TP 3 | not used | | 1p | PCB-Flachst 2.8*0.8, gerade | | | | | |
| 0 | TP 4 | not used | | 1p | PCB-Flachst 2.8*0.8, gerade | | | | | |
| 0 | TP 5 | not used | | 1p | PCB-Flachst 2.8*0.8, gerade | | | | | |
| 0 | TP 6 | not used | | 1p | PCB-Flachst 2.8*0.8, gerade | | | | | |
| 0 | TP 7 | not used | | 1p | PCB-Flachst 2.8*0.8, gerade | | | | | |
| 0 | TP 8 | not used | | 1p | PCB-Flachst 2.8*0.8, gerade | | | | | |
| 0 | XIC 1 | 53.03.0182 | | 24p | DIL 0.3", löt, gerade | | | | | |
| 0 | XIC 2 | 53.03.0182 | | 24p | DIL 0.3", löt, gerade | | | | | |
| 0 | XIC 3 | 53.03.2244 | | 44p | PLCC-Socket | | | | | |
| 0 | XIC 4 | 53.03.2244 | | 44p | PLCC-Socket | | | | | |
| 0 | XIC 5 | 53.03.0168 | | 16p | DIL 0.3", löt, gerade | | | | | |
| 0 | XIC 26 | 53.03.2244 | | 44p | PLCC-Socket | | | | | |
| ٥ | XIC 27 | 53.03.2244 | | 44p | PLCC-Socket | | | | | |
| 0 | XIC 28 | 53.03.2244 | | 44p | PLCC-Socket | | | | | |
| 0 | XIC 31 | 54.10.3730 | | 30p | SIMM-Socket 30p | | | | | |
| 0 | XIC 38 | 53.03.2244 | | 44p | PLCC-Socket | | | | | |
| 0 | XIC 39 | 53.03.2244 | | 44p | PLCC-Socket | | | | | |
| 0 | XIC 40 | 53.03.2244 | | 44p | PLCC-Socket | | | | | |
| 0 | XIC 42 | 53.03.0182 | | 24p | DIL 0.3", löt, gerade | | | | | |
| 0 | XIC 43 | 53.03.0182 | | 24p | DIL 0.3", löt, gerade | | | | | |
| 0 | XIC 60 | 53.03.0168 | | 16p | DIL 0.3", löt, gerade | | | | | |
| 0 | XY 1 | 89.01.1499 | | | QUARZ - ISOLIERPLATTE | | | | | |
| 0 | Y 1 | 89.01.1015 | | 12.288MHz | XTAL HC 49/U | | | | | |
| | | | | | | | | | | |
| | | | | End of List | | | | | | |
| | | | | End of LIST | | | | | | |



Block Diagram Microphone Input





Mic Input 1.942.720.21



Mic Input 1.942.720.21



Mic Input 1.942.720.21 (0)

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| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx. | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|------|------------|------|-------------|-------------------------|--------|---------------|--------------|------|--------------|---|
| | | | | | | 0 | D.8 | 50 60 8001 | | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 1 | 59.60.2349 | | 100p | CER 50V, 5%, C0G, 0805 | 0 0 | D 9 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 2 | 59.60.2333 | | 22p | CER 50V, 5%, COG, 0805 | 0 | D 10 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 4 | 59.60.3537 | | 100n | CER 50V 10% X7R 1210 | 0 | D 11 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 |
| ō | C 5 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | D 12 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 6 | 59.60.3537 | | 100n | CER 50V. 10%. X7R. 1210 | 0 | D 13 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 7 | 59.60.3317 | | 2n2 | CER 50V, 10%, X7R, 0805 | 0 | | 50.09.0117 | | 55344 | Single On-amp low poise |
| 0 | C 8 | 59.22.6100 | | 10u | EL 35V 20% RM5 | 0 | 10.2 | 50 15 0114 | | 9637 | Dual diff Line Receiver |
| 0 | C 9 | 59.22.4002 | | 100u | EL 16V 20% RM5 | ő | IC 4 | 50.61.8203 | | MAX314 | Quad SPST SO 16 |
| 0 | C 10 | 59.60.2353 | | 150p | CER 50V, 5%, C0G, 0805 | 0 | IC 5 | 50.62.1951 | | 74HC4051 | 8ch analog mux/demux |
| 0 | C 12 | 59.60.2333 | | 22u 22p | CER 50V. 5% COG.0805 | 0 | IC 6 | 1.012.021.22 | | | \$W.220 MIC INP MOD (60160313) |
| 0 | C 13 | 59.06.5334 | | 330n | PETP, 63V, 5%, RM5 | 0 | IC 7 | 50.62.6014 | | 74ACT 14 | Hex inverting Schmitt trigger |
| 0 | C 14 | 59.22.3003 | | 220u | EL 10V 20% RM5 | 0 | IC 8 | 50.61.8202 | | ADG433 | Quad SPST SO 16 |
| 0 | C 15 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | IC 9 | 50.61.8103 | | CS5360 | A/D Converter 24bit Ste SSOP20 |
| 0 | C 16 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | IC 10 | 50.10.0104 | | LM317SP | Series regulator 1.5A+37V |
| 0 | C 17 | 50.06.5334 | | 330n | PETP, 63V, 5%, RM5 | 0 | IC 12 | 50.09.0117 | | 33078 | Dig volume control ste SOTe |
| 0 | C 18 | 59.22.3003 | | 220u | EL 10V 20% RM5 | 0 | IC 13 | 50.62.1595 | | 74HC595 | 8bit shift/output register |
| 0 | C 20 | 59.00.3537 | | 330 | CER 50V, 10%, X7R, 1210 | 0 | IC 14 | 50.62.1574 | | 74HC574 | Octal D-FF |
| 0 | C 21 | 59 22 4002 | | 1000 | EL 16V 20% RM5 | 0 | IC 15 | 50.62.1595 | | 74HC595 | 8bit shift/output register |
| ō | C 22 | 59.60.3537 | | 100n | CER 50V. 10%. X7R. 1210 | 0 | IC 16 | 50.04.3200 | | CNY17-2 | Opto-coupler |
| Ō | C 23 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | 10 17 | 1.942.221.00 | | 7.0000 | INSERT SEND BOARD ,A |
| 0 | C 24 | 59.60.2373 | | 1n0 | CER 50V, 5%, C0G, 0805 | 0 | IC 18 | 50.62.1165 | | 74HC165 | 8bit shift register |
| 0 | C 25 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | 10 19 | 50.04.3200 | | GN 117-2 | Series regulator 100mA 271/ |
| 0 | C 26 | 59.22.4002 | | 100u | EL 16V 20% RM5 | 0 | IC 20 | 50.61.9001 | | LW337L | Dual voltage comp. SO 8 |
| 0 | C 27 | 59.60.2337 | | 33p | CER 50V, 5%, C0G, 0805 | 0 | IC 22 | 50 62 1074 | | 74HC 74 | Dual D-type FE, preset clear |
| 0 | C 20 | 59.22.0000 | | 220u | CED 50V 10% Y7D 1210 | Ő | J 1 | 54.21.2203 | | 3p | XLR PCB Winkel |
| 0 | C 30 | 59 60 2373 | | 10011 | CER 50V, 10%, X7R, 1210 | 0 | J 2 | 54.21.2203 | | 3p | XLR PCB Winkel |
| 0 | C 31 | 59.60.3537 | | 100n | CER 50V, 10%, X7B, 1210 | 0 | J 3 | 54.24.0211 | | 2*3p | Bantam-Buchse, 4.4mm |
| 0 | C 32 | 59.22.4002 | | 100u | EL 16V 20% RM5 | 0 | K 1 | 56.04.0198 | | 2*u | 5V 125V 2A Ag/Au |
| 0 | C 33 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | К2 | 56.04.0198 | | 2*u | 5V 125V 2A Ag/Au |
| 0 | C 34 | 59.60.2349 | | 100p | CER 50V, 5%, C0G, 0805 | 0 | L1 | 62.01.0301 | | 110MHz | Breitband-Drossel |
| 0 | C 35 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | L2 | 1.022.231.00 | | 235mH | HF-ASYM. DROSSEL RM5 |
| 0 | C 36 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | | 1 942 220 11 | 000 | TTUNHZ | MIC INPUT MODUL PCR |
| 0 | C 37 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | MP 2 | 43 01 0108 | nce | Label | ESE-WARNSCHILD |
| 0 | C 38 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | MP 3 | 1.042.720.10 | pee | Eubor | NR.ETIKETTE |
| 0 | C 40 | 59.22.3220 | | 22u 100n | CEP 50V 10% X7P 1210 | 0 | MP 4 | 1.022.400.03 | pce | | ISOLATION |
| 0 | C 41 | 59 60 2349 | | 100n | CER 50V, 10%, X/R, 1210 | 0 | MP 5 | 89.01.1499 | рсе | | QUARZ - ISOLIERPLATTE |
| 0 | C 42 | 59 22 4002 | | 100µ | EL 16V 20% RM5 | 0 | MP 6 | 1.101.001.20 | | Label | TEXT-ETIK. 5*20 HARDWARE -20 |
| 0 | C 43 | 59.22.3470 | | 47u | EL 10V 20% RM5 | 0 | P 1 | 54.13.0076 | | 9р | D-Sub, PCB, Winkel |
| n | C 44 | 59 60 2349 | | 100p | CER 50V, 5%, COG, 0805 | 0 | P 2 | 54.14.2054 | | 40p | Stecker gerade Au |
| 0 | C 45 | 59.60.3317 | | 2n2 | CER 50V, 10%, X7R, 0805 | 0 | P3 | 54.01.0020 | | 1p | Pin, 1reihig, gerade |
| 0 | C 46 | 59.22.8100 | | 10u | EL 63V 20% RM5 | 0 | 01 | 54.01.0020 | | TP BC556B | Pin, Heinig, gerade |
| 0 | C 47 | 59.32.1220 | | 22p | CER 10%, 400V | 0 | 02 | 50.03.0492 | | BC546B | BC 546 B NPN |
| 0 | C 48 | 59.60.2349 | | 100p | CER 50V, 5%, C0G, 0805 | 0 | Q 3 | 50.03.0491 | | BC546B | BC 546 B NPN |
| 0 | C 49 | 59.22.4002 | | 100u | EL 16V 20% RM5 | 0 | Q 4 | 50.03.0491 | | BC546B | BC 546 B NPN |
| 0 | C 50 | 59.22.4002 | | 680p | PP 1% 630V | 0 | Q 5 | 50.03.0340 | | BC337-25 | NPN, 800mA |
| 0 | C 52 | 59.05.1681 | | 680p | PP. 1%, 630V | 0 | Q 6 | 50.03.0340 | | BC337-25 | NPN, 800mA |
| 0 | C 53 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | Q 7 | 50.03.0340 | | BC337-25 | NPN, 800mA |
| 0 | C 54 | 59.05.1681 | | 680p | PP, 1%, 630V | 0 | Q 8 | 50.03.0340 | | BC337-25 | NPN, 800mA |
| 0 | C 55 | 59.32.1471 | | 470p | CER 10%, 400V | 0 | R1 | 57.60.1680 | | 68R | MF, 1%, 0204, E24 |
| 0 | C 56 | 59.22.3470 | | 47u | EL 10V 20% RM5 | 0 | R 2 R 3 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 57 | 59.22.3470 | | 47u | EL 10V 20% RM5 | o | R4 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 |
| 0 | C 58 | 59.32.1471 | | 470p | CER 10%, 400V | 0 | R 5 | 57.60.1224 | | 220k | MF, 1%, 0204, E24 |
| 0 | C 60 | 59.00.3537 | | 100n 47u | CER 50V, 10%, X/R, 1210 | 0 | R 6 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| õ | C 61 | 59.22.3470 | | 47u | EL 10V 20% RM5 | 0 | R 7 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 62 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 8 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | C 63 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 9 | 57.92.7012 | | 0.3A | PTC 60V |
| 0 | C 64 | 59.05.1681 | | 680p | PP, 1%, 630V | 0 | R 10 | 57.60.1682 | | 5K8 | MF, 1%, 0204, E24 |
| 0 | C 65 | 59.32.1221 | | 220p | CER 10%, 400V | 0 | R 11 | 57.60.2428 | | 19K1 | MF, 1%, 0204, E96 |
| 0 | C 66 | 59.32.1221 | | 220p | CER 10%, 400V | 0 | R 12 R 13 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 MF 1%, 0204 E24 |
| 0 | C 67 | 59.05.1221 | | 220p | PP, 1%, 630V | 0 | R 14 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | C 68 | 59.05.1221 | | 220p | PP, 1%, 630V | 0 | R 15 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | C 59 | 59.05.1221 | | 220p | PP, 1%, 630V | 0 | R 16 | 57.60.1822 | | 8k2 | MF, 1%, 0204, E24 |
| 0 | C 71 | 59.60 3537 | | 100n | CER 50V 10% X7R 1210 | 0 | R 17 | 57.60.1270 | | 27R | MF, 1%, 0204, E24 |
| 0 | C 72 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 18 | 57.60.1621 | | 620R | MF, 1%, 0204, E24 |
| 0 | C 73 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 19 | 57.92.7011 | | 0.2A | PTC 60V |
| 0 | C 74 | 59.60.2349 | | 100p | CER 50V, 5%, C0G, 0805 | 0 | R 20 | 57.92.7011 | | U.2A | PIC 60V |
| 0 | C 75 | 59.60.2349 | | 100p | CER 50V, 5%, C0G, 0805 | 0 | rt ∠1 ₽ 22 | 57.50.1131 | | 130K | MF, 1%, 0204, E24 |
| 0 | C 76 | 59.60.2349 | | 100p | CER 50V, 5%, C0G, 0805 | 0 | R 23 | 57.60 1000 | | 0R0 | MF. 0204 |
| 0 | 0.77 | 59.32.1221 | | 220p | CER 10%, 400V | ŏ | R 24 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | C 79 | 59 32 1221 | | 220n | CER 10% 400V | 0 | R 25 | 57.60.1391 | | 390R | MF, 1%, 0204, E24 |
| 0 | C 80 | 59.60.3537 | | 100n | CER 50V. 10%, X7R 1210 | 0 | R 26 | 57.60.1181 | | 180R | MF, 1%, 0204, E24 |
| 0 | D 1 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 27 | 57.60.1182 | | 1k8 | MF, 1%, 0204, E24 |
| 0 | D 2 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 28 | 57.60.1270 | | 27R | MF, 1%, 0204, E24 |
| 0 | D 3 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 29 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 |
| 0 | D 4 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 30 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 |
| 0 | D 5 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 32 | 57 60 1222 | | 2k2 | MF 1% 0204, E24 |
| 0 | D 6 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 0 | R 33 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| U | U / | JU.DU.8001 | | ****** | ZUUMA / DV 4/IS SOU 80 | - | | | | | and the second se |

Description

Mic Input 1.942.720.21 (0)

| ption | ldx. Pos. | Part No. | Qty. | Type/Val. |
|--------------------------|-----------|----------|------|-----------|
| 6, 0204, E24 | | | | |
| 6, 0204, E24 | | | | |
| 6, 0204, E24 | | | | |
| 6, 0204, E24 | | | | |
| 6, 0204, E24 | | | | |
| 6, 0204, E24 | | | | |
| 6, 0204, E24 | | | | |
| 6, 0204, E24 | | | | |
| 6, 0204, E96 | | | | |
| 0204 | | | | |
| 6, 0204, E24 | | | | |
| 6, 0204, E2 4 | | | | |
| 6, 0204, E24 | | | | |
| | | | | |

Page: 2 of 2

| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|-------|--------------------------|------|-------------------|-------------------------------------|
| ٥ | R 34 | 57 60 1151 | | 150R | ME 1% 0204 E24 |
| ő | R 35 | 57.60.1151 | | 150R | MF. 1%, 0204, E24 |
| 0 | R 36 | 57.60.1151 | | 150R | MF. 1%, 0204, E24 |
| 0 | R 37 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 38 | 57.60.1331 | | 330R | MF, 1%, 0204, E24 |
| 0 | R 39 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 |
| 0 | R 40 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | R 41 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 |
| 0 | R 42 | 57.60.2364 | | 4k53 | MF, 1%, 0204, E96 |
| 0 | R 43 | 57.60.1000 | | 0R0 | MF, 0204 |
| 0 | R 44 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 |
| ۰ | R 45 | 57.00.1100 | | 106 | MF, 1%, 0204, E24 |
| 0 | R 46 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 |
| 0 | R 4/ | 57.60.1103 | | 1UK | MF, 1%, 0204, E24 |
| 0 | R 40 | 57.00.1472 | | 4K/ | MF, 1%, 0204, E24 |
| 0 | R 49 | 57 60 1479 | | 467 | MF, 1%, 0204, E24 |
| 0 | R 51 | 57 60 1122 | | 1k2 | MF. 1%, 0204, E24 |
| 0 | R 52 | 57.60.1182 | | 1k8 | MF, 1%, 0204, E24 |
| 0 | R 53 | 57.60.1100 | | 10R | MF, 1%, 0204, E24 |
| 0 | R 54 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 55 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| U | K 20 | 57.60.1122 | | 162 | MF, 1%, 0204, E24 |
| 0 | R 57 | 57.60.1473 | | 47k | MF, 1%, 0204, E24 |
| 0 | R 58 | 57.60.1473 | | 47k | MF, 1%, 0204, E24 |
| 0 | R 59 | 57.60.1561 | | 560R | MF, 1%, 0204, E24 |
| 0 | R 60 | 57.60.1392 | | 3k9 | MF, 1%, 0204, E24 |
| 0 | D 62 | 57 60 1102 | | 110 | ME 1% 0004 E24 |
| 0 | R 63 | 57 60 1103 | | 10k | MF 1% 0204 E24 |
| ō | R 64 | 57.60.1561 | | 560R | MF, 1%, 0204, E24 |
| 0 | R 65 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 66 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 67 | 57.60.1152 | | 1 k 5 | MF, 1%, 0204, E24 |
| 0 | R 68 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | R 69 | 57.60.1152 | | 1k5 | MF, 1%, 0204, E24 |
| 0 | R 70 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 71 | 57.99.0250 | | 6k8 | MF 0.1%, 25ppm 0207 |
| 0 | R 73 | 57 60 1223 | | 22k | MF 1% 0204 F24 |
| 0 | R 74 | 57.60.1472 | | 4k7 | MF. 1%, 0204, E24 |
| 0 | R 75 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 76 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 77 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 78 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 79 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 80 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 81 | 57.60.1331 | | 330R | MF, 1%, 0204, E24 |
| 0 | R 82 | 57.60.1222 | | 1KU 22k | MF, 1%, 0204, E24 |
| 0 | R 84 | 57 60 1103 | | 10k | MF 1% 0204 F24 |
| ō | R 85 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 86 | 57.60.1100 | | 10R | MF, 1%, 0204, E24 |
| 0 | R 87 | 57.60.1220 | | 22R | MF, 1%, 0204, E24 |
| 0 | R 88 | 57.60.1220 | | 22R | MF, 1%, 0204, E24 |
| 0 | R 89 | 57.60.1220 | | 22R | MF, 1%, 0204, E24 |
| 0 | R 90 | 57.60.1220 | | 22R | MF, 1%, 0204, E24 |
| 0 | R 91 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | R 92 | 57.60.1303 | | 30k | MF, 1%, 0204, E24 |
| 0 | R 04 | 57 60 1303 | | 30k | MF, 1%, 0204, E24 MF 1% 0204 F24 |
| õ | R 95 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 96 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | R 97 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | R 98 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | RZ 1 | 57.88.4473 | | 47k | 8*R Resistor-Netw 2% SIP9 |
| 0 | S 1 | 55.01.0168 | | 8*a | DIL-Switch, PCB |
| 0 | T1 1 | .022.461.00 | | 00. | HIGH-LEVEL MIC INPUT |
| 0 | XIC 6 | 53.03.0165 80.01.1019 | | 20ρ 22.1184ΜΗ7 | VIL-SOCKET U.3" |
| J | | 58.01.1010 | | 22.1104WITZ | |
| | | | | | |

---- End of List --

Comments:

(21) Software update IC6

Insert Send 1.942.221.00








Line Input 1.942.732.20

Line Input 1.942.732.20



Line Input 1.942.732.20



REGENSDORF

| ldx | Pos. | Part No. | 2ty. | Type/Val. | Description |
|-----|------------|--------------------------|-------|-------------|---|
| 0 | 0.1 | 50 60 2373 | | 100 | CEB 50V 5% COG 0805 |
| ŏ | C2 | 59.60.2373 | | 1n0 | CIR 50V, 5%, COG, 0805 |
| 0 | C 3 | 59.22.5220 | | 22u | EL 25V 20% RM5 |
| 0 | C 4 | 59.60.3337 | | 100n | CIR 50V, 10%, X7R, 0805 |
| 0 | C 5 | 59.22.5220 | | 22u | EL 25V 20% RM5 |
| 0 | C7 | 59.22.5220 | | 220 | El 25V 20% BM5 |
| 0 | C 8 | 59.22.5220 | | 22u | EL 25V 20% RM5 |
| 0 | C 9 | 59.60.3337 | | 100n | CIR 50V, 10%, X7R, 0805 |
| 0 | C 10 | 59.60.3337 | | 100n | CIR 50V, 10%, X7R, 0805 |
| 0 | C 11 | 59.22.5220 | | 22U | EL 25V 20% RM5 |
| 0 | C 13 | 59.00.3337 | | 1001 | EL 35V 20% RM5 |
| 0 | C 14 | 59.60.3337 | | 100n | CIR 50V, 10%, X7R, 0805 |
| 0 | C 15 | 59.60.2337 | | 33p | CIR 50V, 5%, C0G, 0805 |
| 0 | C 16 | 59.60.2337 | | 33p | CIR 50V, 5%, COG, 0805 |
| 0 | 0.19 | 59.22.4002 | | 1000 | EL 16V 20% HM5 |
| ő | C 19 | 59.60.2337 | | 330 | CIR 50V, 5%, COG, 0805 |
| 0 | C 20 | 59.60.2337 | | 33p | CIR 50V, 5%, C0G, 0805 |
| 0 | C 21 | 59.60.3337 | | 100n | CIR 50V, 10%, X7R, 0805 |
| 0 | C 22 | 59.60.3337 | | 100n | CIR 50V, 10%, X7R, 0805 |
| 0 | C 24 | 59.22.4002 | | 1000 | CIE 50V 10% X7E 0805 |
| 0 | C 25 | 59.60.3337 | | 100n | CIR 50V, 10%, X7R, 0805 |
| 0 | C 26 | 59.22.4002 | | 100u | EL 16V 20% RM5 |
| 0 | C 27 | 59.22.4002 | | 100u | EL 16V 20% RM5 |
| 0 | C 28 | 59.60.2349 | | 100p | CIR 50V, 5%, COG, 0805 |
| 0 | C 30 | 50 60 2349 | | 100p | CIR 50V, 5%, COG, 0805 |
| ő | C 31 | 59.60.2349 | | 100p | CIR 50V, 5%, C0G, 0805 |
| 0 | C 32 | 59.22.4002 | | 100u | EL 16V 20% RM5 |
| 0 | C 33 | 59.22.4002 | | 100u | EL 16V 20% RM5 |
| 0 | C 34 | 59.22.4002 | | 100u | EL 16V 20% RM5 |
| 0 | C 36 | 59.22.4002 | | 1000 33n | CIB 50V 5% C0G 0805 |
| ő | C 37 | 59.60.2337 | | 330 | CIR 50V, 5%, COG, 0805 |
| 0 | C 38 | 59.60.2349 | | 100p | CIR 50V, 5%, C0G, 0805 |
| 0 | C 39 | 59.60.2349 | | 100p | CIR 50V, 5%, COG, 0805 |
| 0 | C 40 | 59.60.3337 | | 100n | CIR 50V, 10%, X7R, 0805 |
| 0 | C 42 | not used | | 1001 | Cth 809, 10%, X7H, 0805 |
| ŏ | C 43 | not used | | | |
| 0 | C 44 | 59.05.1331 | | 330p | PF, 1%, 630V |
| 0 | C 45 | 59.05.1331 | | 330p | Pf, 1%, 630V |
| 0 | C 46 | 59.05.1331 50.05.1331 | | 330p | PF, 1%, 630V |
| 0 | C 48 | 59.32.1221 | | 220n | CIB 10% 400V |
| 0 | C 49 | 59.32.1471 | | 470p | CIR 10%, 400V |
| 0 | C 50 | 59.32.1221 | | 220p | CIR 10%, 400V |
| 0 | C 51 | 59.32.1221 | | 220p | CIR 10%, 400V |
| 0 | C 52 | 59.32.1471 | | 470p | CIR 10%, 400V |
| ő | C 54 | 59.60.3337 | | 100n | CIR 50V. 10%, X7R, 0805 |
| 0 | C 55 | 59.32.1221 | | 220p | CIR 10%, 400V |
| 0 | C 56 | 69.32.1221 | | 220p | CIR 10%, 400V |
| 0 | C 57 | 59.32.1221 | | 220p | CIR 10%, 400V |
| 0 | C 59 | 50 60 2340 | | 220p | CIR 50V 5% COG 0805 |
| ő | C 60 | 59.60.2349 | | 100p | CIR 50V, 5%, COG, 0805 |
| 0 | C 61 | 59.60.2349 | | 100p | CER 50V, 5%, COG, 0805 |
| 0 | D 1 | 50.60.8001 | | 4448 | 20m4 75V 4ns SOD 80 |
| 0 | D2 | 50.60.8001 | | 4448 | 20mA 75V 4ns SOD 80 |
| 0 | D 3 | 50.60.8001 | | 4448 | 20mA 75V 4ns SOD 80 |
| 0 | D 4 | 50.60.8001 | | 4448 | 20mA 75V 4ns SOD 80 |
| 0 | De | 50.60.8001 | | 4448 | 20m4 75V 4ns SOD 80 |
| | | | | | |
| 0 | 101 | 50.61.9100 | | LM317SP | Series regulator 1.5A+37V |
| 0 | IC 3 | 50.15.0114 | | 9637 | Dual diff Line Receiver |
| 0 | IC 4 | 50.62.6014 | | 74ACT 14 | He inverting Schmitt trigger |
| 0 | IC 5 | 50.10.0109 | | LM337L | Series regulator 100mA37V |
| 0 | IC 6 | 50.09.0117 | | MC33078 | IC MC 33078 P |
| 0 | IC 7 | 50.09.0117 | | MC33078 | IC MC 33078 P SV 230 LINE INPLIT MODULE |
| v | 100 | 107E.022.21 | | | (5160313, AT89C2051) |
| 0 | IC 9 | 50.62.1951 | | 74HC4051 | 8ci analog mux/demux |
| 0 | IC 10 | 50.61.8301 | | CS3310 | Di volume control ste SO16 |
| 0 | IC 11 | 50.09.0117 | | MC33078 | IC MC 33078 P |
| 0 | IC 13 | 50.62.1165 | | 74HC165 | eu sinnouput register 8bt shift register |
| ŏ | IC 14 | 50.04.3200 | | CNY17-2 | Opto-coupler |
| Ó | IC 15 | 50.04.3200 | | CNY17-2 | Opto-coupler |
| 0 | J 1 | 54,21,2203 | | 30 | XLR PCB Winkel |
| ŏ | J2 | 54.21.2203 | | 3p | XLR PCB Winkel |
| ò | J3 | 54.21.2203 | | Зр | XLR PCB Winkel |
| 0 | J 4 | 54.21.2203 | | Зр | XLR PCB Winkel |
| 0 | К1 | 56.04.0198 | | 2*u | 5 ^y 125V 2A Ag/Au |
| 0 | К2 | 56.04.0198 | | 2*u | 5 ^Y 125V 2A Ag/Au |
| 0 | КЗ | 56.04.0198 | | 2*u | 5Y 125V 2A Ag/Au |
| 0 | n 4 | pd.04.0198 | | ≥'u | or 125V 2A Agrau |
| 0 | MP1 1 | .942.230.11 | . bce | | Lire Input Module pcb |
| 0 | MP2 MP3 | 43.01.0108 | pce | Label | EXE-WAHNSCHILD |
| 0 | MP4 1 | .101.001.20 | , bce | Label | TEXT-ETIK. 5*20 HARDWARE -20 |

Line Input 1.942.732.20

| ldx | Pos. | Part No. Qty. | Type/Val. | Description | ld¢ | Pos. | Part No. | Qty. | Type/Val. | Description |
|-----|--------------|--------------------------|---------------|---------------------------------------|-----|---------|------------|------|---------------|---------------------------|
| | P 1 | 54.01.0020 | In . | Pin treibin gerade | c | B 87 | 57.60.1102 | | 1 k0 | MF. 1%, 0204, E24 |
| ő | P2 | 54.14.2054 | 40p | Stecker gerade Au | ċ | R 88 | 57.60.1102 | | 1 k0 | MF, 1%, 0204, E24 |
| ő | P3 | 54.01.0020 | 1p | Pin, 1reihig, gerade | | | 57.00.4476 | | | |
| 0 | P 4 | 54.13.0076 | 9p | D-Sub, PCB, Winkel | С | HZ 1 | 57.88.4473 | | 47K | 5"H Hesistor-Netw 2% SIP9 |
| 0 | ~ 1 | 50.09.0951 | DC207.0F | DND 900mA | C | S1 | 55.01.0168 | | 8*a | SZ , 8*A, DIL |
| 0 | 02 | 50.03.0351 | BC327-25 | DND 900mA | | | | | | |
| 0 | 0.3 | 50.03.0340 | BC337-25 | 800mA 45V NPN | c | XIC 8 | 53.03.0165 | | 20p | DIL 0.3°, 11, gerade |
| ő | 04 | 50.03.0340 | BC337-25 | 800mA 45V NPN | С | XY 1 | 89.01.1499 | | | QUARZ - ISOLIERPLATTE |
| ő | Q.5 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | | | | | | |
| ō | Q.6 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN | c | Y 1 | 89.01.1016 | | 22.1184MHz | XTAL HC 49/U |
| | | | | NE 10, 0034 E04 | | | | | | |
| 0 | E 0 | 57.00.1101 | 0.24 | NF, 1%, 0294, E24 | | | | | - End of List | |
| 0 | B3 | 57.60 1102 | 1k0 | ME 1% 0214 E24 | Cor | mments: | | | | |
| ő | R 4 | 57.60.1101 | 100R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 5 | 57.60.1331 | 330R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 6 | 57.60.1151 | 150R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 7 | 57.92.7011 | 0.2A | PTC 60V | | | | | | |
| 0 | R 8 | 57.60.1151 | 150R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 9 | 57.60.1151 | 150R | MF, 1%, 0204, E24 | | | | | | |
| 0 | H 10 | 57.60.1151 | 150H | MF, 1%, U2J4, E24 | | | | | | |
| 0 | H 11 B 10 | 57.60.1151 not used | TOUR | MF, 1%, 0204, E24 | | | | | | |
| 0 | B 13 | 57.60 1151 | 150B | ME 1% 0214 E24 | | | | | | |
| ő | B 14 | 57.60.1151 | 150R | MF, 1%, 02)4, E24 | | | | | | |
| 0 | R 15 | 57.92.7011 | 0.2A | PTC 60V | | | | | | |
| 0 | R 16 | 57.60.1151 | 150R | MF, 1%, 0204, E24 | | | | | | |
| 0 | B 17 | 57.60.1000 | ORO | MF, 0204 | | | | | | |
| 0 | R 18 | 57.60.1102 | 1k0 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 19 | 57.60.1151 | 150R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 20 | 57.60.1331 | 330H | MF, 1%, 0204, E24 | | | | | | |
| 0 | H 21 | 57.60.1000 | OHO | MF, 0204 | | | | | | |
| 0 | F 22 | 57.60.1479 57.60.1101 | 40/ 100P | MF, 1%, 0204, E24 ME 1%, 0204, E24 | | | | | | |
| ő | R 24 | 57.60 1822 | 8k2 | ME 1% 0204 E24 | | | | | | |
| ō | R 25 | 57.60.1182 | 1k8 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 26 | 57.60.1101 | 100R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 27 | 57.60.1103 | 10k | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 28 | 57.60.1682 | 6k8 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 29 | 57.60.1682 | 6k8 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 30 | 57.60.1103 | 10k | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 31 | 57.60.1182 | 1k8 | MF, 1%, 0204, E24 | | | | | | |
| 0 | H 32 | 57.60.1101 | 100H | MF, 1%, U2J4, E24 | | | | | | |
| 0 | F 33 | 57.60.1472 | 467 100B | MF, 1%, 0204, E24 MF 1%, 0214, E24 | | | | | | |
| ů. | B 35 | 57.60.1103 | 10k | ME, 1%, 0204, E24 | | | | | | |
| ō | R 36 | 57.60.1103 | 10k | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 37 | 57.60.1103 | 10k | MF, 1%, 0234, E24 | | | | | | |
| 0 | R 38 | 57.60.1103 | 10k | MF, 1%, 0234, E24 | | | | | | |
| 0 | R 39 | 57.60.1100 | 10R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 40 | 57.11.3822 | 8k2 | MF, 1%, 0207 | | | | | | |
| 0 | R 41 | 57.60.1132 | 1 k3 | MF, 1%, 0204, E24 | | | | | | |
| 0 | H 42 | 57.60.1132 | 1K3 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 44 | not used | ONZ | WIF, 116,0207 | | | | | | |
| ő | B 45 | not used | | | | | | | | |
| 0 | R 46 | 57.60.1132 | 1k3 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 47 | 57.60.1132 | 1k3 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 48 | 57.11.3822 | 8k2 | MF, 1%, 0207 | | | | | | |
| 0 | R 49 | not used | | | | | | | | |
| 0 | R 50 | not used | 01-0 | 115 10 0007 | | | | | | |
| 0 | B 52 | 57.11.3022 | 47k | MF, 1%, 0207 MF 1%, 0214 F24 | | | | | | |
| ő | R 53 | 57.60.1182 | 1k8 | MF. 1%, 0204, E24 | | | | | | |
| ő | R 54 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 55 | 57.60.1182 | 1k8 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 56 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 57 | 57.60.1821 | 820R | MF, 1%, 0204, E24 | | | | | | |
| 0 | ri 58 | 57.60.1821 57.60.1001 | d2UH epoti | ME, 176, 0209, E29 | | | | | | |
| 0 | n 09 B 60 | 57.60.1821 | 820B | MF 1% 0204 F24 | | | | | | |
| 0 | R 61 | 57.60.1473 | 47k | MF. 1%, 0204, E24 | | | | | | |
| ő | R 62 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| ō | R 63 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 64 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 65 | 57.60.1104 | 100k | MF, 1%, 0204, E24 | | | | | | |
| · 0 | R 66 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 67 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | H 68 | 57.60.1104 | 100K | MF, 1%, 0204, E24 | | | | | | |
| 0 | 8.70 | 57.60.1104 | 4k7 | ME 1% 0204 E24 | | | | | | |
| 0 | B 71 | 57.60.1472 | 4k7 | MF. 1%, 0204, E24 | | | | | | |
| ŏ | R 72 | 57.60.1104 | 100k | MF, 1%, 0204, E24 | | | | | | |
| Ó | R 73 | 67.60.1104 | 100k | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 74 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 75 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 76 | 57.60.1104 | 100k | MF, 1%, 0204, E24 | | | | | | |
| 0 | H 77 | 57.60.1104 | 100k | MF, 1%, 0204, E24 | | | | | | |
| 0 | H 78 | 57.60.1472 | 487 | MF, 176, U209, E24 | | | | | | |
| 0 | n /9 D en | 57.60.1472 57.60.1104 | 45/ 100k | ME, 1%, 0204, 524 ME 1%, 0204 504 | | | | | | |
| 0 | n 80 R 81 | 57.00.1104 | 4k7 | MF, 1%, 0204, E24 MF 1%, 0204 F24 | | | | | | |
| 0 | B 82 | 57.60.1100 | 10B | MF. 1% 0204. E24 | | | | | | |
| ó | R 83 | 57.60.1220 | 22R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 84 | 57.60.1220 | 22R | MF, 1%, 0204, E24 | | | | | | |
| . 0 | R 85 | 57.60.1220 | 22R | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 86 | 57.60.1220 | 22R | MF, 1%, 0204, E24 | | | | | | |

Block Diagram Digital Input



Digital Input 1.942.740.20



Digital Input 1.942.740.20



Digital Input 1.942.740.20



Digital Input 1.942.740.20 (1)

Page: 1 of 1

| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx. | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|--------|-------------|-------|-----------------|-------------------------------|--------|------------|-----------------|----------|--------------------|---------------------------|
| | | | | | | | | | | | NE 48/ 0004 E0/ |
| 0 | C 1 | 59.60.2337 | | 33p | CER 50V, 5%, C0G, 0805 | 0 | R 3 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | C 2 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 4 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | С 3 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 5 | 57.60.1101 | | 1008 | MF, 1%, 0204, E24 |
| 0 | C 4 | 59.60.2341 | | 47p | CER 50V, 5%, C0G, 0805 | 0 | R 6 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | C 5 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 7 | 57.60.1822 | | 8k2 | MF, 1%, 0204, E24 |
| 0 | C 6 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | R 8 | 57.60.1471 | | 470R | MF, 1%, 0204, E24 |
| 0 | C 7 | not used | | 1u0 | EL 50V 20% RM5 | 0 | R9 | 57.60.1182 | | 168 | MF, 1%, 0204, E24 |
| 0 | C 8 | 59.60.3533 | | 47n | CER 50V, 10%, X7R, 1210 | 1 | R 10 | 57.60.1000 | | URU | MF, 0204 |
| 0 | C 9 | 59.60.3325 | | 10n | CER 50V, 10%, X7R, 0805 | 0 | R 11 | 57.60.1102 | | 1K0 | MF, 1%, 0204, E24 |
| 0 | C 10 | 59.60.2337 | | 33p | CER 50V, 5%, C0G, 0805 | 1 | R 12 | 57.92.7013 | | 0.5A | PTC 60V |
| 0 | C 11 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 13 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | C 12 | 59.60.2337 | | 33p | CER 50V, 5%, C0G, 0805 | 0 | R 14 | 57.60.1100 | | 10R | MF, 194, 0204, E24 |
| 0 | C 13 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 15 | 57.60.1823 | | 82k | MF, 1%, 0204, E24 |
| 0 | C 14 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 16 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 15 | 59.22.5220 | | 22u | EL 25V 20% RM5 | 0 | R 17 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 16 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 18 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| ٥ | C 17 | 50.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 19 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 18 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 20 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 19 | 59.60.3325 | | 10n | CER 50V, 10%, X7R, 0805 | 0 | R 21 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 20 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 22 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 21 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 23 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 |
| 0 | C 22 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 24 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 23 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 25 | 57.00.1500 | | JOR | MF, 1%, 0204, E24 |
| 0 | C 24 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 26 | 57.60.1560 | | 56R | MF, 1%, 0204, E24 |
| 0 | C 25 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 27 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | C 26 | 59.60.3537 | | 100n | CER 50V, 10%, X7R, 1210 | 0 | R 28 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | C 27 | 59.32.1221 | | 220p | CER 10%, 400V | 0 | R 29 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | C 20 | 59.02.1221 | | 220p | OER 10%, 400V | 0 | R 30 | 57.60.1472 | | 4k7 | MF. 1%. 0204. E24 |
| 0 | C 29 | 59.32.1221 | | 220p | CER 10%, 400V | 0 | R 31 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 30 | 59.32.1221 | | 220p | CER 10%, 400V | 0 | R 32 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 31 | 59.60.3537 | | 100n | CER 50V. 10%. X7R. 1210 | 0 | R 33 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 32 | 59.60.3537 | | 100n | CER 50V. 10%. X7R. 1210 | 0 | R 34 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| ő | C 33 | 59 60 3537 | | 100n | CER 50V 10% X7R 1210 | 0 | R 35 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 |
| ő | C 34 | 59.60.3537 | | 100n | CER 50V. 10%. X7R. 1210 | 0 | R 36 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 |
| 0 | C 35 | 59.60.2349 | | 100p | CER 50V. 5% C0G 0805 | 0 | R 37 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| õ | C 36 | 59 60 2349 | | 100p | CER 50V 5% COG 0805 | 0 | R 38 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| ő | C 37 | 59 60 2349 | | 100p | CER 50V 5% COG 0805 | 0 | R 39 | 57.60.1820 | | 82R | MF, 1%, 0204, E24 |
| ő | C 38 | 59 60 3537 | | 100p | CER 50V 10% X7R 1210 | 0 | R 40 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| ŏ | C 39 | 59.60.2373 | | 10011 | CER 50V. 5% COG 0805 | 0 | R 11 | 57.60.1102 | | 10k | ME, 194, 0204, E24 |
| 0 | D 1 | 50 60 8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 42 | 57.60.1820 | | 82R | MF, 1%, 0204, E24 |
| ő | D 2 | 50 60 8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 43 | 57.60.1220 | | 22R | MF, 1%, 0204, E24 |
| ő | 10.1 | 50 15 0114 | | 9637 | Dual diff Line Receiver | 0 | R 44 | 57.60.1100 | | 10R | MF, 1%, 0204, E24 |
| ő | 10.1 | 50 62 1904 | | 74HCU04 | Hex inverter unbuffered | 0 | R 45 | 57.60.1220 | | 22R | MF, 1%, 0204, E24 |
| 0 | 10.2 | 50.62.1904 | | 74110004 | 8ch analog mux/demux | 0 | R 46 | 57.60.1220 | | 22R | MF, 1%, 0204, E24 |
| 0 | IC 4 | 50.13.0204 | | | IC AD 1890 JN A | 0 | R 47 | 57.60.1220 | | 22R | MF, 1%, 0204, E24 |
| 0 | 10.5 | 50 62 0913 | | CS8412 | AES-Receiver | 0 | R 48 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| ő | | 50 62 1032 | | 74HC 32 | Quad 2input OR | 0 | R 49 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| ő | 10.7 1 | 942 923 21 | | 14110 02 | | 0 | R 50 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| v | | .042.023.21 | | (50160212 AT80C | 2051) | 0 | R 51 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | IC 8 | 50.62.1000 | | 74HC 00 | Quad 2input NAND | 0 | RZ 1 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 | IC 9 | 50.15.0114 | | 9637 | Dual diff Line Receiver | 0 | RZ 2 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 | IC 10 | 50.62.1951 | | 74HC4051 | 8ch analog mux/demux | 0 | S 1 | 55.01.0168 | | 8*a | DIL-Switch, PCB |
| 0 | IC 11 | 50.62.1595 | | 74HC595 | 8bit shift/output register | 0 | Т1 | 1.022.647.00 | | 1:1.4 | OUTPUT TRAFO AES/EBU |
| 0 | IC 12 | 50.62.1165 | | 74HC165 | 8bit shift register | 0 | Т2 | 1.022.647.00 | | 1:1.4 | OUTPUT TRAFO AES/EBU |
| 0 | IC 13 | 50.62.1165 | | 74HC165 | 8bit shift register | 0 | XIC 7 | 53.03.0165 | | 20p | DIL-socket 0.3" |
| 0 | IC 14 | 50.62.1595 | | 74HC595 | 8bit shift/output register | 0 | XY 1 | 89.01.1499 | | | QUARZ - ISOLIERPLATTE |
| 0 | IC 15 | 50.15.0114 | | 9637 | Dual diff Line Receiver | 0 | XY 2 | 89.01.1499 | | | QUARZ - ISOLIERPLATTE |
| 0 | IC 16 | 50.04.3200 | | CNY17-2 | Opto-coupler | 0 | Y 1 | 89.01.1009 | | 16.000MHz | XTAL HC 49/U |
| 0 | IC 17 | 50.04.3200 | | CNY17-2 | Opto-coupler | 0 | Y 2 | 89.01.1016 | | 22.1184MHz | XTAL HC 49/U |
| 0 | J 1 | 54.01.0021 | 2 pcs | Jumper | 0.63*0.63mm, Au | | | | | | |
| 0 | L1 1 | .010.321.64 | | Wire | U shaped wire 0.6mm, 4.3*5.0 | | | | | - End of liet | |
| 0 | L2 1 | .010.321.64 | | Wire | U shaped wire 0.6mm, 4.3*5.0 | ~ | | | | LING OF LIST | |
| 0 | L3 1 | .010.321.64 | | Wire | U shaped wire 0.6mm, 4.3*5.0 | Com | ments: | | | | |
| 0 | L4 1 | .010.321.64 | | Wire | U shaped wire 0.6mm, 4.3*5.0 | (01) F | ≺10 22R->0 | RU 57.60.1220-> | 57.60.10 | 000; R12 0.3A->0.5 | A 57.92.7012->57.92.7013 |
| 0 | L 5 | 62.02.3101 | | 100uH | 10%, radial RM 5 | | | | | | |
| 0 | L 6 | 62.02.3101 | | 100uH | 10%, radial RM 5 | | | | | | |
| 0 | MP 1 1 | .942.240.12 | 1 pce | | Digital Input Module PCB | | | | | | |
| 0 | MP 2 | 43.01.0108 | 1 pce | Label | ESE-WARNSCHILD | | | | | | |
| 0 | MP 3 1 | .942.740.10 | 1 pce | | NR.ETIKETTE | | | | | | |
| 0 | MP4 1 | .101.001.20 | 1 pce | Label | TEXT-ETIK, 5*20 HARDWARE -20 | | | | | | |
| 1 | MP 5 | 43.10.0110 | • | A | Revisions-Etikette 5mm h'blau | | | | | | |
| 0 | P 1 | 54.14.2054 | | 40p | Stecker gerade Au | | | | | | |
| 0 | P 2 | 54.21.2203 | | 3р | XLR PCB Winkel | | | | | | |
| 0 | P 3 | 54.21.2203 | | 3р | XLR PCB Winkel | | | | | | |
| 0 | P 4 | 54.21.2006 | | 1*2p | Cinch vertikal PCB | | | | | | |
| 0 | P 5 | 89.10.0121 | | TORX173 | Toslink Receiver | | | | | | |
| 0 | P 6 | 89.10.0121 | | TORX173 | Toslink Receiver | | | | | | |
| 0 | P 7 | 54.13.0076 | | 9p | D-Sub, PCB, Winkel | | | | | | |
| 0 | P 8 | 54.01.0020 | | 1p | Pin, 1reihig, gerade | | | | | | |
| 0 | P 9 | 54.01.0020 | | 1p | Pin, 1reihig, gerade | | | | | | |
| 0 | P 10 | 54.11.0136 | | 2*3p | Pin 0.63*0.63, RM2.54 | | | | | | |
| 0 | Q 1 | 50.03.0340 | | BC337-25 | NPN, 800mA | | | | | | |
| 0 | Q 2 | 50.03.0340 | | BC337-25 | NPN, 800mA | | | | | | |
| 0 | Q 3 | 50.03.0340 | | BC337-25 | NPN, 800mA | | | | | | |
| 0 | Q 4 | 50.03.0340 | | BC337-25 | NPN, 800mA | | | | | | |
| 0 | R 1 | 57.60.1125 | | 1M2 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 2 | 57.60.1182 | | 1k8 | MF, 1%, 0204, E24 | | | | | | |

TB Mic Input 1.942.719.00



On-Air 1000 Digital Mixing Console

STUDER

TB Mic Input 1.942.719.00



)

0 R 24

0 R 25

0 R 26

0 R 27 0 R 28 0 R 29

0 R 30

57.60.1270

57.60.1223

57.60.1102

57.60,1683 57.60.1333

57.60.1270

57.60.1103

27R

22k

1k0

68k 33k

27R

10k

MF, 1%, 0204, E24

MF, 1%, 0204, E24

MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24

TB Mic Input 1.942.719.00

| ldx | Pos. | Part No. Qty. | Type/Val. | Description | ldx | Pos. | Part No. | Qty. | Type/Val. | Description |
|-----|--------------|---------------|-----------|----------------------|-----|------------|--------------|------|------------|-----------------------------|
| 0 | P 11 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 31 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | P 12 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 32 | 57.60.1335 | | 3M3 | MF, 1%, 0204, E24 |
| 0 | P 13 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 33 | 57.60.1335 | | 3M3 | MF, 1%, 0204, E24 |
| 0 | P 14 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 34 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | P 15 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 35 | 57.00.1102 | | 11(0 | MF, 1%, 0204, E24 |
| 0 | P 16 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 36 | 57.60.1273 | | 27k | MF, 1%, 0204, E24 |
| 0 | P 17 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 37 | 57.60.1273 | | 27k | MF, 1%, 0204, E24 |
| 0 | P 18 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 38 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | P 19 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 39 | 57.60.1331 | | 330R | MF, 1%, 0204, E24 |
| 0 | P 20 | 54.01.0020 | 1p | Pin, 1roihig, gorado | 0 | R 10 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | P 21 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 41 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | P 22 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 42 | 57.60.1331 | | 330R | MF, 1%, 0204, E24 |
| 0 | P 31 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 43 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | P 32 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 44 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | P 33 | 54.01.0020 | 1p | Pin, 1roihig, gorado | ٥ | R 15 | 57.60.1472 | | 41.7 | MF, 1%, 0204, E24 |
| 0 | P 34 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 46 | 57.60.1622 | | 6k2 | MF, 1%, 0204, E24 |
| 0 | P 35 | 54.01.0020 | 10 | Pin, 1reihig, gerade | 0 | R 47 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | P 36 | 54.01.0020 | 10 | Pin, 1reihig, gerade | 0 | R 48 | 57.60.1569 | | 5R6 | MF, 1%, 0204, E24 |
| 0 | P 37 | 54.01.0020 | 10 | Pin, 1reibig, gerade | 0 | R 49 | 57.60.1682 | | 6k8 | MF. 1%, 0204, E24 |
| 0 | P 38 | 54.01.0020 | 10 | Pin, 1roihig, gorado | 0 | R 50 | 57.60.1123 | | 12k | MF, 1%, 0204, F24 |
| 0 | P 41 | 54.01.0020 | 10 | Pin, 1reihig, gerade | 0 | R 51 | 57.60.1123 | | 12k | MF, 1%, 0204, E24 |
| 0 | P 42 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 52 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | P 43 | 54 01 0020 | 1p | Pin 1reihig gerade | 0 | R 53 | 57.60.1102 | | 1k0 | MF. 1%, 0204, E24 |
| | P 44 | 54 01 0020 | 1n | Pin 1reihig gerade | 0 | R 54 | 57 60 1100 | | 10R | MF, 1%, 0204, E24 |
| Ő | P 45 | 54.01.0020 | 10 | Pin, 1reihig, gerade | 0 | R 55 | 57.60.1153 | | 15k | MF, 1%, 0204, F24 |
| 0 | P 46 | 54.01.0020 | 1p | Pin, 1reihig, gerade | 0 | R 56 | 57.60.1332 | | 3k3 | MF, 1%, 0204, E24 |
| 0 | P 47 | 54.01.0020 | 10 | Pin, 1reihig, gerade | 0 | R 57 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | P 48 | 54.01.0020 | 10 | Pin 1reihig, gerade | 0 | R 58 | 57.60.1221 | | 220R | MF. 1%, 0204, E24 |
| | | 0110110020 | . P | i ing mening, gerade | 0 | R 59 | 57 60 1182 | | 1k8 | MF, 1%, 0204, E24 |
| 0 | Q 1 | 50.60.1001 | BC857B | PNP 45V 100mA SOT 23 | 0 | R 60 | 57 60 1105 | | 1M | MF. 1%, 0204, E24 |
| 0 | Q 2 | 50.03.0350 | J112 | JFET N-Channel | 0 | R 61 | 57.60.1123 | | 12k | MF, 1%, 0204, E24 |
| 0 | Q 3 | 50.60.0001 | BC847B | NPN 45V 100mA SOT 23 | 0 | R 62 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | D 1 | 57 60 1102 | 104 | ME 19/ 0204 E24 | 0 | R 63 | 57 60 1473 | | 47k | MF, 1%, 0204, E24 |
| 0 | | 57.60.1103 | 106 | MF, 1%, 0204, E24 | Ő | R 64 | 57 60 1270 | | 27R | MF. 1%, 0204, E24 |
| 0 | R 2 D 3 | 57.60.1479 | 4500 | NF, 1%, 0204, E24 | 0 | R 65 | 57 60 1270 | | 27R | MF 1% 0204 E24 |
| 0 | | 57 60 1151 | 150R | ME 1% 0204 E24 | | | | | | |
| 0 | D S | 57.00.1151 | 1200 | ME 1% 0204 E24 | 0 | RA 1 | 58.01.9203 | | 20k | Cermet, 10%, 0.5W, vertical |
| . 0 | | 57.00.1121 | 0.24 | DTC 601/ | 0 | RA 2 | 58.01.9203 | | 20k | Cermet, 10%, 0.5W, vertical |
| 0 | | 57.92.7012 | 0.3A | FTC 60V | 0 | T 4 | 4 000 050 00 | | | |
| 0 | | 57.60.1101 | 1008 | MF, 1%, 0204, E24 | 0 | 11 | 1.022.352.00 | | 4.0.40 | ENOANGOTRAFO |
| 0 | P G | 57.60.1103 | 104 | ME 1% 0204 E24 | U | 12 | 1.022.417.00 | | 1:3.16 | EINGANGSTRAFO 1:3,16 |
| 0 | D 10 | 57.00.1103 | 1600 | ME 1% 0204 E24 | | | | | | |
| 0 | | 57.00.1151 | 1501 | ME 0204 | | | | | - End of L | ist |
| 0 | | 57.60.1000 | | NF, 0204 | Co | mments: | | | | |
| 0 | R 12 | 57.60.1103 | TUK | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 13 | 57.92.7011 | 0.24 | PTC 60V | | | | | | |
| 0 | R 14 | 57.92.7011 | 0.24 | | | | | | | |
| 0 | N 10 | 57.00.1151 | 150K | ME 1% 0204 E24 | | | | | | |
| U | rt 10 | 57.60.1182 | 168 | WF, 1%, U2U4, E24 | | | | | | |
| Û | K 1/ | 57.60.1182 | 168 | WF, 1%, 0204, E24 | | | | | | |
| 0 | K 18 D 40 | 57.60.1151 | 150R | MF, 1%, 0204, E24 | | | | | | |
| Ű | K 19 D 00 | 57.60.1101 | 1008 | WF, 1%, U2U4, E24 | | | | | | |
| Û | R 20 | 57.60.1151 | 150K | MF, 1%, 0204, E24 | | | | | | |
| 0 | K 21 | 57.60.1151 | 150K | WF, 1%, 0204, E24 | | | | | | |
| 0 | R 22 | 57.60.1273 | 27k | M⊢, 1%, 0204, E24 | | | | | | |
| 0 | R 23 | 57.60.1102 | 1k0 | MF, 1%, 0204, E24 | | | | | | |

Insert Module 1.942.660.20



Insert Controller 1.942.161.00



On-Air 1000 Digital Mixing Console

STUDER

Insert Controller 1.942.161.00







4

1 OF







220

P3_3 SEND_24.RIGHT-

P3 10 CHASSIS

P3 6 CHASSIS

| Erst | ellt 🕕 18.10.2000 | D PZ 🔘 | 0 | G | | 0 | | |
|------|-------------------|--------|----------|-------|-------|------|------|----|
| | | | OnAir 18 | 00 | | PAGE | 2 OF | 2 |
| E | TUDER | ANALOG | INSERT | D-SUB | SC 1. | 942. | 163. | 00 |

C46 22u

+6dB

047 22u

018 22u

+6dB

Analog Insert D-Sub 1.942.163.00



| ldx | Poi. | Part No. Qty. | Type/Val. | Description |
|-----|------------|---------------|-------------|-------------------------|
| 0 | C 1 | 59.60.3337 | 100n | CER 50V, 10%, X7R 0805 |
| 0 | C 2 | 59.68.0029 | 100u | EL 6V. 6.3*5.7 |
| 0 | C 3 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C 4 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C 5 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C 6 | 59.60.2373 | 1n0 | CER 50V, 5%, C0G, 0805 |
| 0 | C 7 | 59.60.2373 | 1n0 | CER 50V, 5%, C0G, 0805 |
| 0 | C 8 | 59.60.2373 | 1n0 | CER 50V, 5%, C0G, 0805 |
| 0 | C 9 | 59.60.2373 | 1n0 | CER 50V, 5%, C0G, 0805 |
| 0 | C 10 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C 11 | 59.60.3337 | 100n | CER 50V, 10%, X7R 0805 |
| 0 | C 12 | 59.60.3337 | 100n | CER 50V, 10%, X7R 0805 |
| 0 | C 13 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C 14 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C 15 | 59.60.3337 | 100n | CER 50V, 10%, X7R 0805 |
| 0 | C 16 | 59.60.3337 | 100n | CER 50V, 10%, X7R 0805 |
| 0 | C 1/ | 59.68.0029 | 1000 | EL 6V, 6.3"5.7 |
| 0 | C18 | 59.68.0067 | 220 | EL 16V, 5.0°5.7 |
| 0 | C 19 | 59.68.0067 | 220 | EL 16V, 5.0°5.7 |
| 0 | 620 | 09.00.0007 | 220 | EL 16V 50*57 |
| 0 | 021 | 00.00.0007 | ∠∠u 100n | CER 50V 10% X7R 0805 |
| 0 | C 22 | 50 80 3337 | 100n | CER 50V, 10%, X7R 0805 |
| 0 | C 24 | 59 60 3337 | 100n | CER 50V 10% X7R 0805 |
| 0 | 0.25 | 59 60 3337 | 100n | CER 50V 10% X7B 0805 |
| 0 | C 26 | 59.60.3337 | 100n | CER 50V, 10%, X7R 0805 |
| õ | C 77 | 59 60 3337 | 100n | CER 50V. 10%, X7R. 0805 |
| ñ | C 28 | 59.60.3337 | 100n | CER 50V, 10%, X7R 0805 |
| õ | C 29 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 30 | 59.60.2361 | 330p | CER 50V. 5%, C0G.0805 |
| ò | C 31 | 59.60.2361 | 330p | CER 50V, 5%, COG, 0805 |
| 0 | C 32 | 59.60.2361 | 330p | CER 50V, 5%, COG, 0805 |
| 0 | C (3 | 59.60.2361 | 330p | CER 50V, 5%, C0G, 0805 |
| 0 | C (4 | 59.60.2361 | 330p | CER 50V, 5%, C0G, 0805 |
| 0 | C (5 | 59.60.2361 | 330p | CER 50V, 5%, C0G, 0805 |
| 0 | C 16 | 59.60.2361 | 330p | CER 50V, 5%, C0G, 0805 |
| 0 | C (7 | 59.60.2361 | 330p | CER 50V, 5%, C0G, 0805 |
| 0 | C (8 | 59.60.2257 | 220p | CER 50V, 5%, C0G, 0603 |
| 0 | C (9 | 59.60.2257 | 220p | CER 50V, 5%, C0G, 0603 |
| 0 | C 40 | 59.60.2257 | 220p | CER 50V, 5%, C0G, 0603 |
| 0 | C (1 | 59.60.2257 | 220p | CER 50V, 5%, C0G, 0603 |
| 0 | C +2 | 59.68.0067 | 22u | EL 16V, 5.0*5.7 |
| 0 | C 43 | 59.68.0067 | 22u | EL. 16V, 5.0*5.7 |
| 0 | C 4 | 59.68.0067 | 22u | EL 16V, 5.0*5.7 |
| 0 | C +5 | 59.68.0067 | 22u | EL 16V, 5.0*5.7 |
| 0 | C +6 | 59.68.0067 | 22u | EL 16V, 5.0*5.7 |
| 0 | C 47 | 59.68.0067 | 22u | EL 16V, 5.0*5.7 |
| 0 | C 48 | 59.68.0067 | 22u | EL 16V, 5.0*5.7 |
| 0 | C 49 | 59.68.0067 | 22u | EL 16V, 5.0*5.7 |
| 0 | C 10 | 59.60.2257 | 220p | CER 50V, 5%, COG, 0603 |
| 0 | 0.11 | 59.60.2257 | 220p | CER 50V, 5%, COG, 0603 |
| 0 | 0.12 | 59.60.225/ | 220p | CER 50V, 5%, CUG, 0003 |
| 0 | с ю с и | 53.00.2257 | 220p | CEP 50V 5% C0G 0603 |
| 0 | C /5 | 50.60.2257 | 220p | CER 50V 5% C0G 0603 |
| n | C 16 | 59.60.2257 | 220p | CER 50V 5% COG 0603 |
| õ | C 17 | 59.60.2257 | 220p | CER 50V. 5%. C0G.0603 |
| ō | C 18 | 59.60.2257 | 220p | CER 50V, 5%, C0G, 0603 |
| 0 | C 19 | 59.60.2257 | 220p | CER 50V, 5%, C0G, 0603 |
| 0 | C 10 | 59.60.2257 | 220p | CER 50V, 5%, C0G, 0603 |
| 0 | C (1 | 59.60.2257 | 220p | CER 50V, 5%, C0G, 0603 |
| 0 | C 12 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 13 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 14 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 15 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 16 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 17 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 18 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 19 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C .0 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C '1 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C '2 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C.3 | 59.68.0029 | 100u | EL 6V, 6.3*5.7 |
| 0 | C '4 | 59.63.1105 | 220p | PPS 50V, 2%, 0805 |
| 0 | C '5 | 59.63.1105 | 220p | PPS 50V, 2%, 0805 |
| 0 | C '6 | 59.63.1105 | 220p | PPS 50V, 2%, 0805 |
| 0 | 0.10 | 59.63.1105 | 220p | PPS 50V, 2%, 0805 |
| U | 0.8 | 09.03.1105 | 220p | FF3 30V, 2%, 0805 |
| 0 | C 10 | 50.63.1105 | 220p | PPS 50V 2% 0805 |
| 0 | 5.0 | 00.00.1100 | LEOP | |
| | | | | |

Analog Insert D-Sub 1.942.163.00

| Int Part No. Opy. Type/Viel. Description View Part No. Opy. Type/Viel. 0 C41 56.811151 200 PPS 500, 2%, 6805 0 R38 57.001522 27.7 0 C43 56.81113 1n0 PPS 500, 2%, 6805 0 R40 57.001522 28.6 0 C46 56.81113 1n0 PPS 500, 2%, 6805 0 R44 57.001522 22.7 0 C47 56.81113 1n0 PPS 500, 2%, 6805 0 R44 57.001522 22.7 0 C48 56.81113 1n0 PPS 500, 2%, 6805 0 R44 57.001522 22.7 0 C48 56.01122 100.01 CER 100, 100, XRR, 8005 0 R44 57.001582 56.6 0 C48 56.01122 100.01 CER 100, 100, XRR, 8005 0 R46 57.001582 56.6 0 C48 56.01122 100.01 CER 100, 100, XRR, 8005 0 R46 < | | | | | | | | | | | | |
|---|-----|--------------|--------------|-------|------------|--------------------------------|-----|--------|------------|--------------------------|--------------|--------------------------------------|
| 0 0 C B2 96.83.1113 110 PPS SV/, 2%, 0805 0 R 39 5*.001272 2%7 0 C B3 96.83.1113 110 PPS SV/, 2%, 0805 0 R 40 5*.001522 3%6 0 C B4 56.01.113 110 PPS SV/, 2%, 0805 0 R 44 5*.001522 2%7 0 C B4 56.01.113 110 PPS SV/, 2%, 0805 0 R 44 5*.001522 2%7 0 C B4 56.01.113 110 PPS SV/, 2%, 0805 0 R 44 5*.001522 2%7 0 C B4 56.01.113 110 PPS SV/, 2%, 0805 0 R 44 5*.001522 2%7 0 C B4 56.01.113 110 PPS SV/, 2%7, 0805 0 R 44 5*.001522 2%7 0 C B4 56.01.212 2%7 0 C B4 56.01.222 2%7 0 C B4 56.01.237 100 C R 57.07 0 R 54 57.001.152 | ldx | Pos. | Part No. | Qty. | Type/Val. | Description | ids | P | os. | Part No. Qty | r. Type/Val. | Description |
| C C S | 0 | C 81 | 59.63.1105 | | 220p | PPS 50V, 2%, 0805 | 0 | R | 38 | 57.60.1272 | 2k7 | MF, 1%, 0204, E24 |
| C C S | 0 | C 82 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | 0 | R | 39 | 57.60.1272 | 2k7 | MF, 1%, 0204, E24 |
| 0 Cell 69.63.1113 1n0 PPS EVV. 2%, 6865 0 R 41 67.60.1222 2% 0 Cell 66.63.1113 1n0 PPS EVV. 2%, 6855 0 R 43 57.60.1272 2% 0 Cell 66.63.1113 1n0 PPS EVV. 2%, 6855 0 R 44 57.60.1272 2% 0 Cell 66.63.1113 1n0 PPS EVV. 2%, 6855 0 R 45 57.60.1562 56.6 0 Cell 66.80.3337 100n CER 80V.10N, XTR, 805 0 R 44 57.60.1562 56.6 0 Cell 56.80.3337 100n CER 80V.10N, XTR, 805 0 R 50 57.60.1562 56.6 0 Cell 56.80.3337 100n CER 80V.10N, XTR, 805 0 R 50 57.60.152 56.6 0 Cell 56.80.0607 22.1 E.1 16V.505.7 0 R 53 57.60.152 56.6 0 Cell 56.80.0607 22.1 E.1 16V.505.7< | 0 | C 83 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | 0 | R | 40 | 57.60.1562 | 5k6 | MF, 1%, 0204, E24 |
| C C 66 663.1113 1n0 PPS 6V/, 2%, 0805 0 R 42 67.00.1272 2% C C 66.63.1113 1n0 PPS 6V/, 2%, 0805 0 R 43 57.00.1272 2% C C 66.63.1113 1n0 PPS 6V/, 2%, 0805 0 R 44 57.00.1622 3% C C 66.63.3173 1n0 PPS 6V/, 2%, 0805 0 R 45 57.00.1622 3% C C 66.03.337 100n CER 8V/, 10%, XTR, 1005 0 R 45 57.60.1622 3% C C 28.60.067 22.1 E.1 10%, 50°5.7 0 R 53 57.60.1622 3% C C 66.61.6103 CSS800 AD Corventer 2418 Ibit SSOP20 R 54 57.60.1622 146 C C 66.61.6103 CSS800 AD Corventer 2418 Ibit SSOP20 R 56 57.60.122 2% C C 66.61.61.6103 CSS800 AD Corventer 2418 Ibit SSOP20 R 56 | 0 | C 84 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | 0 | R | 41 | 57.60.1562 | 5k6 | MF, 1%, 0204, E24 |
| C C B SOL 201 PH 3 S P 301422 AX C C C SC 30113 1n0 PPS 50V, 2%, 0855 C R 43 S P 301462 Sc 30 C C SC 30113 1n0 PPS 50V, 2%, 0855 C R 44 S P 301462 Sc 30 C C SC 301337 100n CE R 50V, 10N, XTR, 1005 C R 44 S P 401272 2% 7 C SC 301337 100n CE R 50V, 10N, XTR, 1005 C R 44 S P 401272 2% 7 C SC 301337 100n CE R 50V, 10N, XTR, 1005 C R 45 S P 401272 2% 7 C SC 301 S 680,007 221 E 14V, 50°5.7 C R 54 S P 401122 146 C C 45 S 680,0067 221 E 14V, 50°5.7 C R 56 S P 401122 146 C 10 S 61,810.007 221 E 14V, 50°5.7 C R 56 S P 401132 140 <td>0</td> <td>C 85</td> <td>59.63.1113</td> <td></td> <td>1n0</td> <td>PPS 50V, 2%, 0805</td> <td>0</td> <td>R</td> <td>42</td> <td>57.60.1272</td> <td>2k7</td> <td>MF, 1%, 0204, E24</td> | 0 | C 85 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | 0 | R | 42 | 57.60.1272 | 2k7 | MF, 1%, 0204, E24 |
| C C27 66 63.1113 1n0 PPS 50V, 2%, 0855 V R44 57.60.1562 Mathematic C C88 69 63.1113 1n0 PPS 50V, 2%, 0855 R 44 57.60.1562 345 C 68 56.61.3337 100n CER 80V, 10%, XTR, 1805 R 44 57.60.1562 345 C 68 56.61.3337 100n CER 80V, 10%, XTR, 1805 R 44 67.60.1572 37.7 C 64 56.60.0337 100n CER 80V, 10%, XTR, 1805 R 46 57.60.1572 37.7 C 64 56.86.0067 22u EL 10%, 20.75.7 D R 55. 57.60.1582 56.06 56.06 57.60.1582 36.06 56.07 22u EL 10%, 20.75.7 D R 55. 57.60.1582 36.06 56.07 50.01582 36.01 56.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.01582 36.0 | 0 | C 86 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | 0 | R | 43 | 57.60.1272 | 2K7 | MF, 1%, 0204, E24 |
| C C B Sign 2 Sign 2 <t< td=""><td>0</td><td>C 87</td><td>59.63.1113</td><td></td><td>1n0</td><td>PPS 50V, 2%, 0805</td><td>0</td><td>R</td><td>44</td><td>57.60.1562</td><td>5K6</td><td>MF, 1%, 0204, E24</td></t<> | 0 | C 87 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | 0 | R | 44 | 57.60.1562 | 5K6 | MF, 1%, 0204, E24 |
| C 680 69 68.3113 1100 CPS 60V, 2%, 0655 C R 46 9 160.122 2.47 C 611 59.60.3337 100n CER 60V, 10%, XTR, 0805 C R 44 9 160.1622 345 C 62 59.60.3337 100n CER 50V, 10%, XTR, 0805 C R 44 9 160.1622 345 C 64 59.60.0687 22u E.L 10V, 50°57 C R 55 9 66.0167 22u E.L 10V, 50°57 C R 56 9 66.0167 22u E.L 10V, 50°57 C R 56 9 66.0167 22u E.L 10V, 50°57 C R 56 9 67.01162 148 C 16 50.61 1013 CS5380 AD Converter 24bit Sta SSOP20 C R 67 9 76.01202 240 C 16 50.61 1204 MC33078 Dual Cp-Armo tow noise C R 56 9 76.0132 240 C 16 50.61 1204 MC33078 Dual Cp-Armo tow noise C R 56 9 76.0132 240 C 16 50.61 1204 | 0 | C 88 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | 0 | R | 40 | 51.00.1502 | 5K0 2147 | MF, 1%, 0204, E24 |
| C C C F | 0 | C 89 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | 0 | | 40 | 51.00.1272 | 267 | MF, 1%, 0204, E24 |
| C D 61 58 60.3337 100.n CER 80V, 10%, XTR, 1805 0 R 40 57 60.1262 56 C 62 58 60.0337 100.n CER 80V, 10%, XTR, 1805 0 R 40 57 60.1262 57 C 64 58 60.067 22.u E. 10V, 5757 0 R 53 57 60.1182 168 C 64 58 68.0067 22.u E. 10V, 5757 0 R 54 57 60.1182 168 C 64 56 68.0067 22.u E. 10V, 5757 0 R 54 57 60.1182 168 C 12 56 61.8103 C53830 AD Convertar 2405 Bit S05020 0 R 55 57 60.1182 168 C 12 56 61.0204 M033078 Dual Or-Amp low noise 0 R 58 57 60.1202 240 C 16 56 61.0204 M033078 Dual Or-Amp low noise 0 R 61 57 60.1322 343 C 16 56 61.0204 M033078 Dual Or-Amp low noise 0 R 61 57 60.1332 343 | 0 | C 90 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | | 47 | 57.60.1272 | 2K7 5k6 | ME 1% 0204 E24 |
| C C 22 S 8 (0.3337) TUD CLF S0V, TUN, XTR, BBD C R S 0 TR S0 C R S0 C R S0V, TUN, XTR, BBDS C R S1 S 5 (0.5122) S17 C 24 56 86.0067 22u EL 1 W, S.0757 C R 52 S16 | 0 | C 91 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | R | 40 | 57 60 1562 | 546 | MF 1% 0204 E24 |
| C E43 55.66.0067 Zu E1 10V, 57.57 R 51 F 61 1722 EX C C 64 56.86.0067 Zu E1 10V, 57.57 R 52 57.60.1182 118 C C 75 56.86.0067 Zu E1 10V, 57.57 R 75.5 F 65.1582 118 C C 75 56.86.0067 Zu E1 10V, 57.57 R 75.5 F 65.01182 118 C C 75 56.85.0067 Zu E1 10V, 57.57 R 65.5 57.60.1182 118 C C 75 56.85.0067 Zu E1 10V, 57.57 R 65.5 57.60.1182 118 C C 75 56.85.0067 Zu E1 10V, 57.57 R 65.5 57.60.1182 118 C C 75 56.81.0264 MC33075 Duil Op-Amp tow notise R 75 57.60.1202 20.0 C C 85 56.81.0264 MC33075 Duil Op-Amp tow notise R 64 57.60.1332 33.3 C C 10 56.81.0264 MC33075 Duil Op-Amp tow notise R 76.61.0332 33.3 C 10 12 56.81.0264 MC33075 </td <td>0</td> <td>C 92</td> <td>59.60.3337</td> <td></td> <td>100n</td> <td>CER 50V, 10%, X7R, 0805</td> <td>0</td> <td>R</td> <td>50</td> <td>57 60 1272</td> <td>2k7</td> <td>MF. 1%, 0204, E24</td> | 0 | C 92 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | R | 50 | 57 60 1272 | 2k7 | MF. 1%, 0204, E24 |
| C C <thc< th=""> C <thc< th=""> <thc< th=""></thc<></thc<></thc<> | 0 | 0.93 | 59.60.3337 | | 100n | CER 50V, 10%, X/R, 0805 | 0 | R | 51 | 57 60 1272 | 2k7 | MF. 1%, 0204, E24 |
| 0 | 0 | 0.94 | 59.08.0007 | | 220 | EL 16V, 5.0 5.7 | 0 | R | 52 | 57.60.1562 | 5k6 | MF, 1%, 0204, E24 |
| C 690 D 690 D 690 D 690 D 690 D 700 R 64 F 700 R 700 <thr< td=""><td>0</td><td>C 95</td><td>59.00.0007</td><td></td><td>220</td><td>EL 16V, 5.0*5.7</td><td>0</td><td>R</td><td>53</td><td>57.60.1182</td><td>1k8</td><td>MF, 1%, 0204, E24</td></thr<> | 0 | C 95 | 59.00.0007 | | 220 | EL 16V, 5.0*5.7 | 0 | R | 53 | 57.60.1182 | 1k8 | MF, 1%, 0204, E24 |
| Construction Construction Construction Construction Construction Construction Res 67.01182 148 0 IC 2 50.618103 CSS380 AD Converter 24bit Sta SSOP20 0 R.65 57.001182 148 0 IC 4 50.61824 MC33075 Dual Op-Amp low noise 0 R.65 57.001122 2k0 0 IC 6 50.61824 MC33075 Dual Op-Amp low noise 0 R.60 57.001202 2k0 0 IC 6 50.61824 MC33075 Dual Op-Amp low noise 0 R.61 57.001322 3k3 0 IC 6 50.61024 MC33075 Dual Op-Amp low noise 0 R.63 57.001332 3k3 0 IC 1 50.00124 2142 Audio balanced line driver 0 R.65 57.01332 3k3 0 IC 13 50.01224 MC33075 Dual Op-Amp low noise 0 R.66 57.01332 3k3 0 IC 15 50.81.0224 MC33 | 0 | C 97 | 59.68.0067 | | 220 | EL 16V 5.0*5.7 | 0 | R | 54 | 57.60.1182 | 1k8 | MF, 1%, 0204, E24 |
| 0 10 10 15 56.81.8103 CSS380 A/D Converter 24bit StsSSDP20 0 R 56 57.60.1182 148 0 10.23 50.61.8103 CSS380 A/D Converter 24bit StsSSDP20 0 R 57 57.60.1202 2k0 0 10.24 MC33077 Dual Op-Amp low noise 0 R 50 57.60.1202 2k0 0 10.26 50.61.0224 MC33077 Dual Op-Amp low noise 0 R 61 57.60.1322 3k3 0 10.26 50.61.0224 MC33077 Dual Op-Amp low noise 0 R 62 57.60.1332 3k3 10 10.26 50.60.0124 2142 Audio balanced line drivar 0 R 64 57.60.1332 3k3 10 10.10 50.60.0124 2142 Audio balanced line drivar 0 R 65 57.60.1332 3k3 10 10.13 50.60.0124 2142 Audio balanced line drivar 0 R 66 57.60.1332 3k3 10 10.13 50.6 | Ű | 001 | 00.00.000 | | 22.0 | | 0 | R | 55 | 57.60.1182 | 1k8 | MF, 1%, 0204, E24 |
| 0 10 10 2 56.81.8103 CS3890 AD Converter 24bit SisSOP20 0 R 57 67.80.1202 2k0 10 10 65.80.81024 MC33077 Dual Op-Amp low noise 0 R 56 67.60.1202 2k0 10 10.6 50.61.024 MC33077 Dual Op-Amp low noise 0 R 61 57.60.1322 3k3 10 10.6 50.61.024 MC33078 Dual Op-Amp low noise 0 R 62 57.60.1332 3k3 10 10.6 50.61.024 MC33078 Dual Op-Amp low noise 0 R 64 57.60.1332 3k3 10 10.6 50.60.0124 2142 Audio balanced line drivar 0 R 64 57.60.1332 3k3 10 10.13 50.60.0124 2142 Audio balanced line drivar 0 R 65 57.60.1332 3k3 10 10.13 50.81.024 MC33078 Dual Op-Amp low noise 0 R 69 57.60.133 13k 10 11.5 | 0 | IC 1 | 50.61.8103 | | CS5360 | A/D Converter 24bit Ste SSOP20 | 0 | R | 56 | 57.60.1182 | 1k8 | MF, 1%, 0204, E24 |
| 0 C3 56.61.0204 MC33078 Dual 0p-Amp low noise 0 R 56 57.60.1202 2k0 0 IC 6 56.81.0204 MC33075 Dual 0p-Amp low noise 0 R 60 57.60.1202 2k0 0 IC 6 56.81.0204 MC33075 Dual 0p-Amp low noise 0 R 61 67.60.1202 2k0 0 IC 7 50.61.0204 MC33075 Dual 0p-Amp low noise 0 R 62 57.60.1332 3k3 0 IC 8 50.06.0124 2142 Audio balanced line driver 0 R 66 57.60.1332 3k3 0 IC 11 50.06.0124 2142 Audio balanced line driver 0 R 66 57.60.1332 3k3 0 IC 14 50.61.0204 MC33078 Dual 0p-Amp low noise 0 R 67 57.60.1332 3k3 0 IC 16 50.61.0204 MC33078 Dual 0p-Amp low noise 0 R 70 57.60.1332 3k3 0 IC 16 50.81.0204 MC33078 | 0 | IC 2 | 50.61.8103 | | CS5360 | A/D Converter 24bit Ste SSOP20 | 0 | R | 57 | 57.60.1202 | 2k0 | MF, 1%, 0204, E24 |
| 0 10 10 10 50 61 624 MC33078 Dual 0p-Amp low noise 0 R 60 57.60.1202 2k0 0 10 | 0 | IC 3 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R | 58 | 57.60.1202 | 2k0 | MF, 1%, 0204, E24 |
| 0 CC5 56 56 16 2024 MC33078 Dual Op-Amp low noise 0 R 60 57.80.1202 2k0 0 CC6 55 86 10.204 MC33078 Dual Op-Amp low noise 0 R 61 57.60.1322 3k3 0 CC7 55 81.0204 MC33078 Dual Op-Amp low noise 0 R 63 57.60.1322 3k3 0 CC9 50.09.0124 2142 Audio balanced line drivar 0 R 64 57.60.1322 3k3 0 IC11 50.09.0124 2142 Audio balanced line drivar 0 R 66 57.60.1322 3k3 0 IC12 50.09.0124 2142 Audio balanced line drivar 0 R 66 57.60.1322 3k3 0 IC14 50.09.0124 MC33078 Dual Op-Amp low noise 0 R 65 57.60.1332 3k3 1 IC15 50.81.0204 MC33078 Dual Op-Amp low noise 0 R 72 57.60.1332 3k3 1 IC16 50.81.0204 MC33078 <td>0</td> <td>IC 4</td> <td>50.61.0204</td> <td></td> <td>MC33078</td> <td>Dual Op-Amp low noise</td> <td>0</td> <td>R</td> <td>59</td> <td>57.60.1202</td> <td>2k0</td> <td>MF, 1%, 0204, E24</td> | 0 | IC 4 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R | 59 | 57.60.1202 | 2k0 | MF, 1%, 0204, E24 |
| 0 C 6 50 81 82244 MC33078 Dual 0-Amp tow noise 0 R 61 67 80.1332 343 0 C 7 50 81 0204 MC33078 Dual 0-Amp tow noise 0 R 62 57.60.1332 343 0 C 6 50 81 0204 MC33078 Dual 0-Amp tow noise 0 R 64 57.60.1332 343 0 C 10 50.09 0124 2142 Audio balanced line driver 0 R 66 57.60.1332 343 0 IC 11 50.09.0124 2142 Audio balanced line driver 0 R 66 57.60.1332 343 0 IC 13 50.69.0124 MC33077 Dual 0-Amp tow noise 0 R 67 67.60.1332 343 0 IC 16 50.61.0224 MC33078 Dual 0-Amp tow noise 0 R 70 67.60.1332 343 0 IC 16 50.61.0224 MC33078 Dual 0-Amp tow noise 0 R 71 57.60.1133 13k 0 IC 16 50.19.014 CS4329/439 | 0 | IC 5 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R | 60 | 57.60.1202 | 2k0 | MF, 1%, 0204, E24 |
| 0 C(7) 50/ E1.0204 MC33078 Dual 0p-Amp low noise 0 R 63 57.60.1332 33.3 0 C(8) 50.09.0124 2142 Audio balanced line drivar 0 R 63 57.60.1332 33.3 0 C(7) 50.09.0124 2142 Audio balanced line drivar 0 R 65 57.60.1322 33.3 0 C(11) 50.09.0124 2142 Audio balanced line drivar 0 R 66 57.60.1322 33.3 0 C(13) 50.61.0204 MC33078 Dual Op-Amp low noise 0 R 66 57.60.1332 33.3 0 IC 14 50.61.0204 MC33078 Dual Op-Amp low noise 0 R 70 57.60.1332 33.3 0 IC 16 50.61.0204 MC33078 Dual Op-Amp low noise 0 R 70 57.60.1332 33.4 0 IC 18 50.19.0114 CS4329/439 DA Converter 20/24 bit streso 0 R 74 57.60.133 13.4 0 IP 4 43.01.0108 | 0 | IC 6 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R | 61 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| D DL 8 DB 81,02,024 MCSUP Deal OpeRating from Read D R 64 57,60,1133 13k 0 IC 9 50,00,0124 2142 Audio balanced line driver O R 64 57,60,1132 3k3 0 IC 10 50,00,0124 2142 Audio balanced line driver O R 66 57,60,1132 3k3 0 IC 12 50,00,0124 2142 Audio balanced line driver O R 67 57,60,1132 3k3 0 IC 14 50,81,0224 MC33076 Dual Op-Amp low noise O R 69 57,60,1132 3k3 0 IC 14 50,81,0224 MC33078 Dual Op-Amp low noise O R 70 67,60,1132 3k3 0 IC 16 50,81,014 CS4323/439 D/A Converter 20/24bit taree O R 72 67,60,1133 13k 0 MP 1 1.942,163,11 1.0ce LABD-AMS/CHLB O R 76 57,60,1133 13k 0 MP 2 1.023,567,04 R | 0 | IC 7 | 50.61.0204 | | MC33078 | Dual Op-Amp low hoise | 0 | R | 62 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| b b Los D List 2 Audio balanced ine driver 0 R 66 57 60.1322 3k3 0 IC 10 50.08.0124 2142 Audio balanced ine driver 0 R 66 57 60.1322 3k3 0 IC 11 50.08.0124 2142 Audio balanced ine driver 0 R 66 57 60.1322 3k3 0 IC 13 50.68.1024 MC33078 Dual Op-Amp low noise 0 R 69 67 60.1322 3k3 0 IC 16 50.68.10244 MC33077 Dual Op-Amp low noise 0 R 71 67 60.1332 3k3 0 IC 16 50.61.0224 MC33078 Dual Op-Amp low noise 0 R 71 67 60.1332 3k3 0 IC 17 50.19.0114 CS4329/439 DA Converter 20/24bit stereo 0 R 72 57 60.1133 13k 0 IC 18 50.19.0114 CS4329/439 DA Converter 20/24bit stereo 0 R 74 57 60.1133 13k 0 IP 4 | U | 10.6 | 50.01.0204 | | WIG03078 | Audio balanced line driver | 0 | R | 63 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| International internationalinternatintereation international international international in | 0 | 10.9 | 50.09.0124 | | 2142 | Audio balanced line drivar | 0 | R | 64 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| Control Control <t< td=""><td>0</td><td>10 10</td><td>50.09.0124</td><td></td><td>2142</td><td>Audio balanced line driver</td><td>0</td><td>R</td><td>65</td><td>57.60.1332</td><td>3k3</td><td>MF, 1%, 0204, E24</td></t<> | 0 | 10 10 | 50.09.0124 | | 2142 | Audio balanced line driver | 0 | R | 65 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| No. 1 Sol 10, 200 MC 33075 Dual Op-Amp low noise D R 66 57, 60, 1332 33, 33 0 1C 14 56, 61, 1224 MC 33075 Dual Op-Amp low noise D R 66 57, 60, 1133 13k 0 1C 14 56, 61, 1224 MC 33075 Dual Op-Amp low noise D R 66 57, 60, 1132 3k3 0 1C 15 56, 81, 2224 MC 33075 Dual Op-Amp low noise D R 70 57, 60, 1132 3k3 0 1C 15 50, 19, 0114 CS4322/439 DA Converter 20/24bit streter D R 74 57, 60, 1133 13k 0 MP 1 1.942, 163, 11 1.56 ANAD CGI MSERT PCB D R 74 57, 60, 1133 13k 0 MP 2 1.942, 163, 11 1.56 PLANERT PCB D R 76 57, 60, 1133 13k 0 MP 3 1.942, 163, 10 1.56 PLANERT PCB D R 76 57, 60, 1133 13k 0 P 4 54, 13, 0072 155 | 0 | IC 12 | 50.09.0124 | | 2142 | Audio balanced line driver | U | R | 65 | 57.60.1133 | 13K | MF, 1%, 0204, E24 |
| Instruction Instruction <thinstruction< th=""> <thinstruction< th=""></thinstruction<></thinstruction<> | 0 | IC 13 | 50 61 0204 | | MC33078 | Dual Op-Amp low noise | U | R D | 60 | 57.60.1332 | 3K3 | MF, 1%, 0204, E24 |
| 0 C1 IS S0 IS 0224 MC33077 Dual Op-Amp low noise D R 70 S1 00.1132 33.3 0 IC 16 S0 112014 MC33074 Dual Op-Amp low noise 0 R 71 S1 00.1132 33.3 0 IC 17 S0 19.0114 CS42324/39 DA Converter 2012/bit iteren 0 R 71 S7 00.1133 13k 0 IC 18 S0 19.0114 CS42324/39 DA Converter 2012/bit iteren 0 R 74 S7 00.1133 13k 0 MP 1 1.942/163.11 1.5ce JAAL.OG INSERT PCB 0 R 74 S7 00.1133 13k 0 MP 2 4.30.10108 1.5ce INR-ETIKETTE 5X20 0 R 76 S7 00.1133 13k 0 P 4 54.13.0072 1550 D-Sub, PCB, Winkel 0 R 79 57 00.1133 13k 0 R 1 57 00.132 3 k3 WF, 1%, 0204, E24 0 R 83 57 00.1133 13k 0 R 1 57 00.132 3 k3 <t< td=""><td>0</td><td>IC 14</td><td>50.61.0204</td><td></td><td>MC33078</td><td>Dual Op-Amp low noise</td><td>0</td><td>R D</td><td>60</td><td>57.60.1332 57.60.1133</td><td>383</td><td>MF, 1%, 0204, E24</td></t<> | 0 | IC 14 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R D | 60 | 57.60.1332 57.60.1133 | 383 | MF, 1%, 0204, E24 |
| D D (16) S (16) C (23078) Dual (0p-Amp low noise) C R 77 S (16) S (13) S (13) 0 IC (17) S (19,0114) C (233078) D/A Converter 20/24bit stereo O R 77 S (16,0113) IS)x 0 IC (18) S (19,0114) C (23329/439) D/A Converter 20/24bit stereo O R 73 S (16,0113) IS)x 0 M P 1 1.942,163,110 I (26) AVALOG INSERT PC9 O R 74 S (16,0113) IS)x 0 M P 2 1.942,163,10 I (26) ESE-WARNSOHLID O R 76 S (16,0113) IS)x 0 P 2 1.023,567,04 Ribbon20 FLACHKABEL 20 POL 0.04M O R 78 S (10,0113) IS)x 0 P 4 S (13,0072 I 50 D-Sup, FC8, Winkel O R 80 S (10,0113) IS)x 18 R 2 S (10,0115) I 500 Winkel O R 83 S (10,0113) IS)x 19 S (10,0115) | 0 | IC 15 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | P | 70 | 57.60.1133 | 363 | MF, 1%, 0204, E24 MF 1%, 0204 E24 |
| 0 10 </td <td>0</td> <td>IC 16</td> <td>50.61.0204</td> <td></td> <td>MC33078</td> <td>Dual Op-Amp low noise</td> <td>0</td> <td>R</td> <td>71</td> <td>57 60 1332</td> <td>3k3</td> <td>MF. 1% 0204 E24</td> | 0 | IC 16 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R | 71 | 57 60 1332 | 3k3 | MF. 1% 0204 E24 |
| 0 D(18) 50.15.0114 CS4329/49 D/A Converter 202/4011 intereo 0 P.7.3 F7.60.1133 13k 0 MP1 1.942.163.11 1.0cc Laboration ANALOG INSERT PCB 0 R.7.4 S7.60.1133 13k 0 MP2 1.323.1010 1.0cc Laboration NRLETERXETTE SX20 0 R.7.6 S7.60.1133 13k 0 MP3 1.342.163.10 1.0cc Laboration NRLETKETTE SX20 0 R.7.6 S7.60.1133 13k 0 P.2 1.023.567.04 Ribboration PLACHKABEL 20 POL.0.04M 0 R.7.7 S7.60.1133 13k 0 P.4 S4.13.0072 15p D-Sup. PCB, Winkel 0 R.80 S7.60.1133 13k 0 R.2 S7.60.1151 150R MF.1%.0204.E24 0 R.83 S7.60.1133 13k 0 R.4 S7.60.1151 150R MF.1%.0204.E24 0 R.85 S7.60.1133 13k 0 R.5 </td <td>0</td> <td>IC 17</td> <td>50.19.0114</td> <td></td> <td>CS4329/439</td> <td>D/A Converter 20/24bit stereo</td> <td>0</td> <td>R</td> <td>72</td> <td>57.60.1133</td> <td>13k</td> <td>MF. 1%, 0204, E24</td> | 0 | IC 17 | 50.19.0114 | | CS4329/439 | D/A Converter 20/24bit stereo | 0 | R | 72 | 57.60.1133 | 13k | MF. 1%, 0204, E24 |
| MP1 1.942-163.11 1.5ce ANALOG INSERT PCB 0 R.74 57.60.1133 13k 0 MP2 4.30.1018 1.5ce Label ESE-WARNSCHLD 0 R.75 97.60.1133 13k 0 MP3 1.942.163.10 1.5ce Label ESE-WARNSCHLD 0 R.76 97.60.1133 13k 0 P2 1.023.567.04 Ribbon20 FLACHKABEL 20 POL 0.04M 0 R.77 57.60.1133 13k 0 P3 54.13.0072 155 D-Sub, PCB, Winkel 0 R.78 57.60.1133 13k 0 R1 57.60.132 3k3 MF, 1%, 0204, E24 0 R.81 57.60.1133 13k 0 R2 57.60.1151 150R MF, 1%, 0204, E24 0 R.84 57.60.1133 13k 0 R4 57.60.1151 150R MF, 1%, 0204, E24 0 R.85 57.60.1133 13k 0 R.4 57.60.1151 150R MF, 1%, 0204, E24 | 0 | IC 18 | 50.19.0114 | | CS4329/439 | D/A Converter 20/24bit stereo | 0 | R | 73 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| 0 MP1 1.942/163.11 1.5ce AVALOB INSERT POB 0 R 75 97.01133 13k 0 MP2 3.01.0105 Insee Label NRELTKETTE 5X20 0 R 76 57.60.1133 13k 0 MP3 1.942.163.10 1.5ce NRELTKETTE 5X20 0 R 76 57.60.1133 13k 0 P 2 1.022.567.04 Ribbon200 FLAOHKABEL 20 POL.0.04M 0 R 76 57.60.1133 13k 0 P 4 54.13.0072 15p D-Sub, PCB, Winkel 0 R 80 57.60.1133 13k 0 R 1.0 57.60.1151 150R WF 1%, 0204.E24 0 R 81 57.60.1133 13k 0 R 2 57.60.1151 150R WF 1%, 0204.E24 0 R 84 57.60.1133 13k 0 R 4 57.60.1151 150R WF 1%, 0204.E24 0 R 85 57.60.1133 13k 0 R 5 57.60.1131 150R WF 1%, 0204.E24 | | | | | | NULL OG WOEDT DOD | ō | R | 74 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| 0 0 0 7 4.3.0.1.00 1 50 Label Label <thlabel< th=""> <thlabel< th=""> <thlabel< th=""></thlabel<></thlabel<></thlabel<> | 0 | MP 1 | 1.942.163.11 | 1 500 | Label | ERE WARNSCHILD | 0 | R | 75 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| 0 No. 1 1.32 1.34 1 | 0 | MP 2 | 43.01.0108 | 1 pce | Laber | NR ETIKETTE 5X20 | 0 | R | 76 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| 0 P 2 1.022.667.04 Ribbor200 FLACHKABEL 20 PCL 0.04M 0 R 78 57.60.1133 13k 0 P 3 54.13.0072 15p D-Sub, PCB, Winkel 0 R 79 57.60.1133 13k 0 P 4 54.13.0072 15p D-Sub, PCB, Winkel 0 R 79 57.60.1133 13k 0 R 1 57.60.1131 150R WF.1%, 0204, E24 0 R 81 57.60.1133 13k 0 R 3 57.60.1151 150R WF.1%, 0204, E24 0 R 82 57.60.1133 13k 0 R 3 57.60.1151 150R WF.1%, 0204, E24 0 R 84 57.60.1133 13k 0 R 4 57.60.1151 150R WF.1%, 0204, E24 0 R 85 57.60.1133 13k 0 R 4 57.60.1151 150R WF.1%, 0204, E24 0 R 85 57.60.1133 13k 0 R 4 57.60.1151 150R WF.1%, 0204, E24 0 | U | WP 3 | 1.942.103.10 | 1 pce | | NR.ETIKETTE SKEG | 0 | R | 77 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| 0 P 3 54,13,0072 150 D-Sub, PCB, Winkel 0 R 79 57,60,1133 13k 0 P 4 54,13,0072 150 D-Sub, PCB, Winkel 0 R 80 57,60,1133 13k 0 R 1 57,60,1132 3k3 MF, 1%, 0204, E24 0 R 81 57,60,1131 13k 0 R 2 57,60,1151 150R MF, 1%, 0204, E24 0 R 84 57,60,1131 13k 0 R 4 57,60,1151 150R WF, 1%, 0204, E24 0 R 85 57,60,1133 13k 0 R 4 57,60,1151 150R WF, 1%, 0204, E24 0 R 85 57,60,1133 13k 0 R 6 57,60,1151 150R WF, 1%, 0204, E24 0 R 85 57,60,1133 13k 0 R 8 57,60,1151 150R WF, 1%, 0204, E24 0 R 80 57,60,1133 13k 0 R 8 57,60,1151 150R MF, 1%, 0204, E24 0 R 81 <td>0</td> <td>P 2</td> <td>1.023.567.04</td> <td></td> <td>Ribbon20p</td> <td>FLACHKABEL 20 POL. 0.04M</td> <td>0</td> <td>R</td> <td>78</td> <td>57.60.1133</td> <td>13k</td> <td>MF, 1%, 0204, E24</td> | 0 | P 2 | 1.023.567.04 | | Ribbon20p | FLACHKABEL 20 POL. 0.04M | 0 | R | 78 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| 0 P 4 54,13,0072 159 D-Sub, PCB, Winkel 0 R 80 57,60,1133 13k 0 R 1 57,60,1332 3k3 MF,1%,0204,E24 0 R 81 57,60,1133 13k 0 R 3 57,60,1131 150R MF,1%,0204,E24 0 R 83 57,60,1133 13k 0 R 3 57,60,1151 150R MF,1%,0204,E24 0 R 84 57,60,1133 13k 0 R 4 57,60,1151 150R MF,1%,0204,E24 0 R 85 57,60,1100 10R 0 R 5 57,60,1151 150R MF,1%,0204,E24 0 R 85 57,60,1133 13k 0 R 5 57,60,1151 150R MF,1%,0204,E24 0 R 85 57,60,1133 13k 0 R 9 57,60,1151 150R MF,1%,0204,E24 0 R 89 57,60,1100 10R 0 R 10 57,60,1151 150R MF,1%,0204,E24 0 R 42 53,60,011 | 0 | P 3 | 54.13.0072 | | 15p | D-Sub, PCB, Winkel | 0 | R | 79 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| 0 R.1 57.60.1332 34.2 MF, 1%, 0204, E24 0 R 81 57.60.1133 13k 0 R.2 57.60.1151 150R MF, 1%, 0204, E24 0 R 82 57.60.1133 13k 0 R.3 57.60.1151 150R MF, 1%, 0204, E24 0 R 84 57.60.1133 13k 0 R.4 57.60.1151 150R MF, 1%, 0204, E24 0 R 85 57.60.1133 13k 0 R.4 57.60.1151 150R MF, 1%, 0204, E24 0 R 85 57.60.1100 10R 0 R.6 57.60.1151 150R MF, 1%, 0204, E24 0 R 85 57.60.1100 10R 0 R.6 57.60.1151 150R MF, 1%, 0204, E24 0 R 85 57.60.1100 10R 0 R.8 57.60.1151 150R MF, 1%, 0204, E24 0 R 84 57.60.1100 10R 0 R.10 57.60.1151 150R MF, 1%, 0204, E24 0 R 84 </td <td>0</td> <td>P 4</td> <td>54.13.0072</td> <td></td> <td>15p</td> <td>D-Sub, PCB, Winkel</td> <td>0</td> <td>R</td> <td>80</td> <td>57.60.1133</td> <td>13k</td> <td>MF, 1%, 0204, E24</td> | 0 | P 4 | 54.13.0072 | | 15p | D-Sub, PCB, Winkel | 0 | R | 80 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| R 2 57.60.1151 150R MF, 1%, 0204, E24 0 R 83 57.60.1133 13k R 4 S7.60.1151 150R MF, 1%, 0204, E24 0 R 83 57.60.1133 13k R 4 S7.60.1151 150R MF, 1%, 0204, E24 0 R 84 57.60.1133 13k R 4 S7.60.1151 150R MF, 1%, 0204, E24 0 R 85 57.60.1100 10R R 4 S7.60.1151 150R MF, 1%, 0204, E24 0 R 85 57.60.1133 13k R 7 S7.60.1151 150R MF, 1%, 0204, E24 0 R 85 57.60.1133 13k R 8 S7.60.1151 150R MF, 1%, 0204, E24 0 R 89 57.60.1100 10R R 11 S7.60.1151 150R MF, 1%, 0204, E24 0 R A1 53.60.0117 5k0 R 11 S7.60.1151 150R MF, 1%, 0204, E24 0 R A1 53.60.0117 5k0 R 13 S7.60.1151 150R MF, 1%, 0204, | 0 | R 1 | 57 60 1332 | | 3k3 | MF. 1%. 0204. E24 | 0 | R | 81 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| R S7.80.1151 150R WF 1%, 0204, E24 0 R 84 57.80.1133 13k 0 R 4 S7.80.1151 150R WF 1%, 0204, E24 0 R 84 S7.80.1133 13k 0 R 4 S7.80.1151 150R WF 1%, 0204, E24 0 R 85 S7.80.1133 13k 0 R 6 S7.80.1151 150R WF 1%, 0204, E24 0 R 85 S7.80.1133 13k 0 R 6 S7.80.1151 150R WF 1%, 0204, E24 0 R 85 S7.80.1131 13k 0 R 8 S7.80.1151 150R WF 1%, 0204, E24 0 R 88 S7.80.1100 10R 0 R 8 S7.80.1151 150R WF 1%, 0204, E24 0 R 81 S7.80.1100 10R 0 R 10 S7.80.1151 150R MF 1%, 0204, E24 0 R A1 53.80.0117 5k0 0 R 13 S7.80.1151 150R MF 1%, 0204, E24 0 R A3 53.80.0117 <td>ñ</td> <td>R2</td> <td>57.60.1151</td> <td></td> <td>150R</td> <td>MF, 1%, 0204, E24</td> <td>0</td> <td>R</td> <td>82</td> <td>57.60.1133</td> <td>13k</td> <td>MF, 1%, 0204, E24</td> | ñ | R2 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R | 82 | 57.60.1133 | 13k | MF, 1%, 0204, E24 |
| 0 R.4 57.60.1151 150R WF.1%, 0204.E24 0 R.85 57.60.1133 13A 0 R.6 57.60.132 3K.3 WF.1%, 0204.E24 0 R.85 57.60.1100 10R 0 R.6 57.60.1151 150R WF.1%, 0204.E24 0 R.85 57.60.1133 13k 0 R.6 57.60.1151 150R WF.1%, 0204.E24 0 R.85 57.60.1133 13k 0 R.6 57.60.1151 150R MF.1%, 0204.E24 0 R.85 57.60.1100 10R 0 R.9 57.60.1151 150R MF.1%, 0204.E24 0 R.89 57.60.1100 10R 0 R.9 57.60.1151 150R MF.1%, 0204.E24 0 R.4 3.80.0117 5k0 0 R.11 57.60.1151 150R MF.1%, 0204.E24 0 R.4 5.80.0117 5k0 13 57.60.1151 150R MF.1%, 0204.E24 0 R.4 5.80.0117 5 | 0 | R3 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R | 83 | 57.60.1133 | 136 | NF, 1%, 0204, E24 |
| 0 R 5 57.60.132 3k3 WF 1% 0204.E24 0 R 86 57.60.110 10R 0 R 6 57.60.1151 150R MF 1% 0204.E24 0 R 86 57.60.110 10R 0 R 8 57.60.1151 150R MF 1% 0204.E24 0 R 87 57.60.1133 13k 0 R 8 57.60.1151 150R MF 1% 0204.E24 0 R 88 57.60.1100 10R 0 R 8 57.60.1151 150R MF 1% 0204.E24 0 R 88 57.60.1100 10R 0 R 10 57.60.1151 150R MF 1% 0204.E24 0 R 81 57.60.1100 10R 0 R 11 57.60.1151 150R MF 1% 0204.E24 0 R A1 53.60.0117 5k0 0 R 12 57.60.1151 150R MF 1% 0204.E24 0 R A3 53.60.0117 5k0 0 R 14 57.60.1151 150R MF 1% 0204.E24 0 R A4 53.60.0117 </td <td>0</td> <td>R 4</td> <td>57.60.1151</td> <td></td> <td>150R</td> <td>VIF, 1%, 0204, E24</td> <td>0</td> <td></td> <td>04</td> <td>57.60.1133</td> <td>100</td> <td>MF, 1%, 0204, E24</td> | 0 | R 4 | 57.60.1151 | | 150R | VIF, 1%, 0204, E24 | 0 | | 04 | 57.60.1133 | 100 | MF, 1%, 0204, E24 |
| 0 R 6 57.60.1151 150R WF.1%, 0204, E24 0 R 85 57.60.1133 15K 0 R 7 57.60.1151 150R WF.1%, 0204, E24 0 R 85 57.60.1133 15K 0 R 8 57.60.1151 150R WF.1%, 0204, E24 0 R 89 57.60.1133 15K 0 R 8 57.60.1151 150R MF.1%, 0204, E24 0 R 89 57.60.1100 10R 0 R 10 57.60.1151 150R MF.1%, 0204, E24 0 R A1 53.60.0117 5K0 0 R 11 57.60.1151 150R MF.1%, 0204, E24 0 R A2 53.60.0117 5K0 0 R 13 57.60.1151 150R MF.1%, 0204, E24 0 R A4 53.60.0117 5K0 1 R 13 57.60.1151 150R MF.1%, 0204, E24 0 R A4 53.60.0117 5K0 1 R 14 57.60.1151 150R MF.1%, 0204, E24 0 R A4 | 0 | R 5 | 57.60.1332 | | 3k3 | VIF, 1%, 0204, E24 | 0 | | 86 | 57.60.1100 | 10R | MF 1% 0204 E24 |
| 0 R.7 57 60.1151 150R MF.1%, 0204.E24 0 R.88 57.60.1151 150R MF.1%, 0204.E24 0 R.88 57.60.1133 150K 0 R.8 57.60.1151 150R MF.1%, 0204.E24 0 R.88 57.60.1133 150K 0 R.10 57.60.1151 150R MF.1%, 0204.E24 0 R.89 57.60.1100 10R 0 R.10 57.60.1151 150R MF.1%, 0204.E24 0 R.4.1 53.60.0117 5k0 0 R.11 57.60.1151 150R MF.1%, 0204.E24 0 R.4.3 53.60.0117 5k0 0 R.12 57.60.1151 150R MF.1%, 0204.E24 0 R.4.3 53.60.0117 5k0 0 R.14 57.60.1151 150R MF.1%, 0204.E24 0 R.4.5 53.60.0117 5k0 1 R.14 57.60.1151 150R MF.1%, 0204.E24 0 R.4.6 53.60.0117 5k0 1 R.15 | 0 | R 6 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R | 87 | 57 60 1133 | 13k | MF 1% 0204 E24 |
| 0 R 8 57.80.1151 150R MF.1%, 0204, E24 0 R 89 57.80.1161 150R MF.1%, 0204, E24 0 R 89 57.80.1160 10R 0 R 10 57.80.1151 150R MF.1%, 0204, E24 0 R 89 57.80.1100 10R 0 R 10 57.80.1151 150R MF.1%, 0204, E24 0 R A1 53.80.0117 5K0 0 R 12 57.80.1151 150R MF.1%, 0204, E24 0 R A3 53.80.0117 5K0 0 R 13 57.80.1151 150R MF.1%, 0204, E24 0 R A4 53.80.0117 5K0 0 R 14 57.80.1151 150R MF.1%, 0204, E24 0 R A4 53.80.0117 5K0 0 R 15 57.80.1151 150R MF.1%, 0204, E24 0 R A4 53.80.0117 5K0 10 R 15 57.80.1151 150R MF.1%, 0204, E24 0 R A5 58.80.0117 5K0 12 S 7.80 | 0 | R 7 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R | 88 | 57.60.1133 | 13k | MF. 1%, 0204, E24 |
| 0 R 9 57.60.1151 150R MF, 1%, 0204, E24 0 R 80 57.60.1151 150R MF, 1%, 0204, E24 0 R 40 57.60.1151 150R MF, 1%, 0204, E24 0 R A1 53.80.0117 540 0 R 11 57.60.1151 150R MF, 1%, 0204, E24 0 R A2 53.80.0117 540 0 R 13 57.60.1151 150R MF, 1%, 0204, E24 0 R A3 53.80.0117 540 0 R 13 57.60.1151 150R MF, 1%, 0204, E24 0 R A4 53.80.0117 540 0 R 14 57.60.1151 150R MF, 1%, 0204, E24 0 R A4 53.80.0117 540 0 R 15 57.60.1151 150R MF, 1%, 0204, E24 0 R A5 53.80.0117 540 0 R 16 57.60.1000 0R0 MF, 1%, 0204, E24 0 R A5 58.60.0117 540 0 R 16 57.60.150 0 MF, 1%, 0204, E24 0 | 0 | R 8 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R | 89 | 57,60,1100 | 10R | MF, 1%, 0204, E24 |
| 0 R10 57.60.1151 150R MF, 1%, 0204, E24 0 RA1 53.60.0117 5k0 0 R11 57.60.1151 150R MF, 1%, 0204, E24 0 RA1 53.60.0117 5k0 0 R12 57.60.1151 150R MF, 1%, 0204, E24 0 RA3 53.60.0117 5k0 0 R14 57.60.1151 150R MF, 1%, 0204, E24 0 RA3 53.60.0117 5k0 0 R14 57.60.1151 150R MF, 1%, 0204, E24 0 RA5 53.60.0117 5k0 0 R15 57.60.1151 150R MF, 1%, 0204, E24 0 RA5 53.60.0117 5k0 0 R16 57.60.1000 0R0 MF, 1%, 0204, E24 0 RA6 58.60.0117 5k0 0 R17 57.60.1000 0R0 MF, 1%, 0204, E24 0 RA7 68.60.0117 5k0 0 R18 57.60.1032 3k6 MF, 1%, 0204, E24 0 RA7 < | 0 | R 9 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R | 90 | 57.60.1100 | 10R | MF, 1%, 0204, E24 |
| 0 R11 57.60.1151 150R Mr., %%, 0204, E24 0 RA1 53.60.0117 540 0 R13 57.60.1151 150R MF., %%, 0204, E24 0 RA2 53.60.0117 540 0 R14 57.60.1151 150R MF., %%, 0204, E24 0 RA3 53.60.0117 540 0 R14 57.60.1151 150R MF., 1%%, 0204, E24 0 RA4 53.60.0117 540 0 R15 57.60.1151 150R MF., 1%%, 0204, E24 0 RA5 53.60.0117 540 0 R16 57.60.1051 150R MF., 1%%, 0204, E24 0 RA6 53.60.0117 540 0 R16 57.60.1000 0R0 MF., 1%%, 0204, E24 0 RA7 53.60.0117 540 0 R17 57.60.1000 0R0 MF., 1%%, 0204, E24 0 RA7 53.60.0117 540 0 R18 57.60.1032 346 MF., 1%%, 0204, E24 0 RA7 | 0 | R 10 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | | | | | | |
| 0 R12 57.80.1151 150/R MF.1%, 0204, E24 0 RA3 53.80.0117 5K0 0 R13 57.80.1151 150/R MF.1%, 0204, E24 0 RA3 53.80.0117 5K0 0 R14 57.80.1151 150/R MF.1%, 0204, E24 0 RA4 53.80.0117 5K0 0 R14 57.80.1151 150/R MF.1%, 0204, E24 0 RA4 53.80.0117 5K0 0 R16 57.80.1151 150/R MF.1%, 0204, E24 0 RA6 55.80.0117 5K0 0 R16 57.80.1000 0/R0 MF.1%, 0204 0 RA7 58.80.0117 5K0 0 R17 57.80.10362 3K6 MF.1%, 0204 0 RA7 58.80.0117 5K0 0 R19 57.80.1382 3K6 MF.1%, 0204, E24 0 RA7 58.80.0117 5K0 0 R19 57.80.1382 3K6 MF.1%, 0204, E24 | 0 | R 11 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R | A 1 | 53.60.0117 | oKU | SMD 20%, 0.25W, Cermet |
| 0 R14 0 R04 0 R04 53.00.0117 540 0 R14 57.00.1151 150R MF, 1%, 0204, E24 0 RA4 53.00.0117 540 0 R15 57.00.1151 150R MF, 1%, 0204, E24 0 RA4 53.00.0117 540 0 R16 57.00.1151 150R MF, 1%, 0204, E24 0 RA6 53.00.0117 540 0 R17 57.00.100 0R0 MF, 1%, 0204, E24 0 RA6 53.00.0117 540 0 R17 57.00.100 0R0 MF, 1%, 0204, E24 0 RA7 58.00.0117 540 0 R18 57.00.1382 346 MF, 1%, 0204, E24 0 RA7 58.00.0117 540 0 R20 57.00.1382 346 MF, 1%, 0204, E24 0 RA7 58.00.0117 540 0 R21 57.00.1382 346 MF, 1%, 0204, E24 0 RA7 58.00.0117 540 | 0 | R 12 | 57.60.1151 | | 150R | ME 1% 0204 E24 | U | R F | M 2 M 3 | 58.60.0117 | 540 | SMD 20%, 0.25W, Cernel |
| 0 R 15 57.60.1151 1000r MF, 1%, 0204, E24 0 PA 5 58.60.0117 5k0 0 R 16 57.60.1151 150R MF, 1%, 0204, E24 0 RA 5 58.60.0117 5k0 0 R 16 57.60.1151 150R MF, 1%, 0204, E24 0 RA 6 58.60.0117 5k0 0 R 16 57.60.1150 0R0 MF, 0204 0 RA 7 58.60.0117 5k0 0 R 18 57.60.1000 0R0 MF, 0204 0 RA 8 58.60.0117 5k0 0 R 19 57.60.1362 3k6 MF, 1%, 0204, E24 0 RA 8 58.60.0117 5k0 0 R 21 57.60.1362 3k6 MF, 1%, 0204, E24 | 0 | K 13 | 57.60.1151 | | 150R | ME 1% 0204 E24 | 0 | r P | 44 | 58.60.0117 | 520 | SMD 20%, 0.25W, Cermet |
| 0 R16 57.80.1161 100K Mir. 1%, 0204 0 RA6 58.80.0117 5K0 0 R17 57.80.1161 150K Mir. 1%, 0204 0 RA7 68.80.0117 5K0 0 R17 57.80.1000 0R0 MF. 1%, 0204 0 RA7 68.80.0117 5K0 0 R18 57.80.1082 3K6 MF. 1%, 0204 0 RA7 58.80.0117 5K0 0 R19 57.80.1382 3K6 MF. 1%, 0204.E24 End of Lis 0 R21 57.80.1382 3K6 MF. 1%, 0204.E24 End of Lis 0 R21 57.80.1382 3K6 MF. 1%, 0204.E24 End of Lis | 0 | R 14 | 57.00.1151 | | 150R | ME 1% 0204 E24 | 0 | Þ | A 5 | 58.60.0117 | 5k0 | SMD 20% 0.25W Cermet |
| R 11 G 7,80,100 OR MF, 0204 O RA 7 58,80,0117 5K0 0 R 18 57,80,1000 OR0 MF, 0204 0 RA 8 58,80,0117 5K0 0 R 19 57,80,1362 3K6 MF, 1%, 0204, E24 0 RA 8 58,80,0117 5K0 0 R 21 57,80,1362 3K6 MF, 1%, 0204, E24 | 0 | R 10 | 57.60.1151 | | 150R | MF 1% 0204 E24 | 0 | R | A 6 | 58 60 0117 | 5k0 | SMD 20% 0.25W Cermet |
| R 18 57.80.100 OR MF, 0.204 O RA 8 58.60.0117 5K0 0 R 19 57.80.1032 3k6 MF, 1%, 0.204, E24 End of Lis 50.0117 5K0 0 R 21 57.60.1362 3k6 MF, 1%, 0.204, E24 End of Lis End of Lis 0 R 21 57.60.1362 3k6 MF, 1%, 0.204, E24 Comments; End of Lis | ň | R 17 | 57.60.1000 | | 080 | ME 0204 | ő | R | A 7 | 58 60 0117 | 5k0 | SMD 20% 0.25W Cermet |
| 0 R 19 6 77.0.1382 34.6 MF 1%, 0204, E24 0 R 20 6 78.0.1382 34.6 MF 1%, 0204, E24 0 R 21 6 78.0.1382 34.6 MF 1%, 0204, E24 0 R 21 6 78.0.1382 34.6 MF 1%, 0204, E24 <u>Comments:</u> <u>Comments:</u> | 0 | R 18 | 57.60.1000 | | 0R0 | MF. 0204 | 0 | R | A 8 | 58.60.0117 | 5k0 | SMD 20%, 0.25W, Cermet |
| 0 R 20 57.60.1362 3k6 MF, 1%, 0204, E24 End of Lis 0 R 21 57.60.1362 3k6 MF, 1%, 0204, E24 Comments: | 0 | R 19 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | | | | | | |
| 0 R 21 57.60.1362 3k6 MF, 1%, 0204, E24 Comments: | 0 | R 20 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | | | | | End of | ist |
| 0 D 00 F7 00 4000 21/0 ME 19/ 0204 E24 | 0 | R 21 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | | _ | | | | |
| U N 22 07.00.1302 3K0 NIF, 1%, 0204, E24 | 0 | R 22 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | 9 | onin | ients: | | | |
| 0 R 23 57.60.1362 3k6 MF, 1%, 0204, E24 | 0 | R 23 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | | | | | | |
| 0 R 24 57.60.1362 3k6 MF, 1%, 0204, E24 | 0 | R 24 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | | | | | | |
| 0 R 25 57.60.1362 3k6 MF, 1%, 0204, E24 | 0 | R 25 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | | | | | | |
| 0 R 26 57.60.1362 3k6 MF, 1%, 0204, E24 | 0 | R 26 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | | | | | | |
| U R27 57.60.1479 4R7 MF,1%,0204,E24 | 0 | R 27 | 57.60.1479 | | 4R7 | MF, 1%, 0204, E24 | | | | | | |
| U R 28 57,50,1479 4R7 MF, 1%, 0204, E24 | 0 | R 28 | 57.60.1479 | | 487 | MF, 1%, UZU4, EZ4 | | | | | | |
| U K 29 57.50.1102 TKU MF, 1%, 0204, E24 | 0 | K 28 | 57.60.1102 | | 1KU 1k0 | ME 1% 0204 E24 | | | | | | |
| 0 P.34 57.50.4102 TRU NIT, 178,0204,1224 | 0 | R 30 R 31 | 57.60.1102 | | 110 | ME 1% 0204 E24 | | | | | | |
| 0 R 3 57 50 1102 1k0 MF 140 0204 E24 | n | R 32 | 57.60.1102 | | 1k0 | MF 1% 0204, E24 | | | | | | |
| 0 R 33 57 R0 1272 2 K7 MF 1% D204 E24 | 0 | R 33 | 57 60 1272 | | 2k7 | MF. 1%, 0204, E24 | | | | | | |
| 0 R 34 57.60.1272 2k7 MF.1% 0204 E24 | õ | R 34 | 57.60.1272 | | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 R 35 57.60.1272 2k7 MF 1%, 0204, E24 | 0 | R 35 | 57.60.1272 | | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 R 36 57.80.1272 2k7 MF, 1%, 0204, E24 | 0 | R 36 | 57.60.1272 | | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 R 37 57.60.1562 5k6 MF, 1%, 0204, E24 | 0 | R 37 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 | | | | | | |

Block Diagram Digital Output





Digital Output Module 1.942.624.20



Digital Output Module 1.942.624.20

| ldx | Pos. | Part No. | Qty. | Type/Val. | Description | ldx | Pos. | Part No. Qty. | Type/Vai. | Description |
|--------|-------|--------------|-------|-------------|-------------------------------|------------|------------|---------------|--------------|---------------------------|
| 0 | C 1 | 59 60 2373 | | 1n0 | CER 50V 5% C0G 0805 | 0 | R 25 | 57.60.1470 | 47R | MF. 1%. 0204. E24 |
| 0 | C 2 | 59 60 3337 | | 100n | CER 50V 10% X7R 0805 | 0 | R 26 | 57.60.1470 | 47R | ME 1% 0204 E24 |
| 0 | C 3 | 50.22.3003 | | 22011 | EL 10V 20% RM5 | 0 | R 27 | 57 60 1470 | 47R | ME 1% 0204 E24 |
| n | C 4 | 50 60 3337 | | 1000 | CER 50V 10% X7R 0805 | 0 | R 28 | 57 60 1470 | 47R | ME 1% 0204 E24 |
| 0 | 65 | 59.00.3337 | | 1001 | CER 20V 10% X75, 0002 | 0 | R 20 | 57,60,1470 | 47R | MF, 1%, 0204, E24 |
| 0 | C 6 | 59.60.3337 | | 100n | CER 50V. 10%, X7R, 0805 | 0 | R 30 | 57.60.1470 | 47R | MF, 1%, 0204, E24 |
| 0 | 0.7 | 59 60 3337 | | 100n | CER 50V 10% X7B 0805 | 0 | R 31 | 57.60.1470 | 47R | ME 1% 0204 E24 |
| ñ | 0.8 | 59 60 3337 | | 100n | CER 50V 10% X7R 0805 | 0 | R 32 | 57 60 1470 | 47R | ME 1% 0204 E24 |
| 0 | C 9 | 59 60 3337 | | 100n | CER 50V 10% X7R 0805 | 0 | R 33 | 57 60 1470 | 47R | ME 1% 0204 E24 |
| 0 | 0 10 | 59.60.3337 | | 1000 | QER 50V, 10%, X7B, 0805 | 0 | R 34 | 67.60.1470 | 4713 | ME, 1%, 0204, E24 |
| 0 | C 11 | 59.60.3337 | | 100n | CER 50V. 10%, X7R, 0805 | 0 | R 35 | 57.60.1470 | 47R | MF, 1%, 0204, E24 |
| n n | C 12 | 59 60 3337 | | 100n | CER 50V 10% X7R 0805 | 0 | R 36 | 57 60 1470 | 47R | ME 1% 0204 E24 |
| Ő | C 13 | 59 60 2249 | | 100n | CER 50V 5% COG 0603 | 0 | R 37 | 57 60 1103 | 10k | ME 1% 0204 E24 |
| 0 | C 14 | 59 60 2249 | | 100p | CER 50V 5% COG 0603 | ñ | R 38 | 57 60 1471 | 4708 | ME 1% 0204 E24 |
| 0 | 0.14 | 50 60 2240 | | 100p | CER 50V, 5%, 600, 6003 | 0 | R 30 | 57 60 1105 | 4/01 | ME 1% 0204 E24 |
| 0 | C 16 | 59 60 2249 | | 100p | CER 50V 5% C0G 0603 | | | | | |
| ñ | C 17 | 59.60.2237 | | 33n | CER 50V 5% COG 0603 | 0 | RZ 1 | 57.88.4103 | 10k | 8*R Resistor-Netw 2% SIP9 |
| ő | C 18 | 59 60 2233 | | 22n | CER 50V 5% COG 0603 | 0 | RZ 2 | 57.88.4103 | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 | 0.10 | 33.00.2233 | | zzp | GER 307, 3%, 666, 6666 | 0 | RZ 3 | 57.88.4103 | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 | IC 1 | 50.15.0114 | | 9637 | Dual diff Line Receiver | ٥ | S 1 | 55 01 0168 | 8*0 | 87 8*A DII |
| 0 | IC 2 | 50.14.1009 | | 7C128A | SRAM 2K*8 35ns | n n | 52 | 55.01.0168 | 8*a | SZ 8*A DII |
| 0 | IC 3 | 50.62.1951 | | 74HC4051 | 8ch analog mux/demux | 2 | ~ . | 00.01.0100 | | and yorky bits |
| 0 | IC 4 | 50.62.1951 | | 74HC4051 | 8ch analog mux/demux | 0 | T 1 | 1.022.647.00 | 1:1.4 | OUTPUT TRAFO AES/EBU |
| · 0 | IC 5 | 50.62.1951 | | 74HC4051 | 8ch analog mux/demux | 0 | Т2 | 1.022.647.00 | 1:1.4 | OUTPUT TRAFO AES/EBU |
| 0 | IC 6 | 50.62.1951 | | 74HC4051 | 8ch analog mux/demux | 0 | ТЗ | 1.022.647.00 | 1:1.4 | OUTPUT TRAFO AES/EBU |
| 0 | IC 7 | 1.942.912.22 | | | SW.124 DIGITAL OUT. MOD., uP | 0 | Τ4 | 1.022.647.00 | 1:1.4 | OUTPUT TRAFO AES/EBU |
| | | | | 50160313, 8 | 9C2051 | ~ | VIC 7 | E2 02 0107 | 00- | |
| 0 | IC 8 | 50.62.1014 | | 74HC 14 | Hex Schmitt trigger inverter | 0 | XIC / | 53.03.0165 | 20p | DIL 0.3", löt, gerade |
| 0 | IC 9 | 50.62.1165 | | 74HC165 | 8bit shift register | 0 | XIC 10 | 53.03.2244 | 44p | PLCC-Socket |
| 0 | IC 10 | 1.942.927.20 | | | SW.124 DIGITAL OUT. MOD., PLD | 0 | XIC 11 | 53.03.0165 | 20p | DIL 0.3", löt, gerade |
| | | | | 50634202, E | PLD 7064 | 0 | XIO 12 | 50.00.0100 | 10p | DIL 0.0", lüt, gerade |
| 0 | IC 11 | 1.942.912.22 | | | SW.124 DIGITAL OUT. MOD., uP | 0 | XIC 13 | 53.03.0168 | 16p | DIL 0.3", löt, gerade |
| | | | | 50160313, 8 | 9C2051 | 0 | Y 1 | 89.01.1016 | 22.1184MHz | XTAL HC 49/U |
| 0 | IC 12 | 50.15.0108 | | 26LS31 | Quad diff line driver | - | | | | |
| 0 | IC 13 | 50.15.0108 | | 26LS31 | Quad diff line driver | | | | F | |
| 0 | IC 14 | 50.62.1165 | | 74HC165 | 8bit shift register | | | | - Fnd of Lie | |
| 0 | IC 15 | 50.62.1904 | | 74HCU04 | Hex inverter unbuffered | <u>Cor</u> | nments: | | | |
| | | | | | | | | | | |
| 0 | MP 1 | 1.942.124.11 | 1 pce | | DIGITAL OUTPUT MODULE PCB | | | | | |
| 0 | MP 2 | 43.01.0108 | 1 pce | Label | ESE-WARNSCHILD | | | | | |
| 0 | MP 3 | 1.942.024.10 | 1 poe | | NR.ETIKETTE | | | | | |
| 0 | MP 4 | 89.01.1499 | 1 pce | | QUARZ - ISOLIERPLATTE | | | | | |
| 0 | MP 5 | 1.942.500.21 | 2 pcs | | WINKEL | | | | | |
| 0 | MP 6 | 21.53.0353 | 2 pcs | M3*5 | Z-Schraube Inbus Zn gb chr | | | | | |
| 0 | MP 7 | 24.16.2030 | 2 pcs | | FAECHERSCHEIBE A D 3.2 | | | | | |
| 0 | D 1 | 54 21 2202 | | 3n | XI R PCB Winkel | | | | | |
| 0 | P 2 | 54 21 2202 | | 3n | XLR PCB Winkel | | | | | |
| 0 | D 3 | 54 21 2202 | | 3n | XLR PCB Winkel | | | | | |
| 0 | | 54 21 2202 | | 3p | XLR PCB Winkel | | | | | |
| ñ | P 5 | 54 14 2056 | | 64n | Stecker gerade Au | | | | | |
| ñ | PA | 54 01 0020 | | 1n | Pin 1reibig gerade | | | | | |
| n n | P7 | 54 01 0020 | | 1n | Pin 1reihig gerade | | | | | |
| ñ | P8 | 54.01.0020 | | 1p | Pin 1reihig, gerade | | | | | |
| õ | P 9 | 54.01.0020 | | 1p | Pin, 1reihig, gerade | | | | | |
| | - | | | i. | | | | | | |
| 0 | R 1 | 57.92.7012 | | 0.3A | PTC 60V | | | | | |
| 0 | R 2 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 3 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | | | | | |
| 0 | R 4 | 57.60.1822 | | 8k2 | MF, 1%, 0204, E24 | | | | | |
| 0 | R 5 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 6 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 7 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 8 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 9 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 10 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 11 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 12 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 13 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | | | | | |
| 0 | R 14 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 15 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 16 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 17 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 18 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 19 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 20 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 21 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 22 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 23 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |
| 0 | R 24 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 | | | | | |





Analog Output Module 1.942.621.00



Analog Output Module 1.942.621.00



Analog Output Module 1.942.621.00



Analog Output 1.942.621.00 (1)

Page: 1 of 2

| | | <u> </u> | - | | | <u> </u> | | | | | |
|------|--------|------------|------|-----------|-----------------------------|----------|------|--------------|--------------------|------------|----------------------------------|
| ldx. | . Pos. | Part No. | Qty. | Type/Val. | Description | | ldx. | Pos. | Part No. Qty. | Type/Val. | Description |
| _ | | | | 100 | EL | | 0 | IC 3 | 50.61.0204 | MC33078 | Dual Op-Amp low noise |
| 0 | C 1 | 59.68.0029 | | 1000 | EL 6V, 6.3"5.7 | | 0 | IC 4 | 50 61 8005 | AK4393 | D/A Converter 24bit DS SOP28 |
| 0 | C 2 | 59.68.0025 | | 22u | EL 6V, 4.0*5.7 | | ő | 10.5 | 50 15 0114 | 9637 | Dual diff Line Receiver |
| 0 | C 3 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 105 | 0 | 10.5 | 50.15.0114 | 1 101700 | Series regulator 1 5A +27\/ |
| 0 | C 4 | 59.63.1109 | | 470p | PPS 50V, 2%, 0805 | | 0 | | 50.10.0104 | LIVISTION | B/A Operator 245h DO COD28 |
| 0 | C 5 | 59.63.1109 | | 470p | PPS 50V, 2%, 0805 | | 0 | | 50.61.8005 | AK4393 | D/A Converter 24bit DS SOP26 |
| n | C B | 59 63 1105 | | 220p | PPS 50V, 2%, 0805 | | 0 | | 50.61.0204 | MC33078 | Dual Op Amp roll to roll SO 8 |
| 0 | C 7 | 59.68.0065 | | 10u | EL 16V, 4.0*5.7 | | 0 | 10.9 | 50.01.0200 | 33W22753 | Dual Op-Arrip, rail-to-rail SO 8 |
| 0 | C 8 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 06 | 303 | 0 | 10 10 | 50.01.0206 | 33W22753 | Dual Op-Arrip, rail-to-rail SO 8 |
| 0 | C 9 | 59.68.0025 | | 22u | EL 6V, 4.0*5.7 | | 0 | | 50.61.0206 | 55WI22755 | Dual Op-Amp, rail-to-rail SO 8 |
| 0 | C 10 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | | 0 | 10 12 | 50.61.0206 | SSM2275S | Dual Op-Amp, rail-to-rail SO 8 |
| 0 | C 11 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | | 0 | IC 13 | 50.61.0206 | SSM2275S | Dual Op-Amp, rail-to-rail SO 8 |
| 0 | C 12 | 59.68.0025 | | 22u | EL 6V, 4.0*5.7 | | 0 | 10 14 | 50.01.0200 | 00M22750 | Dual Op-Amp, rail-to-rail 60 8 |
| 0 | C 13 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 105 | 0 | 10 15 | 50.61.0206 | SSIVI22755 | Dual Op Amp, rail-to-rail SO 8 |
| 0 | C 14 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 06 | 603 | 0 | 10.10 | 50.01.0206 | 551022755 | Dual Op-Amp, rail-to-rail SO 8 |
| 0 | C 15 | 59.63.1109 | | 470p | PPS 50V, 2%, 0805 | | 0 | IC 17 | 50.61.0204 | MC33078 | Dual Op-Amp low hoise |
| 0 | C 16 | 59.63.1109 | | 470p | PPS 50V, 2%, 0805 | | 0 | К1 | 56.04.0198 | 2*u | 5V 125V 2A Ag/Au |
| 0 | 0 17 | 59.00.1105 | | 220p | PPO 50V, 2%, 0005 | | 0 | K 2 | 56.04.0198 | 2*u | 5V 125V 2A Ag/Au |
| 0 | C 18 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | L 1 | 62.60.0902 | | SMD Wideband choke |
| 0 | C 19 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | L 2 | 62.60.0902 | | SMD Wideband choke |
| 0 | C 20 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | L 3 | 62.60.0902 | | SMD Wideband choke |
| 0 | C 21 | 59.60.3337 | | 100n | CER 50V. 10%, X7R. 08 | 05 | 0 | L 4 | 62.60.0902 | | SMD Wideband choke |
| 0 | C 22 | 59.63.1113 | | 1n0 | PPS 50V. 2%. 0805 | | 0 | L 5 | 62.60.0902 | | SMD Wideband choke |
| ŏ | C 23 | 59.63.1109 | | 470p | PPS 50V, 2%, 0805 | | U | L 6 | 62.60.0902 | | SMD Wideband choke |
| 0 | C 24 | 59.63.1109 | | 470p | PPS 50V 2% 0805 | | 0 | L 7 | 62.60.0902 | | SMD Wideband choke |
| 0 | C 25 | 59 68 0025 | | 220 | FI 6V 4 0*5 7 | | 0 | L 8 | 62.60.0902 | | SMD Wideband choke |
| 0 | C 26 | 59 68 0025 | | 220 | EL 6V 4.0*5.7 | | 0 | MP 1 | 1.942.121.13 1 pce | | DUAL ANALOG OUTPUT PCB |
| 0 | 0 20 | 50.62.1105 | | 220n | DDS 50V 2% 0805 | | 0 | MP 2 | 43.01.0108 1 pce | Label | ESE-WARNSCHILD |
| | 0.27 | 59.03.1103 | | 2200 | FF3 J0V, 2/8, 000J | | n | MP 3 | 1 942 621 10 1 pce | | NR ETIKETTE |
| 0 | C 20 | 50.68.0067 | | 220 | EL 16V 5.0*5.7 | | 0 | MP 4 | 54.01.0021 4 pcs | Jumper | 0.63*0.63mm, Au |
| 0 | 0.29 | 59.00.0007 | | 220 | EL 16V, 5.0 5.7 | | 0 | MP 5 | 1.942.500.21 2 pcs | | WINKEL |
| 0 | 0.30 | 59.68.0025 | | 22u | EL 6V, 4.0"5.7 | | 0 | MP 7 | 24.16.2030 2 pcs | 3.2/6.0 | Fächerscheibe Form A |
| 0 | C 31 | 59.68.0071 | | 1000 | EL 16V, 8.0*6.3 | | 0 | MP 8 | 21.53.0353 2 pcs | M3*5 | Z-Schraube Inbus Zn ob chr |
| 0 | C 32 | 59.68.0071 | | 100u | EL 16V, 8.0*6.3 | | 1 | MP 9 | 43 10 0110 | A | Revisions-Etikette 5mm h'blau |
| 0 | 0.33 | 50 63 1105 | | 220p | PPS 501/, 2%, 0805 | | 0 | P 1 | 54.14.2056 | 64p | Stecker gerade Au |
| 0 | C 34 | 59.68.0025 | | 22u | EL 6V, 4.0*5.7 | | 0 | P2 | 54 01 0020 | 1n | Pin 1reihig gerade |
| 0 | C 35 | 59.63.1113 | | 1n0 | PPS 50V, 2%, 0805 | | 0 | P3 | 54 01 0020 | 10 | Pin 1reibig gerade |
| 0 | C 36 | 59,63,1109 | | 470p | PPS 50V, 2%, 0805 | | 0 | P4 | 54 01 0020 | 1p 1n | Pin 1reibig gerade |
| 0 | C 37 | 59.63.1109 | | 470p | PPS 50V, 2%, 0805 | | 0 | P 4 | 54.01.0020 | 1p 1p | Pin 1reihig gerade |
| 0 | C 38 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | | F 0 | 54.01.0020 | 10 | Pin, freihig, gerade |
| 0 | C 39 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | P7 | 54 01 0020 | 1p 1n | Pin 1reihig gerade |
| 0 | C 40 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | Р.8 | 54 01 0020 | 1p | Pin 1reihig gerade |
| 0 | C 41 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | DO | 54.01.0020 | 10 | Pin 1reihig gerade |
| 0 | C 42 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | F 9 | 54.01.0020 | 1p 1p | Pin, freihig, gerade |
| 0 | C 43 | 59.68.0029 | | 100u | EL 6V, 6.3*5.7 | | 0 | P 10 | 54.01.0020 | 1p 1= | Pin, freihig, gerade |
| ٥ | C 44 | 50.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | P 11 P 12 | 54.01.0020 | 1p 1p | Pin, Treinig, gerade |
| 0 | C 45 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | F 12 | 54.01.0020 | 10 | Pin, freihig, gerade |
| 0 | C 46 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 06 | 03 | 0 | F 13 | 54.01.0020 | 1p 1- | Pin, freihig, gerade |
| 0 | C 47 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 06 | 603 | 0 | P 14 | 54.01.0020 | 1p | Pin, Treinig, gerade |
| 0 | C 48 | 59.68.0029 | | 100u | EL 6V, 6.3*5.7 | | 0 | P 15 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 49 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | P 16 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 50 | 59.60.2361 | | 330p | CER 50V, 5%, C0G, 08 | 805 | 0 | P 17 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 51 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | P 18 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 52 | 59.60.2361 | | 330p | CER 50V, 5%, C0G, 08 | 805 | 0 | P 19 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 53 | 59.60.2361 | | 330p | CER 50V, 5%, C0G, 08 | 805 | 0 | P 20 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 54 | 59.60.2361 | | 330p | CER 50V, 5%, C0G, 08 | 805 | 0 | P 21 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 55 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 06 | 603 | 0 | P 22 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 56 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 06 | 03 | 0 | P 23 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 57 | 59.68.0029 | | 100u | EL 6V, 6.3*5.7 | | 0 | P 24 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 58 | 59.68.0029 | | 100u | EL 6V, 6.3*5.7 | | 0 | P 25 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 59 | 59.68.0029 | | 100u | EL 6V, 6.3*5.7 | | 0 | P 26 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 60 | 59.68.0029 | | 100u | EL 6V, 6.3*5.7 | | 0 | P 27 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 61 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 08 | 05 | 0 | P 28 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 62 | 59.06.0153 | | 15n | PETP, 63V, 10%, RM5 | | 0 | P 29 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 63 | 59.60.2249 | | 100p | CER 50V, 5%, COG, 06 | 03 | 0 | P 30 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 64 | 59.60.2249 | | 100p | CER 50V, 5%, COG. 06 | 03 | 0 | P 31 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 65 | 59.06.0153 | | 15n | PETP, 63V, 10%. RM5 | | 0 | P 32 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 66 | 59.06.0153 | | 15n | PETP, 63V, 10%, RM5 | | 0 | P 33 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 67 | 59.06.0153 | | 15n | PETP, 63V, 10%, RM5 | | 0 | P 34 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 68 | 59.60.3337 | | 100n | CER 50V. 10%, X7R, 08 | 05 | 0 | P 35 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 69 | 59 60 2373 | | 1n0 | CER 50V 5% C0G 08 | 05 | 0 | P 36 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| ő | C 70 | 59 60 2373 | | 1n0 | CER 50V 5% COG 08 | 105 | 0 | P 37 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| ō | C 71 | 59.60 2373 | | 1n0 | CER 50V. 5% COG 05 | 05 | 0 | P 38 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| ñ | C 72 | 59.60 2373 | | 1n0 | CER 50V. 5% COG 08 | 105 | 0 | P 39 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| ň | C 73 | 59.60 2373 | | 1n0 | CER 50V. 5% COG 08 | 105 | 0 | P 40 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| n | C 74 | 59 60 2373 | | 1n0 | CER 50V 5% 000 0 | 105 | 0 | P 41 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| n | C 75 | 59 60 2373 | | 1n0 | CER 50V 5% 000 0 | 05 | 0 | P 42 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 76 | 59 60 2373 | | 1n0 | CER 50V 5% COG 08 | 105 | 0 | P 43 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| n | C 77 | 59.60 3337 | | 100n | CER 50V 10% X7P 08 | 05 | 0 | P 44 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| n | C 78 | 59 60 3337 | | 100n | CER 50V 10% Y7P 0 | 05 | 0 | P 45 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| n | C 79 | 59 60 3337 | | 100n | CER 50V 10% X78 00 | 05 | 0 | P 46 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | C 80 | 50 60 3337 | | 100n | CEP 50V 10%, X/R, 08 | 05 | 0 | P 47 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | 0.00 | 50 60 2227 | | 1000 | CER 50V, 10%, X/K, 08 | 05 | 0 | P 48 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | | 59.50.3337 | | 10011 | CER DUV, 10%, X/R, 08 | 00 | 0 | P 49 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | וט | 50.80.8001 | | ++++0 | 200mA 75V 4ns SOD | 80 | 0 | P 50 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | | 50.50.8001 | | +++0 | 200mA 75V 4ns SOD | 00 | 0 | P 51 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | 03 | 50.50.8001 | | +440 | 200mA 75V 4ns SOD | 00 | 0 | P 52 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | | 50.60.8001 | | 4448 | ZUUMA /5V 4ns SOD | 00 | 0 | P 53 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | 101 | 50.62.1014 | | /4HU 14 | nex schmitt trigger inverte | И | 0 | P 54 | 54,01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | IC 2 | 50.61.0204 | | MC33078 | oual Op-Amp low noise | | • | ~ • | | · r | ., |

Analog Output 1.942.621.00 (1)

| ldx. | Pos. | Part No. Qty. | Type/Val. | Description |
|--------|--------------|--------------------------|-----------|-------------------------------------|
| 0 | P 55 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | P 56 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | P 57 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | P 58 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | P 59 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | P 60 | not used | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | P 01 D 62 | 54.21.2202 | 3p | XLR PCB Wilkel |
| 0 | P 63 | 54 21 2202 | 3p | XLR PCB Winkel |
| 0 | P 64 | 54.21.2202 | 3p | XLR PCB Winkel |
| 0 | Q 1 | 50.60.0050 | BC817-25 | NPN 45V 800mA SOT 23 |
| 0 | R1 | 57.00.1011 | 010R | MF, 1%, 0204, E24 |
| 0 | R 2 | 57.60.1102 | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 3 | 57.60.1222 | 2k2 | MF, 1%, 0204, E24 |
| 0 | R 4 | 57.60.1100 | 10R | MF, 1%, 0204, E24 |
| 0 | R 5 | 57.60.1101 | 100R | MF, 1%, 0204, E24 |
| 0 | R6 | 57.60.1362 | 3k6 | MF, 1%, 0204, E24 |
| 0 | R8 | 57 60 1182 | 1k8 | MF 1% 0204 E24 |
| 0 | R9 | 57.60.1101 | 100R | MF. 1%, 0204, E24 |
| 0 | R 10 | 57.60.1362 | 3k6 | MF, 1%, 0204, E24 |
| 0 | R 11 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| o | R 12 | 57.00.1332 | 3K3 | MF, 1%, 0204, E24 |
| 0 | R 13 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 14 | 57.60.1101 | 100R | MF, 1%, 0204, E24 |
| 0 | R 15 | 57.92.7013 | 0.5A | PTC 60V |
| 0 | R 16 | 57.60.1104 | 100k | MF, 1%, 0204, E24 |
| 0 | H 17 P 18 | 57.60.1101 | 100R | ME 1% 0204 E24 |
| 0 | R 19 | 57 60 1332 | 3k3 | MF. 1%, 0204, E24 |
| 0 | R 20 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 21 | 57.92.7013 | 0.5A | PTC 60V |
| 0 | R 22 | 57.92.7013 | 0.5A | PTC 60V |
| 0 | R 23 | 57.60.1229 | 2R2 | MF, 1%, 0204, E24 |
| 0 | R 24 | 57.60.1362 | 3k6 | MF, 1%, 0204, E24 |
| 0 | R 25 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 26 | 57.60.1362 | 3k6 | MF, 1%, 0204, E24 |
| 0 | R 27 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 28 | 57.60.1222 57.60.1232 | 242 | ME, 1%, 0204, E24 |
| 0 | R 29 R 30 | 57.60.1352 | 346 | MF, 1%, 0204, E24 |
| 0 | R 31 | 57 60 1332 | 3k3 | MF 1% 0204 E24 |
| 0 | R 32 | 57 60 1362 | 3k6 | MF 1% 0204 F24 |
| 0 | R 33 | 57.60.1229 | 2R2 | MF, 1%, 0204, E24 |
| 0 | R 34 | 57.60.1221 | 220R | MF, 1%, 0204, E24 |
| 0 | R 35 | 57.60.1221 | 220R | MF, 1%, 0204, E24 |
| 0 | R 36 | 57.60.1221 | 220R | MF, 1%, 0204, E24 |
| 0 | R 37 | 57.60.1911 | 910R | MF, 1%, 0204, E24 |
| 0 | R 38 | 57.60.1102 | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 39 | 57.60.1332 | 3K3 | MF, 1%, 0204, E24 |
| 0 | R 41 | 57.60.1332 | 1008 | MF, 1%, 0204, E24 MF 1% 0204 E24 |
| õ | R 42 | 57.60.1331 | 330R | MF, 1%, 0204, E24 |
| 0 | R 43 | 57.60.1102 | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 44 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 45 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 46 | 57.60.1221 | 220R | MF, 1%, 0204, E24 |
| 0 | R 47 | 57.60.1221 | 220R | MF, 1%, 0204, E24 |
| 0 | R 40 D 40 | 57.60.1221 | 22UK | WF, 1%, U204, E24 |
| 0 | R 50 | 57.60.1362 | 3k6 | MF, 1%, 0204, E24 |
| 0 | R 51 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 52 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 53 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 54 | 57.60.1362 | 3k6 | MF, 1%, 0204, E24 |
| 0 | R 55 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 56 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | K 5/ | 57.60.1100 | 108 | MF, 1%, 0204, E24 |
| U C | R 59 | 57.60.1103 | 10k | ME 1% 0204 E24 |
| ō | R 60 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 61 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 62 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 63 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 64 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 65 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 66 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| U C | N 0/ | 57.60.1103 | 106 | ME 1% 0204 E24 |
| 0 | R 69 | 57.60.1223 | 22k | MF. 1%, 0204, E24 |
| 0 | R 70 | 57.60.1103 | 10k | MF, 1%, 0204. E24 |
| Ō | R 71 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 72 | 57.60.1333 | 33k | MF, 1%, 0204, E24 |
| 0 | R 73 | 57.60.1333 | 33k | MF, 1%, 0204, E24 |
| 0 | R 74 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 75 D 76 | 57.60.1333 | 33k | MF, 1%, 0204, E24 |
| U | R /0 | 37.00.1103 | 106 | NIE 1% UZU4 EZ4 |

| | | | | | | Page: 2 of 2 |
|------|------|--------------|------|-----------|-----------------|--------------|
| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | |
| 0 | R 77 | 57.60.1333 | | 33k | MF, 1%, 0204 | . E24 |
| 0 | R 78 | 57.60.1223 | | 22k | MF, 1%, 0204 | , E24 |
| 0 | R 79 | 57.60.1223 | | 22k | MF, 1%, 0204 | , E24 |
| 0 | R 80 | 57.60.1222 | | 2k2 | MF, 1%, 0204 | , E24 |
| 0 | R 81 | 57.60.1222 | | 2k2 | MF, 1%, 0204 | , E24 |
| 0 | R 82 | 57.60.1222 | | 2k2 | MF, 1%, 0204 | E24 |
| 0 | R 83 | 57.60.1222 | | 2k2 | MF, 1%, 0204 | E24 |
| 0 | R 84 | 57.60.1222 | | 2k2 | MF, 1%, 0204 | , E24 |
| 0 | R 85 | 57.60.1181 | | 180R | MF, 1%, 0204 | , E24 |
| 0 | R 86 | 57.60.1181 | | 180R | MF, 1%, 0204 | . E24 |
| 0 | R 87 | 57.60.1181 | | 180R | MF, 1%, 0204 | E24 |
| 0 | R 00 | 57.00.1222 | | 2k2 | MF, 196, 0204 | C24 |
| 0 | R 89 | 57.60.1181 | | 180R | MF, 1%, 0204 | , E24 |
| 0 | R 90 | 57.60.1911 | | 910R | MF, 1%, 0204 | , E24 |
| 0 | R 91 | 57.60.1911 | | 910R | MF, 1%, 0204 | E24 |
| 0 | R 92 | 57.60.1102 | | 1k0 | MF, 1%, 0204 | E24 |
| 0 | R 93 | 57.60.1102 | | 1k0 | MF, 1%, 0204 | E24 |
| 1 | RA 1 | 58.05.0502 | | 5k0 | 10%, 0.5W, Ce | ermet |
| 1 | RA 2 | 58.05.0502 | | 5k0 | 10%, 0.5W, Ce | ermet |
| 1 | RA 3 | 58.05.0502 | | 5k0 | 10%, 0.5W, Ce | ermet |
| 1 | RA 4 | 58.05.0502 | | 5k0 | 10%, 0.5W, Ce | ermet |
| 0 | T1 · | 1.022.368.00 | | | Line Output Tra | afo 6 dB |
| o | T 2 | 1.022.300.00 | | | Line Output Tra | atu o ub |
| 0 | ТЗ - | 1.022.368.00 | | | Line Output Tra | afo 6 dB |
| 0 | Т4 | 1.022.368.00 | | | Line Output Tra | afo 6 dB |

— End of List –

Comments:

(01) RA1, RA2, RA3, RA4 5k0: 58.05.1502->58.05.0502 MP9 added

Block Diagram Monitoring



Monitoring Controller 1.942.182.21



Monitoring Controller 1.942.182.21



Monitoring Controller 1.942.182.21



MONITORING CONTROLLER 1.942.182.21 (1)

Page: 1 of 2

| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx. | Pos. | Part No. | Qty. | Type/Val. | Descri | ption |
|------|--------|-------------------|-------|-----------|--------------------------------|------|--------------|------------|------|-----------|----------|--------------|
| | | | | | | | | 57.00 4470 | | 470 | NE 404 | 0004 504 |
| 0 | C 1 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | RI | 57.60.1470 | | 4/R | ME 10 | 0204, E24 |
| 0 | C 2 | 59.68.0111 | | 22u | EL 35V, 6.3*5.7 | 0 | R 2 | 57.60.1104 | | 100k | MF, 1% | 0, 0204, E24 |
| 0 | C 3 | 59.68.0111 | | 22u | EL 35V, 6.3*5.7 | 0 | RJ | 57.60.1104 | | 100k | MF, 1% | b, 0204, E24 |
| 0 | C 4 | 59.68.0111 | | 22u | EL 35V, 6.3*5.7 | 0 | R 4 | 57.60.1471 | | 470R | MF, 1% | o, 0204, E24 |
| 0 | C 5 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | R 5 | 57.92.7021 | | 0.9A | PIC | 60V |
| 0 | CG | 50.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | R6 | 57.92.7021 | | 0.9A | PIC | 60V |
| 0 | C 7 | 59.60.2233 | | 22p | CER 50V, 5%, C0G, 0603 | 0 | R/ | 57.92.7021 | | 0.9A | ME | 000 |
| 0 | C 8 | 59.60.2233 | | 22p | CER 50V, 5%, C0G, 0603 | 0 | Rð | 57.60.1000 | | | NIF, | 0204 |
| 0 | C 9 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | R9 | 57.60.1470 | | 4/R | MF, 1% | 5, 0204, E24 |
| 0 | C 10 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | R 10 | 57.60.1101 | | 100R | MF, 1% | 6, U2U4, E24 |
| 0 | C 11 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | R 11 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 12 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | R IZ | 57.09.1097 | | 106 | GF 57 | 0000 |
| 0 | C 13 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | RIJ | 57.60.1102 | | IKU | IVIF, 1% | 5, U2U4, E24 |
| 0 | C 14 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 14 | 57.60.1470 | | 4/R | MF, 1% | 5, U2U4, E24 |
| 0 | C 15 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 15 | 57.60.1470 | | 4/R | MF, 1% | 5, 0204, E24 |
| 0 | C 16 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 16 | 57.60.1470 | | 47R | MF, 1% | 5, 0204, E24 |
| Ú | C 17 | 39.00.2249 | | IOOP | CER 30V, 3%, COG, 0003 | 0 | R 17 | 57.60.1470 | | 47R | MF. 1% | 5. 0204. E24 |
| 0 | C 18 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 18 | 57.60.1103 | | 10K | MF, 1% | 5, 0204, E24 |
| 0 | C 19 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 19 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 20 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 20 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 21 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 21 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 22 | 59.60.2249 | | 100p | CER 50V. 5%. C0G. 0603 | 0 | R 22 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 23 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 23 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 24 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 24 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 25 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | 0 | R 25 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 26 | 59.60.2249 | | 100p | CER 50V. 5%, C0G, 0603 | 0 | R 26 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 27 | 59.60.2249 | | 100p | CER 50V. 5%. C0G. 0603 | 0 | R 27 | 57.60.1470 | | 47R | MF, 1% | 5, 0204, E24 |
| ō | C 28 | 59.60.2249 | | 100p | CER 50V, 5%, C0G, 0603 | ٥ | R 29 | 57.60.1007 | | 10k | CF 59 | 6 0603 |
| 0 | C 29 | 59.60.2249 | | 100p | CER 50V. 5%. C0G. 0603 | 0 | R 29 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | C 30 | 59 60 2249 | | 100n | CER 50V 5% C0G 0603 | 0 | R 30 | 57.60.1470 | | 47R | MF, 1% | 5, 0204, E24 |
| ñ | C 31 | 59 60 3337 | | 100p | CER 50V 10% X7B 0805 | 0 | R 31 | 57.60.1470 | | 47R | MF, 1% | 5, 0204, E24 |
| 0 | 0.31 | 50 60 9001 | | 4448 | 200m4 751/ 4nc SOD 80 | 0 | R 32 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | | 50.00.0001 | | 4440 | 200mA 75V 4hs SOD 80 | 0 | R 33 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | D 2 | 50.60.9001 | | 4448 | 200mA 75V 4nc SOD 80 | 0 | R 34 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | 03 | 50.00.0001 | | 4440 | 200mA 75V 4hs SOD 80 | 0 | R 35 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | D 4 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 36 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | D 5 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 37 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | D 6 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 38 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | D7 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | Ū | R 39 | 57.09.1097 | | IOK | CF 07 | 6 0003 |
| 0 | 08 | 50.60.8001 | | 4448 | 200mA 75V 4hs SOD 80 | 0 | R 40 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | D 9 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 41 | 57 60 1102 | | 1k0 | MF. 1% | 0204 E24 |
| 0 | D 10 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 42 | 57 60 1102 | | 1k0 | MF. 1% | 0204 E24 |
| 0 | D 11 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | õ | R 43 | 57 60 1102 | | 1k0 | ME 1% | 0204 F24 |
| 0 | D 12 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | ő | R 44 | 57 60 1102 | | 1k0 | MF 1% | 0204 E24 |
| 0 | DIS | 30.00.0001 | | 4440 | 20011A 73V 4115 30D 00 | ő | R 45 | 57 69 1097 | | 10k | CF 5% | 6 0603 |
| 0 | D 14 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | R 46 | 57 69 1097 | | 10k | CE 5% | 6 0603 |
| 0 | DL 1 | 50.04.2141 | | LR3360 | LED 3.18mm, rot | õ | R 47 | 57 69 1097 | | 10k | CF 5% | 6 0603 |
| 0 | IC 1 | 50.62.1014 | | 74HC 14 | Hex Schmitt trigger inverter | 0 | D 49 | 57 69 1097 | | 10k | CE 5% | 0603 |
| 0 | IC 2 | 50.62.1151 | | 74HC151 | 8 channel multiplexer | ~ | R 40 | 57 60 1000 | | | ME U | 0204 |
| 0 | IC 3 | 50.62.1151 | | 74HC151 | 8 channel multiplexer | 0 | R 49 P 50 | 57.60.1472 | | 4147 | ME 1% | 0204 E24 |
| 0 | IC 4 | 50.62.1595 | | 74HC595 | 8bit shift/output register | 0 | R 50 | 57.60.1472 | | 467 | ME 10/ | 0204, 224 |
| 0 | IC 5 | 50.15.0114 | | 9637 | Dual diff Line Receiver | 0 | R 51 | 57.60.1472 | | 467 | ME 10/ | 0204, 224 |
| 0 | IC 6 | 50.62.1574 | | 74HC574 | Octal D-FF | 0 | R 52 | 57.00.1472 | | 4K/ | ME 10 | 0204, E24 |
| 0 | IC 7 | 50.62.1574 | | 74HC574 | Octal D-FF | 0 | R 53 | 57.00.1472 | | 4K7 | NF, 1% | 0204, E24 |
| 0 | IC 8 1 | .942.913.24 | | | SW.130 MON MODULE (50.16.0311) | 0 | R 34 | 57.00.1472 | | 4K/ | NF, 1% | 0204, E24 |
| 0 | IC 9 | 50.62.1244 | | 74HC244 | Octal buffer line driver/recei | 0 | R 55 | 57.60.1472 | | 4K/ | ME 10/ | 0204, E24 |
| 0 | IC 10 | 50.62.1244 | | 74HC244 | Octal buffer line driver/recei | 0 | R 30 | 57.00.1472 | | 467 | NE 10 | 0204, E24 |
| 0 | IC 11 | 50.62.1595 | | 74HC595 | 8bit shift/output register | 0 | R 57 | 57.00.1472 | | 467 | MF, 1% | 0004 E24 |
| 0 | IC 12 | 50.62.1595 | | 74HC595 | 8bit shift/output register | 0 | R 58 | 57.60.1472 | | 4K/ | MF, 1% | 0, 0204, E24 |
| 0 | IC 13 | 50.99.0111 | | MCT6 | DLQ ILD-74, MCT 6, TLP 504 A | 0 | R 59 | 57.60.1472 | | 4K/ | MF, 1% | 0, 0204, E24 |
| 0 | IC 14 | 50.04.2138 | | PC847 | DLQ PC-847 , EE-CM 4 | 0 | R 60 | 57.00.1472 | | 4K7 | MF, 1% | 0204, E24 |
| 0 | IC 15 | 50.04.2138 | | PC847 | DLQ PC-847 , EE-CM 4 | 0 | ROI | 57.00.1472 | | 4K/ | ME 40 | 0204, E24 |
| 0 | IC 16 | 50.04.2138 | | PC847 | DLQ PC-847 , EE-CM 4 | 0 | R 02 | 57.00.1472 | | 46/ | MF, 1% | 0, 0204, E24 |
| 0 | MP 1 1 | .942.130.12 | 1 pce | | MONITORING CONTROLLER PCB | 0 | R 63 | 57.69.1097 | | 10K | CF 5% | 0204 E24 |
| 0 | MP 2 | 43.01.0108 | 1 pce | Label | ESE-WARNSCHILD | 0 | R 64 | 57.60.1102 | | 1KU | MF, 1% | 0, 0204, E24 |
| 0 | MP 3 1 | .942.182.10 | 1 pce | | NR. ETIKETTE 5X20 | 0 | R 65 | 57.60.1102 | | 1KU | MF, 1% | 5, U2U4, E24 |
| 0 | MP 4 1 | .101.001.21 | 1 pce | | TEXT-ETIK. 5*20 HARDWARE -21 | 0 | R 88 | 57.80.1102 | | 160 | MF, 1% | 5, 0204, E24 |
| 0 | P 1 | 54.14.5540 | | 20p | PCB-Buchse winkel | 0 | R 67 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | P 2 | 54.14.5540 | | 20p | PCB-Buchse winkel | 0 | R 68 | 57.60.1102 | | 1K0 | MF, 1% | 5, U2U4, E24 |
| 0 | P 3 | 54.14.2056 | | 64p | Stecker gerade Au | 0 | R 69 | 57.69.1097 | | 10K | CF 5% | 6 0603 |
| 0 | P 4 | 54.13.0076 | | 9р | D-Sub, PCB, Winkel | 0 | R 70 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | P 5 | 54.13.0076 | | 9р | D-Sub, PCB, Winkel | 0 | R 71 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | P 6 | 54.13.0078 | | 25p | D-Sub, PCB, Winkel | 0 | к 72 Б 72 | 57.60.1102 | | 1KU | MF, 1% | , U2U4, E24 |
| 0 | Q 1 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 73 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | Q 2 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | K 74 | 57.60.1102 | | 1K0 | MF, 1% | o, U204, E24 |
| 0 | Q 3 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 75 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | Q 4 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 76 | 57.60.1102 | | 1k0 | MF, 1% | , 0204, E24 |
| 0 | Q 5 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 77 | 57.60.1472 | | 4k7 | MF, 1% | , 0204, E24 |
| 0 | Q 6 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 78 | 57.60.1472 | | 4k7 | MF, 1% | , 0204, E24 |
| 0 | Q 7 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 79 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | Q 8 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 80 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | Q 9 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 81 | 57.69.1097 | | 10k | CF 5% | 6 0603 |
| 0 | Q 10 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 82 | 57.60.1102 | | 1k0 | MF, 1% | , 0204, E24 |
| 0 | Q 11 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 83 | 57.60.1102 | | 1k0 | MF, 1% | , 0204, E24 |
| 0 | Q 12 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 84 | 57.60.1102 | | 1k0 | MF, 1% | , 0204, E24 |
| 0 | Q 13 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 85 | 57.60.1100 | | 10R | MF, 1% | , 0204, E24 |
| 0 | Q 14 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 86 | 57.60.1220 | | 22R | MF, 1% | , 0204, E24 |
| 0 | Q 15 | 50.60.0050 | | BC817-25 | NPN 45V 800mA SOT 23 | 0 | R 87 | 57.60.1220 | | 22R | MF, 1% | , 0204, E24 |

MONITORING CONTROLLER 1.942.182.21 (1)

ldx. Pos.

Part No.

Qty.

Type/Val.

ldx. Pos. Part No. Qty. Type/Val. Description 0 R 88 57.60.1220 22R MF, 1%, 0204, E24 MF, 1%, 0204, E24 0 R 89 57.60.1220 22R MF, 1%, 0204, E24 0 R90 0 R91 57.60.1100 10R 57.60.1220 22R MF, 1%, 0204, E24 0 R 92 57.60.1220 22R MF, 1%, 0204, E24 0 R 93 0 R 94 57.60.1220 57.60.1220 22R 22R MF, 1%, 0204, E24 MF, 1%, 0204, E24 0 R 95 57.60.1220 22R MF, 1%, 0204, E24 0 R 96 57.60.1220 22R MF, 1%, 0204, E24 MF, 1%, 0204, E24 0 R 97 57.60.1220 22R MF, 1%, 0204, E24 57.60.1220 0 R98 0 R99 22R MF, 1%, 0204, E24 MF, 1%, 0204, E24 57.60.1220 22R 22R 0 R 100 0 R 101 57.60.1220 MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 0204 57.60.1220 22R 1 R 102 57.60.1000 0R0 8*R Resistor-Netw 2% SIP9 0 RZ 1 57.88.4471 470R 55.01.0164 DIL-Switch, PCB 0 \$ 1 4*2 0 XIC 8 53.03.0172 40p DIL 0.6", löt, gerade 0 Y 1 89.60.1004 22.1184MHz SMD Quartz

(1) R102 (10R) changed to 0R0

-- End of List -

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© 16.01.97 VA PAGE 1/2 SC 1.942.133.00
CR Monitor OUT 1.942.133.00



CR Monitor OUT 1.942.133.00





| Onar 1000 PAGE STUDIO MONITOR D-SUB SC 1,942,1 | Erstellt 🖲 18.10.2000 PZ 🤇 |) 0 | 0 | 0 |
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| STUDIER STUDIO MONITOR D-SUB SC 1.942.1 | | 0nA r 1000 |) | PAGE 1 OF 3 |
| | STUDER S | TUDIO MONITOR [| D-SUB SC | 1.942.139.00 |

Studio Monitor D-Sub 1.942.139.00



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Studio Monitor D-Sub 1.942.139.00











| Erstellt 🛞 18.10.2000 | B PZ D | 0 | 0 | | 0 | | |
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| STUDER | \$TUDIC | MONITOR | D-SUB | SC 1 | .942 | .139 | 3.00 |

Studio Monitor D-Sub 1.942.139.00

| | | ldx Pos. | Part No. Qty. | Type/Val. | Descripion |
|---|------|---|--|--|--|
| | | 0 C1 0 C2 0 C3 0 C4 0 C5 | i9.60.2237 i9.60.2237 i9.60.2237 i9.60.2237 i9.68.0029 | 33p 33p 33p 33p 100u | CER 60/, 5%, C0G,0603 CER 50/, 5%, C0G,0603 CER 50/, 5%, C0G,0603 CER 50/, 5%, C0G,0603 EL 6/, 6.3*5.7 |
| | | 0 C 0 0 C 7 0 C 8 0 C 9 0 C 10 0 C 11 | 39.68.0029 39.68.0029 39.68.0029 39.68.0029 39.68.0029 39.68.0029 39.68.0029 | 100u 100u 100u 100u 100u 100u | EL 6/, 6.3°5.7 EL 6/, 6.3°5.7 EL 6/, 6.3°5.7 EL 6/, 6.3°5.7 EL 6/, 6.3°5.7 EL 6/, 6.3°5.7 EL 6/, 6.3°5.7 |
| | | 0 C 12 0 C 13 0 C 14 0 C 15 0 C 16 0 C 17 0 C 18 | 39.60.0029 39.60.3337 39.60.3337 39.60.3337 39.68.0029 39.68.0029 39.68.0029 39.68.0029 | 1000 100n 100n 100n 100u 100u | EL 0, 0.3.3.7 CER 50V, 10%, X7R, 0805 CER 50V, 10%, X7R, 0805 CER 50V, 10%, X7R, 0805 EL 6V, 6.3*5.7 EL 6V, 6.3*5.7 EL 6V, 6.3*5.7 CER 6V, 10%, X7R, 0805 |
| $\begin{array}{c} \hline \\ \hline $ | | 0 C 19 0 C 20 0 C 21 0 C 22 0 C 23 0 C 23 | 59.60.3337 59.60.3337 59.60.2361 59.68.0029 59.68.0029 59.68.0029 | 100n 100n 330p 100u 100u | CER 50/, 10%, X7R, 0805 CER 50/, 10%, X7R, 0805 CER 50/, 10%, X7R, 0805 CER 50/, 5%, C0G, 0805 EL 6/, 6.3*5.7 EL 6/, 6.3*5.7 |
| | | 0 C 25 0 C 26 0 C 27 0 C 28 0 C 29 0 C 30 | 59.68.0029 59.68.0029 59.68.0029 59.60.2237 59.60.2237 59.60.2237 | 100u 100u 100u 33p 33p 1u0 | EL 64, 6.3*5.7 EL 64, 6.3*5.7 EL 64, 6.3*5.7 EL 64, 6.3*5.7 CER 60, 5%, C0G, 0603 CER 5(V, 5%, C0G, 0603 PETP, EV, 10%, RM5 |
| $\begin{bmatrix} c_{1} \\ c_{2} \\ c_{3} \\ c_{5} \\ c_$ | MD 1 | 0 C 31 0 C 32 0 C 33 0 C 34 0 C 35 0 C 36 | 59.60.2361 59.60.3337 59.60.2237 59.60.2237 59.60.2237 59.60.2237 | 330p 100n 33p 33p 33p 33p 33p | CER 5(V, 5%, COG, 0805 CER 5(V, 10%, X7R, 0805 CER 5(V, 5%, COG, 0603 CER 5(V, 5%, COG, 0603 CER 5(V, 5%, COG, 0603 CER 5(V, 5%, COG, 0603 |
| | WH T | 0 C 37 0 C 38 0 C 39 0 C 40 0 C 41 0 C 42 | 59.60.2237 59.60.2237 59.60.2237 59.60.3337 59.60.3337 59.60.3337 | 33p 33p 33p 100n 100n 100n | CER 5(V, 5%, C0G, 0603 CER 5(V, 5%, C0G, 0603 CER 5(V, 5%, C0G, 0603 CER 5(V, 10%, X7R, 0805 CER 5(V, 10%, X7R, 0805 CER 5(V, 10%, X7R, 0805 |
| | | 0 C 43 0 C 44 0 C 45 0 C 46 0 C 46 0 C 47 0 C 48 | 59.60.3337 59.60.2237 59.60.2237 59.60.3337 59.60.3337 59.60.3337 | 100n 33p 33p 100n 100n 100n | CER 5(V, 10%, X7R, 0805 CER 5(V, 5%, COG, 0803 CER 5(V, 5%, COG, 0603 CER 5(V, 10%, X7R, 0805 CER 5(V, 10%, X7R, 0805 CER 5(V, 10%, X7R, 0805 |
| | | 0 C 49 0 C 50 0 C 51 0 C 52 0 C 53 0 C 54 | 59.68.0067 59.68.0067 59.68.0067 59.68.0067 59.60.2257 59.60.2257 | 22u 22u 22u 22u 220p 220p | EL 11V, 5.0*5.7 EL 11V, 5.0*5.7 EL 11V, 5.0*5.7 EL 11V, 5.0*5.7 CER 5/V, 5%, COG, 0603 CER 5/V, 5%, COG, 0603 |
| | | 0 C 55 0 C 56 0 C 57 0 C 58 0 C 59 0 C 60 | 59.60.2237 59.68.0029 59.68.0029 59.60.3337 59.60.2267 59.60.2257 | 33p 100u 100u 100n 220p 220p | CER 5V, 5%, COG, 0803 EL (V, 6.3*5.7 EL (V, 6.3*5.7 CER 5V, 10%, X7R, 0805 CER 5V, 5%, COG, 0603 CER 5V, 5%, COG, 0603 |
| | | 0 C 61 0 C 62 0 C 63 0 C 64 0 C 65 0 C 66 0 C 67 | 59.60.2257 59.60.2257 59.60.2257 59.60.3237 59.60.3337 59.60.3337 | 220p 220p 220p 220p 100n 100n 100n | CER 5V, 5%, COG, 0603 CER 5V, 5%, COG, 0603 CER 5V, 5%, COG, 0603 CER 5V, 5%, COG, 0603 CER 5V, 10%, X7R, 0805 CER 5V, 10%, X7R, 0805 |
| 5 g 18.10.2000 PZ PG RL 0 5 Bote Viso Decked Seen | | 0 C 68 0 IC 1 0 IC 2 0 IC 3 0 IC 4 0 IC 5 | 59.60.3337 50.61.0204 50.61.0204 50.62.1595 50.62.1595 50.62.8053 | 100n MC33078 MC33078 74HC595 74HC595 HC4053 | CER 5/V, 10%, X7R, 0805 Dual Or-Amp low noise Dual Or-Amp low noise 8bit shif/output register 8bit shif/output register Tringle i/h angling muk/demux |
| Wall Dotum Cerr. [cer.] MOX Popular Soite: 1 / 1 | | 0 IC 6 0 IC 7 0 IC 8 0 IC 9 0 IC 10 0 IC 11 0 IC 12 | 50.62.8053 50.62.8053 50.62.8053 50.62.8053 50.62.8053 50.62.8053 50.62.8053 50.62.8053 | HC4053 HC4053 HC4053 HC4053 HC4053 HC4053 HC4053 HC4053 | Trippie ich analog muxidemux Trippie ich analog muxidemux |

Studio Monitor D-Sub 1.942.139.00

| ldx | Pos. | Part No. | Qty. | Type/Val. | Description | ldx | Pos. | Part No. Qty. | Type/Val. | Description |
|-----|--------------|--------------|-------|----------------|--------------------------------------|-----|--------------|---------------|------------|---------------------------------------|
| 0 | IC 13 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 54 | 57.60.1512 | 5k1 | MF, 1%, 0204, E24 |
| 0 | IC 14 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 55 | 57.60.1104 | 100k | MF, 1%, 0204, E24 |
| 0 | IC 15 | 50.11.0140 | | THAT2181C | IC VCA THAT 2181C | 0 | R 56 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| 0 | IC 16 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 57 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| 0 | IC 17 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | n | R 58 | 57 60 1223 | 22k | MF. 1%, 0204, E24 |
| 0 | IC 18 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 59 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| 0 | IC 19 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 60 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| 0 | 10.20 | 50.11.0140 | | THAT2181C | IC VCA THAT 2181C | 0 | R 61 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| | 10 21 | 50.09.0124 | | 2142 | Audio balanced line driver | 0 | R 62 | 57.60.1223 | 22K | MF, 1%, 0204, E24 |
| 0 | IC 22 | 50.61.8003 | | 2142 CS4331 | D/A Converter 18bit Ste SO 8 | 0 | R 63 R 64 | 57.60.1223 | 22K 5k1 | MF, 1%, 0204, E24 MF 1%, 0204, E24 |
| 0 | IC 24 | 50,61,8003 | | CS4331 | D/A Converter 18bit Ste SO 8 | 0 | R 65 | 57.60.1104 | 100k | MF 1% 0204 E24 |
| 0 | IC 25 | 50.61.8003 | | CS4331 | D/A Converter 18bit Ste SO 8 | 0 | R 66 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| 0 | IC 26 | 50.11.0140 | | THAT2181C | IC VCA THAT 2181C | 0 | R 67 | 57.60.1433 | 43k | MF, 1%, 0204, E24 |
| 0 | IC 27 | 50.61.0206 | | SSM2275S | Dual Op-Amp, rail-to-rail SO 8 | 0 | R 68 | 57.60.1512 | 5k1 | MF, 1%, 0204, E24 |
| 0 | IC 28 | 50.11.0140 | | THAT2181C | IC VCA THAT 2181C | 0 | R 69 | 57.60.1104 | 100k | MF, 1%, 0204, E24 |
| 0 | IC 29 | 50.62.8053 | | HC4053 | Tripple 2ch analog mux/demux | 0 | R 70 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| 0 | IC 30 | 50.61.0201 | | TL062 | Dual FET Op-Amp | 0 | R 71 | 57.60.1680 | 68R | MF, 1%, 0204, E24 |
| 0 | MP 1 | 1.942.139.11 | 1 pce | | STUDIO MONITOR D-SUB PCB | 0 | R 72 | 57.60.1680 | 68R | MF, 1%, 0204, E24 |
| 0 | MP 2 | 13.01.0108 | 1 poo | Label | EEE WARNECHILD | 0 | R 73 | 57.60.1433 | 43k | MF, 1%, 0204, E24 |
| 0 | MP 3 | 1.942.139.10 | 1 pce | | NR.ETIKETTE 5X20 | 0 | R /4 | 57.60.1512 | 1004 | MF, 1%, 0204, E24 |
| 0 | P 1 | 54 14 5540 | | 200 | PCP Buchao winkol | 0 | R 76 | 57.60.1223 | 224 | MF, 1%, 0204, C24 |
| | P 2 | 54 14 5540 | | 20p | PCB-Buchse winkel | 0 | R 77 | 57 60 1102 | 1k0 | MF 1% 0204 E24 |
| 0 | P 4 | 54 13 0077 | | 20p 15n | D-Sub PCB Winkel | 0 | R 78 | 57.60.1913 | 91k | MF, 1%, 0204, E24 |
| 0 | P 5 | 54.13.0072 | | 15p | D-Sub, PCB, Winkel | õ | R 79 | 57.60.1680 | 68R | MF, 1%, 0204, E24 |
| 0 | P 6 | 54.13.0071 | | 9p | D-Sub, PCB, Winkel | 0 | R 80 | 57.60.1680 | 68R | MF, 1%, 0204, E24 |
| | D 1 | 57 60 4000 | | 226 | ME 18 0204 E24 | 0 | R 81 | 57.60.1222 | 2k2 | MF, 1%, 0204, E24 |
| | R 1 0 1 | 57.60.1223 | | 22K | MF, 1%, 0204, E24 | 0 | R 82 | 57.60.1101 | 100R | MF, 1%, 0204, E24 |
| | R 2 R 3 | 57.60.1223 | | 22K 22k | MF, 1%, 0204, E24 | 0 | R 83 | 57.60.1222 | 2k2 | MF, 1%, 0204, E24 |
| 0 | R4 | 57.60.1223 | | 22k | MF. 1%, 0204, E24 | 0 | R 84 | 57.60.1101 | 100R | MF, 1%, 0204, E24 |
| 0 | R 5 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | 0 | R 85 | 57.60.1393 | 39k | MF, 1%, 0204, E24 |
| 0 | R 6 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | 0 | K 80 | 57.60.1123 | 12K | MF, 1%, 0204, E24 |
| 0 | R 7 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | 0 | R 8/ | 57.60.1104 | 100K | MF, 1%, 0204, E24 |
| 0 | R 8 | 57 60 1222 | | 2k2 | MF, 1%, 0204, F24 | 0 | R 69 | 57.60.1125 | IOOK | MF, 1%, 0204, E24 |
| 0 | R 9 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | 0 | R 90 | 57.60.1513 | 51k | MF, 1%, 0204, E24 |
| 0 | R 10 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | 0 | R 91 | 57.60.1102 | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 11 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | 0 | R 92 | 57.60.1104 | 100k | MF, 1%, 0204, E24 |
| 0 | R 12 | 57.60.1682 | | 668 | MF, 1%, 0204, E24 | 0 | R 93 | 57.60.1104 | 100k | MF, 1%, 0204, E24 |
| 0 | R 14 | 57.60.1113 | | 22K 11k | MF, 1%, 0204, E24 MF 1% 0204 E24 | 0 | R 04 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| 0 | R 15 | 57.60.1113 | | 11k | MF, 1%, 0204, E24 | 0 | R 95 | 57.60.1223 | 22k | MF, 1%, 0204, E24 |
| 0 | R 16 | 57.60.1223 | | 22k | MF. 1%, 0204, E24 | U | R 96 | 57.60.1000 | ORO | MF, 0204 |
| 0 | R 17 | 57.60.1113 | | 11k | MF, 1%, 0204, E24 | 0 | RA 1 | 58.01.9503 | 50k | Cermet, 10%, 0.5W, vertical |
| 0 | R 18 | 57.60.1113 | | 11k | MF, 1%, 0204, E24 | 0 | RA 2 | 58.01.9503 | 50k | Cermet, 10%, 0.5W, vertical |
| 0 | R 19 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | 0 | RA 3 | 58.01.9503 | 50k | Cermet, 10%, 0.5W, vertical |
| 0 | R 20 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | 0 | RA 4 | 58.01.9503 | 50k | Cermet, 10%, 0.5W, vertical |
| 0 | R 21 | 57.60.1682 | | 6k8 | MF, 1%, 0204, E24 | 0 | RA 5 | 58.01.9103 | 10k | Cermet, 10%, 0.5W, vertical |
| 0 | R 22 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 | 0 | RA 6 | 58.01.9103 | 10k | Cermet, 10%, 0.5W, vertical |
| 0 | R 23 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | U | RA / | 58.01.9103 | TUK | Cermet, 10%, 0.5vv, vertical |
| 0 | R 24 | 57.60.1062 | | 1004 | MF, 1%, 0204, E24 | | | | End of the | -4 |
| 0 | R 26 | 57 60 1223 | | 22k | MF, 1%, 0204, E24 MF 1%, 0204 E24 | | | | End of Li | st |
| 0 | R 27 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | Cor | nments: | | | |
| 0 | R 28 | 57.60.1333 | | 33k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 29 | 57.60.1333 | | 33k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 30 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 31 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 32 | 57.60.1333 | | 33k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 33 | 57.60.1333 | | 33k | MF, 1%, 0204, E24 | | | | | |
| | R 34 D 35 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 36 | 57 60 1223 | | 282 | MF, 1%, 0204, E24 | | | | | |
| 0 | R 37 | 57.60.1682 | | 6k8 | MF 1% 0204 E24 | | | | | |
| 0 | R 38 | 57.60.1113 | | 11k | MF. 1%, 0204, E24 | | | | | |
| 0 | R 39 | 57.60.1113 | | 11k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 40 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 | | | | | |
| 0 | R 41 | 57.60.1272 | | 2k7 | MF, 1%, 0204, E24 | | | | | |
| 0 | R 42 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 | | | | | |
| 0 | R 43 | 57.60.1113 | | 11k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 44 | 57.60.1113 | | 11K 560 | MF, 1%, 0204, E24 | | | | | |
| | R 46 | 57 60 1979 | | 3K0 2k7 | WF, 1%, U2U4, E24 | | | | | |
| | R 47 | 57.60.1272 | | 43k | ME 1% 0204, E24 | | | | | |
| 0 | R 48 | 57.60.1433 | | 43k | MF. 1%, 0204, E24 | | | | | |
| 0 | R 49 | 57.60.1433 | | 43k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 50 | 57.60.1433 | | 43k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 51 | 57.60.1433 | | 43k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 52 | 57.60.1433 | | 43k | MF, 1%, 0204, E24 | | | | | |
| 0 | R 53 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | | | | | |



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CR Monitor D-Sub 1.942.129.00

| | ldx Pos | Part No. Cty. | Type/Val. | Description |
|--|---------|--------------------------|--------------|----------------------------|
| | 0 C1 | 59.22.4002 | 100u | EL 16V 20% RM5 |
| | 0 C 2 | 59.22.4002 | 100u | EL 16V 20% RM5 |
| | 0 C3 | 59.22.4002 | 100u | EL 16V 20% RM5 |
| | 0 C4 | 59.22.4002 | 100u | EL 16V 20% RM5 |
| | 0 06 | 59.60.2337 | 33p 100n | CER 50V, 5%, COB, 0805 |
| | 0 C7 | 59.60.2337 | 33p | CER 50V, 5%, C0G,0805 |
| | 0 C 8 | 59.60.2357 | 220p | CER 50V, 5%, COG,0805 |
| | 0 C 9 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 C10 | 59.60.2337 | 33p | CER 50V, 5%, C0G,0805 |
| | 0 C1: | 59.60.2337 | 33n | CER 50V, 10%, X/R, 0805 |
| | 0 C1; | 59.22.4002 | 100u | EL 16V 20% RM5 |
| | 0 C1. | 59.22.4002 | 100u | EL 16V 20% RM5 |
| | 0 C15 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| | 0 01 | 59.22.4002 | 100u | EL 16V 20% RM5 |
| | 0 C1 | 59.60.3337 | 1000 100n | CER 50V. 10%, X7R.0805 |
| | 0 C 1) | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| | 0 C 21 | 59.22.5220 | 22u | EL 25V 20% RM5 |
| | 0 C 2 | 59.99.2401 | 47u | BIP 16V 10% |
| | 0 C 21 | 59.99.2401 | 4/u 47u | BIP 16V 10% BIP 16V 10% |
| | 0 C 2 | 59.99.2401 | 47u | BIP 16V 10% |
| | 0 C 2i | 59.99.2401 | 47u | BIP 16V 10% |
| | 0 C 26 | 59.22.5220 | 22u | EL 25V 20% RM5 |
| | 0 C 2' | 59.99.2401 | 47u | BIP 16V 10% |
| | 0 C 2i | 59.99.2401 | 47u 47u | BIP 16V 10% |
| | 0 C29 | 59.99.2401 | 47u 47u | BIP 16V 10% |
| | 0 C 3 | 59.99.2401 | 47u | BIP 16V 10% |
| | 0 C 3! | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 C 33 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| R50 R51 R52 R53 R54 R55 R56 R57 R58 R59 UUUUUUU 200 2 4 | 0 C 34 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 C 3i | 59,60,2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 3 | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 33 | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 3) | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 4) | 59.60.2361 | 330p | CER 50V, 5%, C0G, 0805 |
| | 0 C 4 | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 43 | 59.60.2361 | 330p | CER 50V, 5%, C0G, 0805 |
| | 0 C 41 | 59.60.2361 | 330p | CER 50V, 5%, C0G, 0805 |
| | 0 C 45 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| | 0 C 43 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| | 0 C4 | 59.60.3337 | 100n | CER 50V, 10%, X/R, 0805 |
| | 0 C 4) | 59.60.2361 | 330p | CER 50V, 10%, X7R, 0805 |
| | 0 C 5) | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 5I | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 5? | 59.60.2361 | 330p | CER 50V, 5%, COG,0805 |
| | 0 0.5 | 59.60.2361 | 330p 330p | CER 50V, 5%, COG,0805 |
| | 0 C 55 | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 53 | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| P6 P5 P4 | 0 C 5' | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C 53 | 59.60.2361 | 330p | CER 50V, 5%, C0G,0805 |
| | 0 C6) | 59.60.2357 59.60.2357 | ∠∠up 220n | CER 50V, 5%, C0G,0805 |
| | 0 C 6I | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 C 62 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 C 63 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 0.64 | 59.60.2357 59.60.2357 | 220p | CER 50V, 5%, COG,0805 |
| | 0 C 63 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 C 67 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 C 63 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | 0 C 69 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| | | 59.60.2357 | 220p | CER 50V, 5%, COG 0805 |
| 5 1 18 10 2000 P7 PC P1 (0) | 0 C 72 | 59.60.2357 | 220p | CER 50V, 5%, C0G, 0805 |
| | 0 C 73 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| bitur d'er viso vectes seen index | 0 C 71 | 59.60.2357 | 220p | CER 50V, 5%, C0G, 0805 |
| Pogr. 1 / | 0 C 75 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| Serie: / / | 0 07 | 59.60.2357 59.60.2357 | 220p 220n | CER 50V, 5%, C0G, 0805 |
| | 0 C73 | 59.60.2357 | 220p | CER 50V, 5%, C0G,0805 |
| RECENSIONE 話覧 した MUNITUR D-SUB, ESE Z 道覧 1.942.129.00 | 0 C 79 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| NEGENSUOVI 88 | 0 C 80 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| | U C 81 | 59.60.3337 | 100n | GER 50V, 10%, X/R, 0805 |

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| ldx | Pos. | Part No. | Qty. | Type/Val. | Description | ldx | Pos. | Part No. | Qty. | Type/Val. | Description |
|--|--|--|--|--|---|-----|---------|------------|------|-----------|-----------------------------|
| 0 | IC 1 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 54 | 57.60.1272 | | 2k? | MF, 1%, 0204, E24 |
| 0 | IC 2 | 50.61.8003 | | CS4331 | D/A Converter 18bit Ste SO 8 | 0 | R 55 | 57.E0.1272 | | 2k? | MF, 1%, 0204, E24 |
| 0 | IC 3 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 56 | 57.60.1272 | | 2k? | MF, 1%, 0204, E24 |
| 0 | IC 4 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 57 | 57.60.1272 | | 2k? | MF, 1%, 0204, E24 |
| 0 | IC 5 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 58 | 57.60.1272 | | 2k7 | MF, 1%, 0204, E24 |
| 0 | IC 6 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 59 | 57.60.1272 | | 2k7 | MF, 1%, 0204, E24 |
| 0 | IC 7 | 50.61.8103 | | CS5360 | A/D Converter 24bit Ste SSOP20 | 0 | R 60 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 |
| 0 | IC 8 | 50.62.8053 | | HC4053 | Tripple 2ch analog mux/demux | 0 | R 61 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 9 | 50.62.1595 | | 74HC595 | 8bit shift/output register | 0 | R 62 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 10 | 50.62.8053 | | HC4053 | Tripple 2ch analog mux/demux | 0 | R 63 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 11 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 64 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 12 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 65 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 13 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 66 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 14 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 67 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 15 | 50.61.0204 | | MC33078 | Dual Op-Amp low noise | 0 | R 68 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 16 | 50.62.1014 | | 74HC 14 | Hex Schmitt trigger inverter | 0 | R 69 | 57.60.1562 | | 5k8 | MF, 1%, 0204, E24 |
| 0 | IC 17 | 50.62.8053 | | HC4053 | Tripple 2ch analog muxidemux | 0 | R 70 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | IC 18 | 50.62.8053 | | HC4053 | Tripple 2ch analog muxidemux | 0 | R 71 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| | | | | | OD MONITOD IN D SUD DOB | 0 | R 72 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | MP 1 | 1.942.129.11 | 1 008 | | CR MONITOR IN D-SUB POB | 0 | R 73 | 57.60.1562 | | 5k8 | MF, 1%, 0204, E24 |
| 0 | MP 2 | 43.01.0108 | 1 308 | Label | ESE-WARNSCHILD | 0 | R 74 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | MP 3 | 1.942.129.10 | 1 308 | | NR. ETIKETTE 5AZU | 0 | R 75 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | P1 | 54,14,5540 | | 20p | PCB-Buchse winkel | 0 | R 76 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | P2 | 54.14.5540 | | 20p | PCB-Buchse winkel | 0 | R 77 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| 0 | P 4 | 54.13.0072 | | 15p | D-Sub, PCB, Winkel | 0 | R 78 | 57.60.1562 | | 5k8 | MF, 1%, 0204, E24 |
| 0 | P5 | 54.13.0072 | | 15p | D-Sub, PCB, Winkel | 0 | R 79 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| l o | P6 | 54.13.0072 | | 15p | D-Sub, PCB, Winkel | 0 | R 80 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 |
| | - | | | | NE 494 0004 EQ. | 0 | R 81 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 1 | 57.60.1562 | | 5k6 | VIE, 1%, 0204, E24 | 0 | R 82 | 57.60.1113 | | 11k | MF, 1%, 0204, E24 |
| 0 | R 2 | 57.60.1562 | | 5k6 | MF, 1%, 0204, E24 | 0 | R 83 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | R 3 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R 84 | 57.60.1113 | | 11< | MF, 1%, 0204, E24 |
| 0 | R 4 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R 85 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 5 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | R 86 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 6 | 57.60.1151 | | 150R | MF, 1%, 0204, E24 | 0 | | 59.01.0103 | | 107 | Cermet 10% 0.5W vertical |
| 0 | R 7 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | | DA 2 | 58.01.0103 | | 104 | Cermet 10% 0.5W vertical |
| 0 | R 8 | 57.60.1433 | | 43k | MF, 1%, 0204, E24 | | DA 2 | 59.01.0103 | | 102 | Cermet 10% 0.5W vertical |
| 0 | R 9 | 57.60.1223 | | 22k | MF, 1%, 0204, E24 | 0 | RA4 | 58.01.9103 | | 104 | Cermet 10% 0.5W vertical |
| 0 | R 10 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | ő | RA 5 | 58.01.9103 | | 104 | Cermet, 10%, 0.5W, vertical |
| 0 | R 11 | 57.60.1362 | | 3k6 | MF, 1%, 0204, E24 | 0 | RAB | 58.01.9103 | | 104 | Cermet 10% 0.5W vertical |
| 0 | R 12 | 57.60,1362 | | 3k6 | MF, 1%, 0204, E24 | 0 | RA 7 | 58.01.9103 | | 10k | Cermet, 10%, 0.5W, vertical |
| 0 | R 13 | 57.60.1362 | | 366 | MF, 1%, 0204, E24 | 0 | RA 8 | 58 01 9103 | | 10k | Cermet, 10%, 0.5W, vertical |
| 0 | R 14 | 57.60.1000 | | ORO | MF, 0204 | | | | | | |
| 0 | R 15 | 57.60.1362 | | 360 | MF, 1%, 0204, E24 | | | | | End of I | et |
| 0 | K 16 | 57.60.1223 | | 22K | MF, 1%, 0204, E24 | | | | | Life of L | at |
| 0 | R1/ | 57.60.1433 | | 43K | MF, 1%, 0204, E24 | Co | mments: | | | | |
| 0 | R 18 | 57.00.1223 | | 226 | MF, 1%, 0204, E24 | | | | | | |
| 0 | RIE | 57.00.1223 | | 460 | ME 1% 0204 E24 | | | | | | |
| 0 | R ZU | 57.00.1102 | | 116 | ME 1% 0204 E24 | | | | | | |
| 0 | R 21 | 57.00.1113 | | 407 | ME 1% 0204 E24 | | | | | | |
| 0 | R 22 | 57.00.1475 | | 150P | ME 1% 0204 E24 | | | | | | |
| 0 | D 2/ | 57.00.1151 | | 150R | ME 1% 0204 E24 | | | | | | |
| | R 24 | 57 60 1151 | | 150R | MF. 1%, 0204, E24 | | | | | | |
| n 1 | R 26 | 57 60 1101 | | 1k0 | MF, 1%, 0204, E24 | | | | | | |
| n | R 27 | 57.60 1119 | | 11k | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 28 | 57.80 1223 | | 22k | MF, 1%, 0204, E24 | | | | | | |
| n | R 29 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 | | | | | | |
| n n | R 30 | 57,60,1113 | | 11k | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 31 | 57,60,1102 | | 1k0 | MF, 1%, 0204, E24 | | | | | | |
| n n | R 32 | 57 60 1273 | , | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| n n | R 33 | 57.60 1272 | | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 34 | 57.60.1272 | - | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| n n | R 35 | 57.60.1272 | | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 36 | 57,60,1272 | - | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| n | R 37 | 57.60.1272 | 2 | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 38 | 57.60.1272 | 2 | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 39 | 57.60.1272 | 2 | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 40 | 57.60.1272 | 2 | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | | 57.60.1272 | 2 | 2k7 | MF, 1%, 0204, E24 | | | | | | |
| | R 41 | | | 11k | MF. 1%, 0204, E24 | | | | | | |
| 0 | R 41 R 42 | 57.60.1113 | 3 | | | | | | | | |
| 0 | R 41 R 42 R 43 | 57.60.1113 57.60.1223 | 3 | 22k | MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 | R 41 R 42 R 43 R 44 | 57.60.1113 57.60.1223 57.60.1102 | 3 | 22k 1k0 | MF, 1%, 0204, E24 MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 | R 41 R 42 R 43 R 44 R 45 | 57.60.1113 57.60.1223 57.60.1102 57.60.1102 | 3 2 2 | 22k 1k0 3k6 | MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 0 | R 41 R 42 R 43 R 44 R 45 R 46 | 57.60.1113 57.60.1223 57.60.1102 57.60.1362 57.60.1362 | 3 2 2 9 | 22k 1k0 3k6 5R6 | MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 0 0 | R 41 R 42 R 43 R 44 R 45 R 46 R 47 | 57.60.1113 57.60.1223 57.60.1102 57.60.1362 57.60.1362 57.60.1363 57.60.1113 | 3 3 2 2 3 | 22k 1k0 3k6 5R6 11k | MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24 MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 0 0 | R 41 R 42 R 43 R 44 R 45 R 46 R 47 R 43 | 57.60.1113 57.60.1223 57.60.1103 57.60.1363 57.60.1363 57.60.1103 57.60.1113 | 3 2 2 3 3 2 | 22k 1k0 3k6 5R6 11k 1k0 | MF, 1%, 0204, E24 MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 0 0 0 | R 41 R 42 R 43 R 44 R 45 R 46 R 47 R 48 R 49 | 57.60.1110 57.60.1220 57.60.1100 57.60.1360 57.60.1560 57.60.1110 57.60.1110 57.60.1110 | 3 2 2 3 3 2 3 | 22k 1k0 3k6 5R6 11k 1k0 11k | MF, 1%, 0204, E24 MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 0 0 0 0 0 | R 41 R 42 R 43 R 44 R 45 R 46 R 47 R 43 R 49 R 50 | 57.60.1110 57.60.1220 57.60.1360 57.60.1360 57.60.1100 57.60.1110 57.60.1110 57.60.1110 57.60.1110 | 3 2 2 3 3 2 3 2 | 22k 1k0 3k6 5R6 11k 1k0 11k 2k7 | MF, 1%, 0204, E24 MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 0 0 0 0 0 | R 41 R 42 R 43 R 44 R 45 R 46 R 47 R 43 R 49 R 50 R 51 | 57.60.1110 57.60.1223 57.60.1360 57.60.1360 57.60.1101 57.60.1111 57.60.1111 57.60.1111 57.60.11272 57.60.1272 | 3 2 2 3 2 2 3 2 2 2 | 22k 1k0 3k6 5R6 11k 1k0 11k 2k7 2k7 | MF, 1%, 0204, E24 MF, 1%, 0204, E24 | | | | | | |
| 0 0 0 0 0 0 0 0 0 0 | R 41 R 42 R 43 R 44 R 45 R 46 R 47 R 48 R 49 R 50 R 51 R 52 | 57.60.1113 57.60.1223 57.60.1360 57.60.1560 57.60.1560 57.60.1103 57.60.1103 57.60.1103 57.60.1273 57.60.1273 57.60.1273 | 3 2 2 3 2 2 2 2 2 2 | 22k 1k0 3k6 5R6 11k 1k0 11k 2k7 2k7 2k7 | MF 1%, 0204 E24 MF 1%, 0204 E24 | | | | | | |



P1/4, (1) 18.9.01/PZ 1.942.613.00

Level Meter 1.942.613.00



Level Meter 1.942.613.00





P3/4, (1) 18.9.01/PZ 1.942.613.00

157

- 15V



P4/4, (1) 18.9.01/PZ 1.942.613.00

Level Meter 1.942.613.00



LEVEL METER 1.942.613.00 (1)

Page: 1 of 3

| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx | (, | Pos. | Part No. | Qty. | Type/Val. | Description |
|--------|--------------|------------|-------------------|-------------|--|-----|------------|------------------|-------------------------|----------------|-------------------|--------------------------------|
| | | | | | | | | C 318 | 59 68 0067 | 1 nce | 2211 | EL 16V 5.0*5.7 |
| 0 | C 1 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | C 319 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C2 | 59.00.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | C 320 | 59.68.0067 | 1 pce | 22u | EL 16V, 5.0*5.7 |
| 0 | C 4 | 59 60 3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 1 | | C 321 | 59.68.0113 | 1 pce | 47u | EL 35V, 8.0*6.3 |
| 0 | C 5 | 59.68.0073 | 1 pce | 220u | EL 16V, 8.0*10.7 | 0 | | C 322 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 |
| ٥ | C 6 | 50.69.0073 | 1 pee | 220u | EL 16V, 8.0*10.7 | 0 | | C 323 | 59.68.0071 | 1 pce | 100u | EL 16V, 8.0*6.3 |
| 0 | C 10 | 59.68.0115 | 1 pce | 100u | EL 35V, 8.0*10.7 | 0 | | C 401 | 59.60.2249 | ipce ince | 100p | CER 50V, 5%, C0G, 0603 |
| 0 | C 11 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | C 403 | 59.68.0031 | 1 pce | 220u | EL 6V. 8.0*6.3 |
| 0 | C 12 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | C 404 | 59.60.2369 | 1 pce | 680p | CER 50V, 5%, C0G, 0805 |
| 0 | C 13 | 59.00.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | C 405 | 59.68.0033 | 1 pce | 470u | EL 6V, 8.0*10.7 |
| 0 | C 14 C 15 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | C 400 | 59.00.0033 | 1 pce | 470u | EL 0V, 0.0-10.7 |
| 0 | C 16 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | C 407 | 59.60.2373 | 1 pce | 1n0 | CER 50V, 5%, C0G, 0805 |
| 0 | C 17 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | C 408 | 59.60.2373 | 1 pce | 1n0 | CER 50V, 5%, C0G, 0805 |
| 0 | C 18 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 1 | | C 409 | 59.60.3845 | 1 pce | 470n | CER 50V, 10%, X/R, 2220 |
| 0 | C 19 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 1 | | C 4 10 C 4 12 | 59.60.3741 | 1 pce | 4700 220n | CER 50V, 10%, X7R, 2220 |
| 0 | C 20 | 59.00.2005 | 1 µ0 0 | 470µ | CER 50V, 575, COG, 0005 | 0 | | C 413 | 59.68.0071 | 1 pce | 100u | EL 16V, 8.0*6.3 |
| 0 | C 22 | 59.60.3337 | 1 nce | 100n | CER 50V, 10%, X7R, 0805 | 1 | | C 414 | 59.60.3421 | 1 pce | 4n7 | CER 50V, 10%, X7R, 1206 |
| ő | C 23 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 1 | | C 415 | 59.60.3421 | 1 pce | 4n7 | CER 50V, 10%, X7R, 1206 |
| 0 | C 24 | 59.68.0067 | 1 pce | 22u | EL 16V, 5.0*5.7 | 1 | 1 | C 416 | 59.60.3421 | 1 pce | 4n7 | CER 50V, 10%, X7R, 1206 |
| 0 | C 25 | 59.60.3337 | 1 pce | 100n | CER 50V. 10%. X7R. 0805 | 0 | | C 417 | 59.60.2245 | 1 pce | 68p | CER 50V, 5%, C0G, 0603 |
| 0 | C 26 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 1 | | C 421 | 50.04.0510 | 1 pce | 47u 1N5822 | EL 35V, 6.0 0.3 34 Schottky |
| 0 | C 27 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | D 101 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 28 | 59.68.0067 | 1 pce | 22u | EL 16V, 5.0*5.7 | 0 | | D 102 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 29 | 59.68.0067 | 1 pce | 22u | EL 16V, 5.0*5.7 | 0 | | D 103 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 30 | 59.60.3337 | 1 pce 1 pce | 100n | CER 50V, 10%, X7R, 0805 CER 50V, 10%, X7R, 0805 | ٥ | | D 101 | 50.60.9001 | 1 poo | 4449 | 200mA 75V 4ne SOD 80 |
| ō | C 32 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | D 105 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 33 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | D 106 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 101 | 59.60.2249 | 1 pce | 100p | CER 50V, 5%, C0G, 0603 | 0 | | D 201 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 102 | 59.60.2249 | 1 pce | 100p | CER 50V, 5%, C0G, 0603 | 0 | | D 202 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 103 | 50.69.0031 | 1 peo | 220u | EL 6V, 8.0*6.3 | 0 | | D 203 D 204 | 50.60.8001 | i pce 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 104 | 59.60.2369 | 1 pce | 680p | CER 50V, 5%, C0G, 0805 | 0 | | D 205 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 105 | 50.69.0033 | 1 pce | 4700 | EL 6V, 8.0*10.7 | 0 | | D 206 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 100 | 59.60.2373 | 1 pce | 4700 1n0 | CER 50V. 5% C0G 0805 | 0 | | D 301 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 0 | C 108 | 59.60.2373 | 1 pce | 1n0 | CER 50V. 5%, C0G, 0805 | 0 | 1 | D 302 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 1 | C 109 | 59.60.3845 | 1 pce | 470n | CER 50V, 10%, X7R, 2220 | 0 | | 5 303 | 30.00.0001 | i pce | 4440 | 20011A 75V 415 30D 00 |
| 1 | C 110 | 59.60.3845 | 1 pce | 470n | CER 50V, 10%, X7R, 2220 | 0 | | D 304 | 50.60.8001 | i pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 111 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | 0 305 | 50.60.8001 | i pce | 4440 | 200mA 75V 4ns SOD 80 |
| 1 | C 112 | 59.60.3741 | 1 pce | 220n | CER 50V, 10%, X7R, 1812 | 0 | | D 300 | 50.60.8001 | i pce I nce | 4440 | 200mA 75V 4ns SOD 80 |
| 0 | C 113 | 59.68.0071 | 1 pce | 100u | EL 16V, 8.0*6.3 | 0 | | D 402 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 1 | C 114 | 59.60.3421 | 1 pce 1 nce | 417 4n7 | CER 50V, 10%, X7R, 1206 | 0 | 1 | D 403 | 50.60.8001 | 1 pce | 4448 | 200mA 75V 4ns SOD 80 |
| 1 | C 116 | 59.60.3421 | 1 pce | 4n7 | CER 50V, 10%, X7R, 1200 | 0 | 1 | D 404 | 50.60.8001 | l pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 117 | 59.60.2245 | 1 pce | 68p | CER 50V, 5%, C0G, 0603 | 0 | I | D 405 | 50.60.8001 | l pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 118 | 59.68.0067 | 1 pce | 22u | EL 16V, 5.0*5.7 | 0 | | D 406 | 50.60.8001 | pce | 4448 | 200mA 75V 4ns SOD 80 |
| 0 | C 119 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | | DL 1 | 50.60.9414 | pce | gm | SMD LED 2.7*3.5 |
| 0 | C 120 | 59.68.0067 | 1 pce | 22u | EL 16V, 5.0*5.7 | 0 | | JL 2 JL 101 | 50.00.9414 | i pce | | SMD LED 2.7"3.5 |
| 1 | C 121 | 59.68.0113 | 1 pce | 47u | EL 35V, 8.0*6.3 | 0 | | DL 101 | 50.04.2806 ² | loce | DC10SGWA | LED-Bargraph 10*on |
| 0 | C 122 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | 1 | DL 103 | 50.04.2150 · | pce | MV57164 | 10*LED-Bargraf rot diffus |
| 0 | C 201 | 59.00.0071 | 1 pce 1 nce | 100u | CER 50V 5% COG 0603 | 0 | 1 | DL 104 | 50.04.2815 | l pce | 10*yel | LED-Bargraph 10*yellow |
| õ | C 202 | 59.60.2249 | 1 pce | 100p | CER 50V, 5%, C0G, 0603 | 0 | | DL 201 | 50.04.2806 | l pce | DC10SGWA | LED-Bargraph 10*gn |
| 0 | C 203 | 59.68.0031 | 1 pce | 220u | EL 6V, 8.0*6.3 | 0 | 1 | DL 202 | 50.04.2806 | pce | DC10SGWA | LED-Bargraph 10*gn |
| 0 | C 204 | 59.60.2369 | 1 pce | 680p | CER 50V, 5%, C0G, 0805 | 0 | | DL 203 | 50.04.2150 | pce | MV57164 | 10*LED-Bargraf rot diffus |
| 0 | C 205 | 59.68.0033 | 1 pce | 470u | EL 6V, 8.0*10.7 | 0 | | JL 301 | 50.04.2806 | l pce | DC10SGWA | LED-Bargraph 10*gn |
| 0 | C 206 | 59.68.0033 | 1 pce | 470u | EL 6V, 8.0*10.7 | 0 | Ì | 302 | 50.04.2150 1 | Ince | MV57164 | 10*LED-Bargraph To ght |
| 0 | C 207 | 59.60.2373 | 1 pce | 1n0 1n0 | CER 50V, 5%, C0G, 0805 | 0 | 1 | DL 304 | 50.04.2815 | pce | 10*yel | LED-Bargraph 10*yellow |
| 1 | C 209 | 59.60 3845 | 1 DCP | 470n | CER 50V, 10% X7R 2220 | 0 | I | DL 401 | 50.04.2806 1 | l pce | DC10SGWA | LED-Bargraph 10*gn |
| 1 | C 210 | 59.60.3845 | 1 pce | 470n | CER 50V. 10%, X7R, 2220 | 0 | 1 | DL 402 | 50.04.2806 1 | l pce | DC10SGWA | LED-Bargraph 10*gn |
| 1 | C 212 | 59.60.3741 | 1 pce | 220n | CER 50V, 10%, X7R, 1812 | 0 | l | DL 403 | 50.04.2150 | l pce | MV57164 | 10*LED-Bargraf rot diffus |
| 0 | C 213 | 59.68.0071 | 1 pce | 100u | EL 16V, 8.0*6.3 | 0 | | DL 406 | 50.60.9412 | l pce | red | SMD LED 2.7*3.5 |
| 1 | C 214 | 59.60.3421 | 1 pce | 4n7 | CER 50V, 10%, X7R, 1206 | 0 | | DV 101 | 50.60.9017 | l pce | 10V | 5%, 0.2W, SOT 23 |
| 1 | C 215 | 59.60.3421 | 1 pce | 4n7 | CER 50V, 10%, X7R, 1206 | 0 | ì | JV 201 | 50 60 9017 1 | i poe | 10V | 5%, 0.2W, SOT 23 |
| 1 | C 216 | 59.60.3421 | 1 pce | 4n7 | CER 50V, 10%, X7R, 1206 | 0 | i | DV 401 | 50.60.9017 1 | pce | 10V | 5%, 0.2W, SOT 23 |
| 1 | C 217 | 50.69.0113 | 1 pce 1 pce | 66p 47u | CER 50V, 5%, COG, 0603 | 0 | 1 | C 1 | 50.10.0121 1 | , pce | LM2575HV | 5V, 1A Switching Reg |
| 0 | C 301 | 59.60.2249 | 1 pce | 1000 | CER 50V. 5%. C0G.0603 | 0 | | C 2 | 50.15.0114 | pce | 9637 | Dual diff Line Receiver |
| 0 | C 302 | 59.60.2249 | 1 pce | 100p | CER 50V, 5%, C0G, 0603 | 0 | 1 | С 3 | 50.62.1014 | pce | 74HC 14 | Hex Schmitt trigger inverter |
| 0 | C 303 | 59.68.0031 | 1 pce | 220u | EL 6V, 8.0*6.3 | 0 | 1 | C 4 | 50.61.8004 1 | pce | CS4390 | D/A Converter 24bit Ste SO 20 |
| 0 | C 304 | 59.60.2369 | 1 pce | 680p | CER 50V, 5%, C0G, 0805 | 0 | | C 101 | 50.61.0004 1 | pce | C34390 MC33078 | Dual On-Amplow point |
| 0 | C 305 | 59.68.0033 | 1 pce | 470u | EL 6V, 8.0*10.7 | 0 | | C 102 | 50.61.0204 | pce Ipce | MC33078 | Dual Op-Amp low noise |
| 0 | C 306 | 59.68.0033 | 1 pce | 4/0u 1n0 | EL 6V, 8.0*10.7 | 0 | i | C 103 | 50.61.0201 | pce | TL062 | Dual FET Op-Amp |
| 0 | C 308 | 59.60 2373 | 1 pce | 100 | CER 50V, 5%, COG, 0805 | 0 | 1 | C 104 | 50.61.0201 | pce | TL062 | Dual FET Op-Amp |
| 1 | C 309 | 59.60.3845 | 1 pce | 470n | CER 50V, 10%, X7R. 2220 | 0 | I | C 105 | 50.11.0119 1 | l pce | LM3914 | IC LM 3914 N, |
| 1 | C 310 | 59.60.3845 | 1 pce | 470n | CER 50V, 10%, X7R, 2220 | 0 | 1 | C 106 | 50.11.0119 | рсе | LM3914 | IC LM 3914 N, |
| 0 | C 311 | 59.60.3337 | 1 pce | 100n | CER 50V, 10%, X7R, 0805 | 0 | 1 | C 107 | 50.11.0119 1 | pce | LM3914 | IC LM 3914 N, |
| 1 | C 312 | 59.60.3741 | 1 pce | 220n | CER 50V, 10%, X7R, 1812 | 0 | | C 108 | 50.61.0204 1 | pce | MC33078 | Dual Op-Amp low noise |
| 0 | C 313 | 59.68.0071 | 1 pce | 100u | EL 16V, 8.0*6.3 | 0 | | C 110 | 50.61.0201 4 | pce pce | TL062 | Dual FET On-Amn |
| 1 | C 314 | 59.60.3421 | 1 pce | 4n7 4n7 | CER 50V, 10%, X7R, 1206 | 0 | i | C 111 | 50.61.0204 1 | pce | MC33078 | Dual Op-Amp low noise |
| 1 | C 315 | 50 60 2424 | i pce | 411/ 407 | CER 50V, 10%, X/R, 1206 | 0 | i | C 112 | 50.11.0119 1 | pce | LM3914 | IC LM 3914 N, |
| 0 | C 317 | 59.60.2245 | 1 pce | 68p | CER 50V, 5%, COG, 0603 | 0 | 1 | C 201 | 50.61.0204 1 | рсе | MC33078 | Dual Op-Amp low noise |

Date printed: 18.06.02

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| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx. | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|--------|--------------|-------|-----------|-------------------------------|------|--------------|-------------------------|------------|-----------|-----------------------|
| | | | | | | | B 400 | | | 101 | NE 18 0001 E01 |
| 0 | IC 202 | 50.61.0204 | 1 pce | MC33078 | Dual Op-Amp low noise | 0 | R 100 | 57.60.1183 | 1 pce | 18k | MF, 1%, 0204, E24 |
| 0 | IC 203 | 50.61.0201 | 1 pce | TL062 | Dual FET Op-Amp | 0 | R 101 | 57.60.1472 | 1 pce | 4k7 | MF, 1%, 0204, E24 |
| 0 | IC 204 | 50.61.0201 | 1 pce | TL062 | Dual FET Op-Amp | 0 | R 102 | 57.60.1332 | 1 pce | 3k3 | MF, 1%, 0204, E24 |
| 0 | IC 205 | 50.11.0119 | 1 pce | LM3914 | IC LM 3914 N, | 0 | R 103 | 57.60.1183 | 1 pce | 18k | MF, 1%, 0204, E24 |
| 0 | IC 206 | 50 11 0119 | 1 nce | LM3914 | IC LM 3914 N | 0 | R 104 | 57.60.1123 | 1 pce | 12k | MF. 1%, 0204, E24 |
| õ | 10 200 | 50 11 0110 | 1 000 | 1 M2014 | | 0 | P 106 | 57 60 1223 | 1 000 | 224 | ME 1% 0204 E24 |
| 0 | IC 301 | 50 61 0204 | 1 nce | MC33078 | Dual On-Amp low poise | ő | R 107 | 57 60 1182 | 1 nce | 1k8 | MF 1% 0204 F24 |
| 0 | 10 301 | 50.01.0204 | 1 222 | MC33070 | Bual Op Amp low holde | 0 | R 109 | 57.60.116E | 1 000 | 1145 | ME 1% 0204 E24 |
| 0 | IC 302 | 50.61.0204 | 1 pce | MC33078 | Dual Op-Amp low hoise | 0 | R 108 | 57.60.1155 | i pce | CIVIT | MF, 1%, 0204, E24 |
| 0 | IC 303 | 50.61.0201 | 1 pce | TL062 | Dual FET Op-Amp | 0 | R 109 | 57.60.1153 | 1 pce | 15K | MF, 1%, 0204, E24 |
| 0 | IC 304 | 50.61.0201 | 1 pce | TL062 | Dual FET Op-Amp | 0 | R 110 | 57.60.1362 | 1 pce | 3k6 | MF, 1%, 0204, E24 |
| 0 | IC 305 | 50.11.0119 | 1 pce | LM3914 | IC LM 3914 N. | 0 | R 111 | 57.60.1106 | 1 pce | 10M | MF, 1%, 0204, E24 |
| Ū | 10 300 | 50.11.0119 | I DCe | LM3914 | IC LM 3914 N. | 0 | R 112 | 57.00.1182 | 1 µue | 160 | MF, 1%, 0204, E24 |
| 0 | IC 307 | 50 11 0119 | 1 nce | I M3914 | IC 1 M 3914 N | 0 | R 113 | 57.60.1182 | , 1 pce | 1k8 | MF. 1%, 0204, E24 |
| õ | 10 209 | 50.81.0204 | 1 000 | MC22079 | Duel On Ame low point | 0 | D 114 | 57 60 1923 | 1 000 | 824 | ME 1% 0204 E24 |
| 0 | 10 306 | 50.61.0204 | i pce | MC33078 | Dual Op-Amp low hoise | 0 | R 114 | 57.00.1025 | r poe | 028 | NE 400 0004 E24 |
| 0 | IC 309 | 50.11.0110 | 1 pce | | IC MC 1496 L, LM 1496 N | 0 | R 115 | 57.60.1122 | 1 pce | 1K2 | MF, 1%, 0204, E24 |
| 0 | IC 310 | 50.61.0201 | 1 pce | TL062 | Dual FET Op-Amp | 0 | R 116 | 57.60.1222 | 1 pce | 2k2 | MF, 1%, 0204, E24 |
| ۵ | IC 311 | 50.61.0204 | 1 nce | MC33078 | Dual Op-Amp low noise | 0 | R 117 | 57.60.1105 | 1 DCE | 1M | MF. 1%. 0204. E24 |
| 0 | IC 312 | 50.11.0119 | 1 pce | LM3914 | IC LM 3914 N, | 0 | R 118 | 57.60.1122 | 1 pce | 1k2 | MF, 1%, 0204, E24 |
| 0 | IC 401 | 50 61 0204 | 1 nce | MC33078 | Dual Op-Amp low noise | 0 | R 119 | 57.60.1821 | 1 pce | 820R | MF. 1%, 0204, E24 |
| õ | 10 102 | 50 61 0204 | 1 000 | MC33078 | | 0 | R 120 | 57 60 1362 | t nce | 346 | ME 1% 0204 E24 |
| 0 | 10 402 | 50.01.0204 | i pce | WC33078 | | | R 120 | 57.00.1502 | 1 000 | 5001 | NF 40 0004 F04 |
| 0 | IC 403 | 50.61.0201 | 1 pce | 11062 | Dual FET Op-Amp | 0 | R 121 | 57.60.1564 | i pce | SOUK | MF, 1%, 0204, E24 |
| 0 | IC 404 | 50.61.0201 | 1 pce | TL062 | Dual FET Op-Amp | 0 | R 122 | 57.99.0252 | 1 pce | 47 | MF 10%, +4500ppm |
| 0 | IC 405 | 50.11.0119 | 1 pce | LM3914 | IC LM 3914 N, | 0 | R 123 | 57.60.1222 | 1 pce | 2k2 | MF, 1%, 0204, E24 |
| 0 | IC 406 | 50.11.0119 | 1 pce | LM3914 | IC LM 3914 N, | 0 | R 124 | 57.60.1222 | 1 pce | 2k2 | MF, 1%, 0204, E24 |
| 0 | IC 407 | 50.11.0119 | 1 pce | LM3914 | IC LM 3914 N. | 0 | R 125 | 57.60.1222 | 1 pce | 2k2 | MF, 1%, 0204, E24 |
| 0 | 14 | 54 01 0021 | 1 nco | lumper | 0.63*0.63mm Au | ٥ | R 126 | 57 60 1682 | Ince | 6k8 | ME 1% 0204 E24 |
| 0 | J I | 54.01.0021 | i poe | Jumper | 0.00 0.001111, Au | | R 120 | 57.00.1002 | i poo | 01.0 | NF, 1%, 0204, E24 |
| 0 | J 101 | 54.01.0021 | 1 pce | Jumper | 0.63°0.63mm, Au | 0 | R 127 | 57.00.1062 | i pce | OKO | MF, 1%, 0204, E24 |
| 0 | J 102 | 61.01.0021 | 1 pco | Jumpor | 0.69*0.63mm, Au | 0 | R 128 | 57.60.1000 · | 1 peo | ORO | MF, 0204 |
| 0 | J 103 | 54.01.0021 | 1 pce | Jumper | 0.63*0.63mm, Au | 0 | R 129 | 57.60.1000 | 1 pce | 0R0 | MF, 0204 |
| 0 | J 201 | 54.01.0021 | 1 pce | Jumper | 0.63*0.63mm, Au | 0 | R 130 | 57.60.1000 | 1 pce | 0R0 | MF, 0204 |
| 0 | J 301 | 54.01.0021 | 1 pce | Jumper | 0.63*0.63mm. Au | 0 | R 131 | not used ? | 1 pce | 0R0 | MF. 0204 |
| õ | 1 202 | 54.01.0021 | 1 000 | lumpor | 0.62*0.62mm Au | 0 | P 132 | not used ? | | 000 | ME 0204 |
| 0 | J 302 | 54.01.0021 | i pce | Jumper | 0.03 0.03hin, Au | | R 132 | not used | | 0110 | NI, 0204 |
| 0 | J 303 | 54.01.0021 | 1 pce | Jumper | 0.63°0.63mm, Au | 0 | R 133 | not used | i pce | | MF, 0204 |
| 0 | J 401 | 54.01.0021 | 1 pce | Jumper | 0.63*0.63mm, Au | 0 | R 134 | 57.60.1223 | i pce | 22K | MF, 1%, 0204, E24 |
| 0 | L1 | 62.03.0025 | 1 pce | 250uH | 2A Toroid Chocke | 0 | R 135 | 57.60.1223 | l pce | 22k | MF, 1%, 0204, E24 |
| 0 | MP 1 | 1.942.613.12 | 1 pce | | LEVEL METER PCB | 0 | R 136 | 57.60.1103 ⁻ | pce | 10k | MF, 1%, 0204, E24 |
| 0 | MP 2 | 1 942 613 10 | 1 nce | | NR ETIKETTE | 0 | R 137 | 57.60.1152 | Ince | 1k5 | MF. 1%, 0204, E24 |
| õ | MD 2 | 42.01.0109 | 1 000 | Label | | 1 | D 139 | 57 60 1153 | 1 000 | 154 | ME 1% 0204 E24 |
| | WIP 3 | 43.01.0108 | i pce | Laber | | | R 130 | 37.00.1133 | | I JK | NIT, 176, 0204, E24 |
| 0 | MP 4 | 50.20.3011 | 1 pce | | Kunikorper, 10 220, vertikal | | R 139 | 57.60.1155 | i pce | IDK | NF, 1%, 0204, E24 |
| 0 | MP 5 | 50.60.9411 | 3 pcs | d3 | Lichtleiter rund für LED | 0 | R 140 | 57.60.1332 | pce | 3k3 | MF, 1%, 0204, E24 |
| 0 | MP 6 | 89.01.3450 | 1 pce | Mic | Elektret-Kapsel mit Stiften | 0 | R 141 | 57.60.1332 | l pce | 3k3 | MF, 1%, 0204, E24 |
| 1 | MP 7 | 43.10.0110 | 1 pce | Α | Revisions-Etikette 5mm h'blau | 0 | R 142 | 57.60.1223 | pce | 22k | MF, 1%, 0204, E24 |
| 0 | P 5 | 54 01 0020 | 1 nce | 10 | Pin freihig gerade | ٥ | R 143 | 57 60 1223 | Ince | 22k | ME 1% 0204 E24 |
| | FJ | 54.01.0020 | 1 000 | 10 | Pin, freinig, gerade | 0 | D 444 | 57.00.1220 | | 414 | ME 1% 0204 E24 |
| 0 | P6 | 54.01.0020 | 1 DCE | 10 | Pin. Ireinid. derade | 0 | R 144 | 57.60.1105 | DCe | 1M | MF. 1%. 0204. E24 |
| 0 | Ρ/ | 54.01.0020 | 1 pce | ιp | Pin, Treinig, gerade | 1 | R 145 | 57.60.1222 | pce | 2K2 | MF, 1%, 0204, E24 |
| 0 | P 8 | 54.12.0703 | 1 pce | Зр | Stecker gerade PCB | 0 | R 146 | 57.60.1682 | l pce | 6k8 | MF, 1%, 0204, E24 |
| 0 | P 9 | 54.14.2052 | 1 pce | 16p | Stecker gerade Au | 0 | R 147 | 57.60.1471 | l pce | 470R | MF, 1%, 0204, E24 |
| 0 | P 10 | 54.12.0702 | 1 pce | 2n | Stecker gerade PCB | 0 | R 148 | 57.60.1471 | pce | 470R | MF, 1%, 0204, E24 |
| 0 | D 11 | 54 12 0503 | 1 000 | 30 | Power-Pin Stocker | 1 | R 149 | 57 60 1473 | Ince | 47k | ME 1% 0204 E24 |
| 0 | P 11 | 54.12.0505 | i pce | oto - | | | R 148 | 57.00.1475 | μue | 4/6 | NF, 1%, 0204, E24 |
| 0 | P 101 | 54.11.0136 | 1 pce | 2*3p | Pin 0.63*0.63, RM2.54 | 0 | R 150 | 57.60.1103 | i pce | TUK | MF, 1%, 0204, E24 |
| 0 | P 102 | 54.11.0136 | 1 pce | 2*3p | Pin 0.63*0.63, RM2.54 | 0 | R 151 | 57.60.1153 1 | l pce | 15k | MF, 1%, 0204, E24 |
| 0 | P 201 | 54.11.0136 | 1 pce | 2*3p | Pin 0.63*0.63, RM2.54 | 1 | R 152 | 57.60.1331 1 | l pce | 330R | MF, 1%, 0204, E24 |
| 0 | P 301 | 54.11.0136 | 1 pce | 2*3p | Pin 0.63*0.63, RM2.54 | 0 | R 153 | 57.60.1682 1 | l pce | 6k8 | MF, 1%, 0204, E24 |
| 0 | P 302 | 54.11.0136 | 1 pce | 2*3p | Pin 0.63*0.63. RM2.54 | 0 | R 154 | 57.60.1153 1 | DCe | 15k | MF, 1%, 0204, E24 |
| 0 | B 401 | 54 11 0126 | 1 000 | 2*3p | Pin 0.63*0.63 PM2.54 | 0 | P 155 | 57 60 1332 | | 313 | ME 1% 0204 E24 |
| ~ | P 401 | 59.11.0130 | 1 000 | E SP | DND 451/ 400m4 COT 22 | õ | D 150 | 57.00.1932 | | 940 | ME 18 0204 E24 |
| 0 | Q 101 | 50.60.1002 | i pce | BC000C | PNP 45V 100IIA 301 23 | 0 | R 150 | 57.00.1622 | i poe | 012 | NF, 1%, 0204, E24 |
| 0 | Q 102 | 50.60.1050 | 1 pce | BC807-25 | PNP 45V 800MA SOT 23 | 0 | R 157 | 57.60.1822 1 | pce | 862 | MF, 1%, 0204, E24 |
| 0 | Q 103 | 50.60.1050 | 1 pce | BC807-25 | PNP 45V 800mA SOT 23 | 0 | R 158 | 57.60.1471 1 | l pce | 470R | MF, 1%, 0204, E24 |
| 0 | Q 104 | 50.60.1002 | 1 pce | BC860C | PNP 45V 100mA SOT 23 | 0 | R 159 | 57.60.1471 1 | l pce | 470R | MF, 1%, 0204, E24 |
| 0 | Q 201 | 50.60.1002 | 1 pce | BC860C | PNP 45V 100mA SOT 23 | 0 | R 160 | 57.60.1223 1 | l pce | 22k | MF, 1%, 0204, E24 |
| 0 | Q 202 | 50.60.1050 | 1 pce | BC807-25 | PNP 45V 800mA SOT 23 | 0 | R 161 | not used 1 | pce | 390R | MF, 1%, 0204, E24 |
| ñ | 0.203 | 50 60 1050 | 1 nce | BC807-25 | PNP 451/ 800mA SOT 23 | ń | R 162 | not used | | 390R | ME 1% 0204 E24 |
| ~ | 0.004 | 50.00.1000 | . poo | BC960C | | ~ | D 102 | not used | . p.00 | 2008 | ME 19/ 0204 E24 |
| U | Q 204 | 00.00.1002 | i pce | 00000 | FINE 45V IUUINA SUI 23 | U | N 103 | not used 1 | i pce | | IVII , 170, UZU4, EZ4 |
| 0 | Q 301 | 50.60.1002 | i pce | D0000 | PNP 45V 100MA SOI 23 | υ | rt 104 | not used 1 | pce | SHOK | IVIF, 1%, UZU4, EZ4 |
| 0 | Q 302 | 50.60.1050 | 1 pce | BC807-25 | PNP 45V 800mA SOT 23 | 0 | R 165 | not used 1 | pce | 390R | MF, 1%, 0204, E24 |
| 0 | Q 303 | 50.60.1050 | 1 pce | BC807-25 | PNP 45V 800mA SOT 23 | 0 | R 166 | 57.60.1182 1 | l pce | 1k8 | MF, 1%, 0204, E24 |
| 0 | Q 304 | 50.60.1002 | 1 pce | BC860C | PNP 45V 100mA SOT 23 | 0 | R 200 | 57.60.1183 1 | pce | 18k | MF, 1%, 0204, E24 |
| 0 | 0 401 | 50 60 1002 | 1 nce | BC860C | PNP 45V 100mA SOT 23 | 0 | R 201 | 57 60 1472 1 | nce | 4k7 | ME 1% 0204 E24 |
| Š | 0 400 | 50.00.1002 | 1 | BC807.05 | | õ | R 201 | E7 60 1332 4 | | ara | ME 19 0204 E24 |
| 0 | Q 402 | 50.60.1050 | 1 pce | BC807-25 | PNP 45V 800mA SOT 23 | 0 | R 202 | 57.60.1332 | pce | зка | MF, 1%, 0204, E24 |
| 0 | Q 403 | 50.60.1050 | 1 pce | BC807-25 | PNP 45V 800mA SOT 23 | 0 | R 203 | 57.60.1183 1 | pce | төк | MF, 1%, 0204, E24 |
| 0 | Q 404 | 50.60.1002 | 1 pce | BC860C | PNP 45V 100mA SOT 23 | 0 | R 204 | 57.60.1123 1 | pce | 12k | MF, 1%, 0204, E24 |
| 0 | R 1 | 57.60.1222 | 1 pce | 2k2 | MF, 1%, 0204, E24 | 0 | R 206 | 57.60.1223 1 | рсе | 22k | MF, 1%, 0204, E24 |
| 0 | R 2 | 57.60.1122 | 1 pce | 1k2 | MF, 1%, 0204, E24 | 0 | R 207 | 57.60.1182 1 | pce | 1k8 | MF, 1%, 0204, E24 |
| ń | R 3 | 57.60 1391 | 1 pce | 390R | MF. 1%, 0204, E24 | n | R 208 | 57.60 1155 1 | pce | 1M5 | MF. 1%. 0204. E24 |
| ~ | P / | 57 60 4004 | 1 000 | 300P | ME 1% 0204 E24 | ~ | P 200 | 57 60 4459 4 | 000 | 15k | ME 1% 0204 E24 |
| Ű | 15.4 | 57.00.1391 | i pce | 4000 | NI , 170, 0204, E24 | 0 | N 208 | 57.00.11531 | pue no: | 156 | NI , 1/0, 0204, E24 |
| 0 | К 5 | 57.60.1121 | 1 pce | 120R | MF, 1%, 0204, E24 | 0 | K 210 | 57.60.1362 1 | pce | 360 | MF, 1%, 0204, E24 |
| 0 | R 6 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 | 0 | R 211 | 57.60.1106 1 | pce | 10M | MF, 1%, 0204, E24 |
| 0 | R 9 | 57.92.7012 | 1 pce | 0.3A | PTC 60V | 0 | R 212 | 57.60.1182 1 | pce | 1k8 | MF, 1%, 0204, E24 |
| 0 | R 10 | 57.92.7012 | 1 pce | 0.3A | PTC 60V | 0 | R 213 | 57.60.1182 1 | pce | 1k8 | MF, 1%, 0204, E24 |
| ń | R 11 | 57 60 1103 | 1 nce | 10k | ME 1% 0204 E24 | Ó | R 214 | 57 60 1823 | | 82k | MF 1% 0204 F24 |
| ~ | | E7 60 4400 | 1 000 | 104 | ME 10/ 0204 E24 | ~ | D 015 | 57 60 4400 | | 11/2 | ME 1% 0204 E24 |
| 0 | 13 13 | 37.00.1103 | , hce | | IVII , 170, UZU4, EZ4 | 0 | 11210 | 57.00.1122 1 | pue | 154 | NII , 170, U204, E24 |
| 0 | K 15 | 57.92.7012 | 1 pce | 0.3A | PIC 60V | 0 | R 216 | 57.60.1222 1 | pce | 262 | MF, 1%, 0204, E24 |
| 0 | R 16 | 57.92.7012 | 1 pce | 0.3A | PTC 60V | 0 | R 217 | 57.60.1105 1 | l pce | 1M | MF, 1%, 0204, E24 |
| 0 | R 17 | 57.60.1103 | 1 pce | 10k | MF, 1%, 0204, E24 | 0 | R 218 | 57.60.1122 1 | l pce | 1k2 | MF, 1%, 0204, E24 |
| 0 | R 18 | 57.60.1101 | 1 pce | 100R | MF, 1%, 0204, E24 | 0 | R 219 | 57.60.1821 1 | pce | 820R | MF, 1%, 0204, E24 |
| n | R 19 | 57 60 1101 | 1 nce | 100R | ME 1% 0204 E24 | 0 | R 220 | 57 60 1362 | DCe | 3k6 | MF 1% 0204 F24 |
| ~ | | 57.00.1101 | 1 poo | 000 | ME 0004 | ~ | D 004 | E7 60 1502 | | EROL | ME 40/ 0204 E24 |
| υ | rt 20 | 57.00.1000 | i pce | URU | IVIF, U204 | U | n 221 | or.00.1564 1 | i pce | JUUK | WIF, 170, UZU4, EZ4 |

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| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description | ldx. | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|-------|--------------|-------|------------|------------------------------|--------|------------------|----------------|-----------|-----------------------|--------------------------------------|
| 0 | B 222 | 57 00 0252 1 | 000 | 47 | ME 10% +4500ppm | 0 | P 403 | 57 60 1183 1 | | 184 | ME 1% 0204 E24 |
| 0 | P 223 | 57 60 1222 1 | nce | 242 | ME 1% 0204 E24 | 0 | R 403 | 57 60 1123 1 | | 10k | MF 1% 0204 F24 |
| 0 | R 223 | 57 60 1222 1 | i pce | 212 | MF, 1%, 0204, E24 | 0 | R 404 | 57.60 1223 1 | | 224 | ME 1% 0204 E24 |
| 0 | R 224 | 57.00.1222 1 | pce | 2K2 | MF, 1%, 0204, E24 | 0 | R 400 | 57.00.1223 | i pce | 22K | ME 1% 0204 E24 |
| 0 | R 225 | 57.00.1222 1 | pce | 2K2 | MF, 1%, 0204, E24 | 0 | R 407 | 57.60.1182 | pce | 180 | MF, 1%, 0204, E24 |
| 0 | R 220 | 57.00.1002 1 | pce | OKO OLO | MF, 1%, 0204, E24 | 0 | R 406 | 57.60.1155 | pce | 1005 | MF, 1%, 0204, E24 |
| 0 | R 227 | 57 60 1000 1 | pce | ORO | MF, 1%, 0204, E24 ME 0204 | 0 | R 409 R 410 | 57.60.1362.1 | pce | 10K 3k6 | MF, 1%, 0204, E24 MF 1%, 0204 E24 |
| 0 | R 220 | 57.00.1000 1 | nee | | MF, 0204 | 0 | D 411 | 57.60.1106.1 | | 1014 | ME 1% 0204 E24 |
| 0 | R 229 | 57.00.1000 1 | pce | ORO | MF, 0204 | 0 | R 411 | 57.00.1100 | pce | 10101 | MF, 1%, 0204, E24 |
| 0 | R 230 | 57.00.1000 1 | pce | URU ORO | MF, 0204 | 0 | R 412 | 57.60.1182 | pce | 110 | MF, 1%, 0204, E24 |
| 0 | R 231 | not used 1 | pce | URU | MF, 0204 | 0 | R 413 | 57.60.1182 | pce | 168 | MF, 1%, 0204, E24 |
| 0 | R 232 | not used 1 | pce | ORO | MF, 0204 | 0 | R 414 | 57.60.1823 1 | pce | 82K | MF, 1%, 0204, E24 |
| 0 | R 233 | not used 1 | pce | | MF, 0204 | 0 | R 415 | 57.60.1122 | pce | 162 | MF, 1%, 0204, E24 |
| 0 | R 234 | 57.60.1223 1 | pce | 22K | MF, 1%, 0204, E24 | 0 | R 410 | 57.00.1222 | pce | 2R2 | MF, 1%, 0204, E24 |
| 1 | R 238 | 57.60.1153 1 | pce | 15K | MF, 1%, 0204, E24 | 0 | R 417 | 57.60.1105 | pce | 110 | MF, 1%, 0204, E24 |
| 1 | R 239 | 57.60.1153 1 | pce | 15K | MF, 1%, 0204, E24 | 0 | R 418 | 57.60.1122 1 | pce | 1K2 | MF, 1%, 0204, E24 |
| 1 | R 245 | 57.60.1222 1 | pce | 2k2 | MF, 1%, 0204, E24 | 0 | R 419 | 57.60.1821 1 | pce | 820R | MF, 1%, 0204, E24 |
| 1 | R 249 | 57.60.1473 1 | DCE | 47k | MF. 1%. 0204. E24 | 0 | R 420 | 57.60.1362 1 | DCE | 3k6 | MF. 1%. 0204. E24 |
| 1 | R 252 | 57.60.1331 1 | pce | 330R | MF, 1%, 0204, E24 | 0 | R 421 | 57.60.1564 1 | pce | 560K | MF, 1%, 0204, E24 |
| 0 | R 300 | 57.60.1183 1 | pce | 18K | MF, 1%, 0204, E24 | 0 | R 422 | 57.99.0252 1 | pce | 47 | MF 10%, +4500ppm |
| 0 | R 301 | 57.60.1472 1 | pce | 4K/ | MF, 1%, 0204, E24 | 0 | R 423 | 57.60.1222 1 | pce | 2k2 | MF, 1%, 0204, E24 |
| 0 | R 302 | 57.60.1332 1 | pce | 3k3 | MF, 1%, 0204, E24 | 0 | R 424 | 57.60.1222 1 | pce | 2k2 | MF, 1%, 0204, E24 |
| 0 | R 303 | 57.60.1183 1 | pce | 18k | MF, 1%, 0204, E24 | 0 | R 425 | 57.60.1222 1 | pce | 2k2 | MF, 1%, 0204, E24 |
| 0 | R 304 | 57.60.1123 1 | pce | 12K | MF, 1%, 0204, E24 | 0 | R 426 | 57.60.1682 1 | pce | 068 | MF, 1%, 0204, E24 |
| 0 | R 306 | 57.60.1223 1 | pce | 22K | MF, 1%, 0204, E24 | 0 | R 427 | 57.60.1682 1 | pce | 668 | MF, 1%, 0204, E24 |
| 0 | R 307 | 57.60.1182 1 | pce | 1k8 | MF, 1%, 0204, E24 | 0 | R 428 | 57.60.1000 1 | pce | 0R0 | MF, 0204 |
| 0 | R 308 | 57.60.1155 1 | pce | 1M5 | MF, 1%, 0204, E24 | 0 | R 429 | 57.60.1000 1 | pce | 0R0 | MF, 0204 |
| 0 | R 309 | 57.60.1153 1 | pce | 15k | MF, 1%, 0204, E24 | 0 | R 430 | 57.60.1000 1 | pce | 0R0 | MF, 0204 |
| ٥ | R 310 | 57.60.1362 1 | poo | 346 | MF, 1%, 0201, E21 | 0 | R 131 | not used 1 | poo | ORO | MF, 0204 |
| 0 | R 311 | 57.60.1106 1 | pce | 10M | MF, 1%, 0204, E24 | 0 | R 432 | not used 1 | pce | 0R0 | MF, 0204 |
| 0 | R 312 | 57.60.1182 1 | pce | 1k8 | MF, 1%, 0204, E24 | 0 | R 433 | not used 1 | pce | 0R0 | MF, 0204 |
| 0 | R 313 | 57.60.1182 1 | pce | 1k8 | MF, 1%, 0204, E24 | 0 | R 434 | 57.60.1223 1 | pce | 22k | MF, 1%, 0204, E24 |
| 0 | R 314 | 57.60.1823 1 | pce | 82k | MF, 1%, 0204, E24 | 1 | R 438 | 57.60.1153 1 | pce | 15k | MF, 1%, 0204, E24 |
| 0 | R 315 | 57.60.1122 1 | pce | 1k2 | MF, 1%, 0204, E24 | 1 | R 439 | 57.60.1153 1 | pce | 15k | MF, 1%, 0204, E24 |
| 0 | R 316 | 57.60.1222 1 | pce | 2k2 | MF, 1%, 0204, E24 | 1 | R 445 | 57.60.1222 1 | pce | 2k2 | MF, 1%, 0204, E24 |
| 0 | R 317 | 57.60.1105 1 | pce | 1M | MF, 1%, 0204, E24 | 1 | R 449 | 57.60.1473 1 | pce | 47k | MF, 1%, 0204, E24 |
| 0 | R 318 | 57.60.1122 1 | pce | 1k2 | MF, 1%, 0204, E24 | 1 | R 452 | 57.60.1331 1 | pce | 330R | MF, 1%, 0204, E24 |
| 0 | R 319 | 57.60.1821 1 | pce | 820R | MF, 1%, 0204, E24 | 0 | RA 101 | 58.05.1104 1 | рсе | 100k | 10%, 0.5W, Cermet |
| 0 | R 320 | 57.60.1362 1 | pce | 3k6 | MF, 1%, 0204, E24 | 0 | RA 102 | 58.60.0113 1 | pce | 1k0 | SMD 20%, 0.25W, Cermet |
| v | R 321 | 37.00.1304 1 | hce | 300K | MF, 1%, 0204, E24 | o | RA 103 | 30.00.0121 1 | pce | 20K | SMD 20%, 0.23W, Cennet |
| 0 | R 322 | 57.99.0252 1 | рсе | 47 | MF 10%, +4500ppm | 0 | RA 105 | 58.60.0115 1 | pce | 2k0 | SMD 20%, 0.25W, Cermet |
| 0 | R 323 | 57.60.1222 1 | pce | 2k2 | MF, 1%, 0204, E24 | 0 | RA 106 | 58.60.0117 1 | pce | 5k0 | SMD 20%, 0.25W, Cermet |
| 0 | R 324 | 57.60.1222 1 | рсе | 2k2 | MF, 1%, 0204, E24 | 0 | RA 201 | 58.05.1104 1 | рсе | 100k | 10%, 0.5W, Cermet |
| 0 | R 325 | 57.60.1222 1 | рсе | 2k2 | MF, 1%, 0204, E24 | 0 | RA 202 | 58.60.0113 1 | pce | 1k0 | SMD 20%, 0.25W, Cermet |
| 0 | R 326 | 57.60.1682 1 | nce | 6k8 | MF. 1%. 0204. E24 | 0 | RA 203 | 58.60.0121 1 | nce | 20k | SMD 20%. 0.25W. Cermet |
| 0 | R 327 | 57.60.1682 1 | pce | 6k8 | MF, 1%, 0204, E24 | 0 | RA 204 | 58.60.0119 1 | pce | 10k | SMD 20%, 0.25W, Cermet |
| 0 | R 328 | 57.60.1000 1 | рсе | 0R0 | MF, 0204 | 0 | RA 301 | 58.05.1104 1 | рсе | 100k | 10%, 0.5W, Cermet |
| 0 | R 329 | 57.60.1000 1 | рсе | 0R0 | MF, 0204 | 0 | RA 302 | 58.60.0113 1 | рсе | 1k0 | SMD 20%, 0.25W, Cermet |
| 0 | R 330 | 57.60.1000 1 | рсе | 0R0 | MF, 0204 | 0 | RA 303 | 58.60.0121 1 | рсе | 20k | SMD 20%, 0.25W, Cermet |
| 0 | R 331 | not used 1 | рсе | 0R0 | MF, 0204 | 0 | RA 305 | 58.60.0115 1 | рсе | 2k0 | SMD 20%, 0.25W, Cermet |
| 0 | R 332 | not used 1 | pce | 0R0 | MF, 0204 | 0 | RA 306 | 58.60.0117 1 | pce | 5k0 | SMD 20%, 0.25W, Cermet |
| 0 | R 333 | not used 1 | рсе | 0R0 | MF, 0204 | 0 | RA 401 | 58.05.1104 1 | pce | 100k | 10%, 0.5W, Cermet |
| 0 | R 334 | 57.60.1223 1 | pce | 22k | MF, 1%, 0204, E24 | 0 | RA 402 | 58.60.0113 1 | рсе | 1k0 | SMD 20%, 0.25W, Cermet |
| 0 | R 335 | 57.60.1223 1 | рсе | 22k | MF, 1%, 0204, E24 | 0 | RA 403 | 58.60.0121 1 | pce | 20k | SMD 20%, 0.25W, Cermet |
| 0 | R 336 | 57.60.1103 1 | pce | 10k | MF, 1%, 0204, E24 | 0 | RA 404 | 58.60.0119 1 | pce | 10k | SMD 20%, 0.25W, Cermet |
| 0 | R 337 | 57.60.1152 1 | pce | 1k5 | MF, 1%, 0204, E24 | 0 | TP 1 | 54.02.0320 | | 1р | PCB-Flachst 2.8*0.8, gerade |
| 1 | R 338 | 57.60.1153 1 | pce | 15k | MF, 1%, 0204, E24 | 0 | TP 101 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 1 | R 339 | 57.60.1153 1 | pce | 15k | MF, 1%, 0204, E24 | 0 | TP 102 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 340 | 57.60.1332 1 | pce | 3k3 | MF, 1%, 0204, E24 | 0 | TP 103 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 341 | 57.60.1332 1 | рсе | 3k3 | MF, 1%, 0204, E24 | 0 | TP 104 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 342 | 57.60.1223 1 | pce | 22k | MF, 1%, 0204, E24 | 0 | TP 105 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 343 | 57.60.1223 1 | рсе | 22k | MF, 1%, 0204, E24 | 0 | TP 106 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 344 | 57.60.1105 1 | рсе | 1M | MF, 1%, 0204, E24 | 0 | TP 107 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 1 | R 345 | 57.60.1222 1 | pce | 2k2 | MF, 1%, 0204, E24 | 0 | TP 201 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 346 | 57.60.1682 1 | рсе | 6k8 | MF, 1%, 0204, E24 | 0 | TP 202 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 347 | 57.60.1471 1 | рсе | 470R | MF, 1%, 0204, E24 | 0 | TP 203 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 348 | 57.60.1471 1 | рсе | 470R | MF, 1%, 0204, E24 | 0 | TP 204 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 1 | R 349 | 57.60.1473 1 | pce | 47k | MF, 1%, 0204, E24 | 0 | TP 301 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 350 | 57.60.1103 1 | рсе | 10k | MF, 1%, 0204, E24 | 0 | TP 302 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 351 | 57.60.1153 1 | рсе | 15k | MF, 1%, 0204, E24 | 0 | TP 303 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 1 | R 352 | 57.60.1331 1 | pce | 330R | MF, 1%, 0204, E24 | 0 | TP 304 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 353 | 57.60.1682 1 | pce | 6k8 | MF, 1%, 0204, E24 | 0 | TP 305 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 354 | 57.60.1153 1 | рсе | 15k | MF, 1%, 0204, E24 | 0 | TP 306 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 355 | 57.60.1332 1 | pce | 3k3 | MF, 1%, 0204, E24 | 0 | TP 307 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 356 | 57.60.1822 1 | рсе | 8k2 | MF, 1%, 0204, E24 | 0 | TP 401 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 357 | 57.60.1822 1 | pce | 8k2 | MF, 1%, 0204, E24 | 0 | TP 402 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 358 | 57.60.1471 1 | рсе | 470R | MF, 1%, 0204, E24 | 0 | TP 403 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 359 | 57.60.1471 1 | pce | 470R | MF, 1%, 0204, E24 | 0 | TP 404 | 54.02.0320 | | 1p | PCB-Flachst 2.8*0.8, gerade |
| 0 | R 360 | 57.60.1223 1 | рсе | 22k | MF, 1%, 0204, E24 | 0 | W 1 1 | .023.555.01 1 | рсе | | FLACHKABEL 14 POL. 0,055M |
| 0 | R 361 | not used 1 | pce | 390R | MF, 1%, 0204, E24 | 0 | W2 1 | .023.555.01 1 | рсе | | FLACHKABEL 14 POL. 0.055M |
| 0 | R 362 | not used 1 | pce | 390R | MF, 1%, 0204, E24 | - | | | • | | |
| 0 | R 363 | not used 1 | pce | 390R | MF, 1%, 0204, E24 | | | | | End of List | |
| 0 | R 364 | not used 1 | рсе | 390R | MF, 1%, 0204, E24 | Comn | nents: | | | | |
| 0 | R 365 | not used 1 | рсе | 390R | MF, 1%, 0204, E24 | (01) S | everal foil caps | s changed to d | eramic, s | several resistors ada | apted due to production problems |
| 0 | R 366 | 57.60.1182 1 | рсе | 1k8 | MF, 1%, 0204, E24 | | · | | | | |
| 0 | R 400 | 57.60.1183 1 | рсе | 18k | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 401 | 57.60.1472 1 | рсе | 4k7 | MF, 1%, 0204, E24 | | | | | | |
| 0 | R 402 | 57.60.1332 1 | рсе | 3k3 | MF, 1%, 0204, E24 | | | | | | |







| | | | | | | | | | | - | | - |
|--------|-------|--------------|-------|-------------|--------------------------------|---------|---|--------------|--------------|-------|--------------|--------------------------------------|
| ldx | Pos. | Part No. | Qty. | Type/Val. | Description | ldx | | Pos. | Part No. | Qty. | Type/Val. | Description |
| 0 | C 1 | 59.60.2237 | | 33p | CER 50V, 5%, C0G, C603 | 0 | ł | MP 6 | 21.53.0353 | 1 pcs | M3*5 | Z-Schraube inbus Zn gb chr |
| 0 | C 2 | 59.60.2373 | | 1n0 | CER 50V, 5%, C0G, 0805 | 0 | 1 | MP 7 | 20.24.7280 | 6 pcs | | LIN-FORMSCH.ZNSW,KS D2.5* 8 |
| 0 | C 3 | 59.60.2237 | | 33p | CER 50V, 5%, COG, 0603 | 0 | 8 | P 1 | 54.14.2103 | | 20p | P STECKER 20 P.AU, VR, GERADE |
| 0 | C 4 | 59.68.0007 | | 220 100n | CER 50V 10% X7R 0805 | 0 | 5 | P 2 | 54.21.2203 | | Зр | XLR PCB Winkel |
| 0 | C 6 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | - | P 3 | 54.21.2021 | | BNC | BNC 1p, angle, PCB |
| 0 | C7 | 59.68.0067 | | 22u | EL 16V, 5.0*5.7 | 0 | - | P 4 | 54.21.2021 | | BNC | BNC 1p, angle, PCB |
| 0 | C 8 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | | P5 | 54.21.2021 | | BNC | BNC 1p, angle, PCB |
| 0 | C 9 | 59.60.3333 | | 47n | CER 50V, 10%, X7R, 0805 | 0 | | P 0 D 7 | 54.01.0020 | | 1p 1n | Pin, Treinig, gerade |
| 0 | C 10 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | | F / P 8 | 54.01.0020 | | 1p 1n | Pin, freihig, gerade |
| 0 | C 11 | 59.60.3441 | | 220n | CER 50V, 10%, X7R, 1206 | | | | 04.01.0020 | | 10 | r in, noing, gerade |
| 0 | C 12 | 59.60.3325 | | 10n | CER 50V, 10%, X7R, 0805 | 0 | - | R 1 | 57.60.1822 | | 8k2 | MF, 1%, 0204, E24 |
| 0 | 0.13 | 59.00.3337 | | 1000 | EL 46V 40*57 | 0 | F | R 2 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | C 14 | 59.00.0000 | | 100n | CER 50V 10% X7R 0805 | 0 | F | R 5 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| ő | C 16 | 59.60.3317 | | 2n2 | CER 50V. 10%, X7R, 0805 | 0 | 1 | R 6 | 57.80.1470 | | 47K | MF, 1%, 0204, E24 |
| 0 | C 17 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | | R / R 8 | 57.60.1470 | | 47R 47R | MF, 1%, 0204, E24 |
| 0 | C 18 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | Ē | R 9 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 19 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | F | R 10 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 20 | 59.68.0065 | | 10u | EL 16V, 4.0*5.7 | 0 | F | R 11 | 57.92.7013 | | 0.5A | PTC 60V |
| 0 | C 21 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | F | R 12 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | C 22 | 59.68.0065 | | 10u | EL 16V, 4.0*5.7 | 0 | F | R 13 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 |
| 0 | C 23 | 59.60.3337 | | 100n | CER 50V, 10%, X/R, 0805 | 0 | F | R 14 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | 0.24 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | F | R 15 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | 0.20 | 60 60 37/3 | | 330n | CER 50V, 10%, X7R 1812 | 0 | F | R 16 | 57.60.1222 | | 2k2 | MF, 1%, 0204, E24 |
| ő | C 27 | 59 60 3337 | | 100n | CER 50V. 10%, X7R, 0805 | 0 | ł | R 17 | 57.60.1682 | | 6688 | MF, 1%, 0204, E24 |
| ő | C 28 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | | R 15 P 10 | 57.60.1000 | | 1008 | MF, 0204 ME 194 0204 E24 |
| 0 | C 29 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | | R 19 R 20 | 57 60 1333 | | 332 | MF, 1%, 0204, E24 MF 1%, 0204 F24 |
| 0 | C 30 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | i | R 21 | not used | | 0R0 | MF. 0204 |
| 0 | C 31 | 59.60.3335 | | 68n | CER 50V, 10%, X7R, 0805 | 0 | F | R 22 | 57.60.1683 | | 68k | MF, 1%, 0204, E24 |
| 0 | C 32 | 59.68.0065 | | 10u | EL 16V, 4.0*5.7 | 0 | F | R 23 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | C 33 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, C805 | 0 | F | R 24 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | C 34 | 59.60.3323 | | 6n8 | CER 50V, 10%, X7R, C805 | 0 | 8 | R 25 | 57.60.1100 | | 10R | MF, 1%, 0204, E24 |
| 0 | C 35 | 59.68.0127 | | 1u0 | EL 50V, 4.0*5.7 | 0 | 5 | R 26 | 57.60.1221 | | 220R | MF, 1%, 0204, E24 |
| 0 | C 36 | 59.68.0127 | | 100 | EL 50V, 4.0-5.7 | 0 | 5 | R 27 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | 0.37 | 59.60.33337 | | 100n | CER 50V, 10%, X/R, 0005 | 0 | 8 | R 28 | 57.60.1272 | | 2k? | MF, 1%, 0204, E24 |
| 0 | 0.30 | 59.32.1221 | | 220p | CER 10% 400V | 0 | | R 29 | 57.60.1103 | | 10% | MF, 1%, 0204, E24 |
| ő | C 40 | 59 68 0065 | | 100 | EI 16V 40*5.7 | 0 | | R 30 | 57.60.1104 | | 104 | ME 1% 0204 E24 |
| õ | C 41 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | 1 | R 31 R 32 | 57.60.1000 | | ORD | MF 0204 |
| 0 | C 42 | 59.60.2249 | | 100p | CER 50V, 5%, COG, 0603 | ő | - | R 33 | 57.60.1472 | | 4k7 | MF, 1%, 0204, E24 |
| 0 | C 43 | 59.60.2365 | | 470p | CER 50V, 5%, COG, 0805 | ő | - | R 34 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | C 44 | 59.60.3743 | | 330n | CER 50V, 10%, X7R, 1812 | 0 | - | R 35 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | C 45 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | 1 | R 36 | 57.60.1101 | | 100R | MF, 1%, 0204, E24 |
| 0 | C 46 | 59.60.2249 | | 100p | CER 50V, 5%, COG, 0603 | 0 | 1 | R 37 | 57.60.1102 | | 1k0 | MF, 1%, 0204, E24 |
| 0 | C 47 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, 0805 | 0 | 1 | R 38 | not used | | 0R0 | MF, 0204 |
| 0 | C 48 | 59.60.3337 | | 100n | CER 50V, 10%, X7R, C805 | 0 | 1 | R 39 | 57.60.1274 | | 270k | MF, 1%, 0204, E24 |
| 0 | C 49 | 59.60.3325 | | 10n | CER 50V, 10%, X7R, 0805 | 0 | | R 40 | 57.60.1106 | | 10M | MF, 1%, 0204, E24 |
| 0 | C 50 | 59.60.3337 | | 100n | GER 50V, 10%, X/R, 0805 | 0 | 1 | K 41 | 57.60.1103 | | TUK | MF, 1%, 0204, E24 |
| 0 | D 1 | not used | | 4448 | 200mA 75V 4ns SOD 80 | 0 | | R 42 D 49 | 57.60.1084 | | 100k | ME 1% 0204 E24 |
| 0 | D 2 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | | R 43 R 44 | 57 60 1222 | | 2k2 | MF 1% 0204 E24 |
| 0 | D 3 | 50.60.8001 | | 4448 | 200mA 75V 4ns SOD 80 | 0 | i | R 45 | 57.60.1511 | | 510R | MF, 1%, 0204, E24 |
| 0 | 10.1 | 1 042 014 20 | | | SW 135 SYNC MODULE | ő | | R 46 | 57.60.1152 | | 1k5 | MF, 1%, 0204, E24 |
| | 101 | 1.092.019.20 | | (50160313.8 | 9C2051 | 0 | 1 | R 47 | 57.60.1470 | | 47R | MF, 1%, 0204, E24 |
| 0 | IC 2 | 50.62.1074 | | 74HC 74 | Dual D-type FF, preset clear | 0 | 1 | R 48 | 57.60.1104 | | 100k | MF, 1%, 0204, E24 |
| 0 | IC 3 | 50.62.1014 | | 74HC 14 | Hex Schmitt trigger inverter | 0 | 1 | R 49 | 57.60.1820 | | 82R | MF, 1%, 0204, E24 |
| 0 | IC 4 | 50.62.1163 | | 74HC163 | Synchr preset 4bit counter bin | 0 | | R 50 | 57.60.1000 | | ORO | MF, 0204 |
| 0 | IC 5 | 50.62.0913 | | CS8412 | AES-Receiver | 0 | 1 | R 51 | 57.60.1681 | | 680R | MF, 1%, 0204, E24 |
| 0 | IC 6 | 50.62.1074 | | 74HC 74 | Dual D-type FF, preset clear | 0 | 1 | K 52 | 57.60.1102 | | 1K0 | MF, 1%, 0204, E24 |
| 0 | IC 7 | 50.62.1153 | | 74HC153 | Juai 4ch multiplexer | Û | | rt 03 | 57.60,1820 | | 02K | WIF, 176, UZ04, EZ4 |
| 0 | 10.8 | 50.62.4946 | | /4HCT9046 | PLL with bandgap contr VCU | 0 | 1 | RA 1 | 58.60.0115 | | 2k0 | SMD 20%, 0.25W, Cermet |
| U O | 10 10 | 1.000.910.20 | | HC4053 | Svv (20 MOLOGRA (50.83.4202) | ~ | | | 1 000 647 00 | | 4.4.4 | OUTPUT TRAFO AFR/ERU |
| 0 | IC 11 | 89.01.1510 | | 19 200MH+ | VCXO Xtal-Oscillator voltage n | U | | | 1.022.047.00 | | 1.1.4 | UUTPUT TRAPU AES/EBU |
| n | IC 12 | 50.11 0145 | | LM1881 | C LM 1881 N. | 0 |) | XIC 1 | 53.03.0165 | | 20p | DIL 0.3", löt, gerade |
| 0 | IC 13 | 50.62.1163 | | 74HC163 | Synchr preset 4bit counter bin | 0 | 2 | XIC 9 | 53.03.2244 | | 44p | PLCC-Socket |
| 0 | IC 14 | 50.62.0463 | | DS34C86 | 4*RS 422 Line Receiver | n | , | XY 1 | 89.01 1499 | | | QUARZ - ISOLIERPLATTE |
| 0 | IC 15 | 50.61.0205 | | TS272CD | Dual Op-Amp CMOS SO 8 | 0 | | | | | | |
| 0 | IC 16 | 50.62.4946 | | 74HCT9046 | PLL with bandgap contr VCO | 0 | , | Y 1 | 89.01.1016 | | 22.1184MHz | XTAL HC 49/U |
| 0 | JP 1 | 54.01.0021 | 1 pce | Jumper | 0.63*0.63mm, Au | | | | | | — End of Lis | t |
| 0 | L 1 | 62.60.0125 | | 100uH | 10%, SMD 1210 | Co | m | ments: | | | | |
| 0 | L 2 | 62.60.0125 | | 100uH | 10%, SMD 1210 | <u></u> | | | | | | |
| 0 | L 3 | 62.60.0125 | | 100uH | 10%, SMD 1210 | | | | | | | |
| ~ | MD 1 | 1 040 106 11 | 1.000 | | SYNC MODULE RCB | | | | | | | |
| 0 | MP 2 | 43.01.0109 | 1 nce | Label | ESE-WARNSCHILD | | | | | | | |
| õ | MP 3 | 1.942.635.10 | 1 pce | | NR.ETIKETTE | | | | | | | |
| ő | MP 4 | 1.942.500.21 | 1 pce | | WINKEL. | | | | | | | |
| 0 | MP 5 | 24.16.2030 | 1 pcs | | FAECHERSCHEIBE A D 3.2 | | | | | | | |

Telephone Hybrid Interface 1.942.640.20



Telephone Hybrid Interface 1.942.640.20



RS 232 Interface 1.942.645.00



| | | | 1. A. | |
|-----------------|-------|--------|---|--------------|
| @ 12.05.1998 LC | 0 0 | | 0 | 0 |
| | | OnAir | | PAGE 1 DF 1 |
| STUDER | RS232 | Module | SC | 1.942.145.00 |

RS 232 Interface 1.942.645.00

| $\begin{bmatrix} P3 \end{bmatrix}$ |) C1) C2) C3) C4) C5) C6) C7) C8) C9) C10) C11) C12) C12 | 59.01.0104 59.21.5220 59.01.0104 59.21.8479 59.21.8479 59.21.8479 59.21.8479 59.21.8479 59.21.8479 59.21.8479 59.21.8479 59.21.8479 59.21.8479 | 10)n 22j 10)n 4uř 4uř 4uř 4uř 10)n | PETP, 63%, 10%, RM5 EL 25V 20% RM5 PETP, 63%, 10%, RM5 EL 50V 20% RM5 EL 50V 20% RM5 EL 50V 20% RM5 |
|---|--|--|---|--|
| $\begin{array}{c} \hline \\ \hline $ |) C6) C7) C8) C9) C10) C11) C12 | 59.21.8479 59.21.8479 59.01.0104 59.21.8479 59.34151 | 4u² 4u² 10)n | EL 50V 20% RM5 |
| $-\underline{R1} - \underline{R2} - \underline{C2} + C$ |) C 12 | 59.34.4151 59.04.0104 | 4u² 15)p 15)p 10)n | EL 30V 20% RM5 PETP, 63V, 10%, RM5 EL 50V 20% RM5 CER 63V,5%, N750 CER 63V,5%, N750 PETP, 63V, 10%, RM5 |
| |) IC1) IC2) IC3 | 59.04.0104 50.14.0108 50.14.0114 50.14.0120 | 100n 26_S31 9687 MAX232 | PETP, 63%, 10%, RM5 Quad diff lihe driver Dual diff Lihe Receiver IC MAX 212 CPE |
| |) MP1) MP2) MP3 | 1.942.45.12 1 pce 43.0.0108 1 pce 1.942.45.10 1 pce | Læel | RS 232 M0DULE PCB ESE-WAR√SCHILD NR.ETIKEITE |
| $ \begin{array}{ c c c c c } \hline \hline$ |) P1) P2) P3 | 54.13.0076 54.13.0071 54.14.2102 | 9p 9p 165 | D-Sub, PC3, Winkel D-Sub, PC3, Winkel P STECKER 16 P,AU,VR,GERADE |
| |) R1) R2) R3) R4) R5) R6) R7) R8 | 57.11.3103 57.92.7012 57.11.3103 57.11.3103 nkt used nkt used 57.11.3332 57.11.3332 | 10x 0.3A 10x 10x 0F0 0F0 3k3 150B | MF, 1%, 207 PTC 60 MF, 1%, 207 MF, 1%, 207 MF, 207 MF, 207 MF, 1%, 207 MF, 1%, 207 |
| | D R9 D R10 D R11 D R12 D R13 D R14 | 57.11.3000 57.11.3000 57.11.3332 57.11.3103 57.11.3103 57.11.3103 | 0F0 0F0 3K3 10k 10k | MF, (207 MF, (207 MF, 1%, (207 MF, 1%, (207 MF, 1%, (207 MF, 1%, (207 |
| | D R 15 D R 15 D R 16 D S 1 | net used net used 55.12.0003 | 0F0 0F0 0F0 273ST | MF, (207 MF, (207 S SCHIE3E.2*3U, PRINT |
| | 0 XIC 1 0 XIC 2 0 XIC 3 | 53.03.0168 53.03.0166 53.03.0168 | 16p 8r 16p | DIL 0.3", kt, gerade DIL 0.3", kt, gerade DIL 0.3", kt, gerade |
| P1 P2 | Comments: | | End of L | .ist |
| | | | | |
| | | | | |
| | | | | |
| STUDER No. 2000 F RS232 Module, ESE Z No. 2000 F No. 2000 F <td></td> <td></td> <td></td> <td></td> | | | | |

Time Sync Interface 1.942.650.22



Time Sync Interface 1.942.650.22



Time Sync Interface 1.942.650.22



TIME SYNC 1.942.650.22 (0)

| TI | ME | SYNC ' | 1.942.6 | 50.22 (0) |
|------|--------------|--------------------|-----------------|--|
| ldx. | Pos. | Part No. Qty. | Type/Val. | Description |
| | C 1 | 59 60 3337 | 1000 | CER 50V 10% X7R 0805 |
| 0 | C 2 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0005 |
| ő | C 3 | 59 60 3337 | 100n | CER 50V. 10%, X7R, 0805 |
| 0 | C 4 | 59.60.3337 | 100n | CER 50V. 10%, X7R, 0805 |
| 0 | C 5 | 59.68.0067 | 22u | EL 16V, 5.0*5.7 |
| 0 | C 6 | 59.68.0065 | 10u | EL 16V. 4.0*5.7 |
| 0 | C 7 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 8 | 59.68.0109 | 10u | EL 35V, 5.0*5.7 |
| 0 | C 9 | 59.60.2373 | 1n0 | CER 50V, 5%, C0G, 0805 |
| 0 | C 10 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| 0 | C 11 | 59.68.0109 | 10u | EL 35V, 5.0*5.7 |
| 0 | C 12 | 59.68.0109 | 10u | EL 35V, 5.0*5.7 |
| 0 | C 13 | 59.68.0109 | 10u | EL 35V, 5.0*5.7 |
| 0 | C 14 | 59.68.0109 | 10u | EL 35V, 5.0*5.7 |
| 0 | C 15 | 59.60.3321 | 4n7 | CER 50V, 10%, X/R, 0805 |
| 0 | C 16 | 59.60.3321 | 4n7 | CER 50V, 10%, X/R, 0805 |
| 0 | C 12 | 50.60.2365 | 170p | CEP 50V/ 10% X7P 0805 |
| 0 | C 10 | 59.60.2249 | 100n | CER 50V, 10%, X/X, 0003 |
| 0 | C 20 | 59.00.2249 | 100p | CER 50V, 5%, COG, 0003 |
| 0 | C 21 | 59.60.3333 | 47n | CER 50V, 10% X7R 0805 |
| 0 | C 22 | 59 60 2249 | 100n | CER 50V, 5%, COG 0603 |
| ŏ | C 23 | 59.60.2241 | 47p | CER 50V, 5%, C0G, 0603 |
| 0 | C 24 | 59.60.2237 | 33p | CER 50V, 5%, C0G, 0603 |
| 0 | C 25 | 59.60.2237 | 33p | CER 50V, 5%, C0G, 0603 |
| 0 | C 26 | 59.60.2237 | 33p | CER 50V, 5%, C0G, 0603 |
| 0 | C 27 | 59.60.3337 | 100n | CER 50V, 10%, X7R, 0805 |
| Ŭ | G 20 | 59.00.0107 | 4u7 | EL 35V, 4.0°5.7 |
| 0 | C 29 | 59.60.3743 | 330n | CER 50V, 10%, X7R, 1812 |
| 0 | IC 1 | 1.942.928.26 | | SW.150 TIME SYNC MODULE |
| 0 | IC 2 | 50.16.0201 | SCC2691 | IC SCC 2691 AE 1 N 24 ,A |
| 0 | IC 3 | 50.17.1573 | 74HC573 | 74 HC 573(A) |
| 0 | IC 4 | not used | 74HC541 | Octal bus buffer |
| 0 | 10.5 | 50.05.0283 | LM393 | Dual Comparator |
| 0 | 10.6 | 50.05.0283 | LM393 | |
| 0 | | 50.15.0127 | 34007 | IC DS 34 C 87 TN, MC34C67P ,A |
| 0 | | 50.15.0120 | 7440104 | |
| 0 | IC 10 | 50.17.1904 | 74HC004 9637 | Dual diff Line Receiver |
| ő | IC 11 | 50.09.0122 | TLC272 | Dual Op-Amp CMOS DIP 8 |
| o | IC 12 | 50.09.0122 | TLC272 | Dual Op-Amp CMOS DIP 8 |
| 0 | MP 1 | 1.942.150.11 1 pce | | TIME SYNC PCB |
| 0 | MP 2 | 43.01.0108 1 pce | Label | ESE-WARNSCHILD |
| 0 | MP 3 | 1.042.650.10 1 pce | | NR ETIKETTE |
| 0 | MP 4 | 1.101.001.20 1 pce | Label | TEXT-ETIK. 5*20 HARDWARE -20 |
| 0 | MP 9 | 54.01.0021 4 pcs | Jumper | 0.63*0.63mm, Au |
| 0 | P 1 | 54.14.2103 | 20p | 1/20" Au, gerade, Verrieg |
| 0 | P 2 | 54.13.0076 | 9p | D-Sub, PCB, Winkel |
| 0 | P 3 | 54.13.0076 | 9p | D-Sub, PCB, Winkel |
| 0 | P 4 | 54.11.0136 | 2*3p | Pin 0.63*0.63, RM2.54 |
| 0 | P 10 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | P 11 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | P 12 | 54.01.0020 | 1p | Pin, 1reihig, gerade |
| 0 | P 13 | 54.01.0020 | 1p aka | Pin, Treinig, gerade |
| 0 | R 1 | 57.60.1332 | 3K3 47D | MF, 1%, 0204, E24 |
| 0 | R 2 D 2 | 57.60.1470 | 47R 47P | MF, 1%, 0204, E24 |
| 0 | R 4 | 57 60 1470 | 47R | MF 1% 0204 E24 |
| 0 | R 5 | 57.60.1470 | 47R | MF, 1%, 0204, E24 |
| ō | R6 | 57.92.7013 | 0.5A | PTC 60V |
| 0 | R 7 | 57.60.1104 | 100k | MF, 1%, 0204, E24 |
| 0 | R 8 | 57,60.1822 | 8k2 | MF, 1%, 0204, E24 |
| 0 | R 9 | 57.60.1472 | 4k7 | MF, 1%, 0204, E24 |
| 0 | R 10 | 57.60.1184 | 180k | MF, 1%, 0204, E24 |
| 0 | R 11 | 57.60.1473 | 47k | MF, 1%, 0204, E24 |
| 0 | R 12 | 57.60.1272 | 2k7 | MF, 1%, 0204, E24 |
| 0 | R 13 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 14 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 15 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 16 | not used | 0R0 | MF, 0204 |
| 0 | R 17 | not used | 0R0 | MF, 0204 |
| 0 | к 18 П 40 | 57.60.1103 | 10K | MF, 1%, U2U4, E24 |
| 0 | K 19 | 57.00.1105 | 1M 10k | WF, 1%, U2U4, E24 |
| 0 | R 20 | 57.00.1103 | | IVIF, 1%, U2U4, E24 |
| U | R 21 | 57.60.1000 | 080 | WF, U∠U4 |
| 0 | R 23 | 57 60 1471 | 470R | wir-, 170, 0∠04, E∠4 MF 1% 0204 E24 |
| 0 | R 24 | 57.60.1000 | 0R0 | MF. 0204 |
| ō | R 25 | 57.60.1104 | 100k | MF, 1%, 0204. E24 |
| ō | R 26 | 57.60.1153 | 15k | MF, 1%, 0204. E24 |
| ō | R 27 | 57.60.1333 | 33k | MF, 1%, 0204, E24 |
| 0 | R 28 | 57.60.1333 | 33k | MF, 1%, 0204, E24 |
| 0 | R 29 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 30 | 57.60.1103 | 10k | MF, 1%, 0204, E24 |
| 0 | R 31 | 57.60.1332 | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 32 | 57.60.1151 | 150R | MF, 1%, 0204, E24 |

| ldx. | Pos. | Part No. | Qty. | Type/Val. | Description |
|------|--------|--------------|------|------------|---------------------------|
| 0 | R 33 | 57.60.1332 | | 3k3 | MF, 1%, 0204, E24 |
| 0 | R 34 | not used | | 0R0 | MF, 0204 |
| 0 | R 35 | not used | | 0R0 | MF, 0204 |
| 0 | R 36 | 57.60.1821 | | 820R | MF, 1%, 0204, E24 |
| 0 | R 37 | 57.60.1563 | | 56k | MF, 1%, 0204, E24 |
| 0 | R 38 | 57.60.1103 | | 10k | MF, 1%, 0204, E24 |
| 0 | RZ 1 | 57.88.4103 | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 | RZ 2 | not used | | 10k | 8*R Resistor-Netw 2% SIP9 |
| 0 | S 1 | 55.01.0168 | | 8*a | DIL-Switch, PCB |
| 0 | S 2 | not used | | 8*a | DIL-Switch, PCB |
| 0 | T 1 | 1.022.459.00 | | | AUSGANGSTRAFO 1:1 |
| 0 | XIC I | 53.03.017Z | | 40µ | DIL 0.0", Iül, yerade |
| 0 | XIC 7 | 53.03.0168 | | 16p | DIL 0.3", löt, gerade |
| 0 | XIC 8 | 53.03.0168 | | 16p | DIL 0.3", löt, gerade |
| 0 | XIC 10 | 53.03.0166 | | 8p | DIL 0.3", löt, gerade |
| 0 | XY 1 | 89.01.1499 | | | QUARZ - ISOLIERPLATTE |
| 0 | XY 2 | 89.01.1499 | | | QUARZ - ISOLIERPLATTE |
| 0 | Y 1 | 89.01.1002 | | 3.6864MHz | XTAL HC 18 U |
| 0 | Y 2 | 89.01.1016 | | 22.1184MHz | XTAL HC 49/U |
| | | | | | |

----- End of List -----

(26) SW update IC1

Page: 1 of 1







1.942.803 2 OF C 1 ; 3 : 3 : ■ 3 3 . - P8 PAGE Ъ M-90 _ EQUIPMENT WITH START/STOP PULJE REMOTE CONTROL f 0 ۱ İ JUMPER POSITIONS I CONTROL INTERFACE APPLICATION 2 FADER START WITH START / STOP PULSE SART . 8 SOP 뮹 Ĵ D25-M L 5 1 L (1) 21.1.99 SB P1.4 CTRL_IN_A CTRL_IN_B k2.1 ° k2.1 ° k2.2 ° k1.1 s k1.1 s k1.1 s ON AIR k1.2 o P2.8 P2.21 P2.20 P2.18 P2.6 P2.5 P2.7 P2.9 P1.8 STUDEF RECENSDORF SWITZERLAND 14.10.98 SB H 20 20 ٢ PCB 1942.802.11 942803 Ĺ 1 Ð Ł 0 2<u></u>{ ŝ 1 010 020 030 ŝ ¥., ₹Ĭ 1 MONOSTABLE I IDATIZONO İ I £ CONTROL INTERFACE 1.942.803 24 5 030-I I mm 15 1 ^2-10-1 10-1 10-1 I +5 ₹ 25 ZZZ +5V L Н ____ ZZ ZZ +5V +5< Ì 0 (m / q6d P1.1 P1.5 P1.3 P1.3 P1.2 P1.9 P2.17 P2.14 P2.15 P2.16 P2.2 P2.3 P2.4 CTRL_OUT_1A CTRL_OUT_1B CTRL_OUT_2A CTRL_OUT_2B 0 45V_SUPPLY 6ND 6ND GND OFF_IN -- NI_NO OFF_IN + EXT_CTRL_IN + EXT_CTRL_IN -+ NI~NO Ē 0 126 Ε < 25p ſ INPUT MODULE CTRL CONNECTOR D9-M 0 I ON AR CTRL OUT 14 -CTRL OUT 18 -+5V SUPPLY GND Í I I I

Control Interface Box 1.942.803 (Option)

STUDER



Control Interface Box 1.942.803 (Option)

PAGE 4 0F 4 1.942.803 _ 1 2 3 4 **3 4** M-90 E ::s \square E 0 1:3 •::: P3,P7 . . . - CTRL IN B C CTRL IN A 12 ••• I I ON AIR 2000 INPUT MODULE CTRL CONNECTOR D9-M 1 D25-M JUMPER POSITIONS R. I CONTROL INTERFACE APPLICATION F EXTERNAL ON SWITCH 0 IJ Y BS Y (1) 21.1.99 CTRL_IN_B CTRL_IN_A P2.18 k1.1 o P2.6 k1.1 r P2.5 k1.1 d P2.7 k1.2 o k2.1 c k2.1 c k2.1 c ON AIR P2.8 P2.21 P2.20 P2.9 F1.4 P1.8 STUDER RECENSDORF SWITZERLAND н 6.11.98 SB PCB 1:342.802.11 942803 <u>م</u> م Ð 0 5 030 - B İ Ó ł °. 8 9d 9_0 9-0~9 P4 P4 1 I 1 1 Π 65 mm I 1 I ^\$+ 1 CONTROL INTERFACE 1.942.803 20 5 5 5 5 I I I I H mm 15 İ L İ Ì tsv þþþþ 1 25 +5V -**─**-**I** ≩ J-↓ İ ---Ì 25 <u>+5</u>∨ I 1 + 0 L 0 p9p/m н P1.1 P1.5 P1.3 P1.3 P2.17 P2.17 P1.2 P1.9 P2.3 EXT_CTRL_IN + P2.2 P2.14 P2.15 P2.4 P2.16 CTRL_OUT_1A CTRL_OUT_1B CTRL_OUT_2A CTRL_OUT_2B +5V_SUPPLY GND 45V_SUPPLY GND 1 + NI_NO OFF_IN + OFF_IN -- NI_NO EXT_CTRL_IN 26 Ε ν < 25p ĕ₽Ĕ ¢ INPUT MODULE CTRL CONNECTOR D9-M 0 I ON AIR +5V SUPPLY ---CTRL OUT 2A -CTRL OUT 2B -I I 1

I

I

Control Interface Box 1.942.803 (Option)



Control Interface 1.942.802.00 (Option)


Control Interface 1.942.802.00 (Option)

| | ldx. | Pos. | Part No. Qty. | Type/Val. | Description |
|--|---|---|--|---|--|
| | | | not used | | |
| | | | and sound | 100- | DETD 631/ 109/ DME |
| | 0 | 01 | not used | 100n | PETP, 63V, 10%, RM5 |
| | 0 | 03 | not upod | 100n | PETP, 63V, 10%, RM5 |
| | 0 | C 4 | 59.06.0104 | 100n | PETP, 63V, 10%, RM5 |
| | 0 | C 5 | 59,06.0104 | 100n | PETP, 63V, 10%, RM5 |
| | 0 | C 6 | 59.22.3470 | 47u | EL 10V, 20%, RM5 |
| | 0 | C7 | 59.06.0104 | 100n | PETP, 63V, 10%, RM5 |
| | 0 | C 8 | 59.06.0104 59.06.0105 | 100n - 1u0 | PETP, 50V, 10%, RM5 |
| | 0 | C 10 | 59.06.0105 | 100 | PETP, 50V, 10%, RM5 |
| | 0 | C 11 | 59.06.0103 | 10n | PETP, 63V, 10%, RM5 |
| \sim $(3)(3) - (22)$ | | | | | |
| | 0 | D1 | 50.04.0125 | 1N4448 | 75V, 150mA, 4ns, DO-35 75V, 150mA, 4ns, DO-35 |
| | 0 | 02 | 50.04.0125 | 1114-10 | 101, 10010 (110, 20100 |
| P3 $C5$ $P5$ | 0 | IC 1 | 50.99.0126 | 4N26 | Optocoupler |
| | 0 | IC 2 | 50.99.0126 | 4N26 | Optocoupler |
| | 0 | IC 3 | 50.99.0126 | 4N26 | Optocoupler |
| (R10) + P4 | 0 | 10 4 | 50.17.1000 | 74HG00 74HC123 | IC 74 HC 00 ., ,A |
| | Ų | 10 5 | 50.17.1125 | 14110120 | 10 / HO 120 ., ,A |
| | 0 | J 5 | 54.01.0021 7 pcs | Jumper | 0.63 * 0.63mm |
| | ٥ | K 1 | 56.04.0198 | 2u | 5V 125V 2A Aq/Au |
| | 0 | К 2 | 56.04.0198 | 2u | 5V 125V 2A Ag/Au |
| | 0 | MP 1 | 1 942 802 11 1 009 | | CONTROLL INTERFACE POR |
| | õ | MP 2 | 1.942.802.10 1 pce | | NRETIKETTE |
| | õ | MP 3 | 43.01.0108 1 pce | Label | ESE-WARNSCHILD |
| 4.2 0 0 0 0 0 0 0 0 | | | | | |
| | 0 | P 1 | 54.13.0076 | 9p | D-Sub, PCB, Winkel |
| | 0 | P2 | 54.13.0078 | 25p | D-Sub, PCB, Winkel |
| | 0 | P3 | 54.11.0136 | 2 op 2*3n | Pin 0.63*0.63, RM2.54 |
| | , u o | P4 P3 | 54.11.0130 | 2.0p 2*0p | Pin 0.03*0.03, RM2.54 |
| | 0 | P 6 | 54.11.0136 | 2*3p | Pin 0.63*0.63, RM2.54 |
| | 0 | P 7 | 54.11.0136 | 2*3p | Pin 0.63*0.63, RM2.54 |
| R | 0 | P 8 | 54.01.0020 | 1p | Pin 0.63*0.63 |
| | 0 | P 9 | 54.01.0020 | 1p | Pin 0.63*0.63 |
| MP 1 | n | P 10 | 54 01 0020 | 1p | Pin 0.63*0.63 |
| | 0 | Q 1 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN |
| | 0 | Q 2 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN |
| | 0 | Q3 | 50.03.0340 | BC337-25 | 800mA, 45V, NPN |
| ε | 0 | Q4 | 50.03.0340 | BC337-25 | SUUMA, 45V, NPN |
| | 0 | Q 5 | 50.03,0340 | BC337-25 | 00011A, 45V, NPN |
| | 0 | R 1 | 57.11.3152 | 1k5 | MF. 1%, 0207 |
| 5 8 10 6 00 00 AE 0 | Ő | R2 | 57.11.3152 | 1k5 | MF, 1%, 0207 |
| S S 20.00. 30 AF 100 | 0 | R 3 | 57.11.3152 | 1k5 | MF, 1%, 0207 |
| ui ≪ Datum Gez. Cepr. Ges. Index Copy to: | 0 | R 4 | 57.11.3272 | 2k7 | MF, 1%, 0207 |
| Kopie fuer: | 0 | R 5 | 57.11.3272 | 2k7 | MF, 1%, 0207 |
| | 0 | R 6 | 57.11.3272 | 2k7 | MF, 1%, 0207 |
| RECENSOORF BE CONTINUE INTENTAGE BE 1.942.002.00 | 0 | К7 В 9 | 57.11.3103 | 10K | WF, 1%, U2U7 |
| | 0 | Ra | 57 11 3000 | ORO | ME, 120, 0207 ME, 0207 |
| | 0 | R 10 | 57.92,7011 | 0.24 | POLY- PTC, 60V |
| | ō | R 11 | 57.11.3472 | 4k7 | MF, 1%, 0207 |
| | 0 | R 12 | 57.11.3220 | 22R | MF, 1%, 0207 |
| | 0 | R 13 | 57.11.3474 | 470k | MF, 1%, 0207 |
| | | | | 10k | MF, 1%, 0207 |
| | 0 | R 14 | 57.11.3103 | 1011 | |
| | 0 | R 14 R 15 | 57.11.3103 57.11.3103 | 10k | MF, 1%, 0207 |
| | 0 | R 14 R 15 R 16 R 17 | 57.11.3103 57.11.3103 57.11.3474 57.11.3472 | 10k 470k | MF, 1%,0207 MF, 1%,0207 MF, 1%,0207 |
| | 0000 | R 14 R 15 R 16 R 17 R 18 | 57.11.3103 57.11.3103 57.11.3474 57.11.3472 57.11.3472 | 10k 470k 4k7 4k7 | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 |
| | 0 0 0 | R 14 R 15 R 16 R 17 R 18 R 19 | 57.11.3103 57.11.3103 57.11.3474 57.11.3472 57.11.3472 57.11.3472 57.11.3472 | 10k 470k 4k7 4k7 4k7 | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 |
| | 0 0 0 | R 14 R 15 R 16 R 17 R 18 R 19 R 20 | 57.11.3103 57.11.3103 57.11.3474 57.11.3472 57.11.3472 57.11.3472 57.11.3472 | 10k 470k 4k7 4k7 4k7 4k7 4k7 | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 |
| | 000000000000000000000000000000000000000 | R 14 R 15 R 16 R 17 R 18 R 19 R 20 R 21 | 57.11.3103 57.11.3103 57.11.3474 57.11.3472 57.11.3472 57.11.3472 57.11.3472 57.11.3472 57.11.3472 | 10k 470k 4k7 4k7 4k7 4k7 4k7 10k | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 |
| | 000000000000000000000000000000000000000 | R 14 R 15 R 16 R 17 R 18 R 19 R 20 R 21 R 22 | 57,11,3103 57,11,3474 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3103 57,11,3103 | 10k 470k 4k7 4k7 4k7 4k7 4k7 10k 10k | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 |
| | | R 14 R 15 R 16 R 17 R 18 R 19 R 20 R 21 R 22 R 22 R 23 | 57.11.3103 57.11.3103 57.11.3474 57.11.3472 57.11.3472 57.11.3472 57.11.3472 57.11.3472 57.11.3103 57.11.3103 57.11.3103 | 10k 470k 4k7 4k7 4k7 4k7 10k 10k 10k | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 |
| | | R 14 R 15 R 16 R 17 R 18 R 19 R 20 R 21 R 22 R 23 XIC 1 | 57,11,3103 57,11,3474 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3103 57,11,3103 57,11,3103 53,03,0164 | 10k 470k 4k7 4k7 4k7 4k7 4k7 10k 10k 10k 10k | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 DIL 0.3", löt, perade |
| | | R 14 R 15 R 16 R 17 R 18 R 19 R 20 R 21 R 22 R 23 XIC 1 XIC 2 | 57,11,3103 57,11,3474 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3103 57,11,3103 57,11,3103 53,03,0164 53,03,0164 | 10k 470k 4k7 4k7 4k7 4k7 10k 10k 10k 6p 6p | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 DIL 0.3", löt, gerade DIL 0.3", löt, gerade |
| | | R 14 R 15 R 16 R 17 R 19 R 20 R 21 R 22 R 23 XIC 1 XIC 2 XIC 3 | 57.11.3103 57.11.3103 57.11.3474 57.11.3472 57.11.3472 57.11.3472 57.11.3472 57.11.3472 57.11.3403 57.11.3103 57.11.3103 53.03.0164 53.03.0164 53.03.0164 | 10k 470k 4k7 4k7 4k7 4k7 10k 10k 10k 6p 6p | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 DIL 0.3", löt, gerade DIL 0.3", löt, gerade DIL 0.3", löt, gerade |
| | | R 14 R 15 R 15 R 17 R 18 R 19 R 20 R 21 R 22 R 23 XIC 1 XIC 2 XIC 3 | 57.11.3103 57.11.3103 57.11.3474 57.11.3472 57.11.3472 57.11.3472 57.11.3472 57.11.3472 57.11.3403 57.11.3103 57.11.3103 53.03.0164 53.03.0164 | 10k 470k 4k7 4k7 4k7 4k7 4k7 10k 10k 10k 6p 6p 6p | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 DIL 0.3", löt, gerade DIL 0.3", löt, gerade |
| | | R 14 R 15 R 15 R 17 R 18 R 19 R 20 R 21 R 22 R 23 XIC 1 XIC 2 XIC 3 | 57,11,3103 57,11,3103 57,11,3474 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3472 57,11,3403 57,11,3103 57,11,3103 53,03,0164 53,03,0164 | 10k 470k 4k7 4k7 4k7 4k7 4k7 10k 10k 10k 6p 6p 6p 6p | MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 MF, 1%, 0207 DIL 0.3", löt, gerade DIL 0.3", löt, gerade |