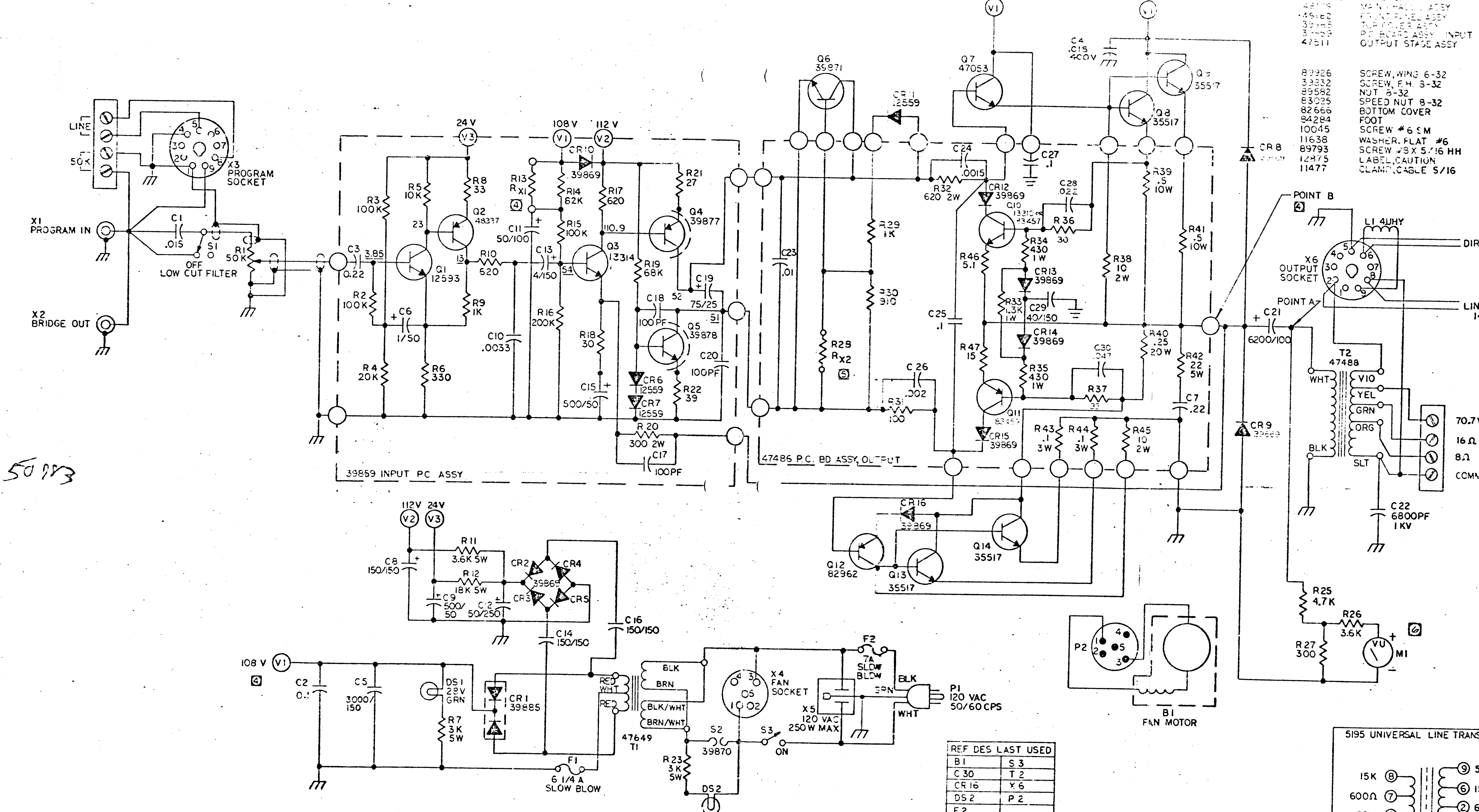

48179	MAIN CHASSIS ASSY
49182	FRONT PANEL ASSY
39555	TOP COVER ASSY
39559	P.C. BOARD ASSY INPUT
47511	OUTPUT STAGE ASSY
89926	SCREW, WING 6-32
33332	SCREW, F.H. 8-32
89582	NUT 8-32
83025	SPEED NUT 8-32
82666	BOTTOM COVER
84284	FOOT
10045	SCREW #6 SM
11638	WASHER, FLAT #6
89793	SCREW #8 X 5/16 HH
12875	LABEL, CAUTION
11477	CLAMP, CABLE 5/16

REF DES	LAST USED
B1	S3
C30	T2
CR16	X6
DS2	P2
F2	
L1	
M1	
Q14	
R47	

12 SEE DWG 48217 REF FOR TEST SPECIFICATIONS.
11 APPROVED ENG SAMPLE, MODEL 6015B, CURRENT REVISION, MAINTAINED IN ENG. DEPT.
10 APPLY ZINC OXIDE THERMAL COMPOUND BETWEEN OUTPUT TRANSISTORS (Q7-Q12) AND INSULATOR, AND INSULATOR AND HEAT SINK.
9 ALL GROUND SYMBOLS ARE PHYSICALLY CONNECTED TO ONE COMMON CHASSIS POINT.
8 DIRECT OUT 4Ω: REMOVE WHT PRIMARY WIRE OF T2 AT POINT A, AND WITH 18 AWG JUMPER CONNECT POINT A TO PIN 9 OF X6.
7 XX IS DC VOLTAGE WITH VTVM REFERRED TO CHASSIS GROUND WITH NO SIGNAL.
6 AT 120W OUT VU METER TO READ ZERO.
5 ADJUST R_{X2} FOR 21.5 MV MIN, 41MV MAX ACROSS 0.25Ω COLLECTOR RESISTOR AT THE OUTPUT TRANSISTOR. VOM LEADS MUST BOTH FLOAT FROM CHASSIS GND POTENTIAL.
4 ADJUST R_{X1} FOR 1/2 OF (V1) AT POINT B.
3 JBL RESERVES THE RIGHT TO MAKE MINOR COMPONENT CHANGES WITHOUT NOTICE.
2 CAPACITORS IN MICROFARADS. THOSE OVER 1.0 UFD ARE POLARIZED ELECTROLYTICS, POLARITY SHOWN.
1 RESISTORS IN OHMS 1/2 WATT 5% EXCEPT AS NOTED.
NOTES: UNLESS OTHERWISE SPECIFIED

ECN	LET.	CHANGE	DATE	BY
[illegible]	E	PER E.O.	11-72	[illegible]
[illegible]	D	PER E.O.	10-72	[illegible]
[illegible]	C	PER E.O.	[illegible]	[illegible]
[illegible]	B	[illegible]	5-72	[illegible]
8133	A	REL FOR PROD		

DRAWN 9-24-71 CHECK 9/24/71 APPR 10-12-71

6015B

MFD ITEM - 60

SIZE D MFD ITEM DWG DRAWING NUMBER 48185 SHEET 1 OF