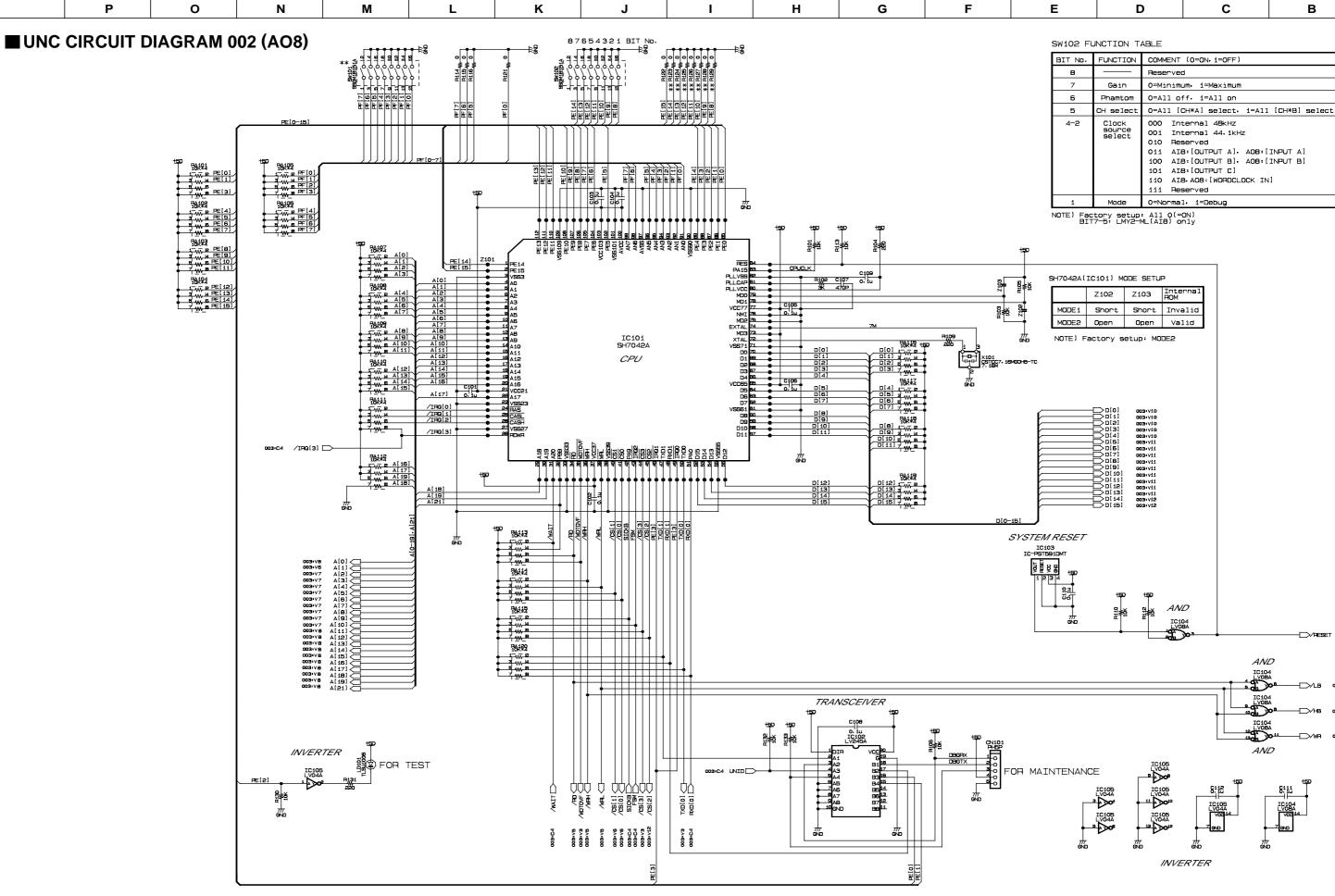
ANALOG OUTPUT BOX AOS CIRCUIT DIAGRAM

CONTENTS

UNC	· ·
MB1	5
IFC3	9
IPC2	15
IPC4	16
I FD2	17
AOCOM ······	18



Components having special characteristics are marked \triangle and must be replaced with parts having specification equal to those originally installed.

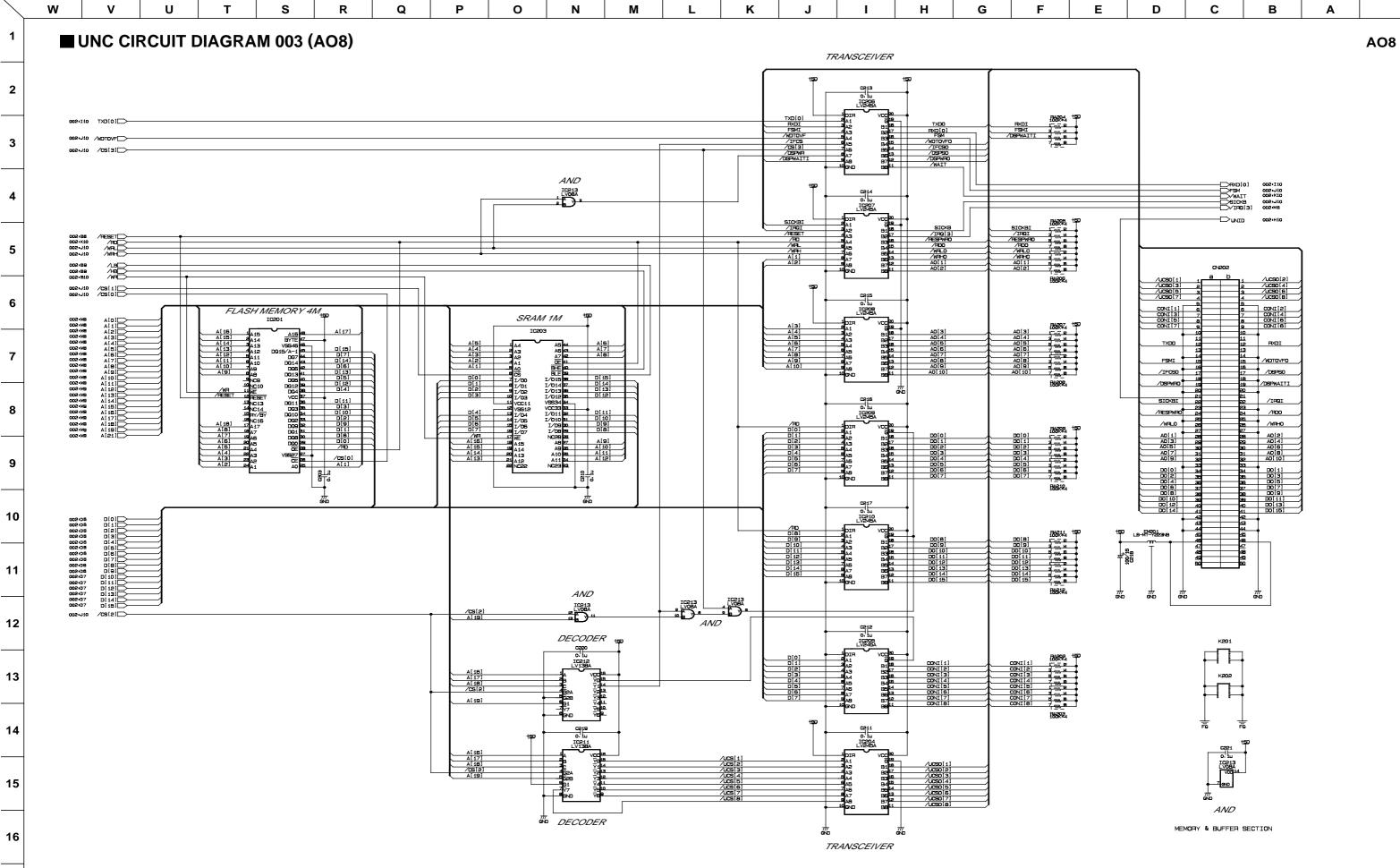


■ UNC CIRCUIT DIAGRAM 002 (AO8)

BIT No.	FUNCTION	COMMENT (0=0N, 1=0FF)						
8		Reserved						
7	Gain	O=Minimum, 1=Maximum						
6	Phamtom	O=All off, i=All on						
5	CH select	O=All [CH*A] select, 1=All [CH*B] select						
4-2	Clock source select	000 Internal 48kHz 001 Internal 44.1kHz 010 Reserved 011 AIB:[OUTPUT A]. A08:[INPUT A] 100 AIB:[OUTPUT B]. A08:[INPUT B] 101 AIB:[OUTPUT C] 110 AIB:A08:[WORDCLOCK IN] 111 Reserved						
1	Mode	0=Normal, 1=Debug						

AO8

Α



KEC-92504-3

4

17

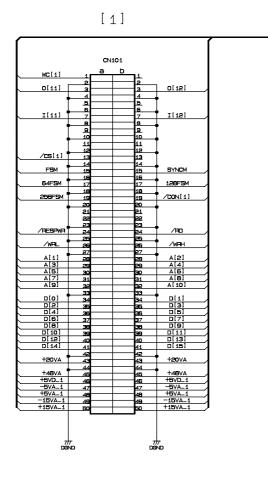
UNC CIRCUIT DIAGRAM 003 (AO8)

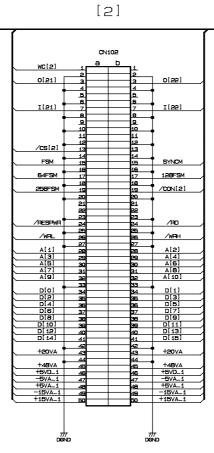
Р	О	N	м	L	к	J	I	н	G	F	Е
					•	•					

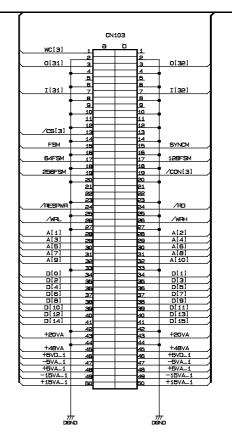
■ MB1 CIRCUIT DIAGRAM 002 (AO8)

SLOT No.

[3]







AO8

Α

1

2

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9

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11

12

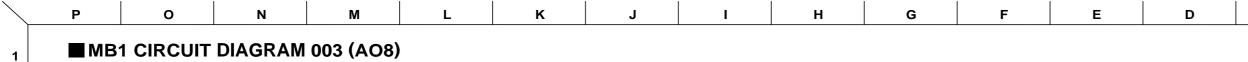
В

		005 N2
	-DI[21] -DI[22]	005+N2 005+N2
·	-DI[31] -DI[32]	005+N2 005+N2
		005+N4 005+N4
		005+N5 005+N5
		005+N5 005+N5
	CS[1]	004+CB 004+CB 004+C9
		004+C9 004+C9 004+C9
	-Dwc[1] -Dwc[2] -Dwc[3]	005:NB 005:NB 005:NB
	55M 5YNCM 64F5M 256F5M 7256F5M 7859WR 770 7859WR 780 780 780 780 780 780 780 780 780 780	003+05+004+04+005+N7 003+05+004+04+005+N7 003+05+004+04+005+N7 003+05+004+04+005+N7 003+05+004+04+005+N7 003+05+004+04+005+N7 003+05+004+04+005+N7 003+07+004+05+005+N7
	AI11 AI21 AI31 AI41 AI51 AI61 AI61 AI61 AI61 AI91 AI91 AI10	003+C7+004+C8+005+N3 003+C7+004+C8+005+N3 003+C7+004+C8+005+N3 003+C7+004+C8+005+N3 003+C7+004+C8+005+N30 003+C7+004+C8+005+N30 003+C7+004+C8+005+N30 003+C7+004+C8 003+C7+004+C8
	0101 0111 0112 0112 0112 0112 0112 0112	003 029 004 07 1005 N10 003 029 004 07 1005 N11 003 029 004 029 005 N11
	+48VA +20VA	
·	+5VD-1 -5VA-1 +5VA-1	
	-15¥A-1 +15¥A-1	

С

D

■ MB1 CIRCUIT DIAGRAM 002 (AO8)



SLOT No.

2

3

4

5

6

7

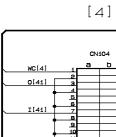
8

9

10

11

12



/cs[4]

FSM

64FSM

256FSM

/RESPWR

/WRL

D[0] D[2] D[4] D[6] D[8] D[8] D[10] D[12] D[14]

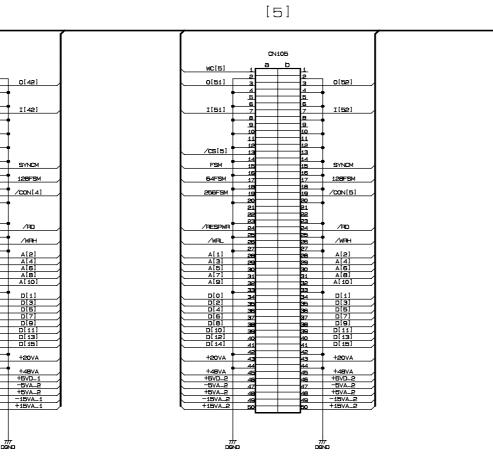
+20VA

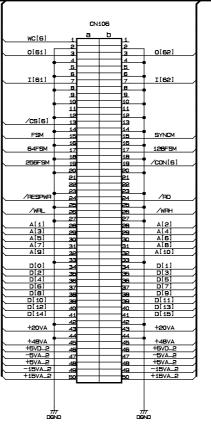
+48VA 5VD_1 5VA_2

-15VA_1 +15VA_1

7/7 DGND

, ™ DGND





[6]

С

в

Α

AO8

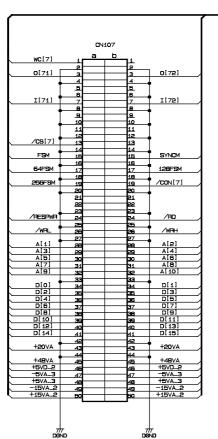
	DI[41] DI[42]	005 IN3 005 IN3
	DI [54] DI [52]	005 IN3
	DI[61] DI[62]	005 • N3 005 • N3
		005 : N5
		005 : N5 005 : N5
		005 : N5 005 : N5
	CS[4]	004+CB 004+CB 004+CB
		004+C9 004+C9 004+C9
		005+N9 005+N9 005+N8
	SYNCM SYNCM 64FSM 256FSM 256FSM (PESPWR (NRL WRL (WRL	002+05+004+04+005+N7 002+05+004+04+005+N7 002+05+004+024+005+N7 002+05+004+024+005+N7 002+05+004+024+005+N7 002+07+004+024+005+N7 002+07+004+024+005+N7 002+07+004+024+005+N7
	A[1] A[2] A[3] A[4] A[5] A[5] A[6] A[7] A[8] A[9] A[10]	002:071004:05:005:NB 002:071004:05:005:NB 002:071004:05:005:NB 002:071004:05:005:NB 002:071004:05:005:NB 002:071004:05:005:NI 002:071004:05:005:NI 002:071004:05:005:NI 002:05:004:05 002:05:004:05
	0101 0111 0111 0111 0111 0111 0111 011	002:0010040710051140 002:0010040710051140 002:0010040710051140 002:0010040710051140 002:0010040710051140 002:0010040710051140 002:0010040710051140 002:0010040710051141 002:0010040710051141 002:0010040510051141 002:0010040510051141
	+201A +5/12-1	
-	+ <u>5vp</u> _2	
	-5 <u>va</u> _2 +5 <u>va</u> _2	
	-15YA-2	
+	-15YA-1	
	15YA_2	
+	15YA_2	

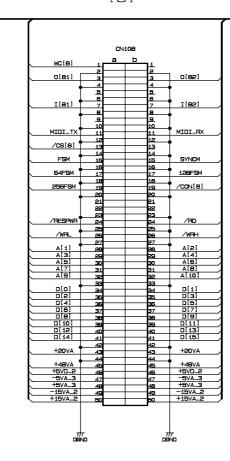
Р	0	N	м	L	к	J		н	G	F	E

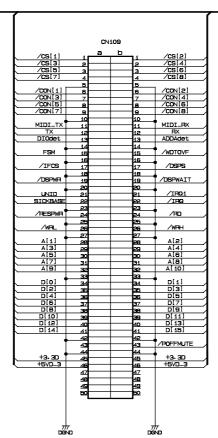
■ MB1 CIRCUIT DIAGRAM 004 (AO8)

SLOT No.





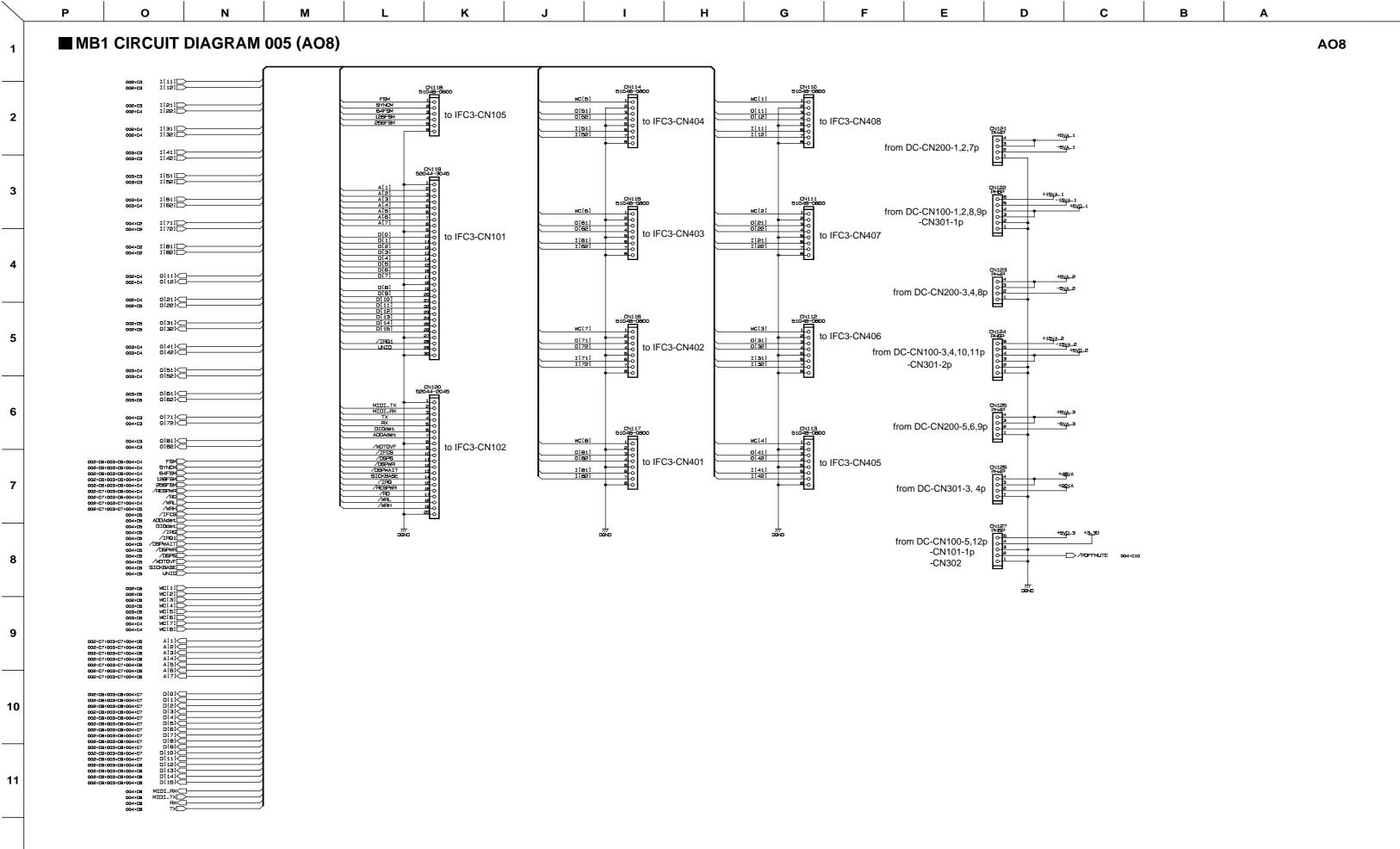




E	D	с	В	A	
				AO8	1
			005+NS 005+N4 005+N4 005+N4		2
			005+145 005-145 005-145 005-147		3
			4 002:05:003:05:005:N7 M 002:07:003:05:005:N7 002:07:003:05:005:N7 002:07:003:07:005:N7		4
			002-071003-0710051N7 1 0051N8 0051N8 0051N8 0051N8 0051N8 1 0051N8 0051N8 0051N8		5
		Al11 Al21 Al31 Al41 Al41 Al41 Al61 Al61 Al61 Al61 Al61 Al61 Al61 Al6	002:127:003:127:005:149 002:127:003:127:005:149 002:127:003:127:005:149 002:127:003:127:005:149 002:127:003:127:005:140 002:127:003:127:005:140 002:127:003:127:005:140 002:127:003:127 002:105:103:127		6
			022-0310210310310051110 002-0310231031051110 002-0310231031051110 002-0310231031051110 002-0310231031051110 002-031021051111 002-031021051111 002-031021051111 002-031021051111 002-031021051111 002-031021051111		7
			X 005-N11 005-N11 005-N11 002-C5 002-C5		8
			003-05 003-05 003-05 21 002-05 31 002-05 41 003-05		9
		+2017 +48174 			10
		د <u>مره-</u> د <u>مرها-</u> 2 م <u>رها-</u> 2 مړ ها + 08 ₄ 8+			11

■ MB1 CIRCUIT DIAGRAM 004 (AO8)

7



8

12

■ MB1 CIRCUIT DIAGRAM 005 (AO8)