

ANALOG OUTPUT BOX


AO 8

CIRCUIT DIAGRAM

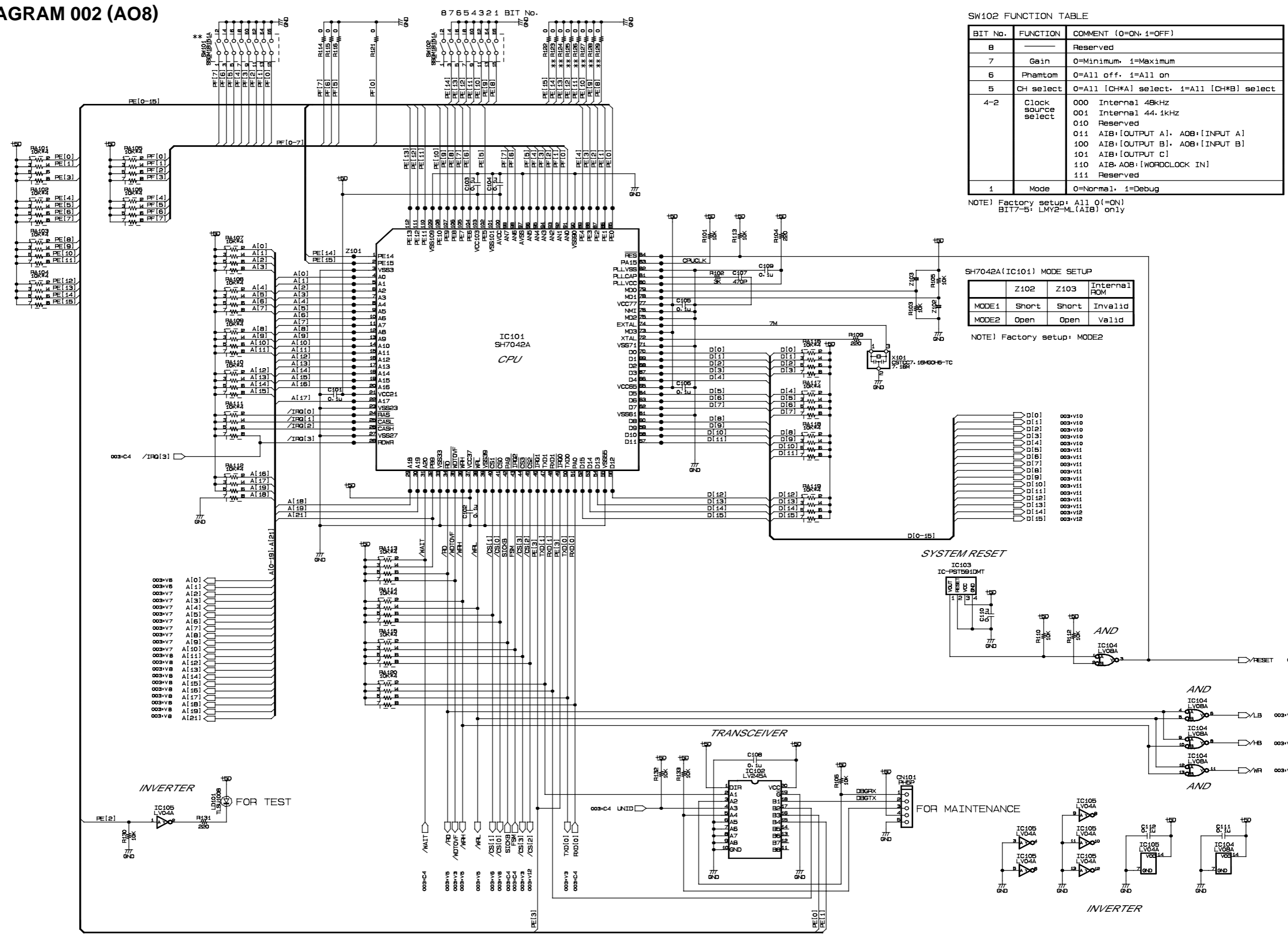
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■ WARNING

Components having special characteristics are marked  and must be replaced with parts having specification equal to those originally installed.

UNC CIRCUIT DIAGRAM 002 (AO8)



SW102 FUNCTION TABLE

BIT No.	FUNCTION	COMMENT (0=ON, 1=OFF)
8	Reserved	
7	Gain	0=Minimum, 1=Maximum
6	Phantom	0=All off, 1=All on
5	CH select	0=All [CH*A] select, 1=All [CH*B] select
4-2	Clock source select	000 Internal 48kHz 001 Internal 44.1kHz 010 Reserved 011 A1B:[OUTPUT A], A0B:[INPUT A] 100 A1B:[OUTPUT B], A0B:[INPUT B] 101 A1B:[OUTPUT C] 110 A1B:A0B:[WORDCLOCK IN] 111 Reserved
1	Mode	0=Normal, 1=Debug

NOTE) Factory setup: All 0 (=ON)
BIT7-5: LMY2-ML(A1B) only

SH7042A(IC101) MODE SETUP

	Z102	Z103	Internal ROM
MODE1	Short	Short	Invalid
MODE2	Open	Open	Valid

NOTE) Factory setup: MODE2

MB1 CIRCUIT DIAGRAM 002 (AO8)

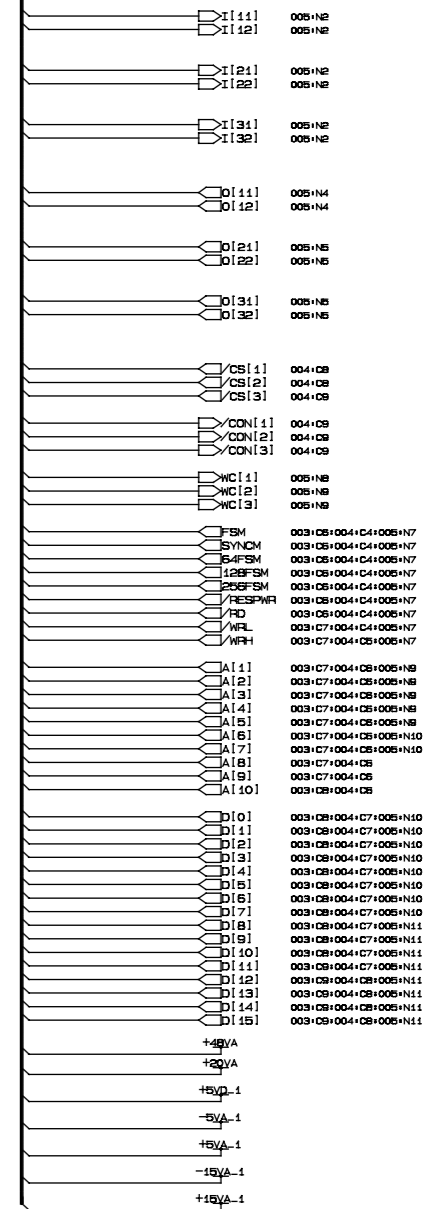
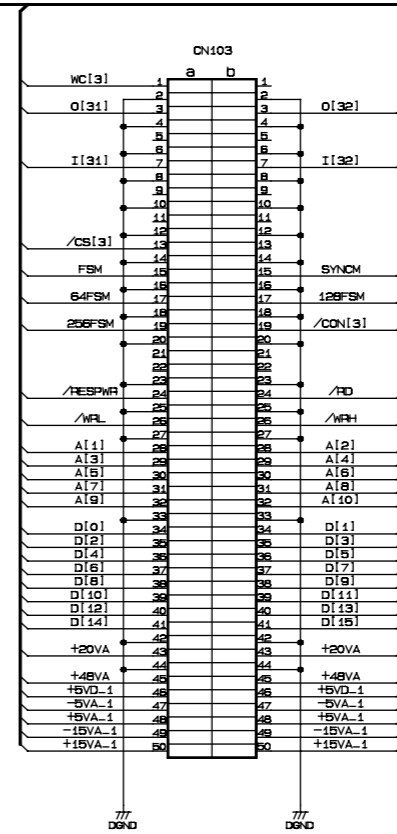
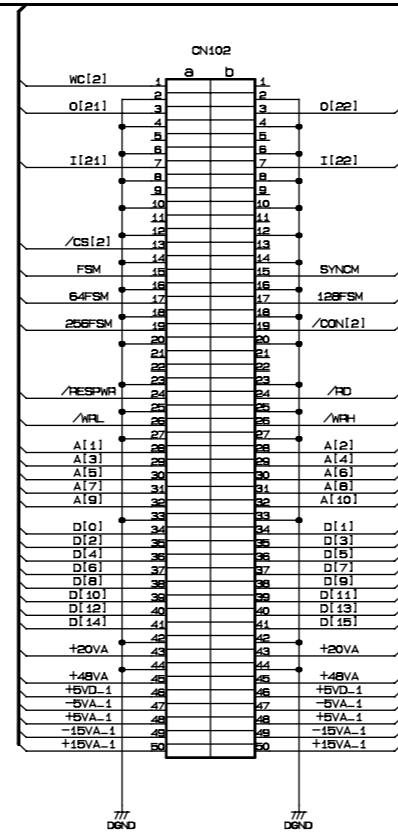
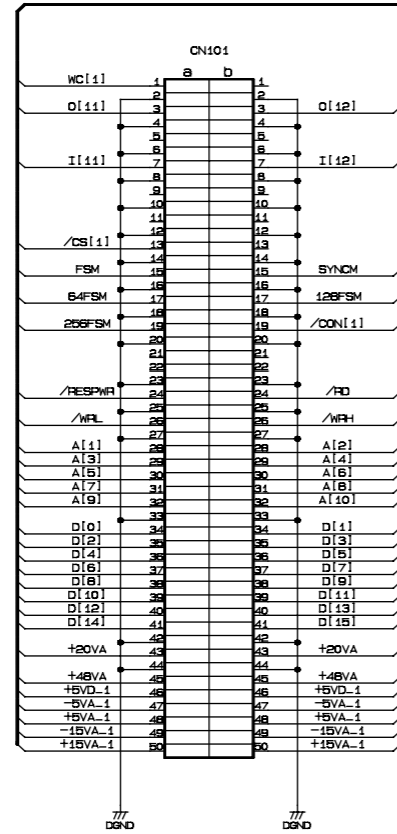
AO8

SLOT No.

[1]

[2]

[3]



MB1 CIRCUIT DIAGRAM 003 (AO8)

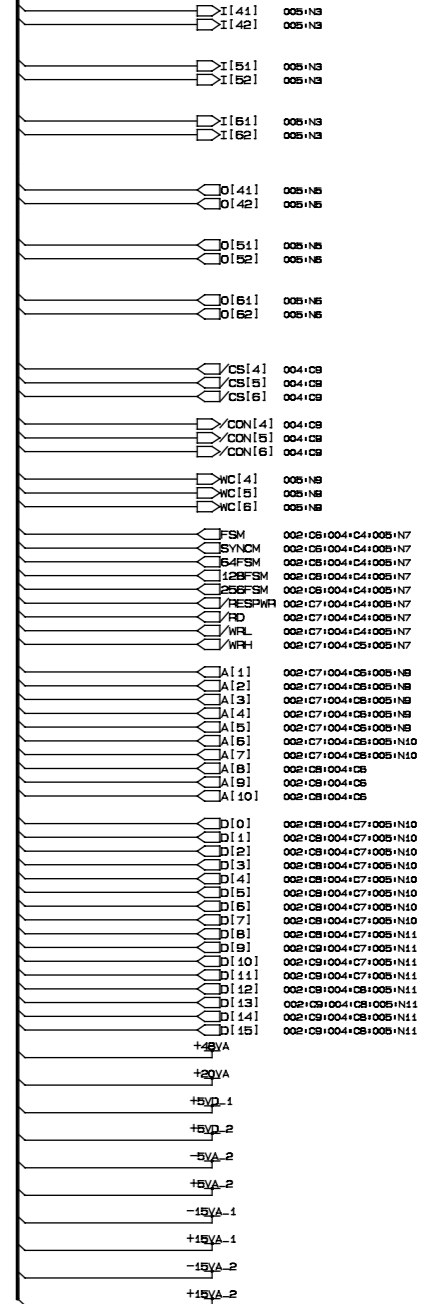
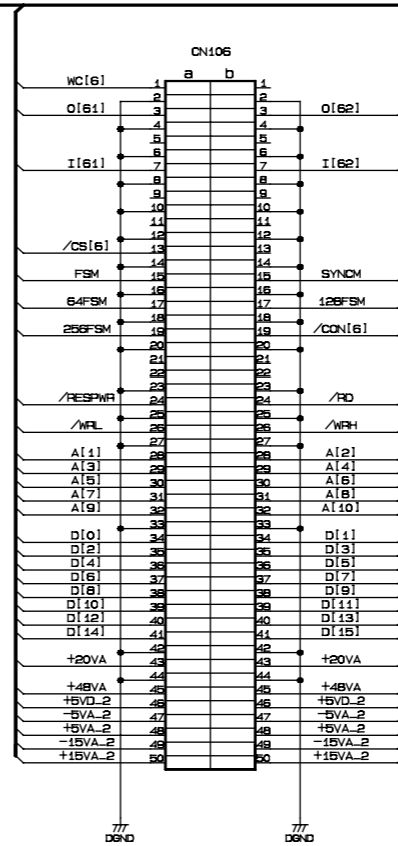
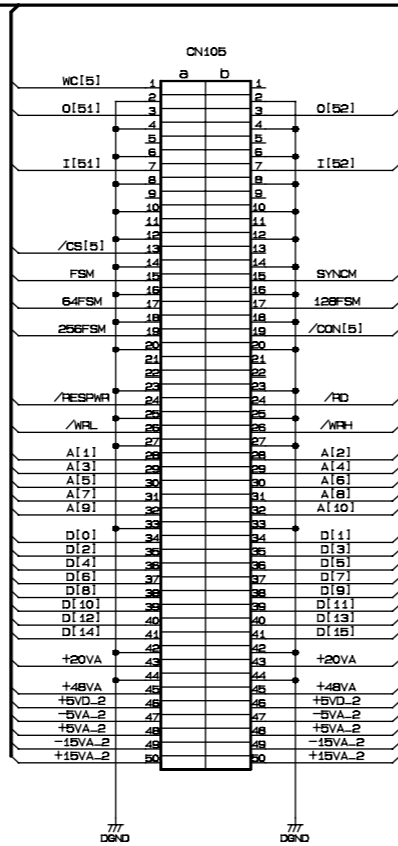
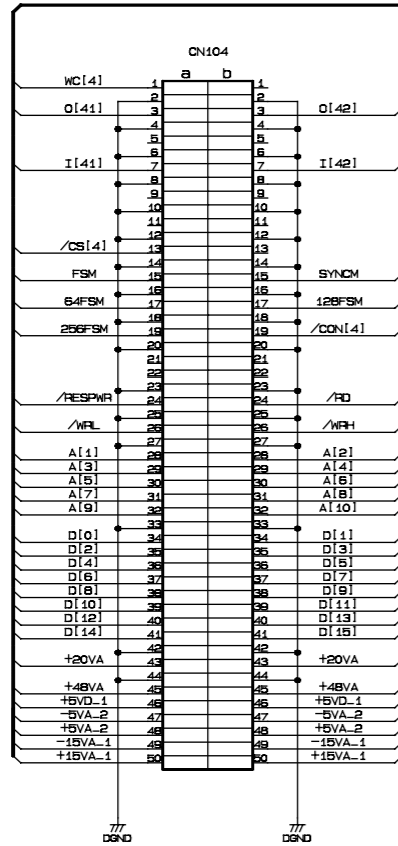
AO8

SLOT No.

[4]

[5]

[6]



MB1 CIRCUIT DIAGRAM 004 (AO8)

AO8

SLOT No.

[7]

[8]

