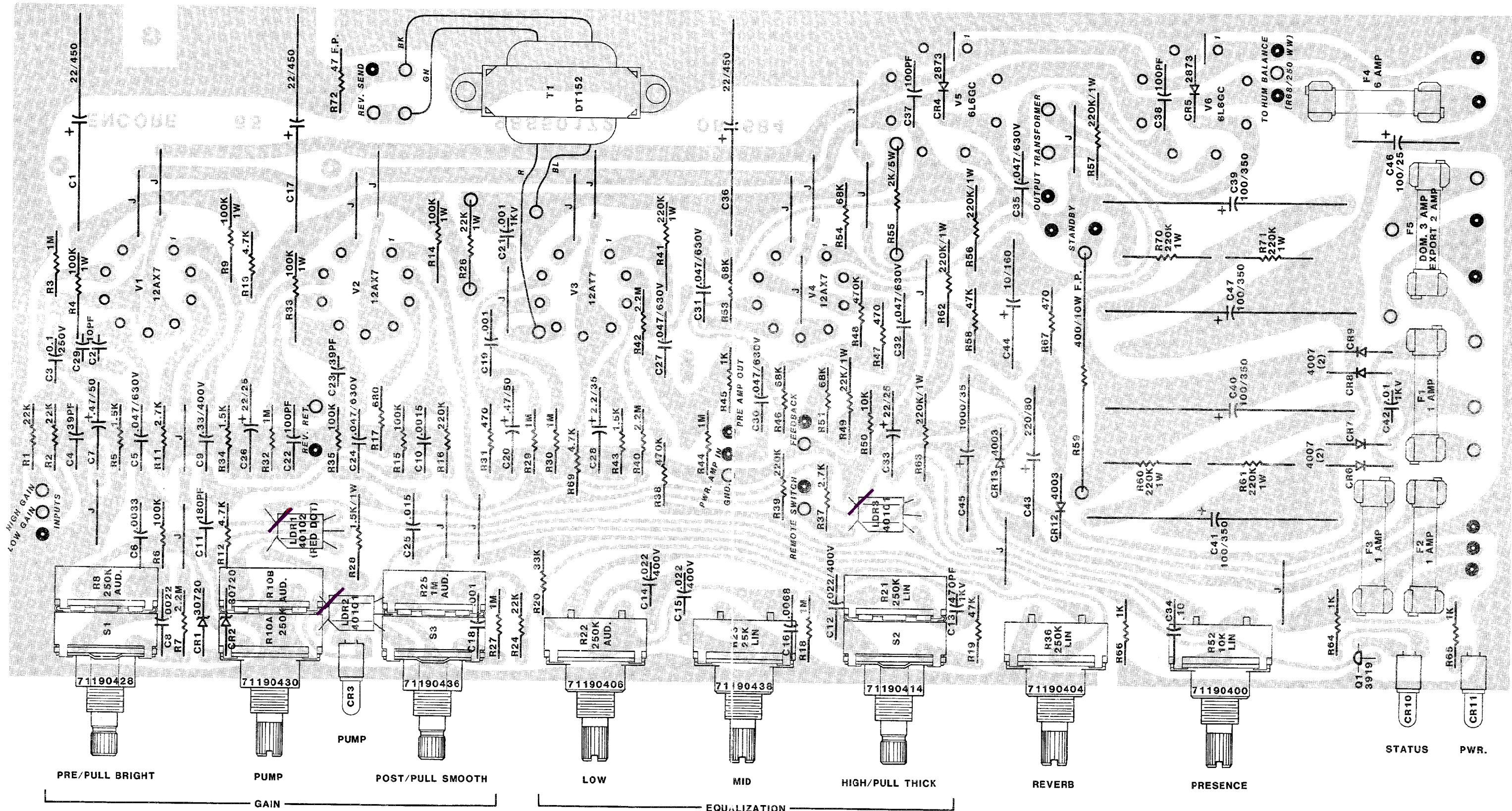


NOTES  
 ALL POTS ARE LINEAR UNLESS NOTED  
 ALL RESISTORS 1/4W UNLESS NOTED  
 ALL RESISTORS IN OHMS UNLESS NOTED  
 ALL CAPACITORS IN MFD UNLESS NOTED  
 ALL DIODES IN4148 UNLESS NOTED  
 LAST REF. DESIGNATOR USED:  
 R72 C49 CR13 S6 J6 Q1 F5 LDR3

**ENCORE 65**  
 BD. # 98850171 (10/3/83)  
 BD. # 98850172 (8/16/84)

REVISED 9/12/84 <i>REJ</i>	
PEAVEY ELECTRONICS CORP. MERIDIAN, MS.	
DRAWN BY <i>REJ</i> DATE 10/26/83	
C.B. CK. <i>REJ</i>	
CK. BY <i>REJ</i>	
APPR. BY <i>REJ</i>	



\*Please Note: The LDR's are drawn incorrectly on the layout. The flats should be positioned away from the pots as per the added lines. The schematic itself is correct.

ENCORE 65