



Adresse	PH	PH3	PH2	PH1	PH0	PA	PA3	PA2	PA1	PA0	DEC	H
ROM 1	1	1	1	1	1	X	X	X	X	X	3000 - 3FFF	
ROM 2	1	1	0	0	0	X	X	X	X	X	6000 - 6FFF	
ROM 3	1	0	1	0	0	X	X	X	X	X	5000 - 5FFF	139A
ROM 4	1	0	0	0	0	X	X	X	X	X	4000 - 4FFF	
RAM 1	0	0	0	0	1	X	X	X	X	X	0800 - 0BFF	
RAM 2	0	0	0	1	0	X	X	X	X	X	1000 - 13FF	
RAM 3	0	0	1	0	0	X	X	X	X	X	1500 - 1BFF	156
RAM 4	0	1	0	0	0	X	X	X	X	X	2000 - 23FF	
RAM 5	0	1	0	1	0	X	X	X	X	X	2500 - 2BFF	
MONO	0	1	0	0	0	X	X	X	X	X	3000 - 37FF	156
I/O	0	1	1	1	1	1	1	1	1	1	3E00 - 3FFF	
PIA	0	1	1	1	1	0	0	0	0	0	3C00 - 3DFF	
GPIA	0	1	1	1	0	1	1	1	1	1	3A00 - 3BFF	139B
RESET	0	1	1	1	0	0	0	0	0	0	3800 - 39FF	

**ADRET ELECTRONIQUE**

CE DOCUMENT NE PEUT ETRE COMMUNIQUE EN REPRODUCTION SANS AUTORISATION

CPU		CPU	
DATE: 15/04/82	ETUDE: DESB	VERSION: JR	J970275080000

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