

SPECIFICATIONS

1. ELECTRICAL: PNP SILICON TRANSISTOR, 2N2904 FAMILY OR EQUIV.

- A. $H_{FE} @ V_{CE} = 10V$ AT $I_C = 50mA$ 120 MIN.
 $I_C = 100\mu A$. 100 MIN.
- B. BV_{CEO} $I_C = 1mA$ 50V MIN.
- C. BV_{EBO} $I_E = 10mA$ 4.5V MIN.
- D. I_{CBO} AT $V_{CB} = 50V @ 100^\circ C$ $10\mu A$. MAX.
- E. V_{CE} (SAT) AT $I_C = 50mA @ 25^\circ C$,
 $I_B = 10mA$.5V MAX.
- F. ALL OTHER SPECIFICATIONS TO BE THOSE OF BASIC FAMILY.

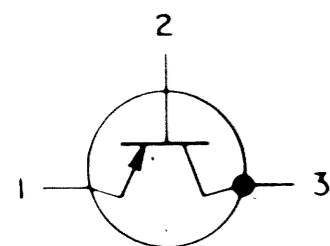
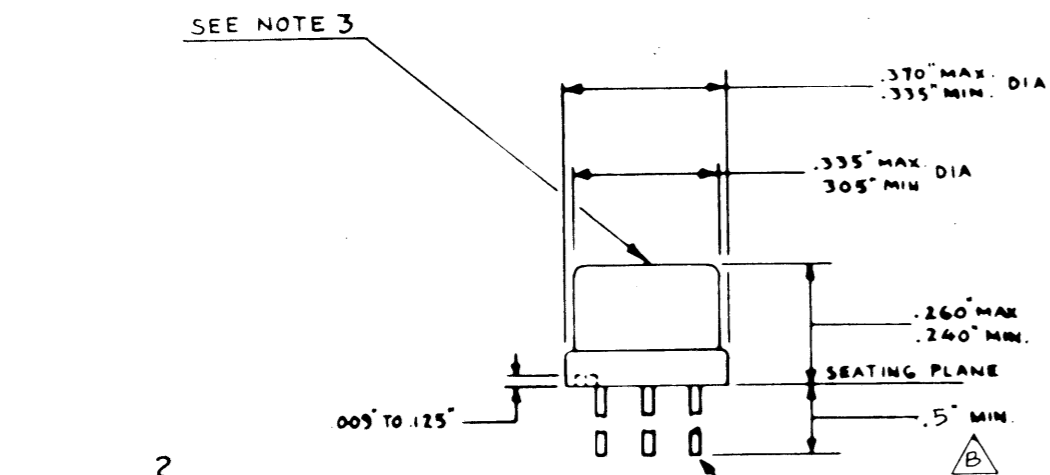


FIG. 2

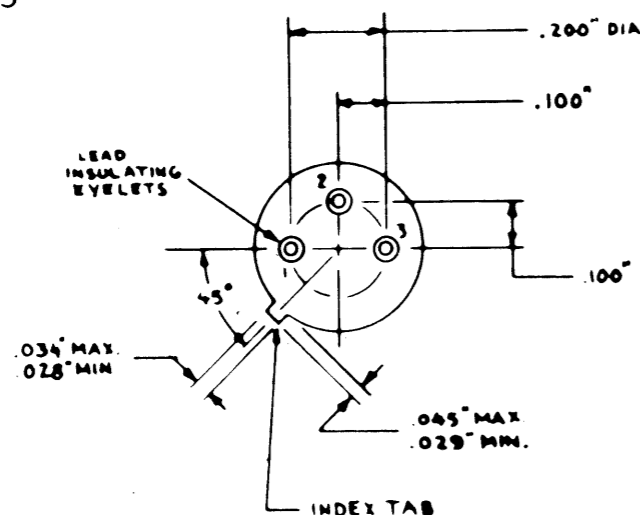


FIG. 1

2. MECHANICAL: OUTLINE JEDEC No TO-5 (FIG.1)

3. MARKING: STAMP TOP OF CASE AS FOLLOWS:

FBN-L118

4. MANUFACTURERS CODE: MOTOROLA #SS606

NATIONAL SEMICONDUCTOR # NS 63393

5. LEADS: HOT SOLDER DIPPED OR ELECTRO TIN PLATED

6. To protect against damage due to electrostatic discharge, these units must be manufactured, handled and shipped in accordance with DOD-STD-1686.

ISSUE *as*

A	ORIGINAL	<i>BS</i>	<i>11/11/68</i>
B	LEAD LENGTH WAS 1.5 MIN ECO 3003	<i>BS</i>	<i>10/8/73</i>
C	ADDED NAT'L SEMI-CONDUCTOR AS VENDOR PER ECO 3049	<i>BS</i>	<i>1/9/74</i>
D	ADDED NOTE 6 PER ECO A02568	<i>2H</i>	<i>3-4-81</i>
E	ADDED TO NOTE 5 PER ECO A02568	<i>2H</i>	<i>10-28-88</i>

USED ON		DWG.	
APPL'S			
DR. <i>W.E.B.</i>	CH	ENG.	
MATERIAL:			
FINISH:			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND TOLERANCES ARE: DEC 2 000; FRACTIONS 2 1/64 ANGLES 2 1/8°			
TITLE:			
TRANSISTOR			
SCALE: NONE			
LAMBDA ELECTRONICS CORP.			
MELVILLE		NEW YORK	

DWG. SIZE	LAMBDA PART NO. & DWG. NO.	REV.
B	FBN-L118	E