

SPECIFICATIONS

1. ELECTRICAL: NPN SILICON TRANSISTOR

A. RATINGS @ 25°C CASE TEMPERATURE

$V_{ce0}$  \_\_\_\_\_ 400V.  
 $V_{ceX}$  @  $V_{be} = -4V$  \_\_\_\_\_ 450V.  
 $V_{ebo}$  \_\_\_\_\_ 7V.  
 $I_c$  PEAK \_\_\_\_\_ 30A.  
 $I_c$  CONTINUOUS \_\_\_\_\_ 15A.  
 $I_b$  \_\_\_\_\_ 5A.  
 $P_d$  \_\_\_\_\_ 150W.

JUNCTION TEMP: OPERATING \_\_\_\_\_ -10 TO +150°C. [B]  
 STORAGE \_\_\_\_\_ -65 TO +200°C.

THERMAL RESISTANCE:  $\theta_{JC}$  \_\_\_\_\_ 0.9°C/W

B.  $I_{cer}$  @  $V_{ce} = 500V$ ,  $R_{be} = 50\Omega$ ,  $T_A = 125°C$  \_\_\_\_\_ 2MA MAX.

C.  $I_{ebo}$  @  $V_{eb} = 7V$ ,  $T_A = 25°C$ ,  $I_c = 0$  \_\_\_\_\_ 10 $\mu$ A MAX.

D.  $V_{be}$  (SAT) @  $I_c = 10A$ ,  $I_b = 2A$ ,  $T_A = 125°C$  \_\_\_\_\_ 1.5V MAX.

E.  $V_{ce}$  (SAT) @  $I_c = 10A$ ,  $I_b = 2A$ ,  $T_A = 125°C$  \_\_\_\_\_ 1.5V MAX.

F.  $H_{fe}$  @  $I_c = 10A$ ,  $V_{ce} = 3V$ ,  $0°C \leq T_j \leq +150°C$  \_\_\_\_\_ 9 MIN.

G. SWITCHING TIMES WITH  $I_c = 10A$ : [B]

$I_{b1} = 1.0A$ ,  $I_{b2} = 1.2A$ ,  $T_j = 140°C$ .

1.  $T_s$  STORAGE TIME (NON-SATURATING) \_\_\_\_\_ 2.0 $\mu$ SEC.

2.  $T_f$  FALL TIME \_\_\_\_\_ 1.5 $\mu$ SEC.

3.  $T_r$  RISE TIME \_\_\_\_\_ 2.0 $\mu$ SEC.

2. MECHANICAL: OUTLINE - JEDEC NO. TO-3 (FIG. 1).

3. MARKING: STAMP TOP OF CASE AS FOLLOWS: - FBN-L200

4. MANUFACTURER: NEC 25C1871A

5. LEADS: HOT SOLDER DIPPED (NOTE MAX. LEAD LENGTH)

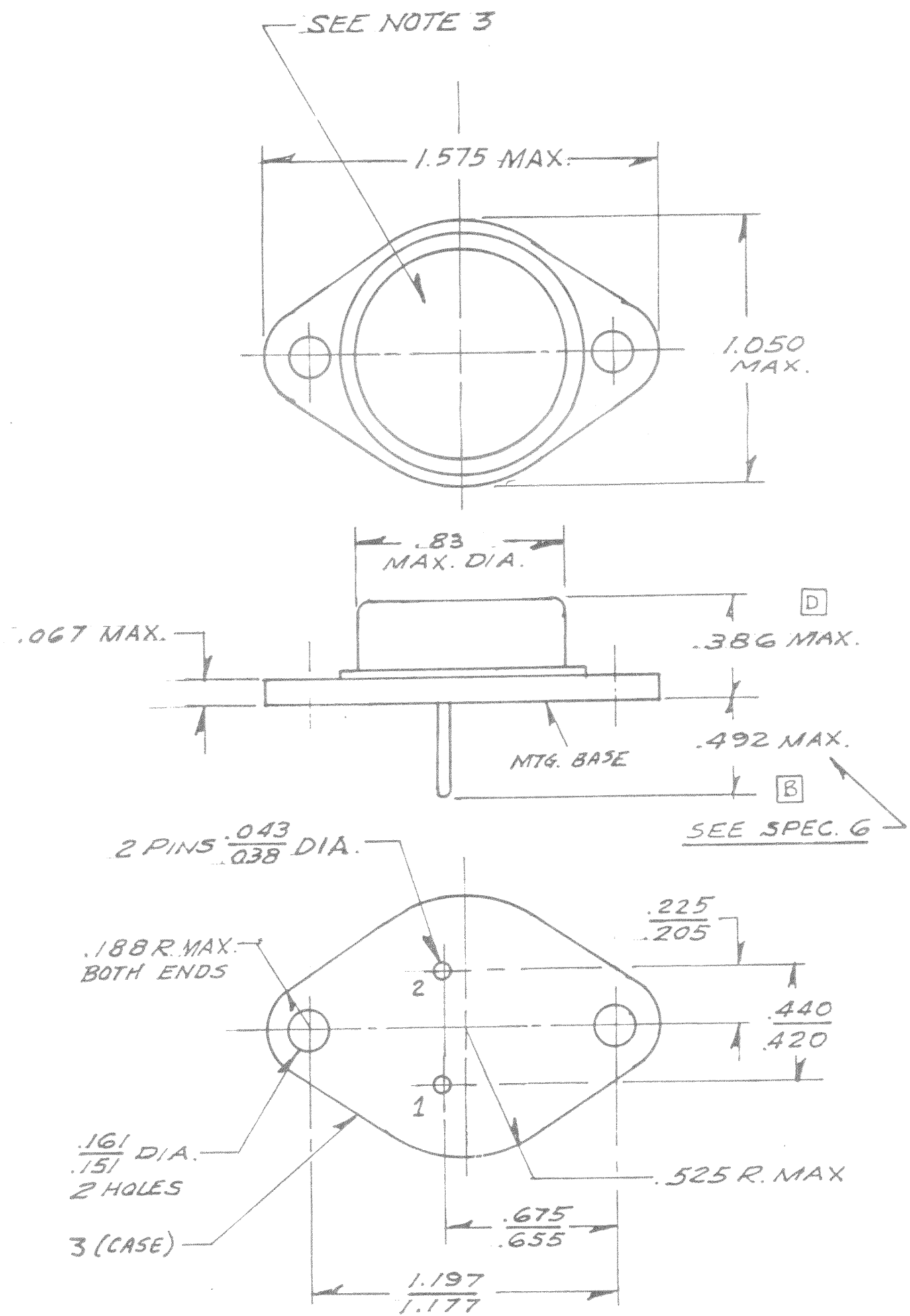
6. LAMBDA TO TRIM LEADS TO .420/.400

NOTES:

1. INCOMING INSPECTION ROUTE TO SUBASSEMBLY DEPT. FOR LEAD-TRIM PRIOR TO PLACING INTO STOCK.

2. To protect against damage due to electrostatic discharge, these units must be manufactured, handled and shipped in accordance with DOD-STD-1686.

REVISIONS			
SYM.	DESCRIPTION	DATE	APPROVAL
A	ORIGINAL	2/25/78	S.F.
B	-10 TO -150°C WAS -10 TO 200°C 9 MIN. WAS 10 MIN. ADDED NOTE TO TRIM LEAD LENGTH AT LAMBDA	10/6/78	S.F.
C	ADDED NOTE #1 (LEAD-TRIM) PER ENGINEERING CHANGE	5/29/79	I.C.S
D	DELETED .326/.308 ADDED .386 MAX. ECO# 5032 P.C. MoB	7/17/81	C.M
E	REDRAWN WITHOUT CHANGE	4/4/84	CW
F	ADDED NOTE 2 PER ECO A01987 JL	3-5-87	JH



PIN	FUNCTION
1	BASE
2	EMITTER
3	COLLECTOR

FIG. 1

REQ'D	PART NO.	ITEM	DESCRIPTION	MAT'L OR MFR.	MAT'L SPEC OR CAT NO OR REMARKS
LIST OF MATERIAL					
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND TOLERANCES AS FOLLOWS: DEC. $\pm$ .005 FRACTIONS $\pm$ 1/64 ANGLES $\pm$ 1/2°			MATERIAL:	FINISH	
SIGNATURE			DATE	TITLE	
DR. MoB			7/2/84	SWITCHING TRANSISTOR FBN-L200	
CHK.					
ENGR. GGG			3/2/84		
APPD.					
LGS-G				LAMBDA ELECTRONICS A DIVISION OF NEW YORK INSTRUMENTS INC.	
LGS-G					
DWG SIZE	USED ON	NEXT ASSY.	SCALE 2:1	LAMBDA PART NO. & DWG. NO. REV.	
C				FBN-L200 F	
APPLICATION				SHEET	

FILE COPY