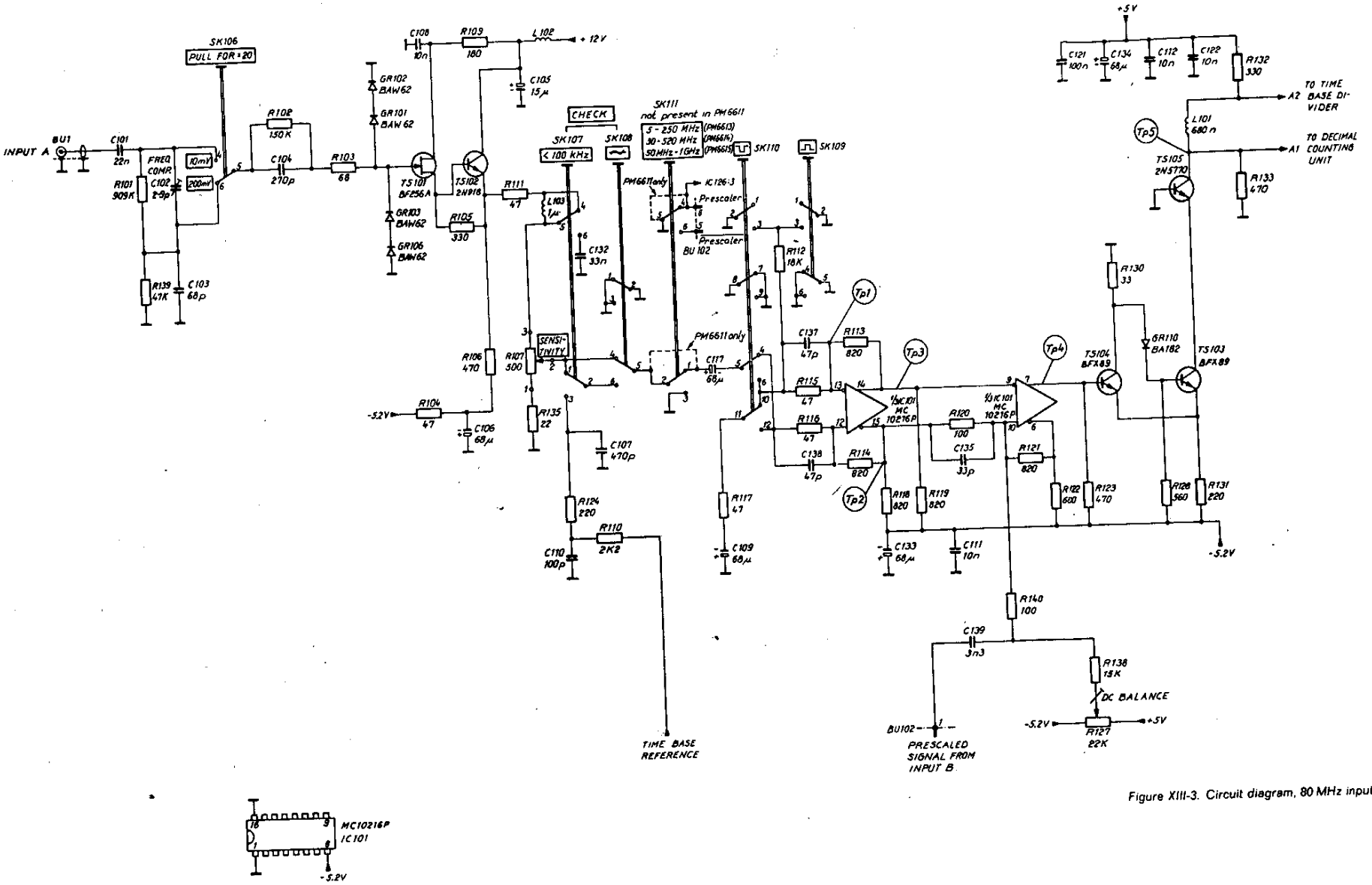
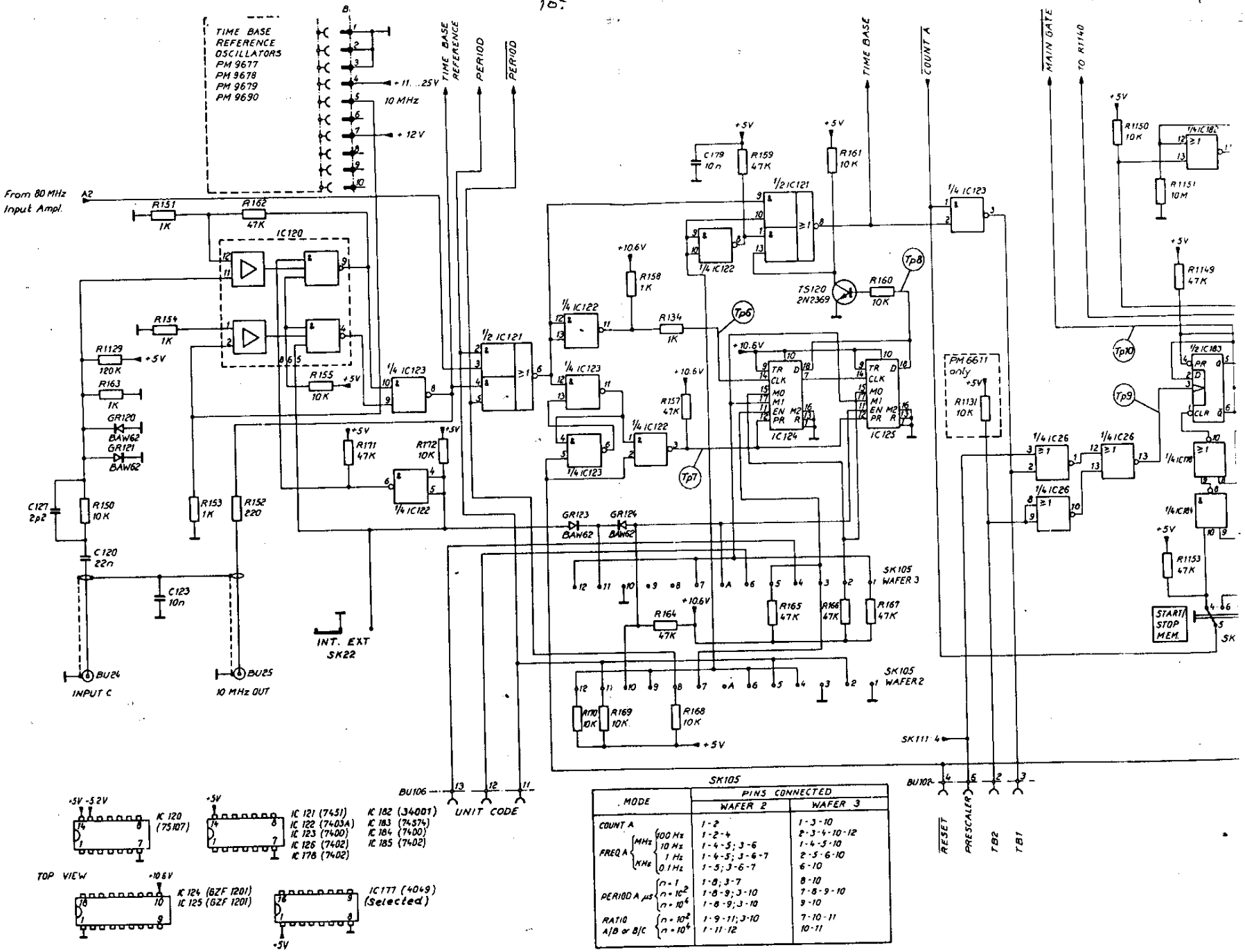
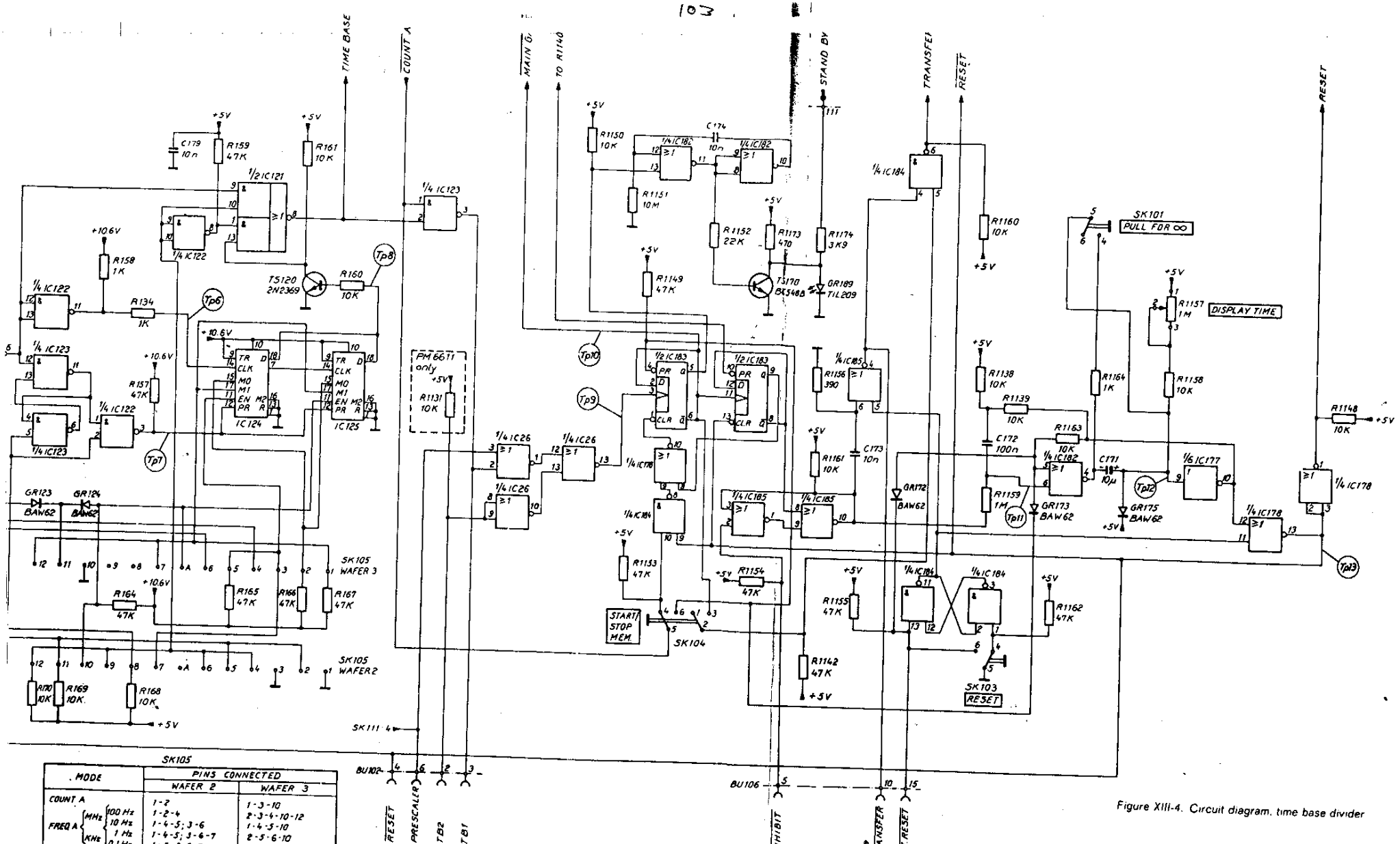


Figure XIII-3. Circuit diagram, 80 MHz input amplifier



MODE		PINS CONNECTED WAFER 2	PINS CONNECTED WAFER 3
COUNT A		1-2	1-3-10
FREQ A	MHz 100 Hz	1-2-4	2-3-4-10-12
	MHz 10 Hz	1-4-5; 3-6	1-4-5-10
	MHz 1 Hz	1-4-5; 3-6-7	2-5-6-10
	kHz 0.1 Hz	1-5; 3-6-7	6-10
PERIOD A µs	n = 1	1-8; 3-7	8-10
	n = 10^2	1-8-9; 3-10	7-8-9-10
	n = 10^4	1-8-9; 3-10	9-10
RATIO A/B or B/C	n = 10^2	1-9-11; 3-10	7-10-11
	n = 10^4	1-11-12	10-11



SK105

MODE		PINS CONNECTED WAFER 2	PINS CONNECTED WAFER 3
COUNT A		1-2	1-3-10
FREQ A	MHz 100 Hz	1-2-4	2-3-4-10-12
	MHz 10 Hz	1-4-5; 3-6	1-4-5-10
	MHz 1 Hz	1-4-5; 3-6-7	2-5-6-10
	KHz 0.1 Hz	1-5; 3-6-7	6-10
PERIOD A µs	n = 1	1-8; 3-7	8-10
	n = 10^2	1-8-9; 3-10	7-8-9-10
	n = 10^4	1-8-9; 3-10	9-10
RATIO A/B or B/C	n = 10^2	1-9-11; 3-10	7-10-11
	n = 10^4	1-11-12	10-11

Figure XIII-4. Circuit diagram, time base divider

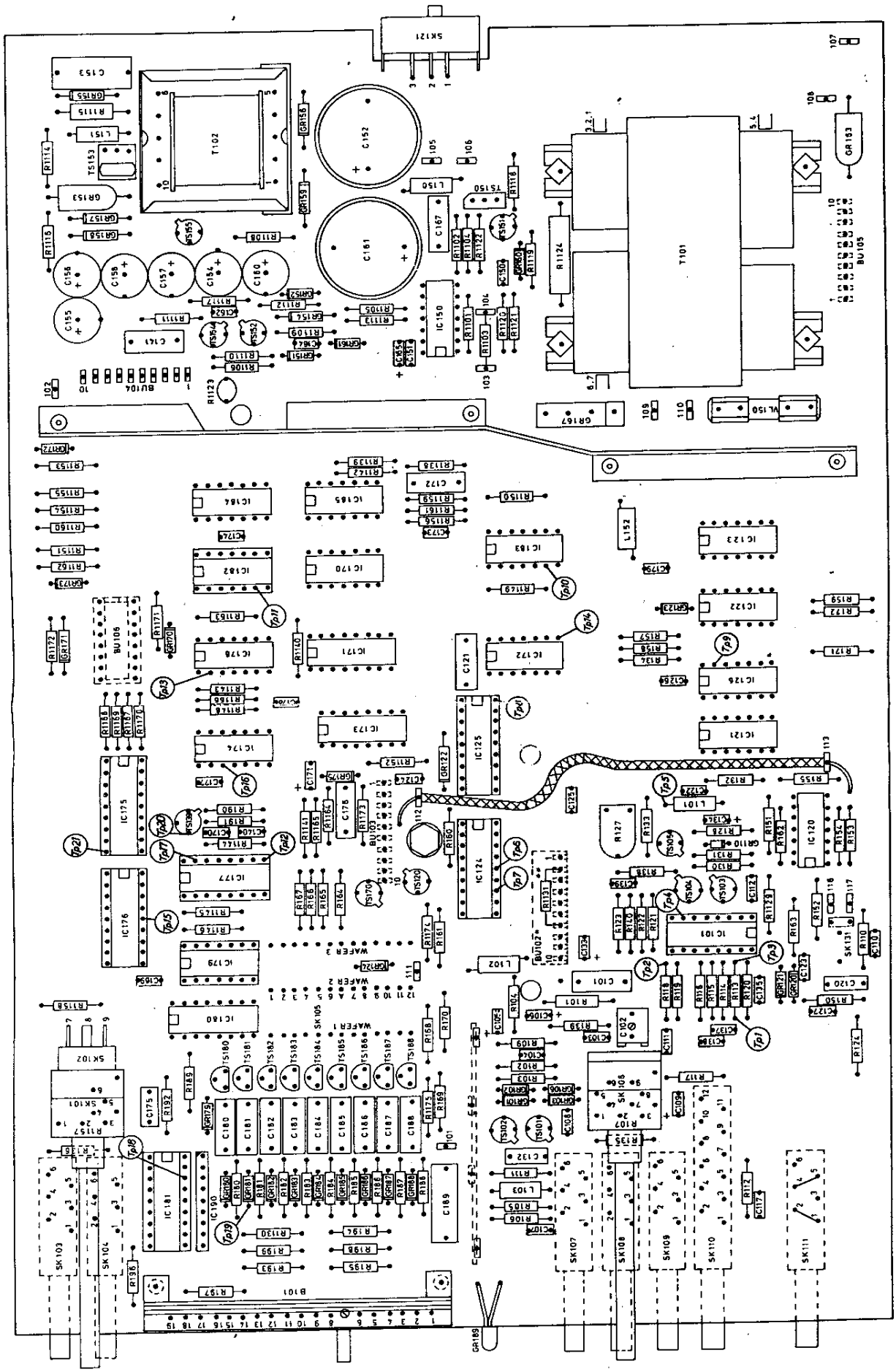


Figure XIII-2. Component layout, unit 1

* not present in PM 6611
** not present in PM 6613, PM 6614 and PM 6615