

S7002A	A	V
S7002B	A	mV
S7004A	B	V
S7004B	B	mV
S7006A	MTB	S
S7006B	MTB	μS
S7008A	DTB	S
S7008B	DTB	μS
S7010A	DTM	←
S7010B	DTM	→
S7012	A	AUTO / SET
S7013	A	0
S7014	A	AC / DC
S7015	A / B	
S7016	ALT	CHOP
S7017	TRIG	VIEW
S7018	B	ADD INVERT
S7019	B	0
S7020	B	AC / DC
S7021	MTB	TB SELECT
S7022	MTB	TB TRIG MODE
S7023	MTB	RESET
S7024	MTB	TRIG OR SOURCE
S7025	MTB	
S7026	MTB	TRIG COUPL
S7027	DTB	DTB TRIG
S7028	DTB	
S7029	X	DEFL
S7030		MENU
S7031	EXT	AC / DC
S7032	TB	MAGN

REF NO	TYPE
D7001	8052AH
D7002	HEF4053
D7003	74HC1259
D7004	0Q0044
D7006	74LS244
N7001	LM339

Figure 10.6 Circuit diagram of front unit

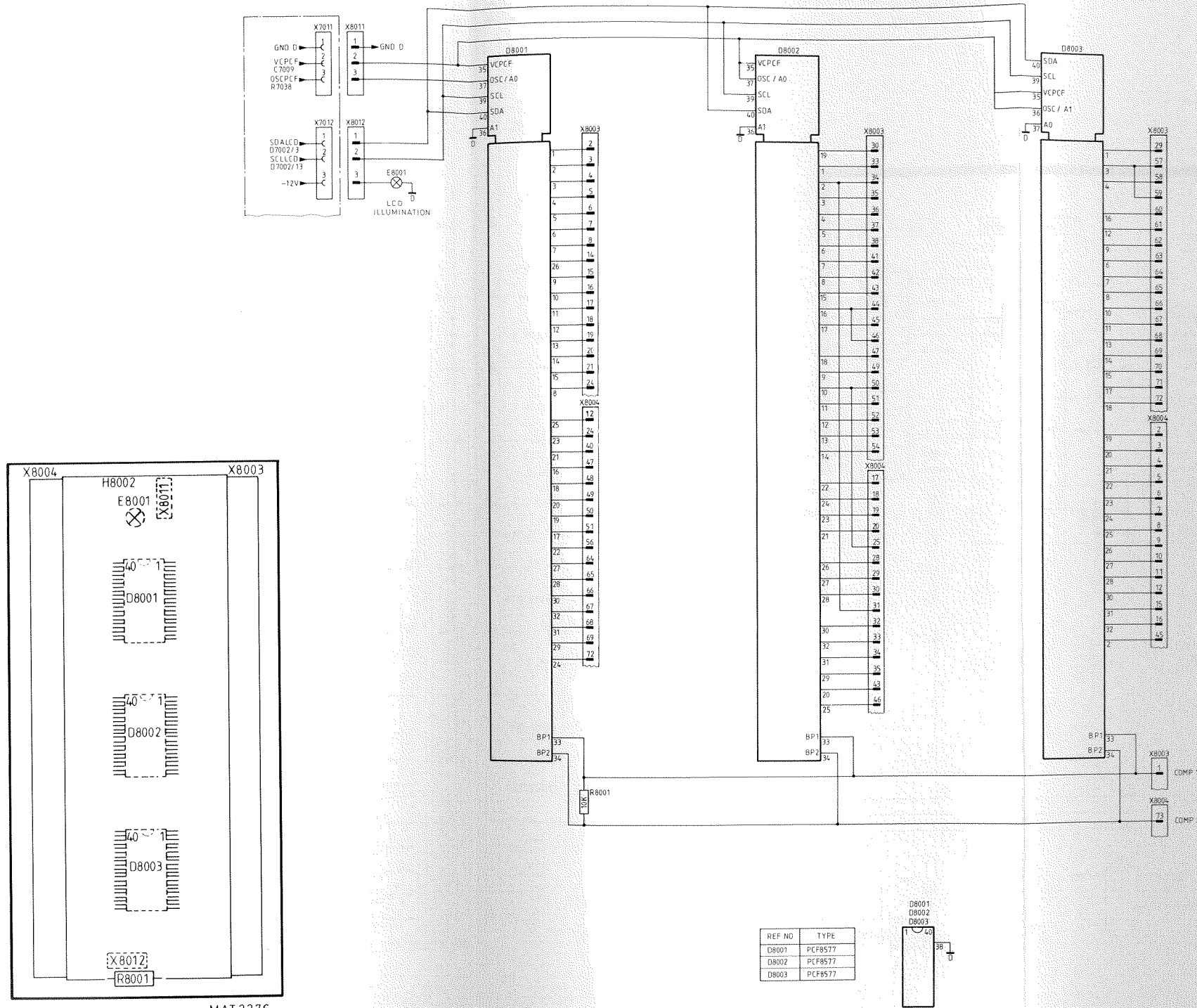


Figure 10.7 LCD unit p.c.b.

PIN	COM1		COM2	
	DISPLAY / SEGMENT	CH	DISPLAY / SEGMENT	CH
73			COM2	
72	>	A	INV	
71	NC		NC	
70	NC		NC	
69	1	a	A 1	f
68	1	g	A 1	e
67	1	c	A 1	d
66	1	b	A	P1
65			TRIG VIEW	A
64			ADD	B
63	NC		NC	
62	NC		NC	
61	NC		NC	
60	NC		NC	
59	NC		NC	
58	NC		NC	
57	NC		NC	
56	>	B	INV	B
55	NC		NC	
54	NC		NC	
53	NC		NC	
52	NC		NC	
51	3	a	B 3	f
50	3	g	B 3	e
49	3	c	B 3	d
48	3	b	B	P2
47			TRIG'D	NOT
46			XDEFL	TB
45				M
44	NC		NC	
43	TRIG		AUTO	
42	NC		NC	
41	NC		NC	
40			>	MTB
39	NC		NC	
38	NC		NC	
37	NC		NC	
36	NC		NC	
35	5	a	MTB 5	f
34	5	g	MTB 5	e
33	5	c	MTB 5	d
32	5	b	MTB	P3
31			EXT	MTB
30			B	A
29			HF	LF
28			DC	P-P
27	NC		NC	
26	NC		NC	
25			*	DTB
24			>	DTB
23	NC		NC	
22	NC		NC	
21	NC		NC	
20	7	a	DTB 7	f
19	7	g	DTB 7	e
18	7	c	DTB 7	d
17	7	b	DTB	P4
16			B	STARTS
15			EXT	A
14			DC	AC
13	NC		NC	DTM
12			MENU	REMOTE
11	9	a	DTM 9	f
10	9	g	DTM 9	e
9	9	c	DTM 9	d
8	9	b	DTM	P5
7	10	a	DTM 10	f
6	10	g	DTM 10	e
5	10	c	DTM 10	d
4	10	b	DTM	P6
3	11	a	DTM 11	f
2	11	g	DTM 11	e
1	NC		NC	

INV 0.0 μ V ACDC
 > 0.0 μ V ACDC
 A TRIG VIEW ALT
 B ADD CHOP
 INV 0.0 μ V ACDC
 > 0.0 μ V ACDC
NOT TRIG D ARMED
 MTBI X-DEFL DTB
 AUTO TRIG SINGLE
 * 0.0 μ S
 > 0.0 μ S
 AEXTBACDC LINE
 LFHF OPTION X
 P-PDCTIVE: + X
 * 0.0 μ S
 > 0.0 μ S
 STARTS TVL X
 AEXTBACDC X
REMOTE MENU
 0.0000 μ S
 P5 P6 P7 P8 DIV

PIN	COM1		COM2	
	DISPLAY / SEGMENT	CH	DISPLAY / SEGMENT	CH
1			COM1	
2	a	A	2	f
3	b	A		
4	g	A	2	e
5	c	A	2	d
6	r	A	7	A
7	v	A	'c	A
8	OC	A	A	A
9	NC		NC	
10	NC		NC	
11	NC		NC	
12	NC		NC	
13	NC		NC	
14			CHOP	ALT
15	4	a	B 4	f
16	4	b	B	
17	4	g	B 4	e
18	4	c	B 4	d
19	r	B	7	B
20	v	B	'c	B
21	DC	B	AC	B
22	NC		NC	
23	NC		NC	
24			ARMED	
25	NC		NC	
26	NC		NC	
27	NC		NC	
28	NC		NC	
29			DTB	I
30			SINGLE	*
31	NC		NC	MTB
32	NC		NC	
33	6	a	MTB 6	f
34	6	b	MTB	
35	6	g	MTB 6	e
36	6	c	MTB 6	d
37			7	n
38			s	MTB
39	NC		NC	
40	NC		NC	
41			DC	AC
42			OPTION	TVI
43			LINE	=
44				MTB
45				MTB
46				MTB
47				MTB
48	NC		NC	
49	8	a	DTB 8	f
50	8	b	DTB	
51	8	g	DTB 8	e
52	8	c	DTB 8	d
53			7	n
54			s	DTB
55	NC		NC	
56	NC		NC	
57			DTB	TVL
58			DTB	*
59			DTB	
60	12	a	DTM 12	f
61	13	a	DTM 13	f
62	13		DTM	
63			7	n
64			s	DTM
65			v	DTM
66	13	c	DTM 13	d
67	13	g	DTM 13	e
68	12	b	DTM	P8
69	12	c	DTM 12	d
70	12	g	DTM 12	e
71	11	b	DTM	P7
72	11	c	DTM 11	d
73	NC		NC	

Figure 10.8 Circuit diagram of LCD unit