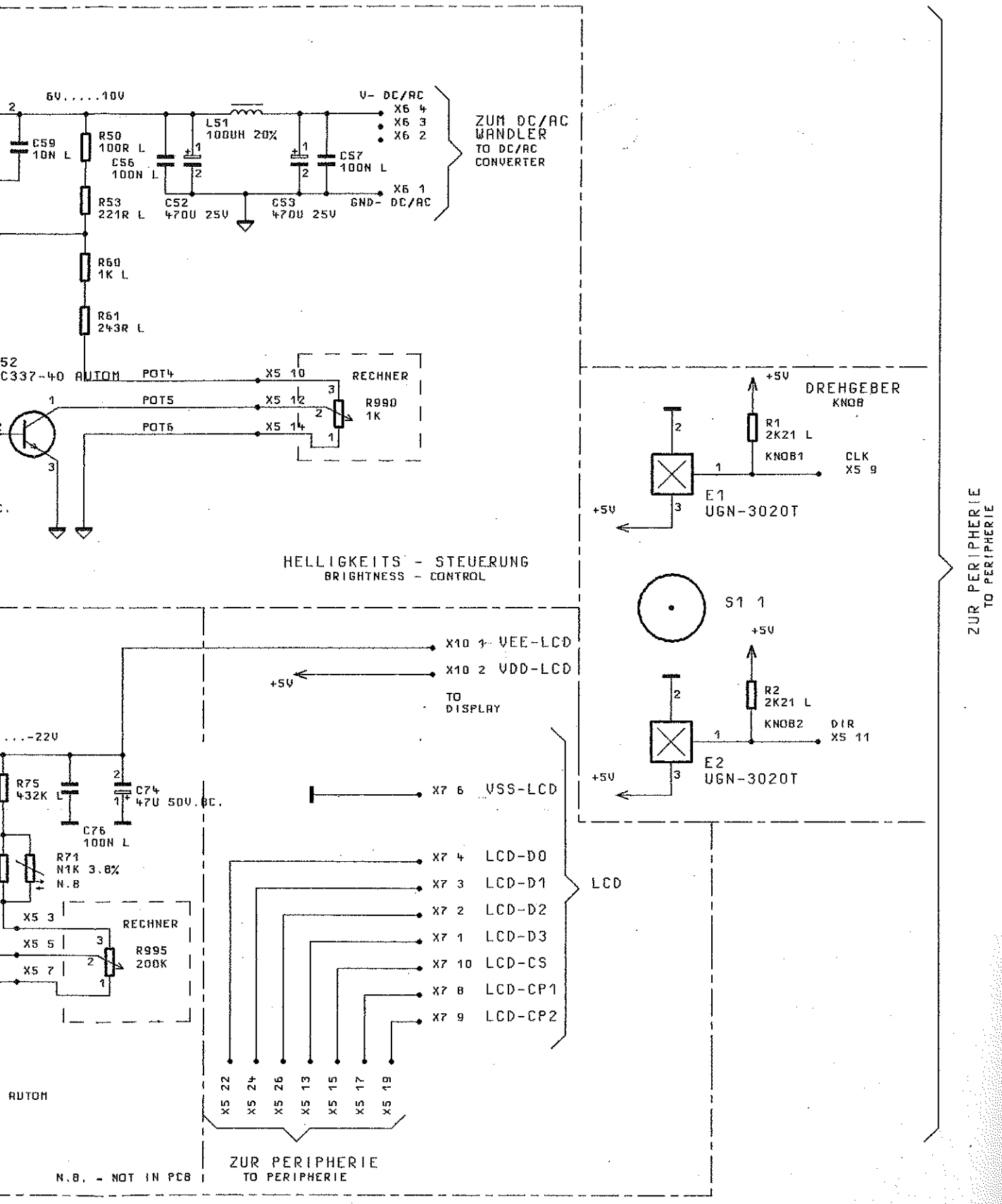


04/	48730 90	07.04.94	JN
/			
AEND. IND.	AENDERUNGS-MITTEILUNG.	DATUM	NAME

1GPK	TAG	NAME
BEARB.		JN
GEPR.		DR
NORM		
PLOTT	07.04.94	

ROHDE&SCHWARZ

ZU GERAET SMP

BENENNUNG

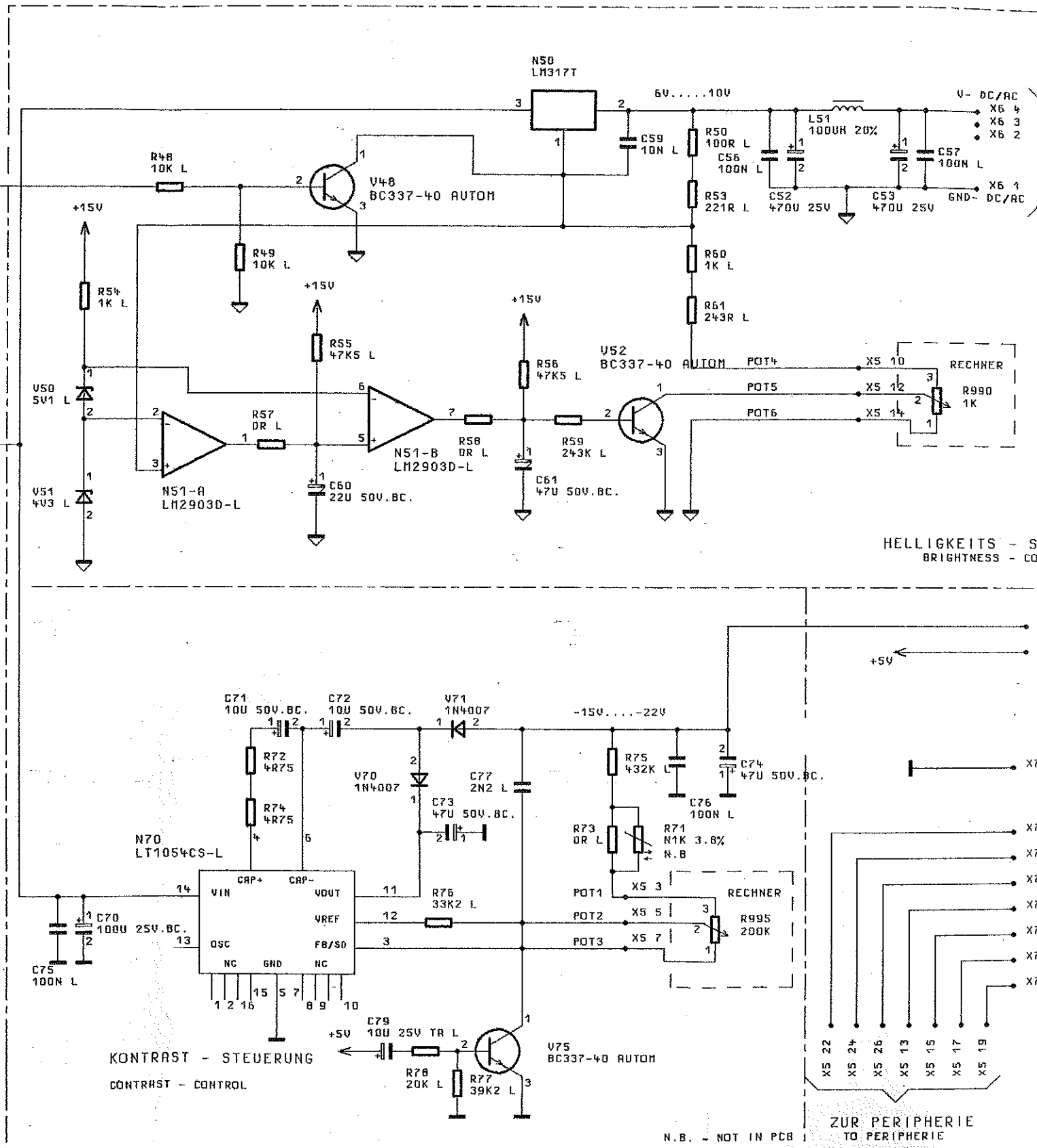
DREHGEBER
KNOB ASSEMBLY

ZEICHN.-NR. 1035.5592.01S

BLATT-NR. 1

V. BL.

REG.I.V. 1035.5005 ERSTE Z. 1035.5440



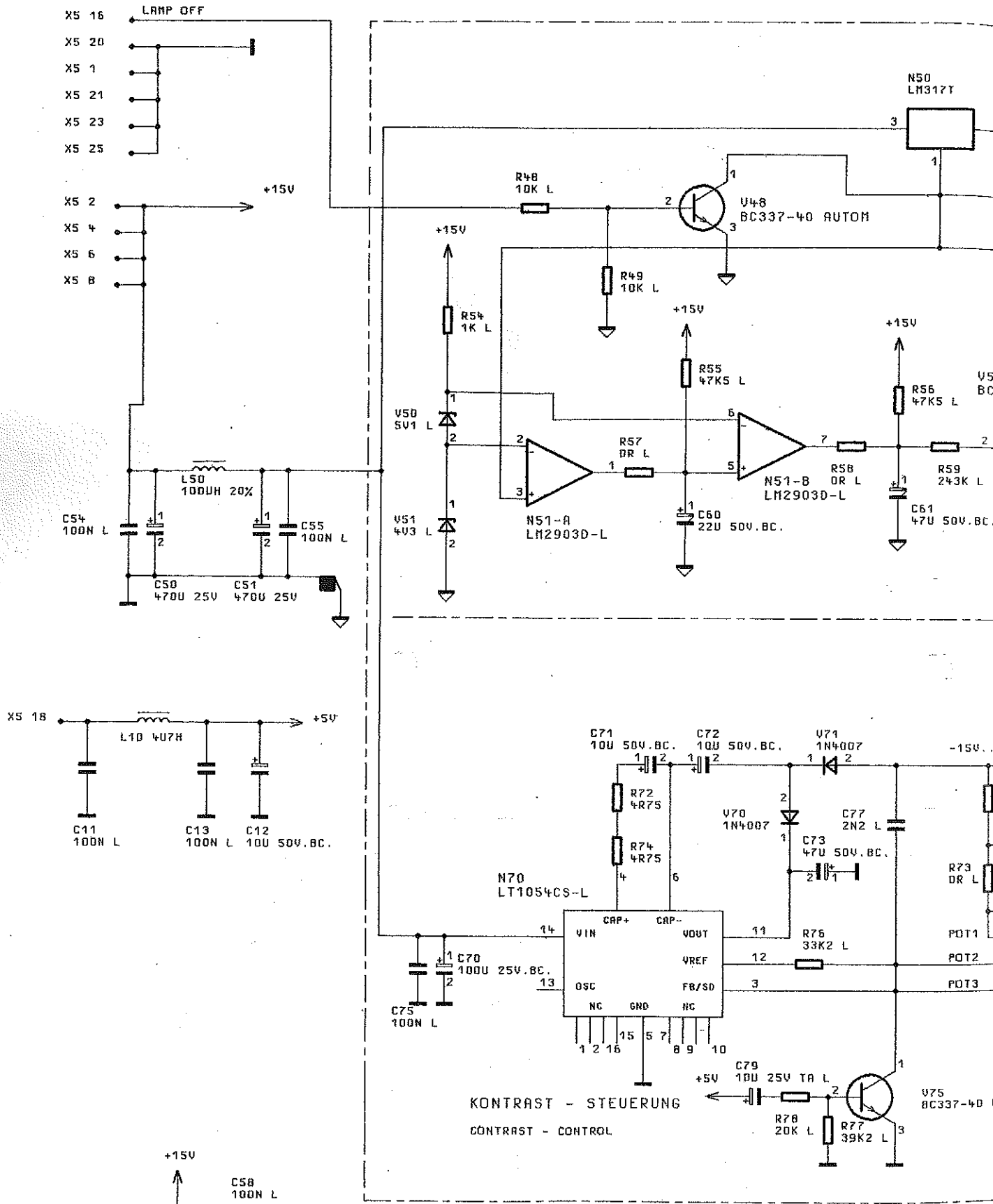
STROMLAUF GILT FUER VAR.02

CIRCUIT DIAGRAM IS VALID FOR MOD.02

ACHTUNG: ESD!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

04/	48730 90	07.04.94	JN	1GPK	
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	0
/					
AEND. IND.	AENDERUNGS-MITTEILUNG.	DATUM	NAME	ROHD	
				ZU GERRET	

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

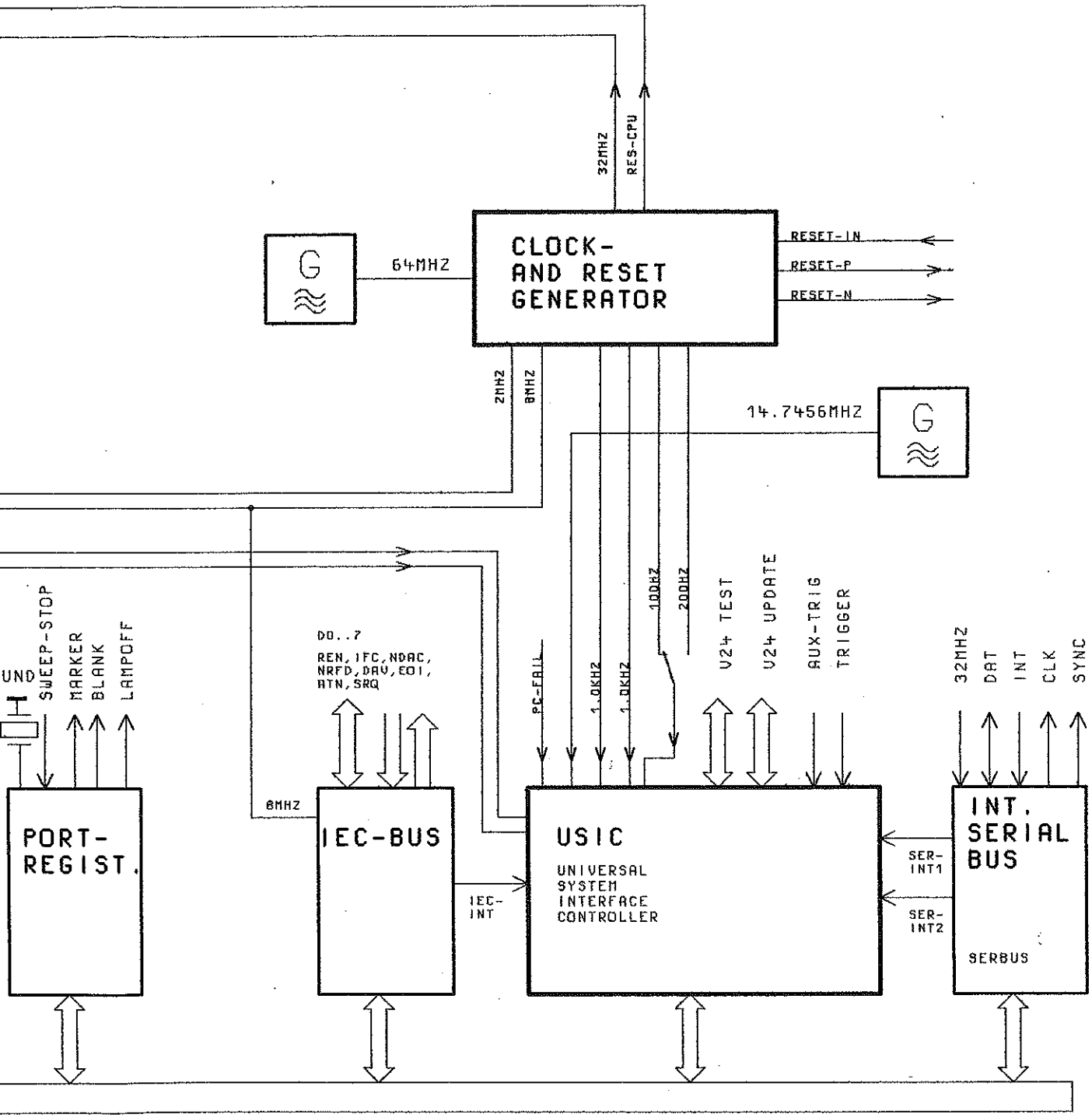


STROMLAUF GILT FUER VAR.02

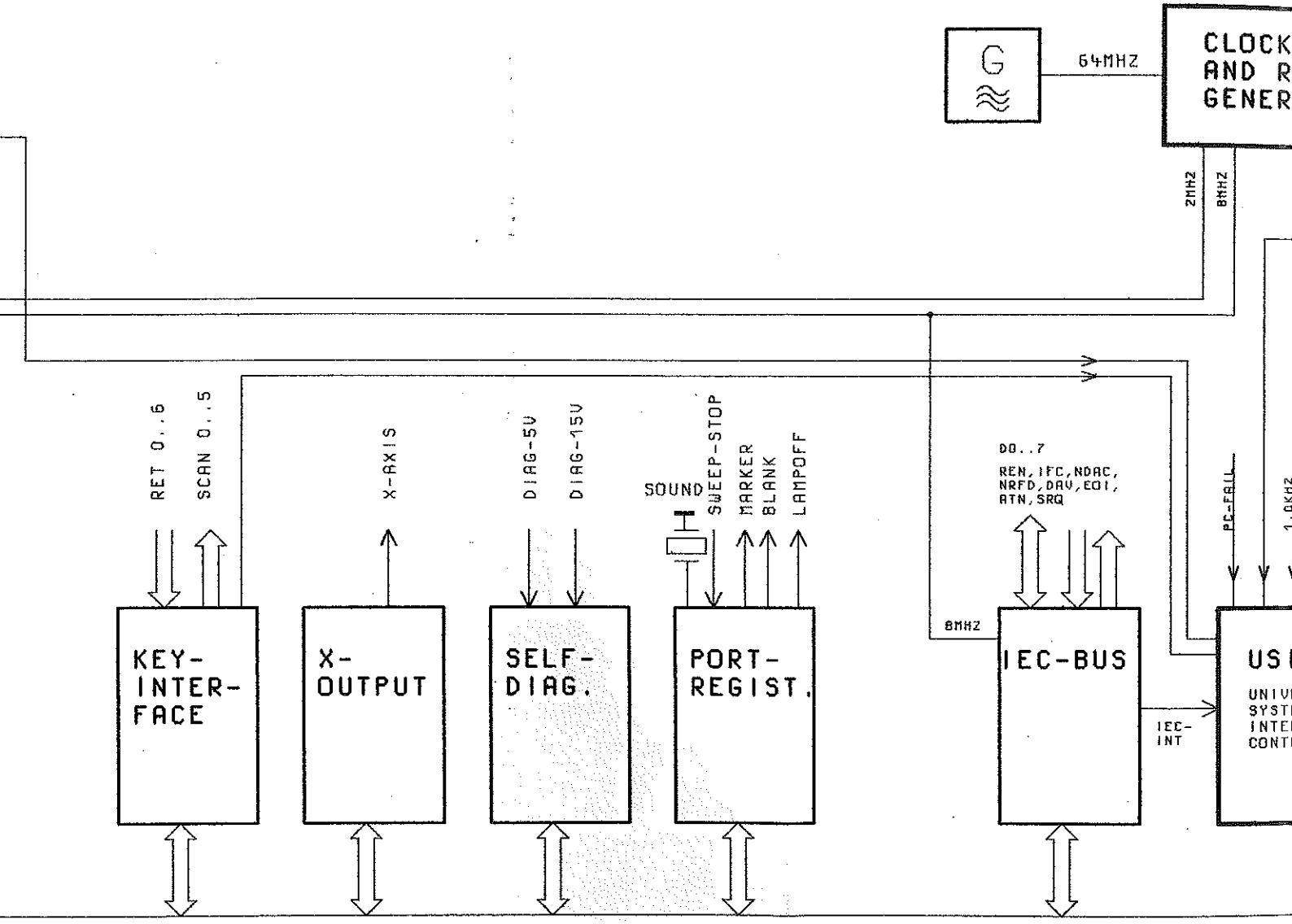
CIRCUIT DIAGRAM IS VALID FOR MOD.02

ACHTUNG! EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

ZEICHN.-NR.



04/	48730	02.05.94	DR	16PK	TAG	NAME	BENENNUNG	
				BEARB.		DR	RECHNER	
				GEPR.			PROCESSOR	
				NORM				
				PLOTT	03.05.94			
/							ZEICHN.-NR.	BLATT-NR.
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			1035.7308.01S	1+
				ZU GERAET	SMP		REG.I.V. 1035.5005	ERSTE Z. 1035.5440

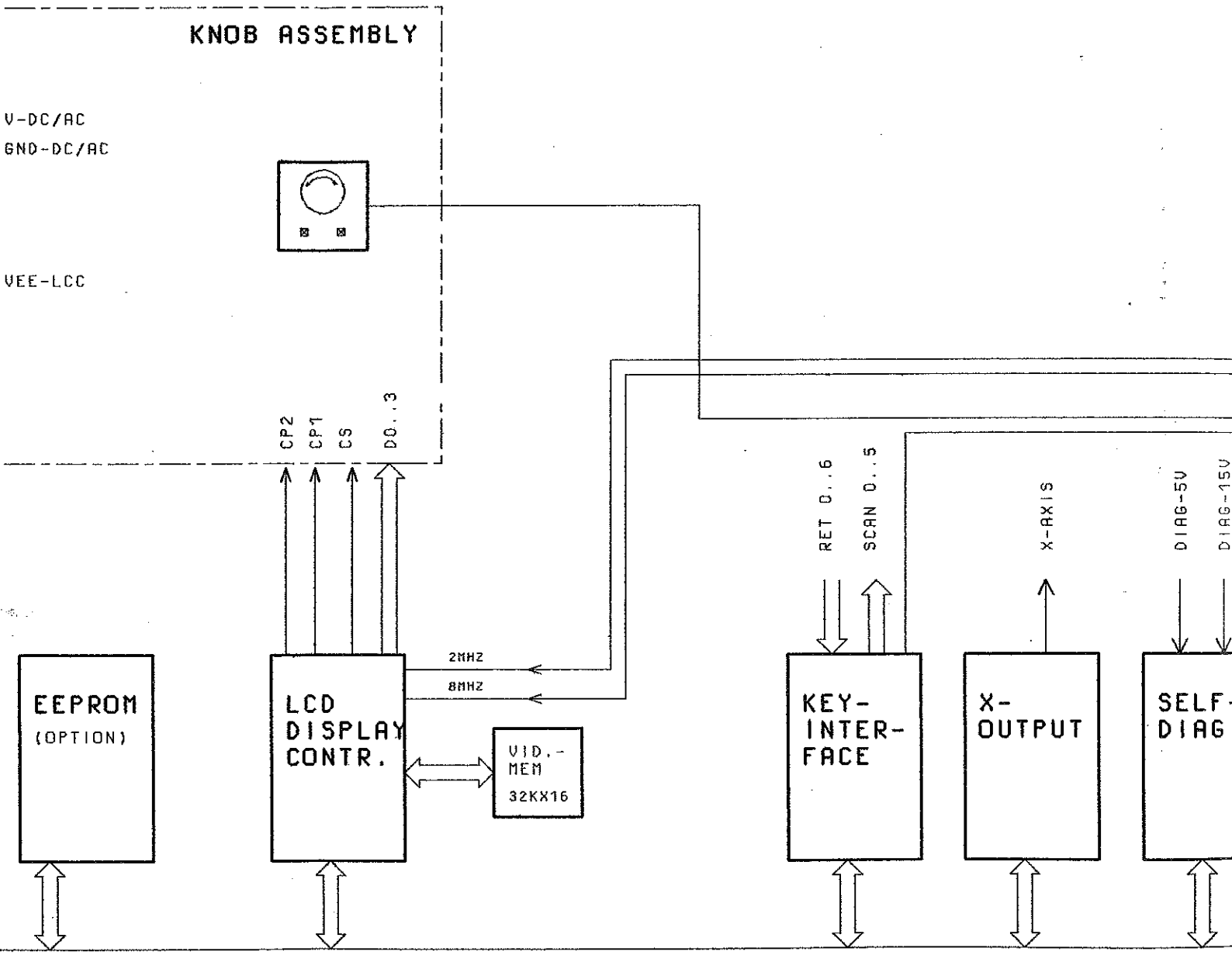


T FUER VAR.02

VALID FOR MOD.02

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

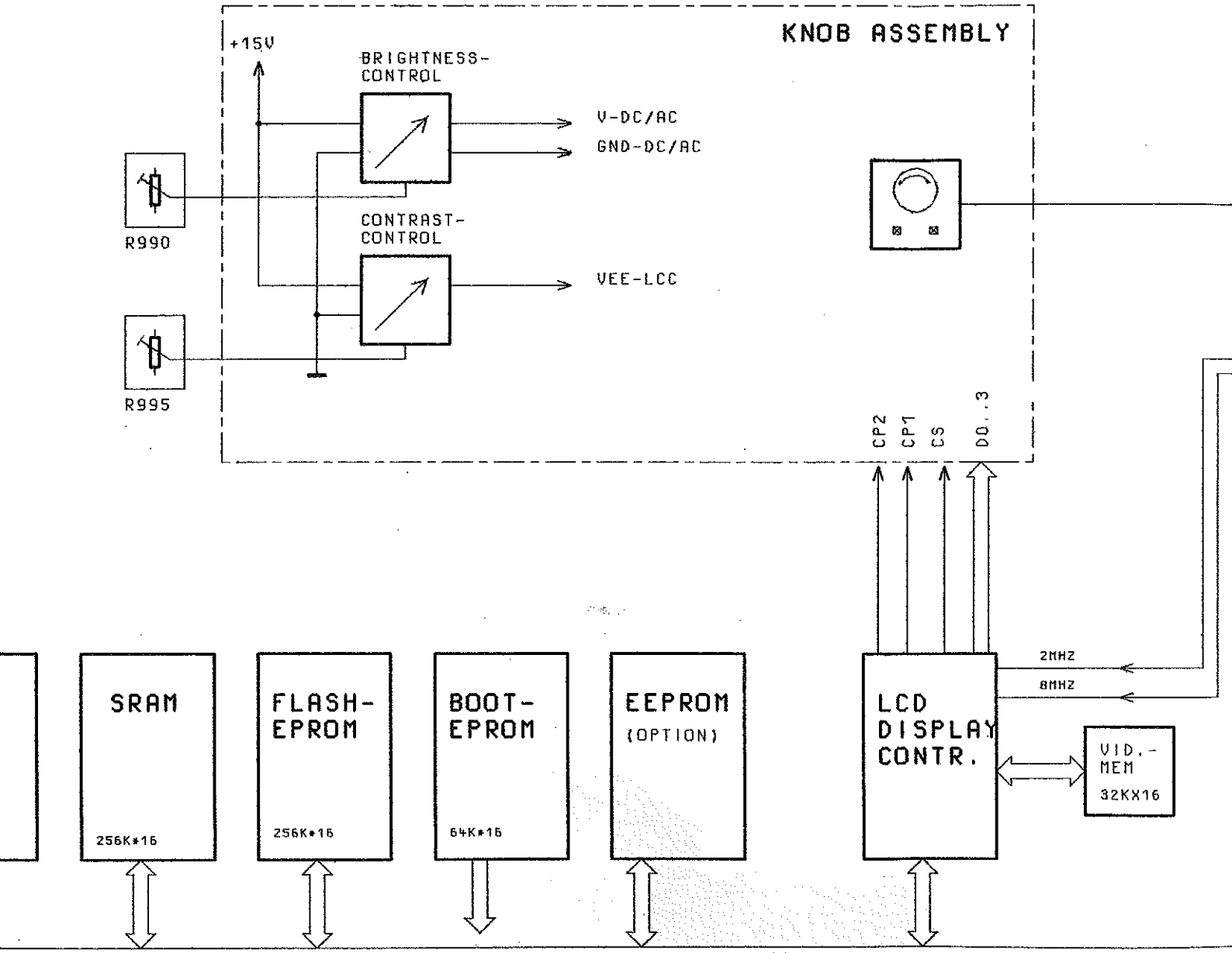
04/	48730	02.05.94	DR	16PK	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	03.05.94
/					
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHW	
				ZU GERAET	SMP



STROMLAUF GILT FUER VAR.02

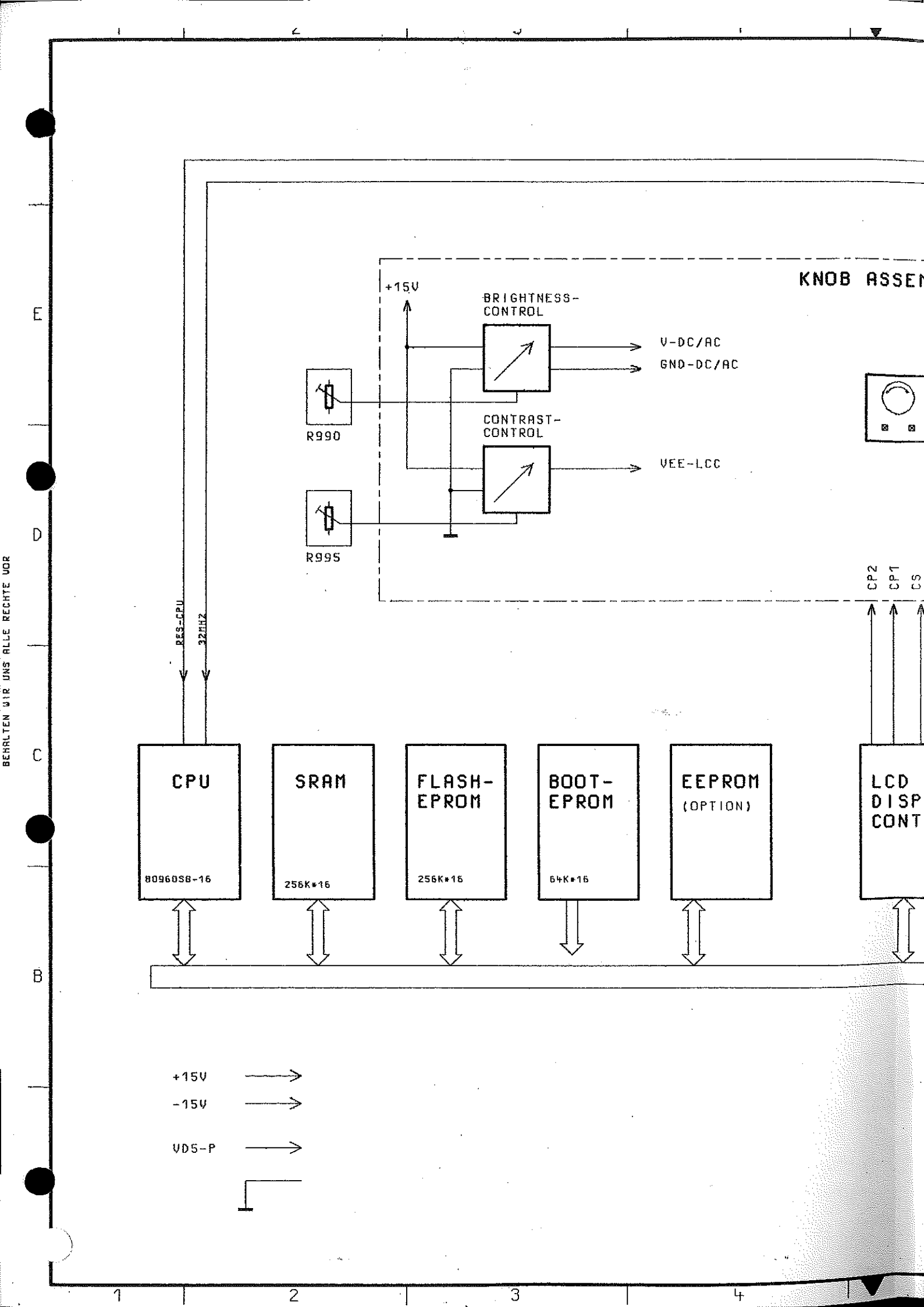
CIRCUIT DIAGRAM IS VALID FOR MOD.02

ACHTUNG: E
ELEKTROSTATISCH GE
BAUELEMENTE ERFOR
BESONDERE HAND
ATTENTION E
ELECTROSTATIC SENSI
REQUIRE A SPECIAL

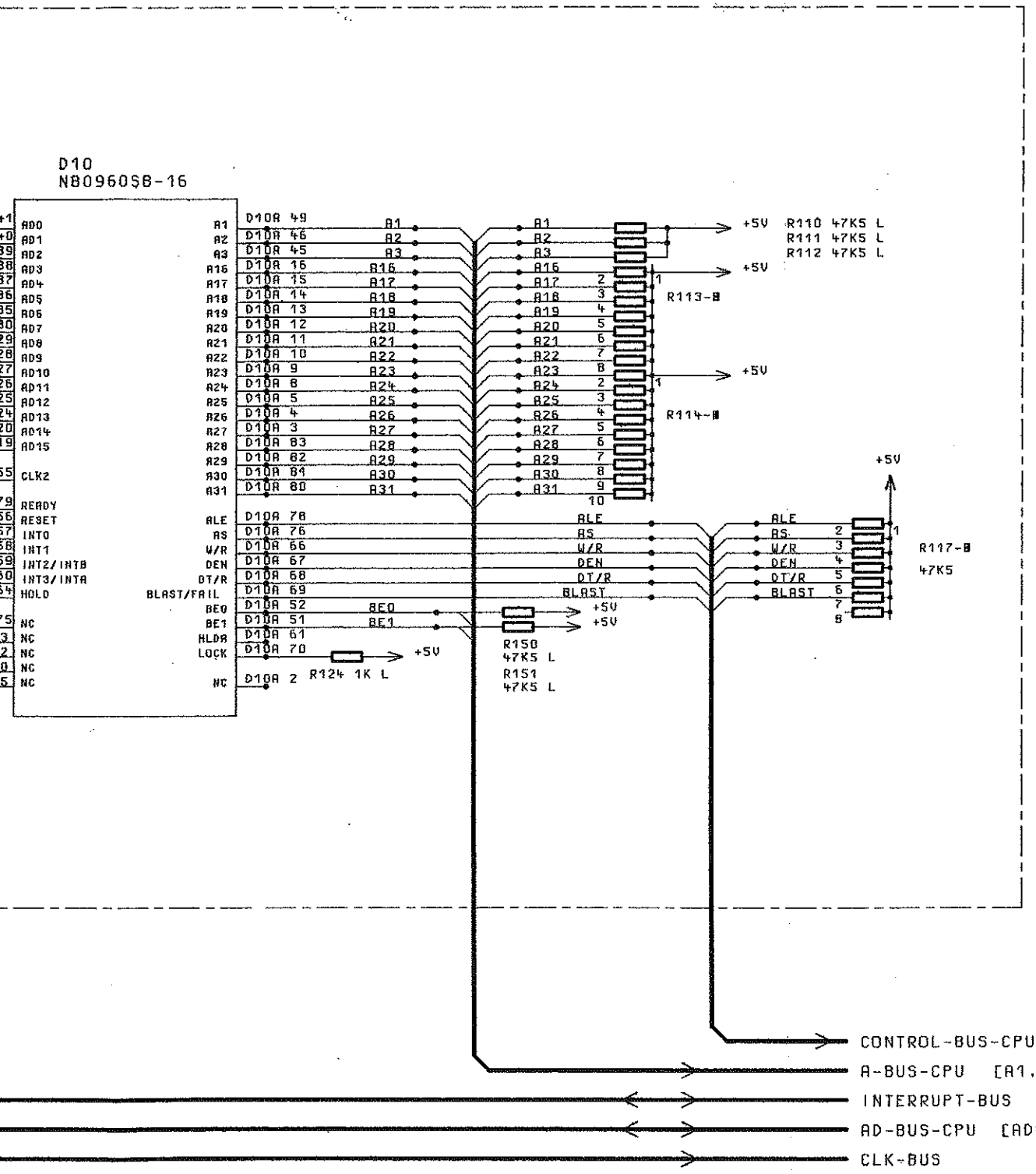


STROMLAUF G

CIRCUIT DIAGRAM

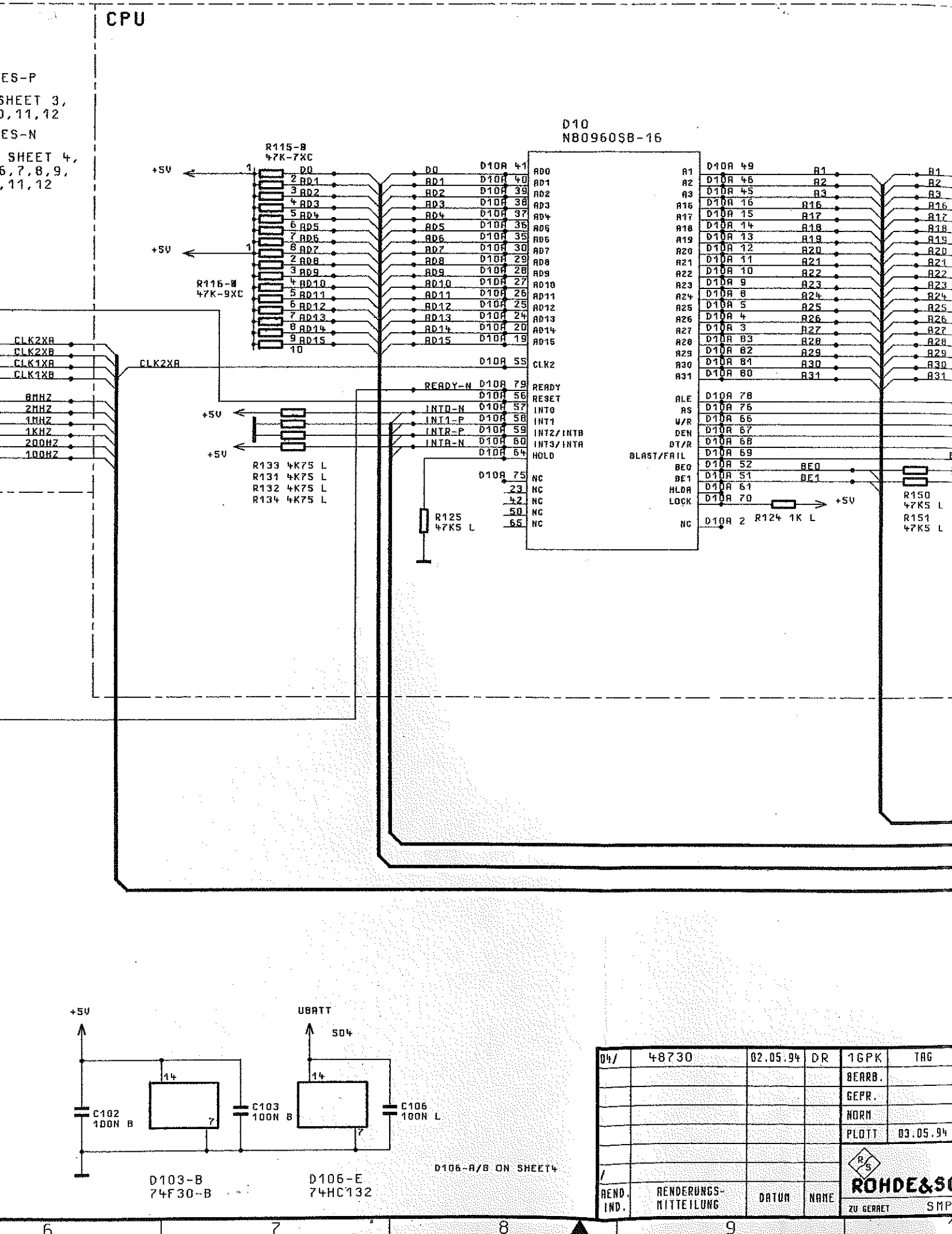


BEHALTEN WIR UNS ALLE RECHTE VOR



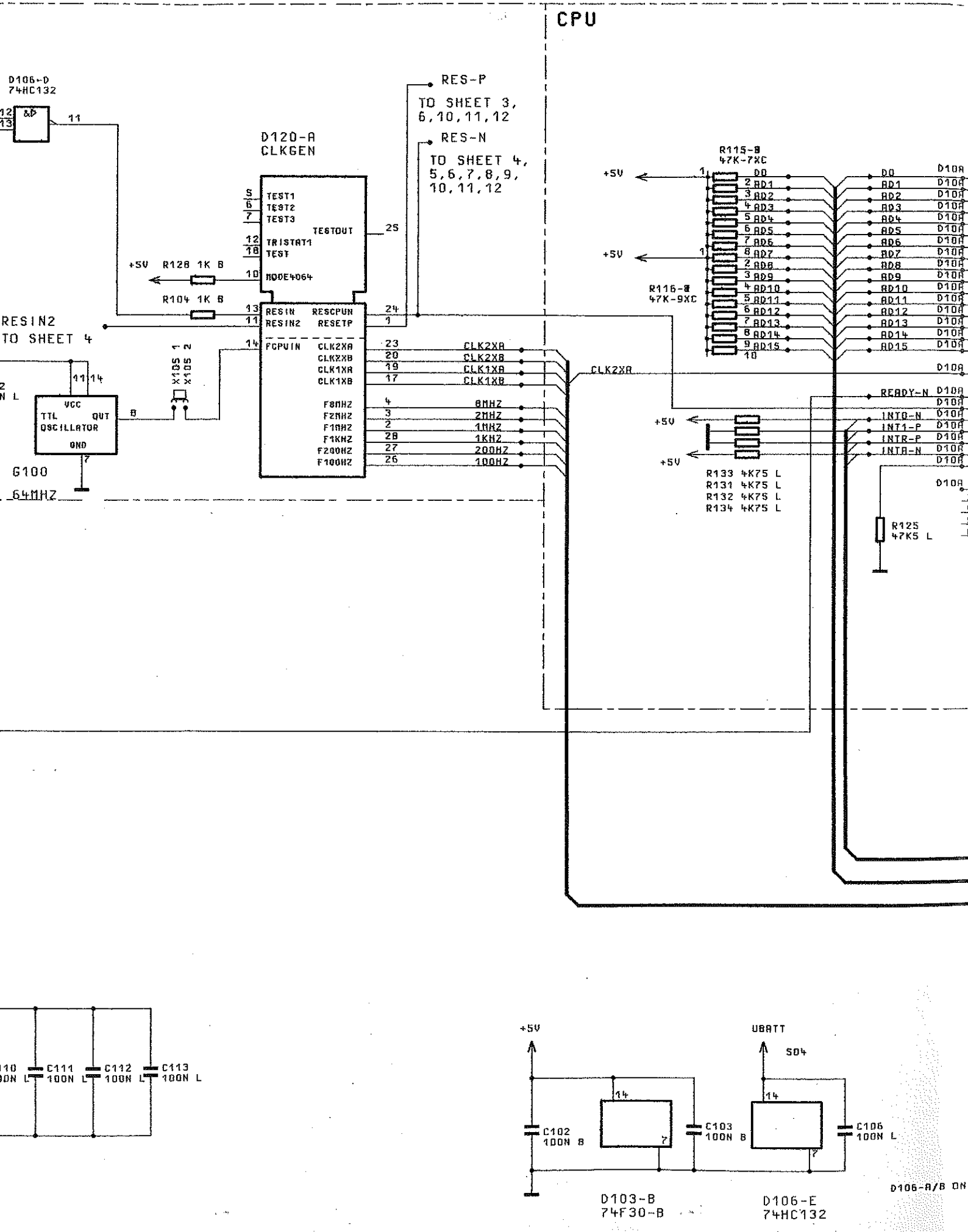
04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7308.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			BLATT-NR. 2+
				ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440

SHEET4

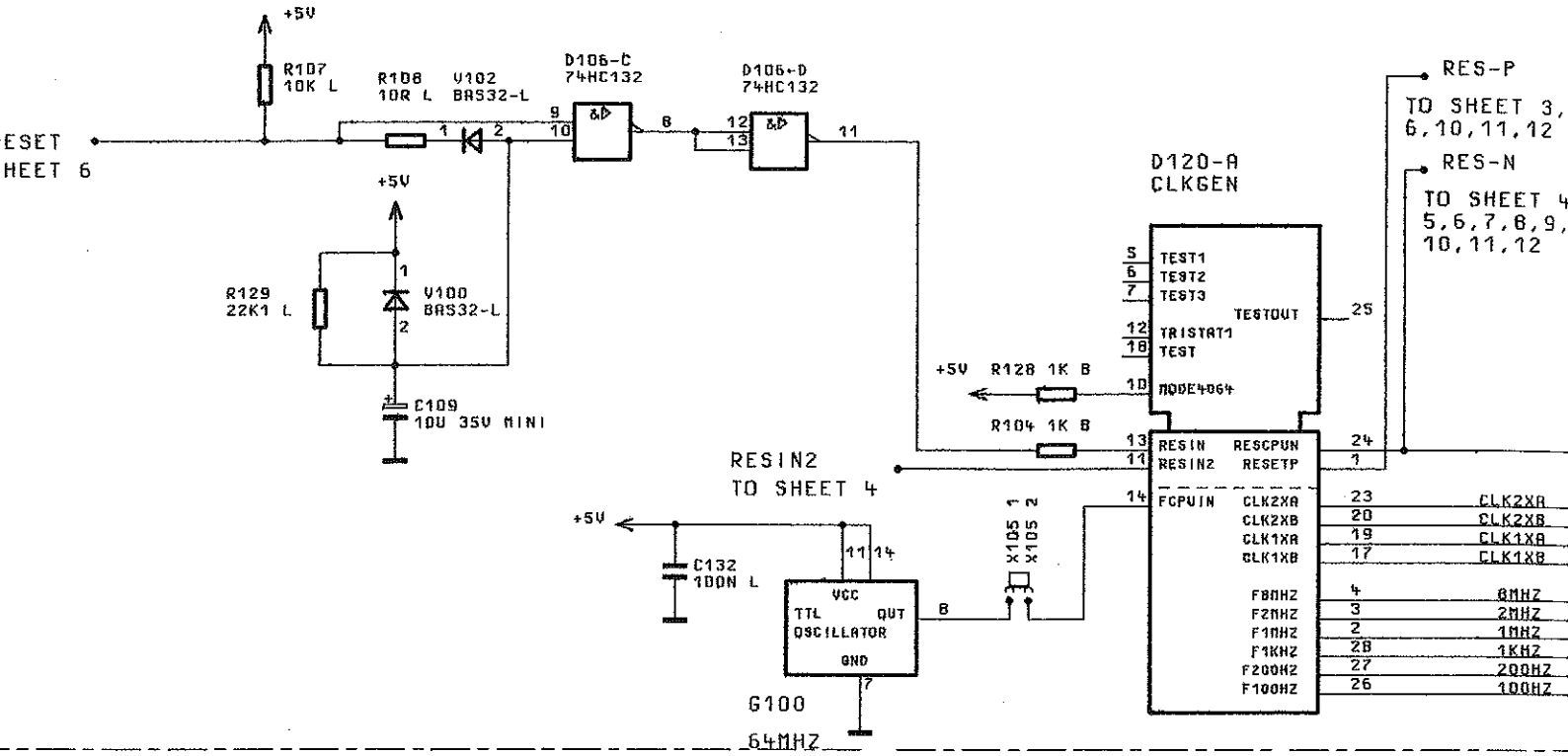


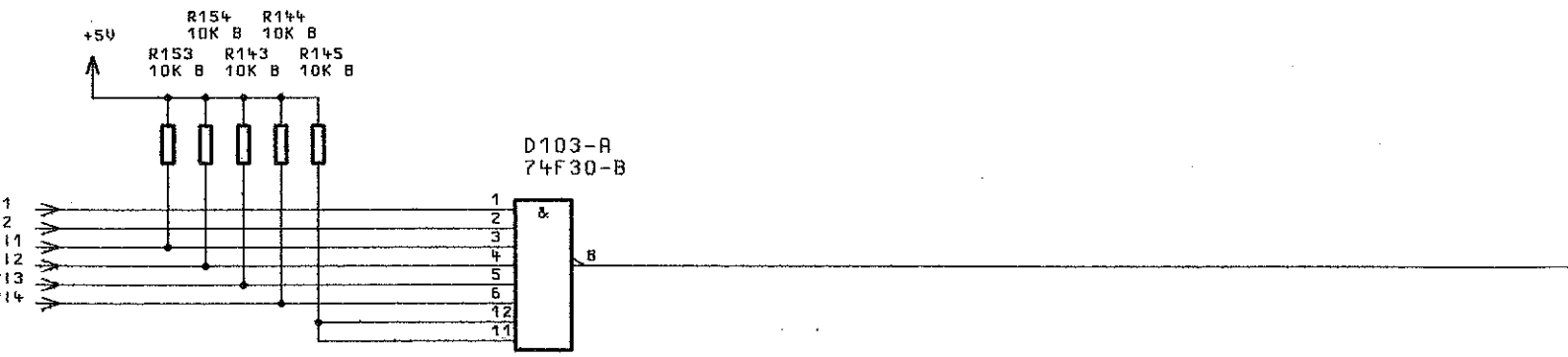
04/	48730	02.05.94	DR	1GPK	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	03.05.94
/					
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP

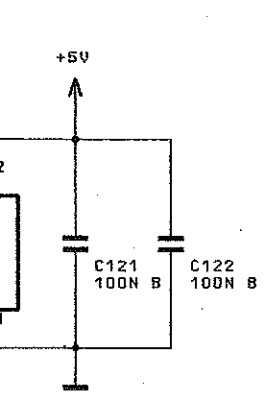
ROHDE&SC

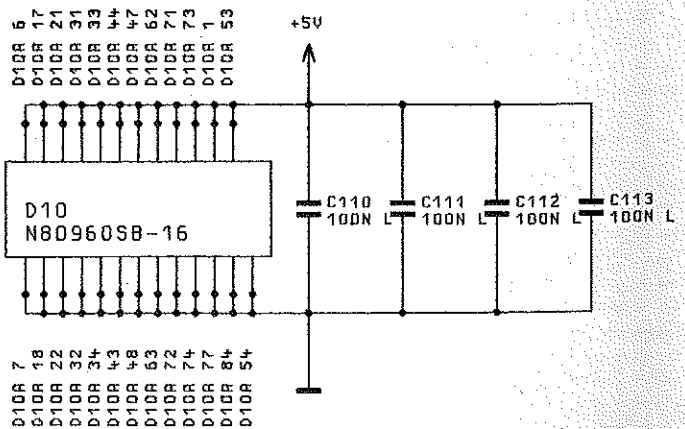

CPU
D106-D
74HC132
RES-P
TO SHEET 3,
6,10,11,12
RES-N
TO SHEET 4,
5,6,7,8,9,
10,11,12
D120-A
CLKGEN
R115-B
47K-7XC
R116-B
47K-9XC
+5V
R128 1K B
R104 1K B
RESIN2
TO SHEET 4
TEST1
TEST2
TEST3
TESTOUT
TRISTAT1
TEST
MODE4064
RESIN
RESCPUN
RESIN2
RESETP
FCPUIN
CLK2XA
CLK2XB
CLK1XA
CLK1XB
F8MHZ
F2MHZ
F1MHZ
F1KHZ
F200HZ
F100HZ
8MHZ
2MHZ
1MHZ
1KHZ
200HZ
100HZ
X10S 1
X10S 2
VCC
TTL OSCILLATOR
OUT
GND
G100
64MHZ
R133 4K75 L
R131 4K75 L
R132 4K75 L
R134 4K75 L
R125
47K5 L
READY-N
INTO-N
INT1-P
INTR-P
INTA-N
D10A
C110
100N L
C111
100N L
C112
100N L
C113
100N L
C102
100N B
C103
100N B
C106
100N L
UBATT
SO4
D103-B
74F30-B
D106-E
74HC132
D106-A/B ON

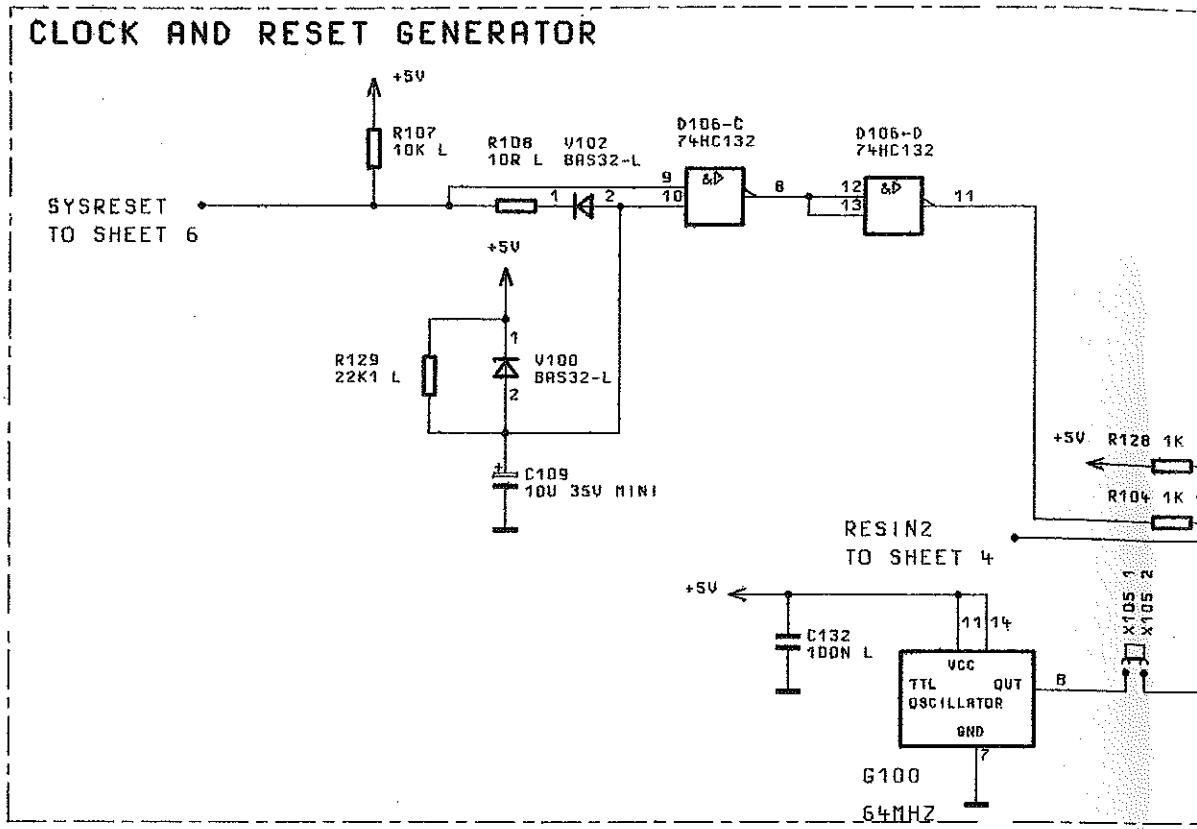
CK AND RESET GENERATOR

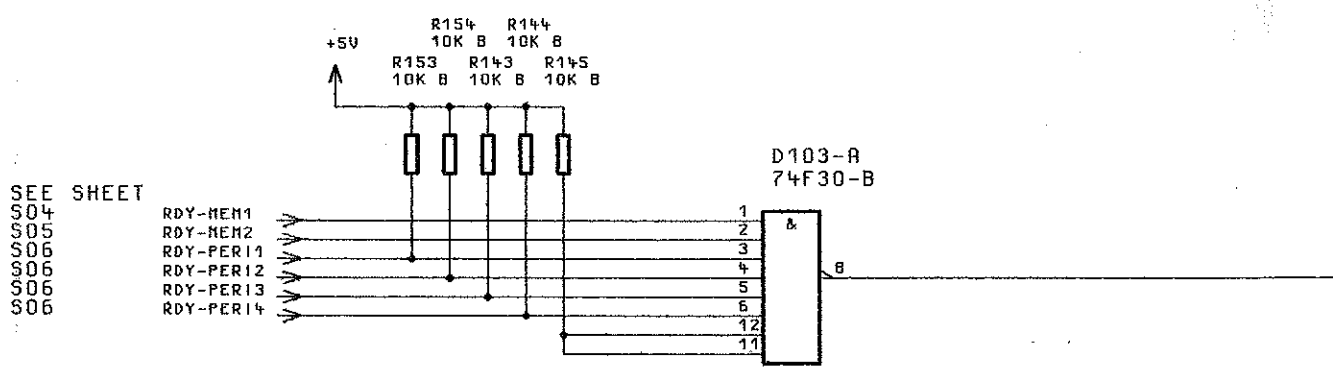


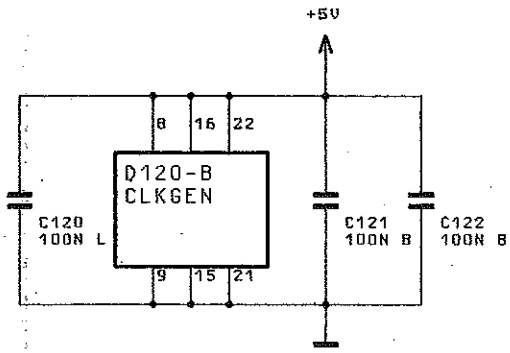


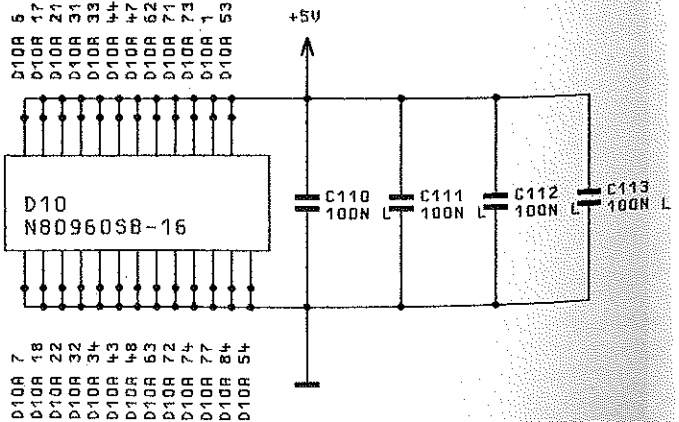






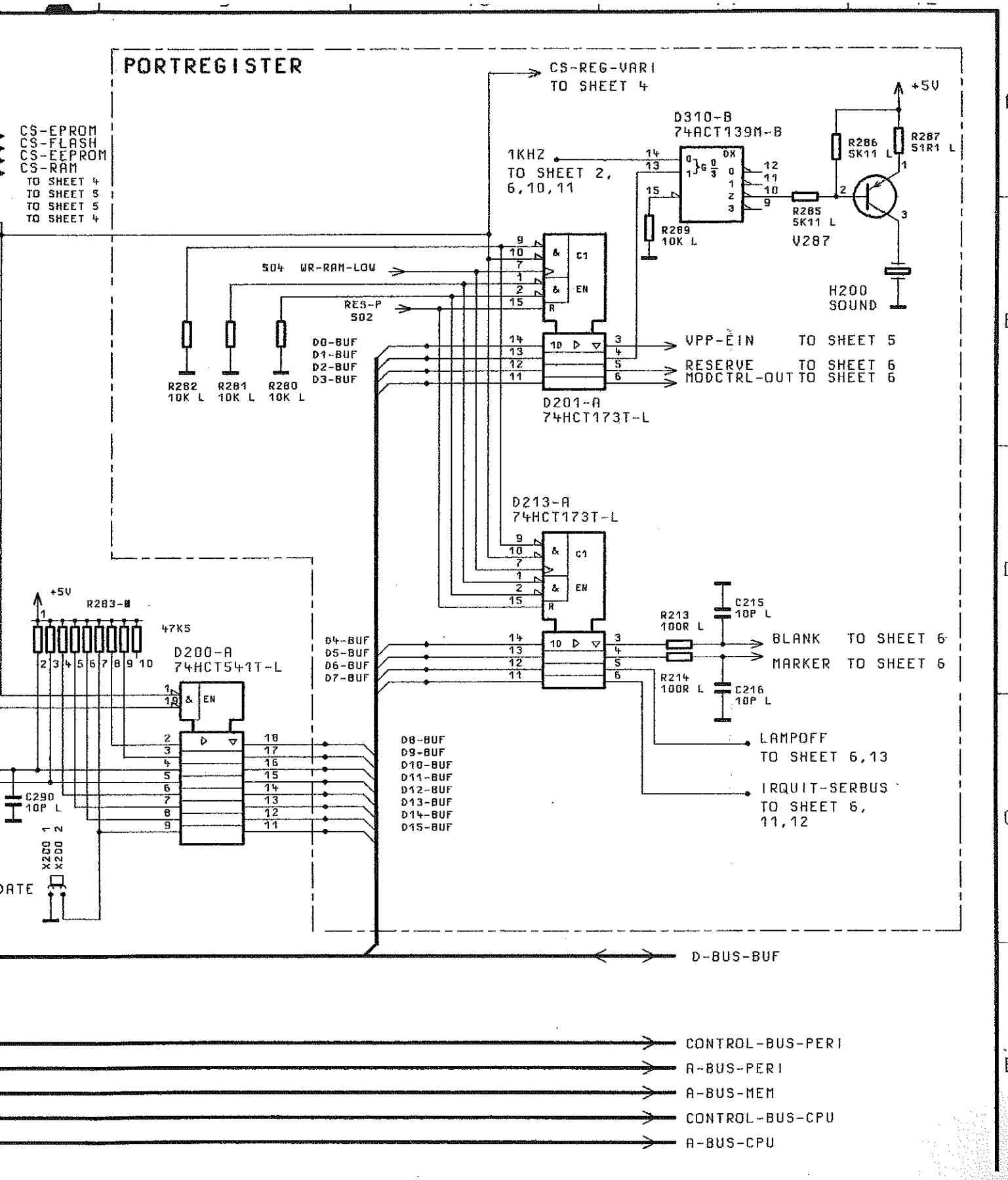






FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

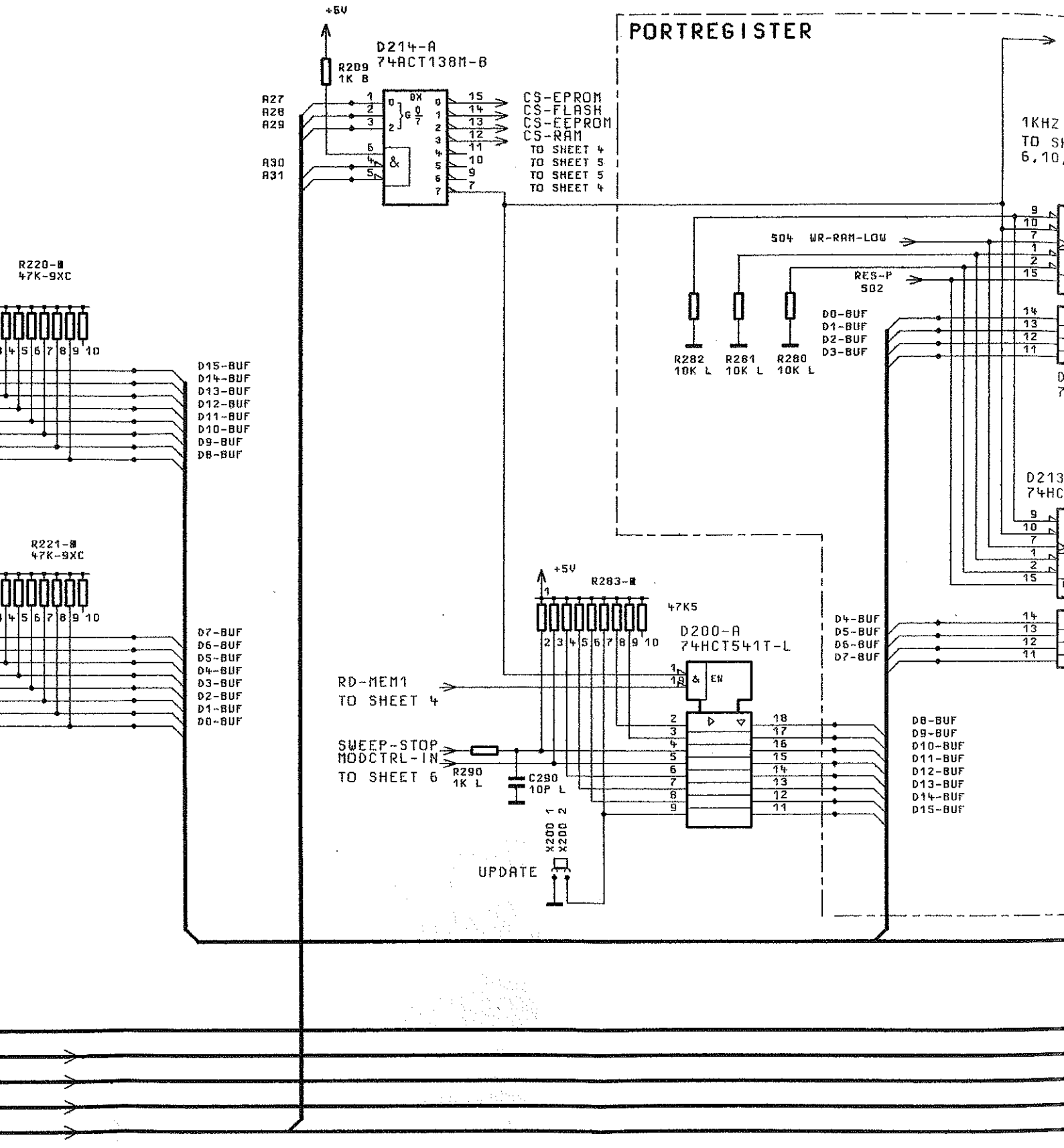
ZEICHN.-NR.



STROMLAUF GILT FUER VAR.02 CIRCUIT DIAGRAM IS VALID FOR MOD.02

04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7308.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME				BLATT-NR. 3+

ROHDE&SCHWARZ — ZU GERAET SMP — REG.I.V. 1035.5005 — ERSTE Z. 1035.5440



C212
100N B

C214
100N L

C213
100N L

D214-B
74ACT138M-B

D216-B
74ACT573M-L

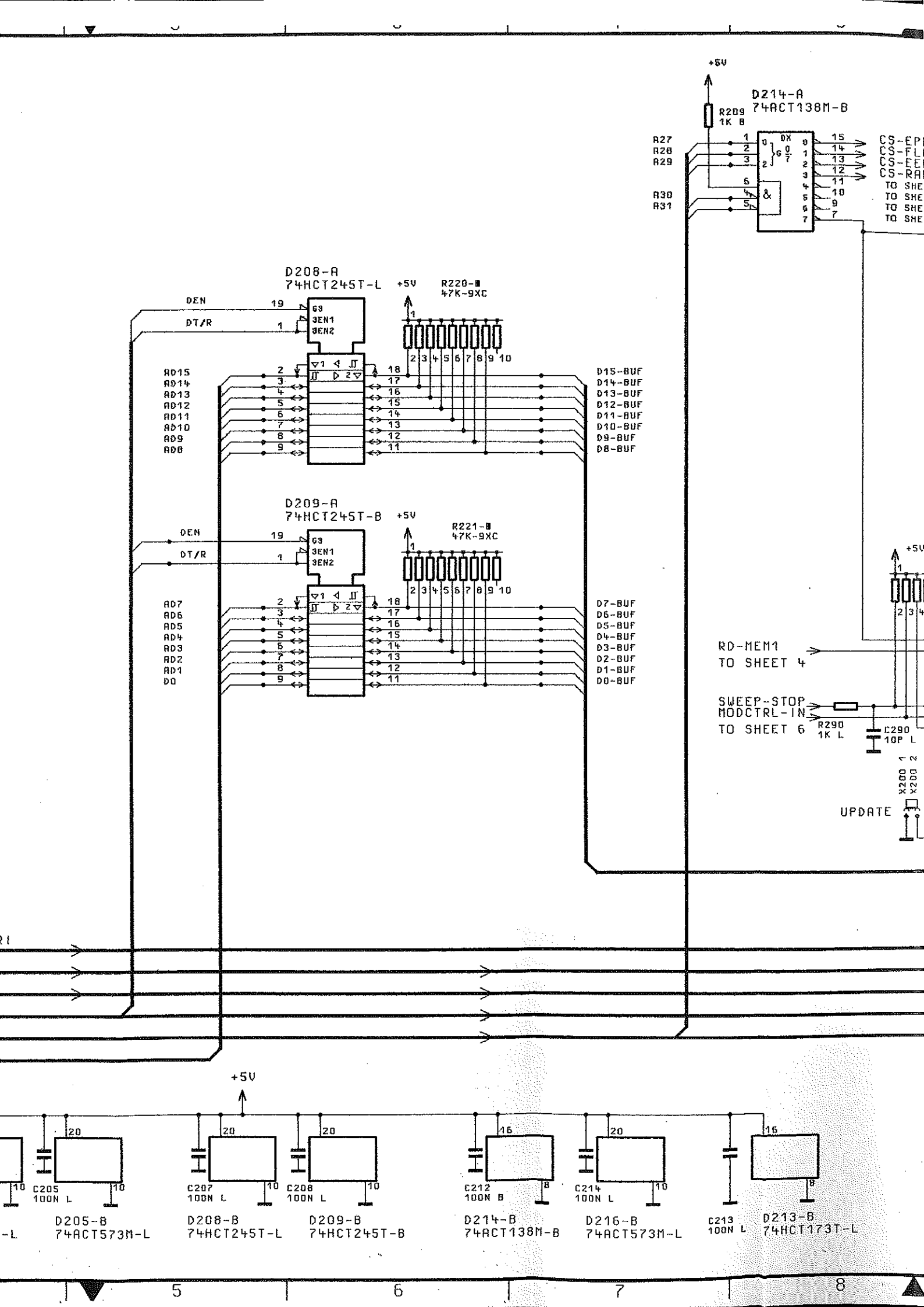
D213-B
74HCT173T-L

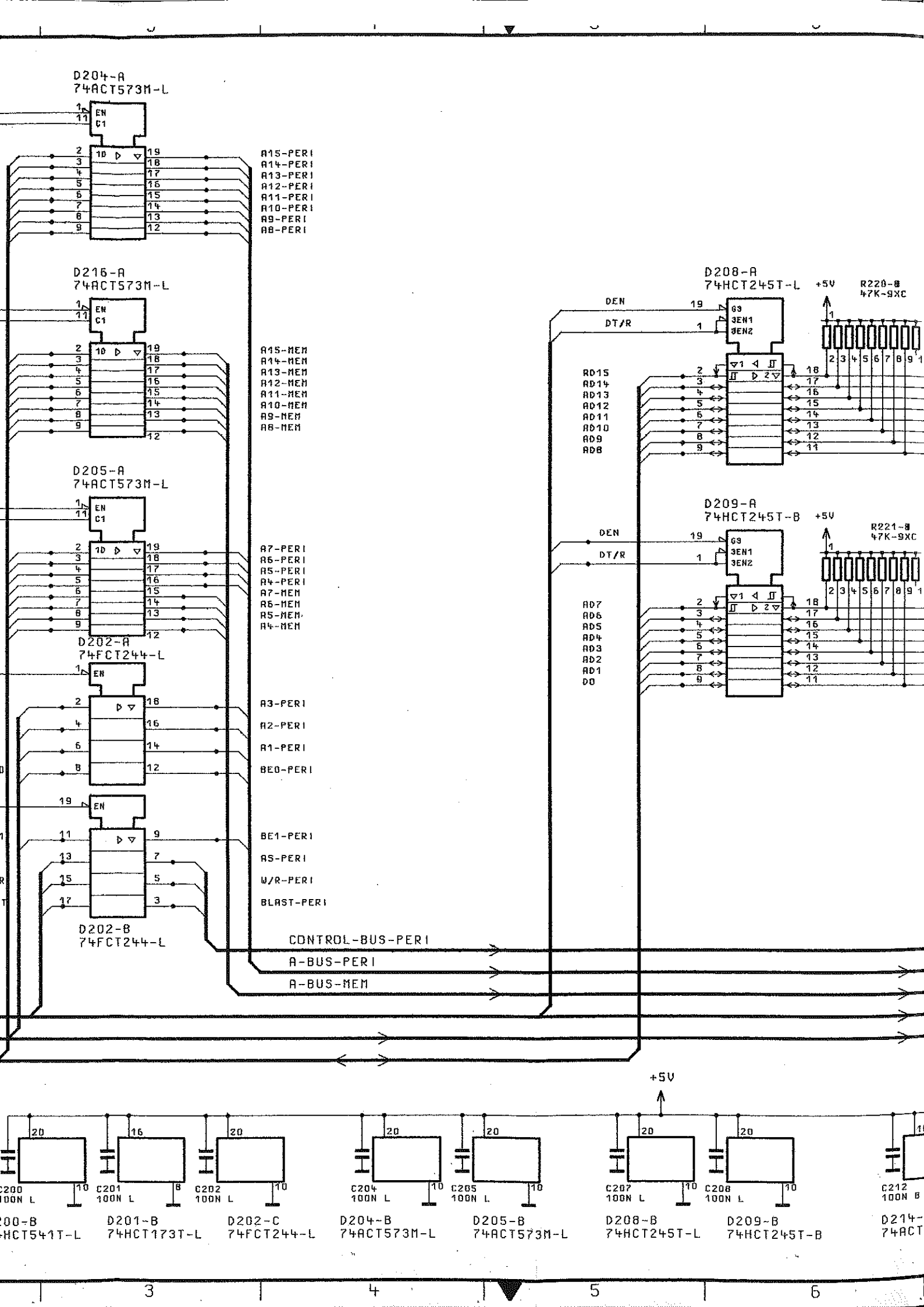
STROMLAUF GILT FUER VAR.02 CIRCUIT

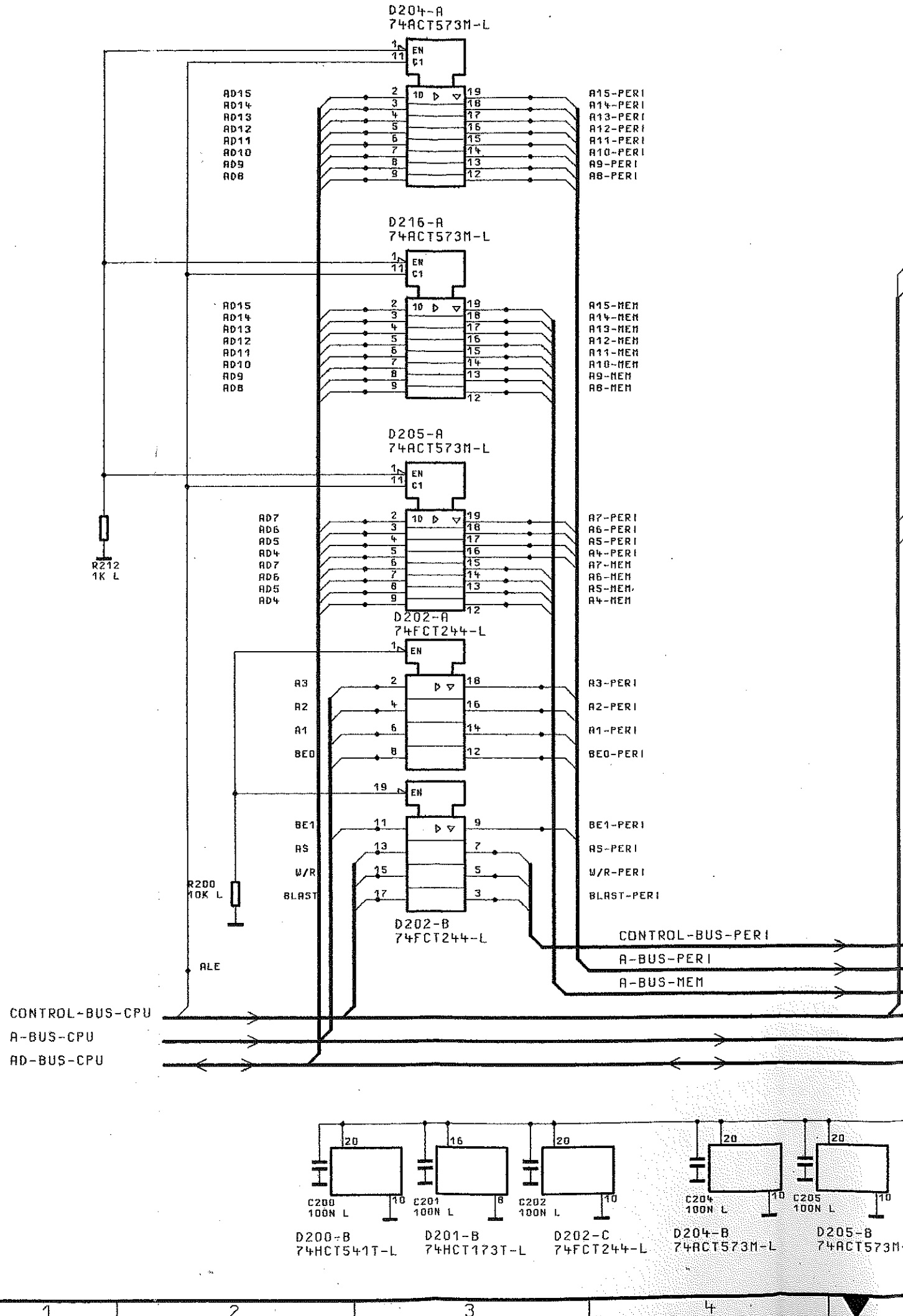
B4/	48730	02.05.94	DR	1GPK	TAG	NAME
				BEARB.		DR
				GEPR.		
				NORM		
				PLOTT	03.05.94	
				/		
REND. IND.	AENDERUNGS-MITTEILUNG		DATUM	NAME		

ROHDE&SCHWARZ

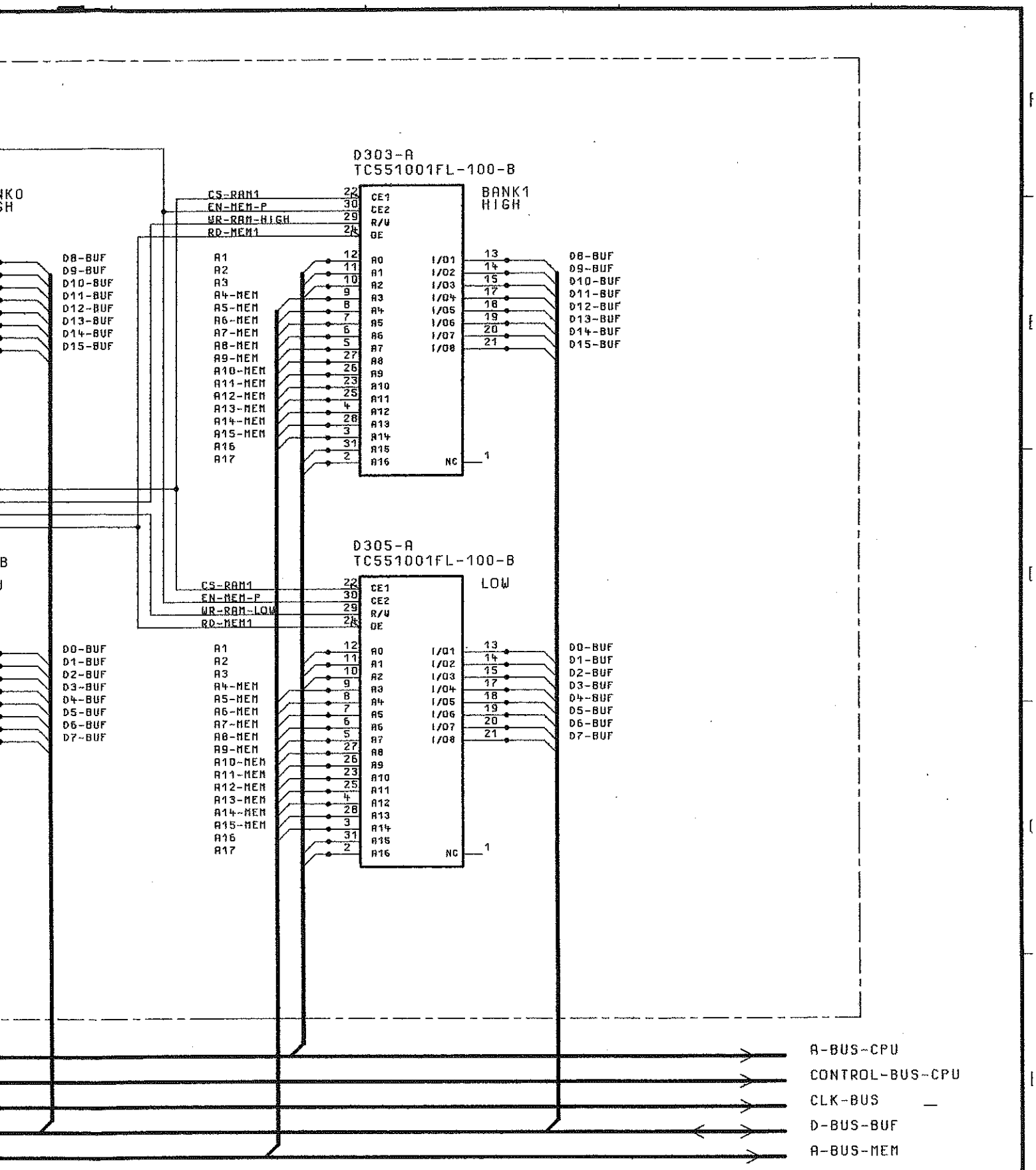
ZU GERAET SMP


+5V
D214-A
74ACT138M-B
R209
1K B
A27
A28
A29
A30
A31
CS-EP
CS-FL
CS-EE
CS-RA
TO SHE
TO SHE
TO SHE
TO SHE
D208-A
74HCT245T-L
+5V
R220-B
47K-9XC
DEN
DT/R
AD15
AD14
AD13
AD12
AD11
AD10
AD9
AD8
D15-BUF
D14-BUF
D13-BUF
D12-BUF
D11-BUF
D10-BUF
D9-BUF
D8-BUF
D209-A
74HCT245T-B
+5V
R221-B
47K-9XC
DEN
DT/R
AD7
AD6
AD5
AD4
AD3
AD2
AD1
D0
D7-BUF
D6-BUF
D5-BUF
D4-BUF
D3-BUF
D2-BUF
D1-BUF
D0-BUF
+5V
RD-MEM1
TO SHEET 4
SWEEP-STOP
MODCTRL-IN
TO SHEET 6
R290
1K L
C290
10P L
X200
X200
UPDATE
+5V
C205
100N L
D205-B
74ACT573M-L
C207
100N L
D208-B
74HCT245T-L
C208
100N L
D209-B
74HCT245T-B
C212
100N B
D214-B
74ACT138M-B
C214
100N L
D216-B
74ACT573M-L
C213
100N L
D213-B
74HCT173T-L

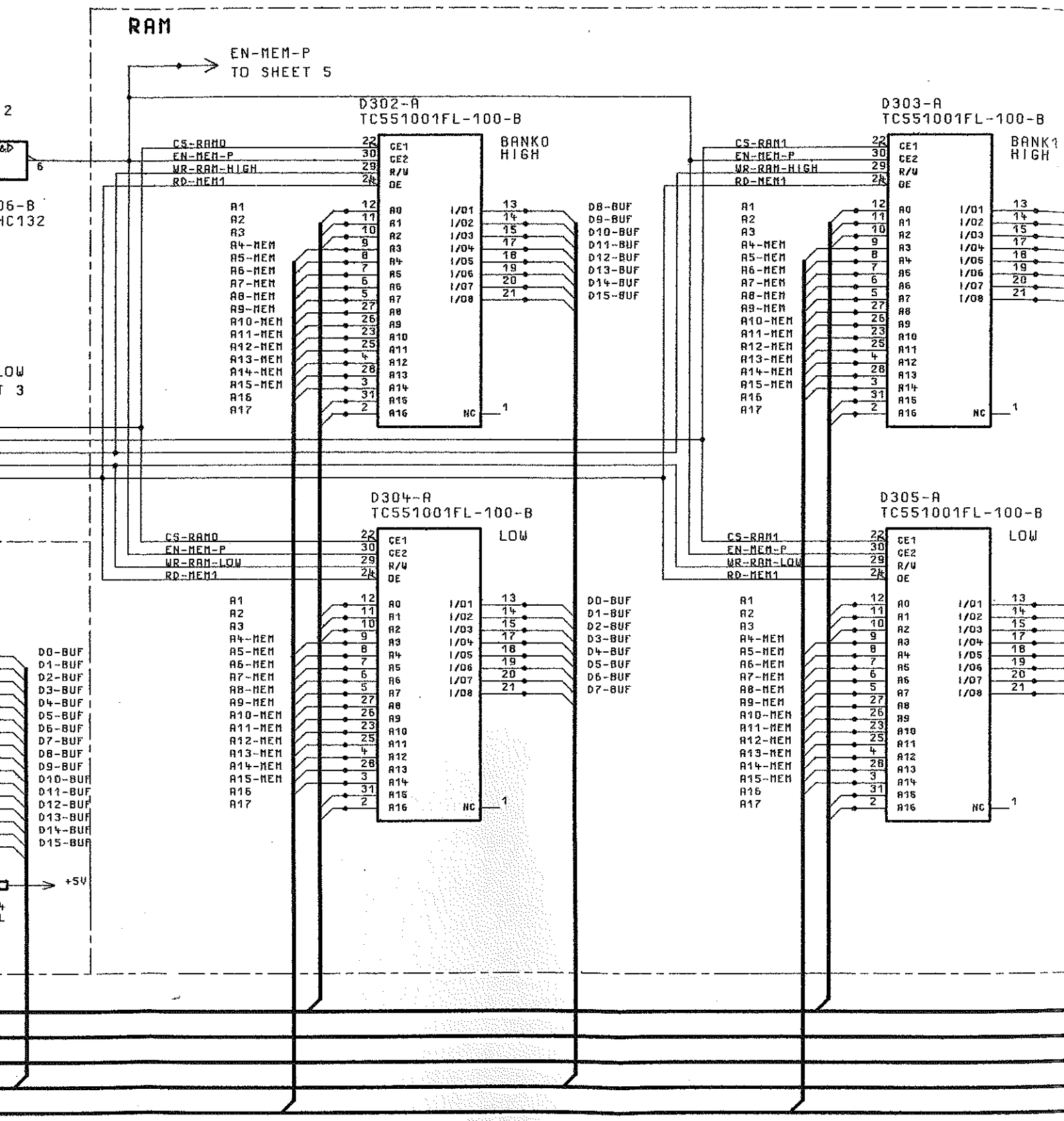

D204-A
74ACT573M-L
A15-PERI
A14-PERI
A13-PERI
A12-PERI
A11-PERI
A10-PERI
A9-PERI
A8-PERI
D216-A
74ACT573M-L
A15-MEM
A14-MEM
A13-MEM
A12-MEM
A11-MEM
A10-MEM
A9-MEM
A8-MEM
D205-A
74ACT573M-L
A7-PERI
A6-PERI
A5-PERI
A4-PERI
A7-MEM
A6-MEM
A5-MEM
A4-MEM
D202-A
74FCT244-L
A3-PERI
A2-PERI
A1-PERI
BE0-PERI
BE1-PERI
AS-PERI
W/R-PERI
BLAST-PERI
D202-B
74FCT244-L
D208-A
74HCT245T-L
+5V
R220-8
47K-9XC
DEN
DT/R
AD15
AD14
AD13
AD12
AD11
AD10
AD9
AD8
D209-A
74HCT245T-B
+5V
R221-8
47K-9XC
DEN
DT/R
AD7
AD6
AD5
AD4
AD3
AD2
AD1
D0
CONTROL-BUS-PERI
A-BUS-PERI
A-BUS-MEM
+5V
C200
100N L
C201
100N L
C202
100N L
C204
100N L
C205
100N L
C207
100N L
C208
100N L
C212
100N B
D200-B
HCT541T-L
D201-B
74HCT173T-L
D202-C
74FCT244-L
D204-B
74ACT573M-L
D205-B
74ACT573M-L
D208-B
74HCT245T-L
D209-B
74HCT245T-B
D214-
74ACT
3
4
5
6



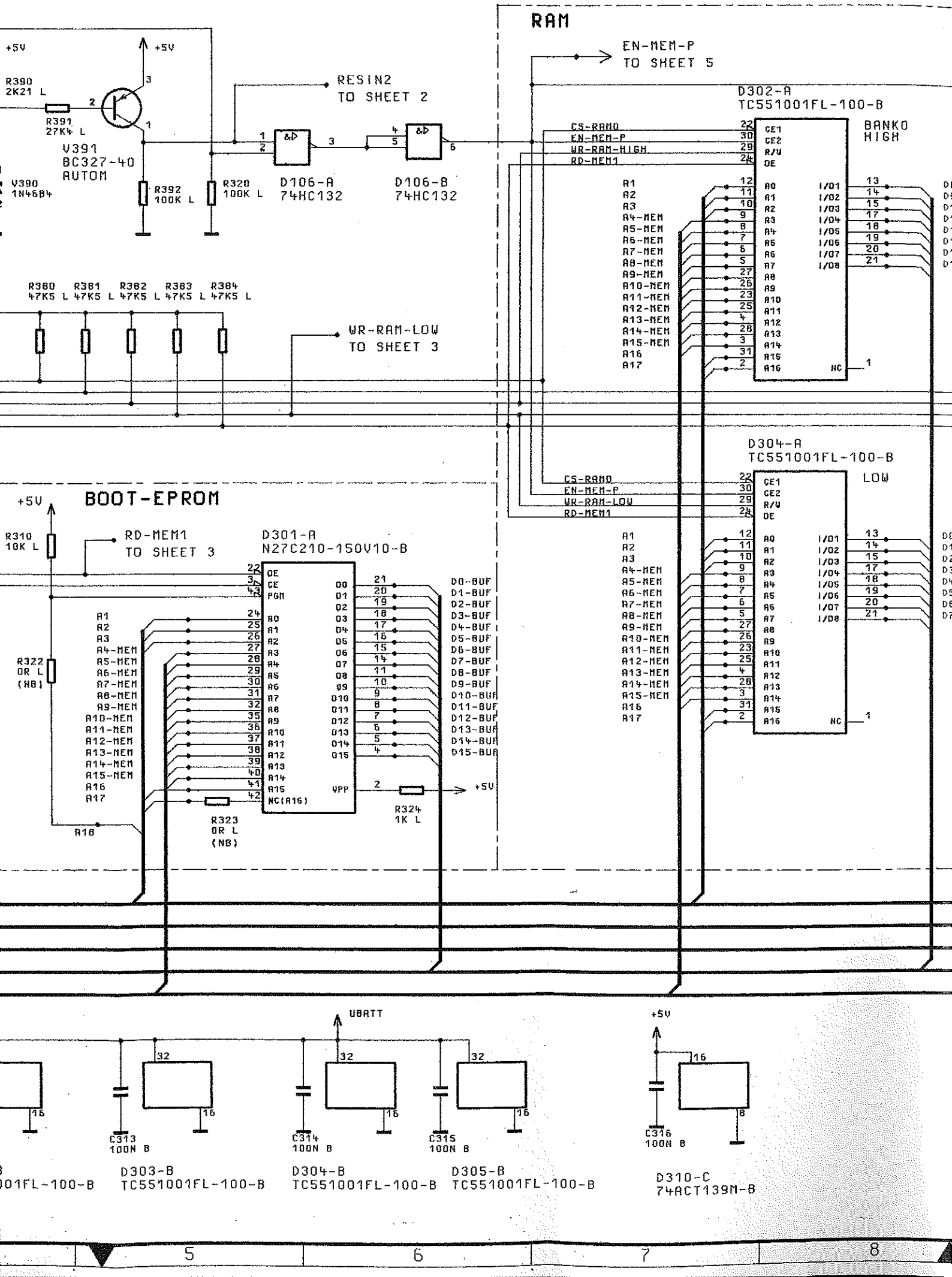
BEHALTEN WIR UNS ALLE RECHTE VOR

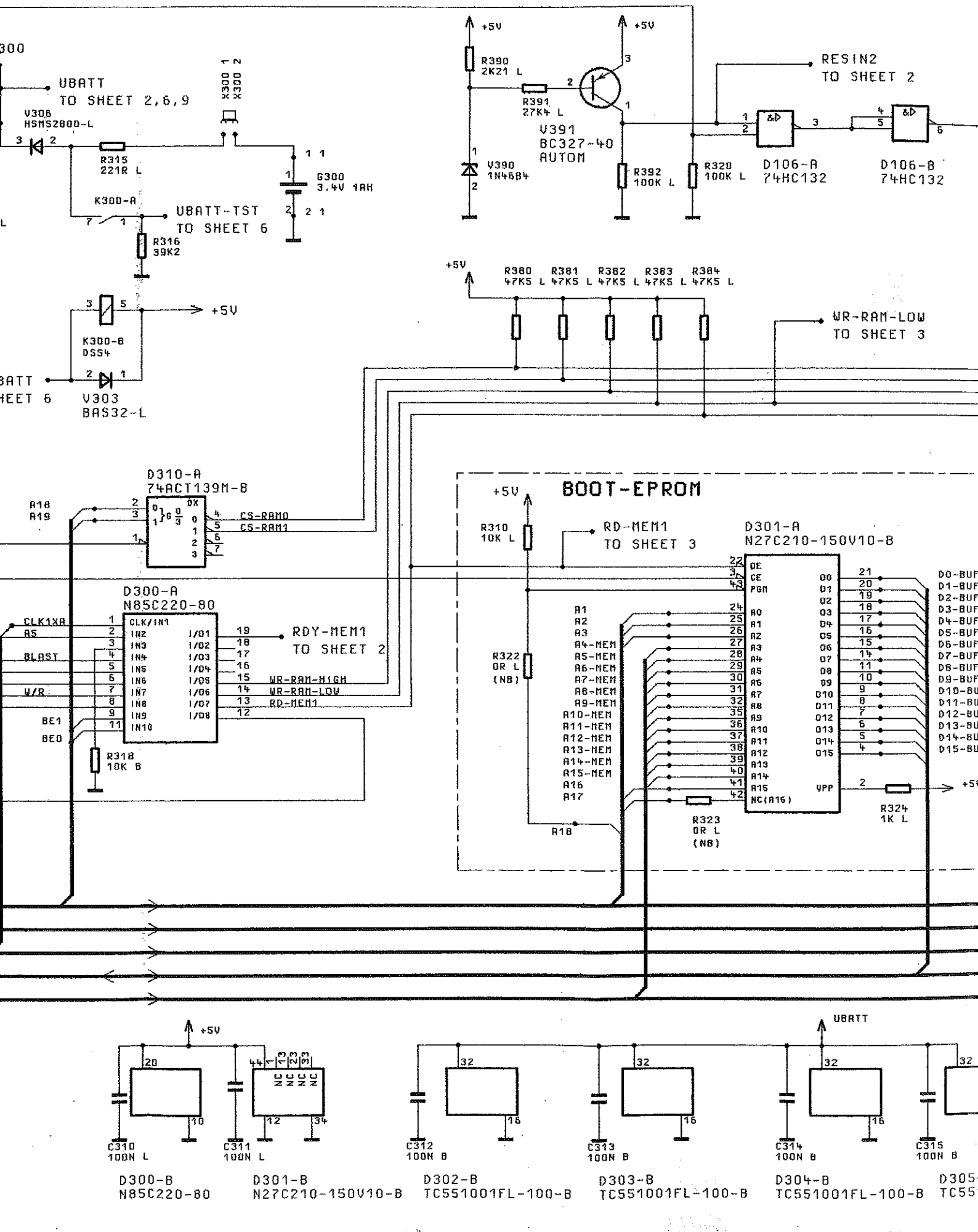


D4/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7308.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			BLATT-NR. 4+
				ZU GERAET SMP			REG.NR. 1035.5005 ERSTE Z. 1035.5440

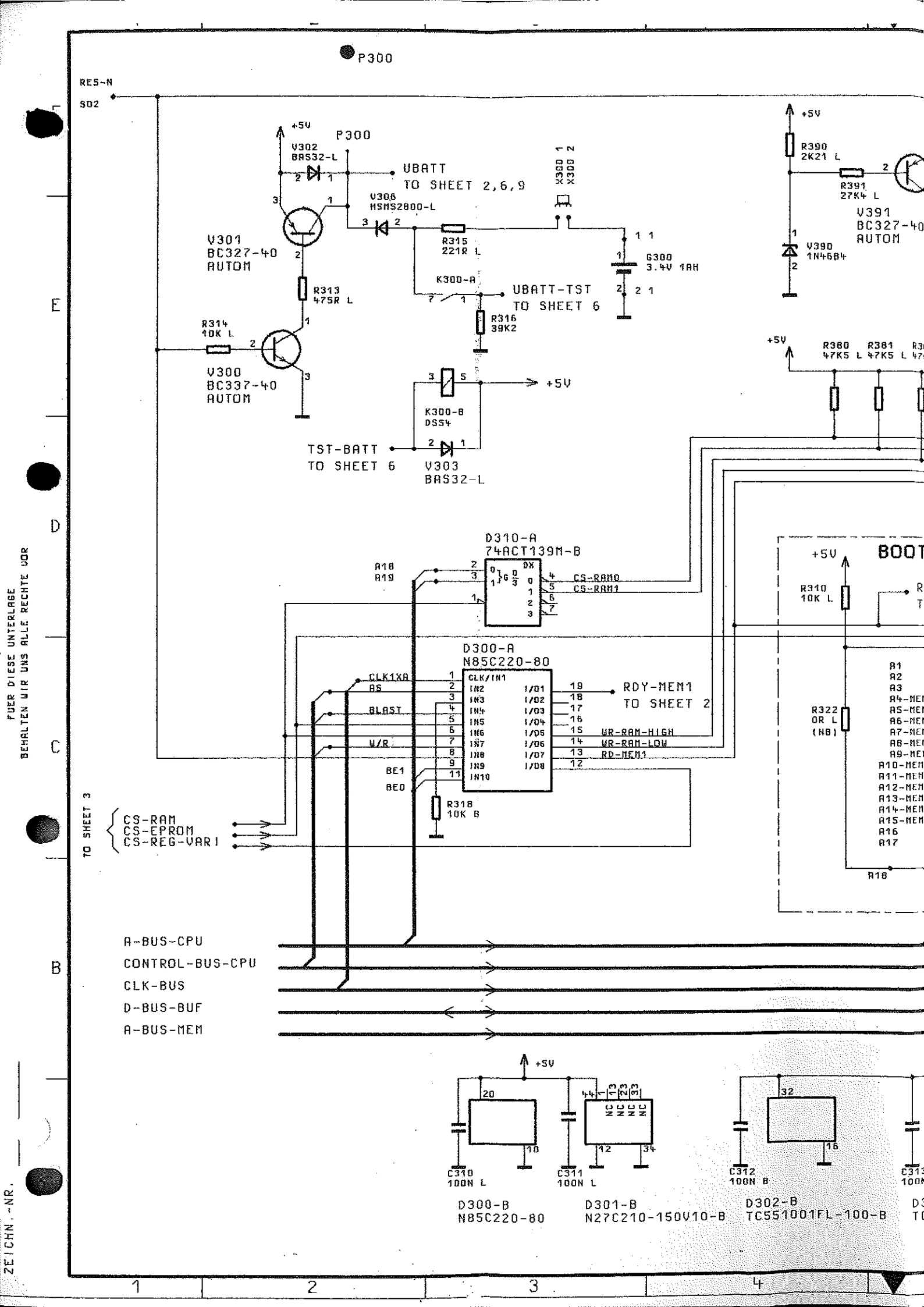


04/	48730	02.05.94	DR	1GPK	TAG	NA
				BEARB.		D
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWA		
				ZU GERAET	SMP	

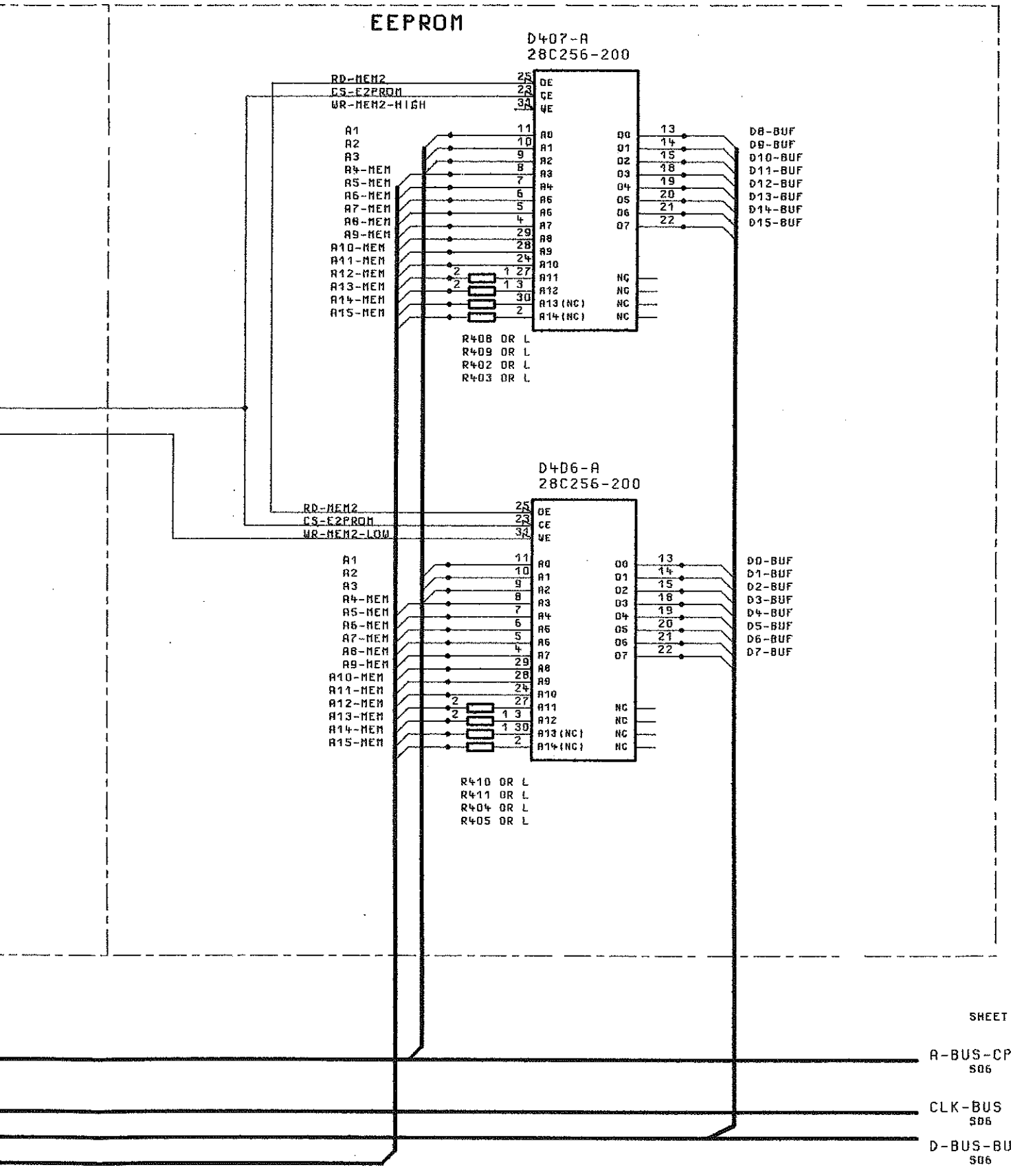

RAM
+5V
R390
2K21 L
R391
27K4 L
V391
BC327-40
AUTOM
V390
1N4684
R392
100K L
R320
100K L
RESIN2
TO SHEET 2
D106-A
74HC132
D106-B
74HC132
EN-MEM-P
TO SHEET 5
D302-A
TC551001FL-100-B
BANKO
HIGH
CS-RAM0
EN-MEM-P
WR-RAM-HIGH
RD-MEM1
R360 47K5 L
R361 47K5 L
R362 47K5 L
R363 47K5 L
R364 47K5 L
WR-RAM-LOW
TO SHEET 3
D304-A
TC551001FL-100-B
LOW
BOOT-EPROM
R310
10K L
RD-MEM1
TO SHEET 3
D301-A
N27C210-150V10-B
R322
0R L
(NB)
R323
0R L
(NB)
R324
1K L
A18
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
UBATT
C313
100N B
C314
100N B
C315
100N B
C316
100N B
D303-B
TC551001FL-100-B
D304-B
TC551001FL-100-B
D305-B
TC551001FL-100-B
D310-C
74ACT139M-B



D300-B N85C220-80 | D301-B N27C210-150V10-B | D302-B TC551001FL-100-B | D303-B TC551001FL-100-B | D304-B TC551001FL-100-B | D305 TC55


P300
RES-N
SO2
+5V
V302
BAS32-L
P300
UBATT
TO SHEET 2,6,9
V306
HSMS2800-L
X300 1
X300 2
V301
BC327-40
AUTOM
R315
221R L
G300
3.4V 1AH
K300-A
UBATT-TST
TO SHEET 6
R313
475R L
R316
39K2
R314
10K L
V300
BC337-40
AUTOM
K300-B
DS54
+5V
TST-BATT
TO SHEET 6
V303
BAS32-L
R390
2K21 L
R391
27K4 L
V391
BC327-40
AUTOM
V390
1N4684
R360
47K5 L
R381
47K5 L
D310-A
74ACT139M-B
A18
A19
CS-RAM0
CS-RAM1
D300-A
N85C220-80
CLK1XA
BS
BLAST
W/R
BE1
BE0
RDY-MEM1
TO SHEET 2
WR-RAM-HIGH
WR-RAM-LOW
RD-MEM1
R318
10K B
TO SHEET 3
CS-RAM
CS-EPROM
CS-REG-VARI
BOOT
R310
10K L
R322
OR L
(NB)
A18
A-BUS-CPU
CONTROL-BUS-CPU
CLK-BUS
D-BUS-BUF
A-BUS-MEM
C310
100N L
C311
100N L
C312
100N B
D300-B
N85C220-80
D301-B
N27C210-150V10-B
D302-B
TC551001FL-100-B
FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR
ZEICHN.-NR.

EEPROM



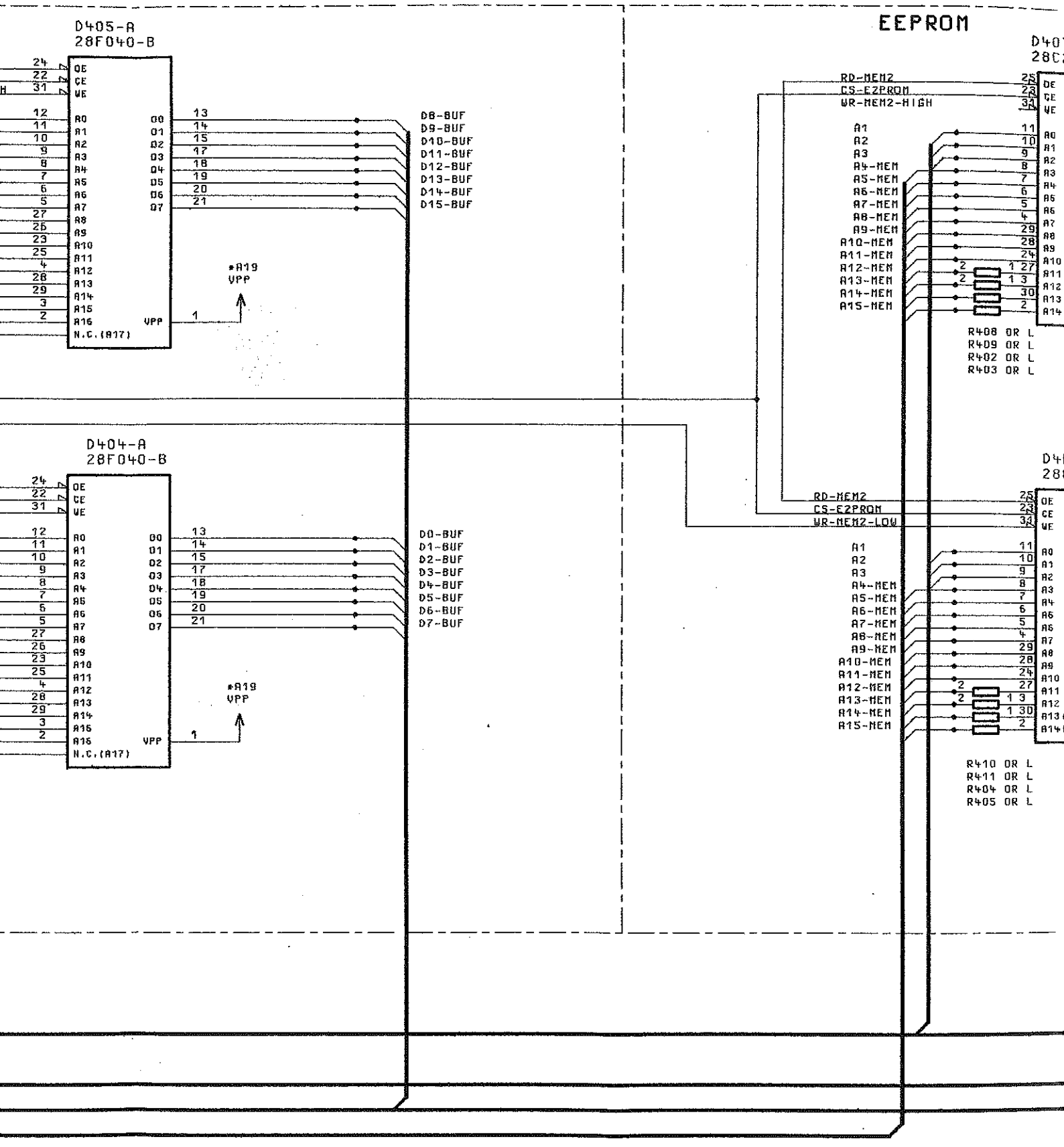
SHEET

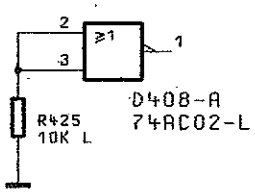
A-BUS-CPU
S06

CLK-BUS
S06

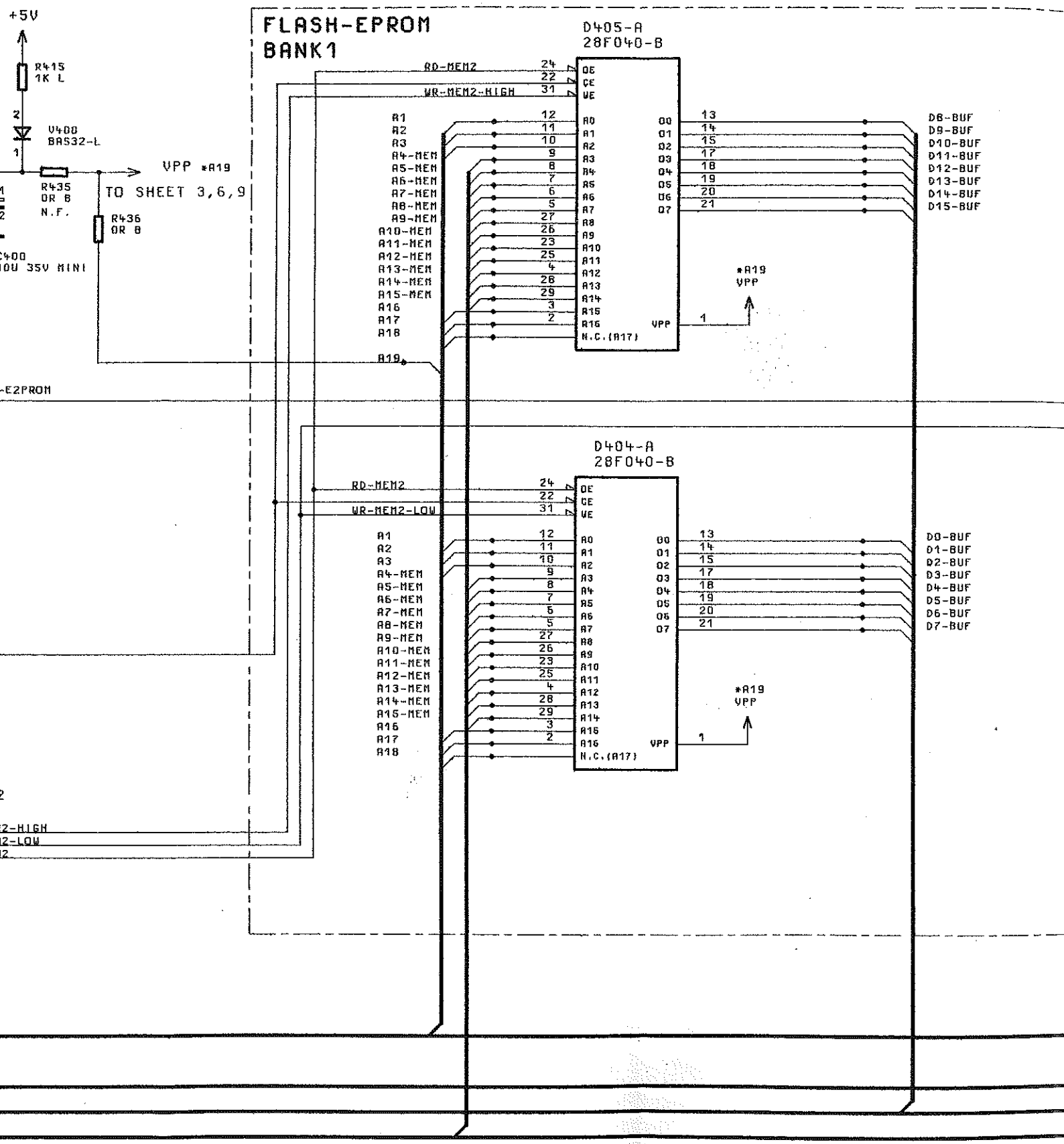
D-BUS-BUF
S06

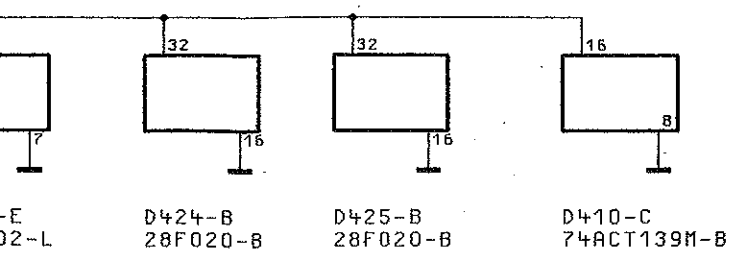
04/	48730	02.05.94	DR	16PK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7308.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			BLATT-NR. 5+
				ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440

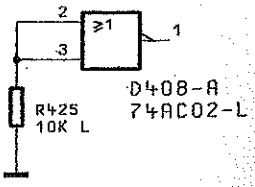


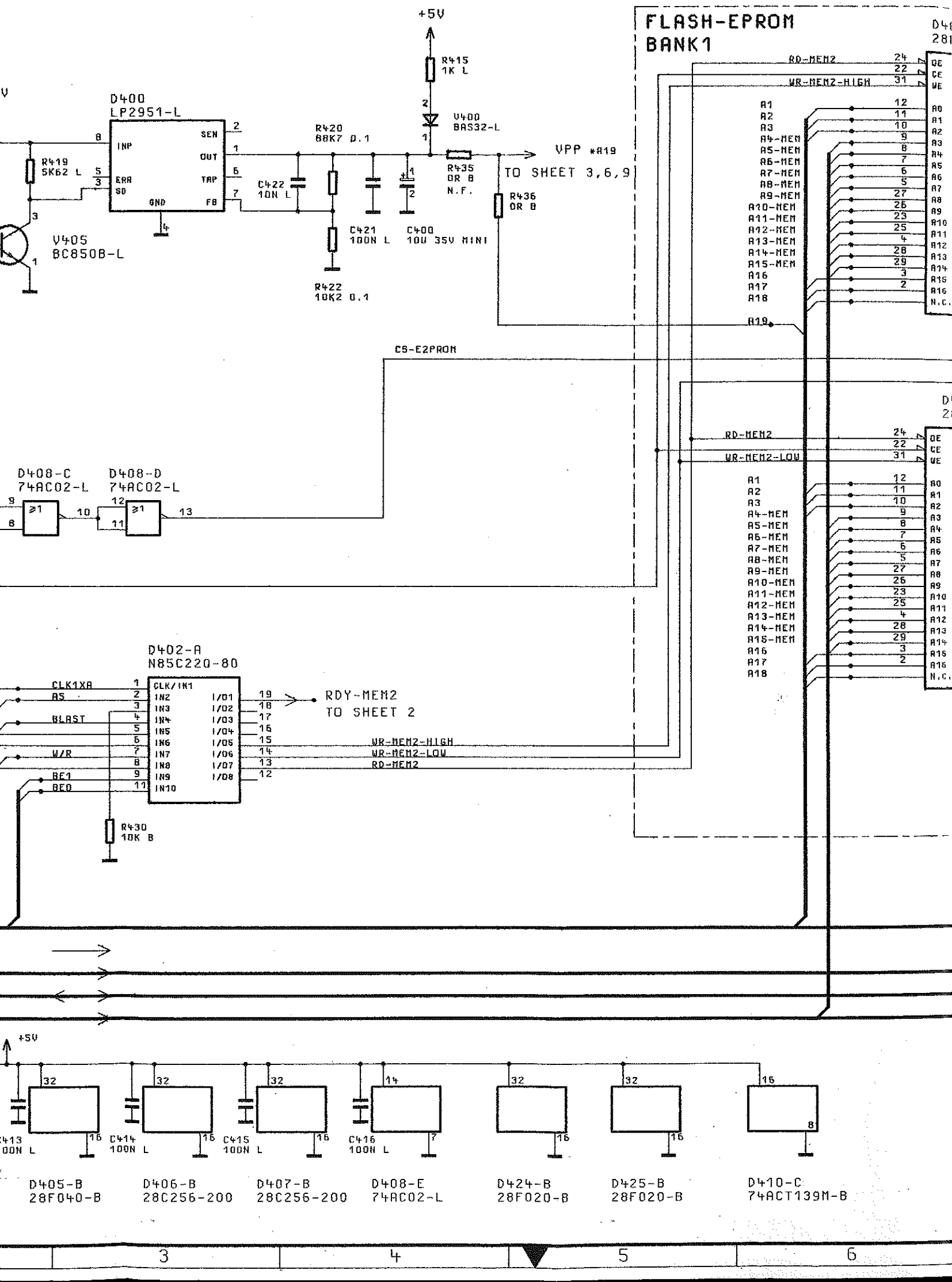


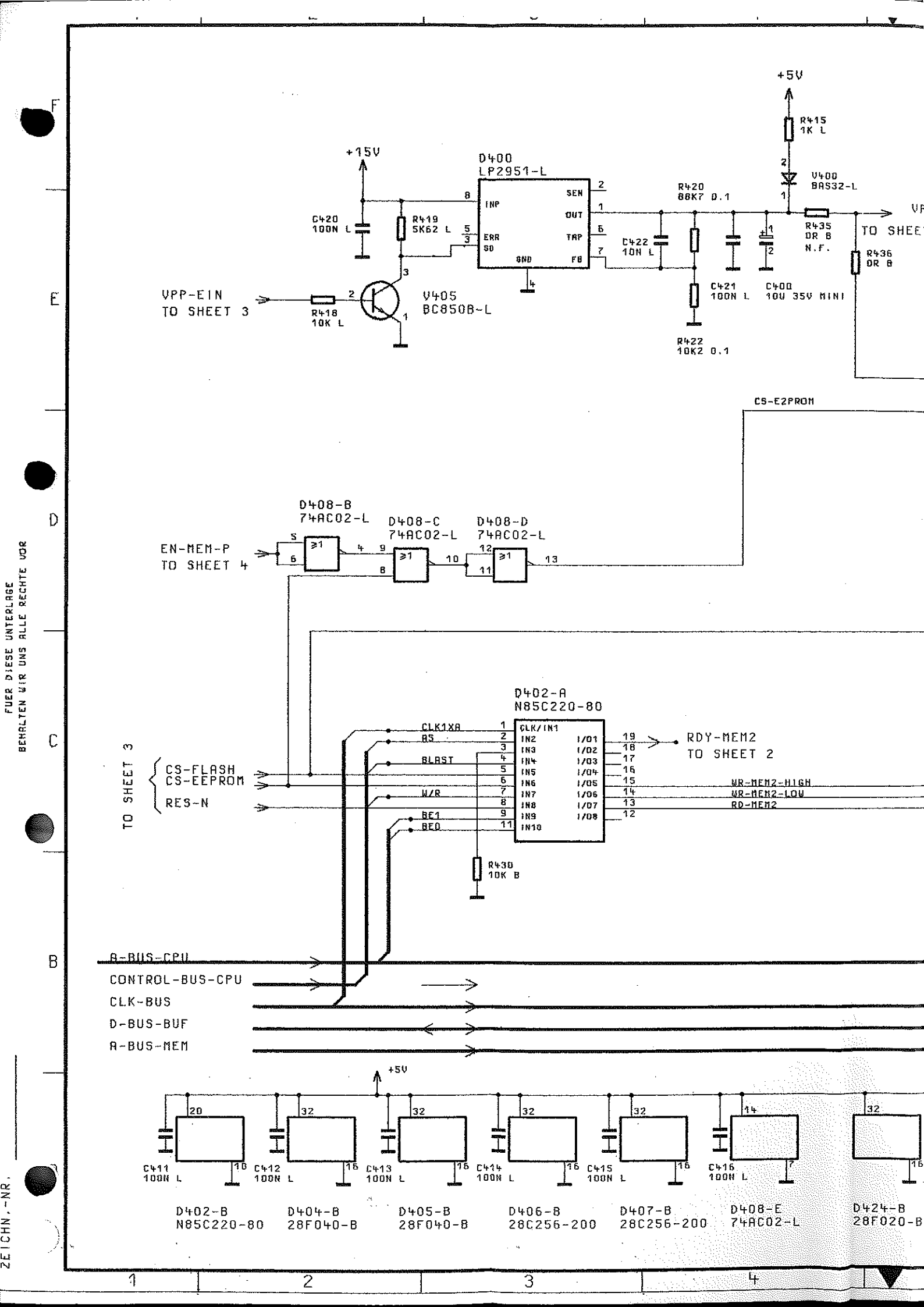
04/	48730	02.05.94	DR	16PK	TAG	NAME
				BEARB.		DR
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ		
				ZU GERAET	SMP	


+5V
R415
1K L
V400
BAS32-L
VPP *A19
TO SHEET 3,6,9
R435
0R 8
N.F.
R436
0R 8
C400
10U 35V MINI
-E2PROM
FLASH-EPROM
BANK1
D405-A
28F040-B
RD-MEM2
WR-MEM2-HIGH
A1
A2
A3
A4-MEM
A5-MEM
A6-MEM
A7-MEM
A8-MEM
A9-MEM
A10-MEM
A11-MEM
A12-MEM
A13-MEM
A14-MEM
A15-MEM
A16
A17
A18
A19
OE
CE
WE
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
A16
N.C.(A17)
O0
O1
O2
O3
O4
O5
O6
O7
VPP
*A19
VPP
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
D404-A
28F040-B
RD-MEM2
WR-MEM2-LOW
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
2-HIGH
2-LOW
2


32
16
32
16
16
8
7
D424-B
28F020-B
D425-B
28F020-B
D410-C
74ACT139M-B

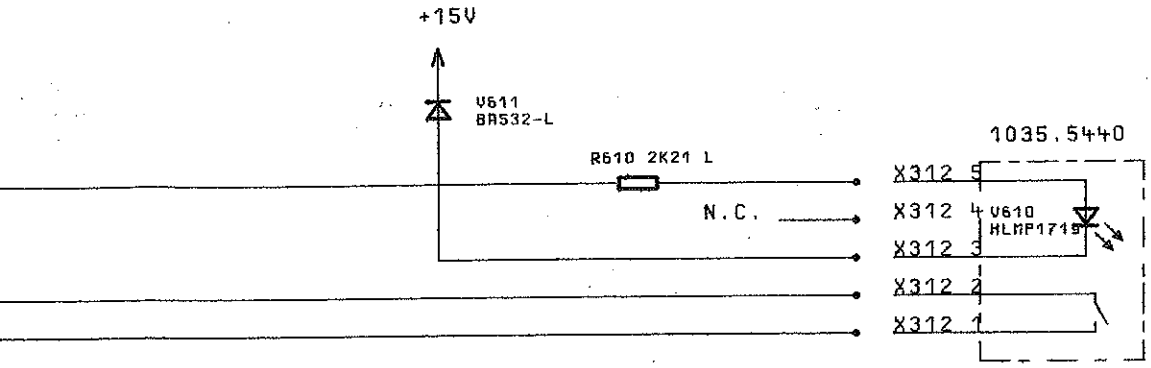

2
≥1
1
3
R425
10K L
D408-A
74AC02-L


FLASH-EPROM
BANK1
+5V
D400
LP2951-L
INP
SEN
OUT
TAP
FB
GND
ERR
SD
R419
5K62 L
R420
88K7 0.1
R415
1K L
V400
BAS32-L
R435
0R 8
N.F.
R436
0R 8
VPP *A19
TO SHEET 3,6,9
C422
10N L
C421
100N L
C400
10U 35V MINI
R422
10K2 0.1
V405
BC850B-L
RD-MEM2
WR-MEM2-HIGH
WR-MEM2-LOW
A1
A2
A3
A4-MEM
A5-MEM
A6-MEM
A7-MEM
A8-MEM
A9-MEM
A10-MEM
A11-MEM
A12-MEM
A13-MEM
A14-MEM
A15-MEM
A16
A17
A18
A19
CS-E2PROM
D408-C
74AC02-L
D408-D
74AC02-L
≥1
D402-A
N85C220-80
CLK1X8
AS
BLAST
W/R
BE1
BE0
CLK/IN1
IN2
IN3
IN4
IN5
IN6
IN7
IN8
IN9
IN10
I/O1
I/O2
I/O3
I/O4
I/O5
I/O6
I/O7
I/O8
RDY-MEM2
TO SHEET 2
R430
10K B
+5V
C413
100N L
C414
100N L
C415
100N L
C416
100N L
D405-B
28F040-B
D406-B
28C256-200
D407-B
28C256-200
D408-E
74AC02-L
D424-B
28F020-B
D425-B
28F020-B
D410-C
74ACT139M-B



FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

ZEICHN.-NR.

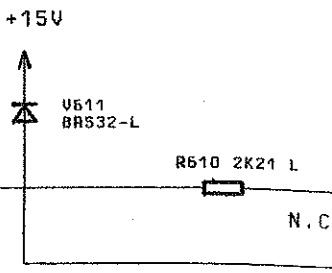


RXD1
TXD1
CTS1
RTS1

X-AXIS
DIAG-5V
DIAG-15V
TRIGGER
AUX-TRIG
SERBUS-CLK
SERBUS-DAT
SERBUS-INT
SERBUS-SYNC
SYSRESET
AC-FAIL

BLANK
MARKER
SWEEP-STOP
MODCTRL-IN
MODCTRL-OUT
RESERVE
RES-P

04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7308.01S
				ZU GERAET SMP			BLATT-NR. 6+
				REG.I.V. 1035.5005			ERSTE Z. 1035.5440



VS12-P

POWER SWITCH

POWER SWITCHGND

N.C.

X-AXIS

DIAG-5V

DIAG-15V

TRIGGER

AUX-TRIG

SERBUS-CLK

SERBUS-DAT

SERBUS-INT

SERBUS-SYNC

SYSRESET

AC-FAIL

BLANK

MARKER

SWEEP-STOP

MODCTRL-IN

MODCTRL-OUT

RESERVE

RES-P

R611
10R L

04/	48730	02.05.94	DR	1GPK	TAG	
				BEARB.		
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHW		
				ZU GERRET	SMP	

X31B 27 VS12-P

X31B 26 POWER SWITCH
X31B 25 POWER SWITCHGND

X31B 50 N.C.
X31B 49
X31B 48
X31B 47
X31B 46

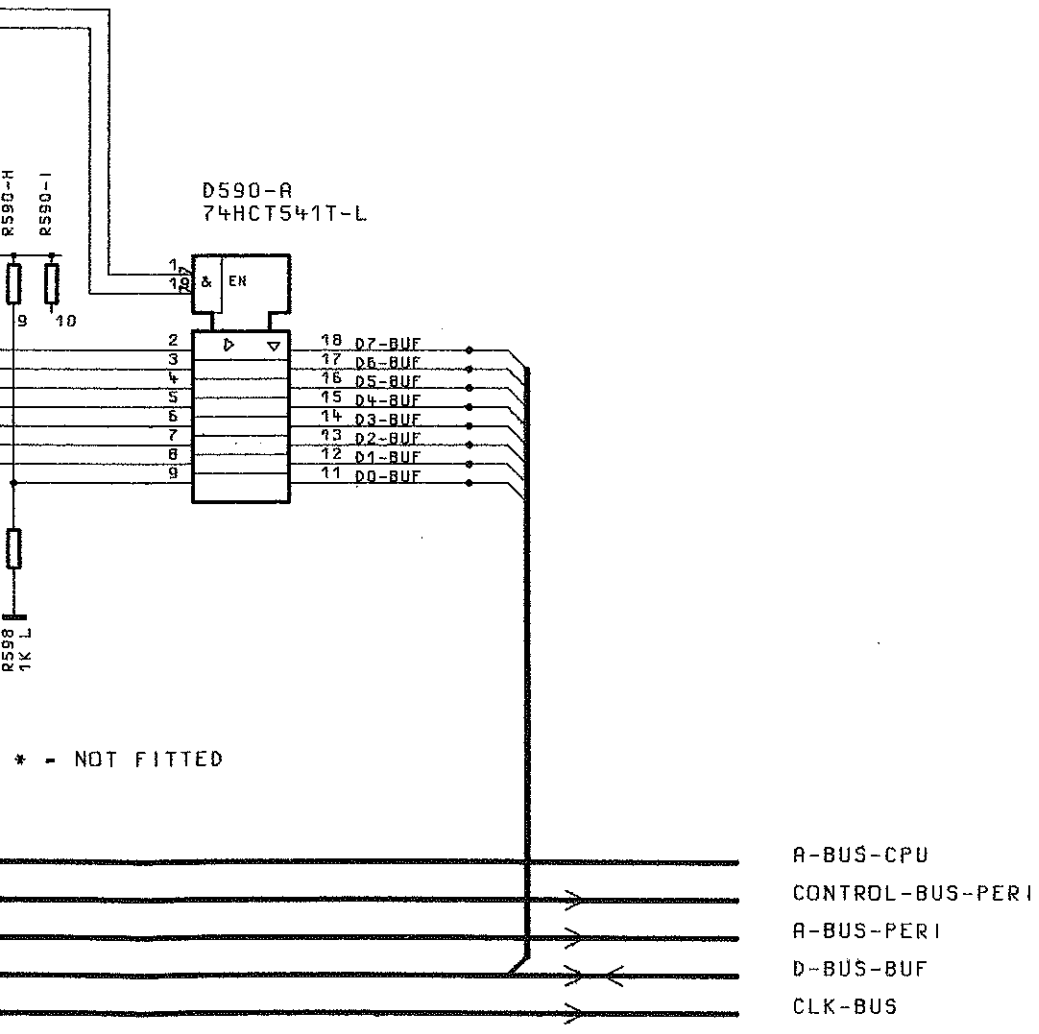
X31B 45 X-AXIS
X31B 44 DIAG-5V
X31B 43 DIAG-15V
X31B 42 TRIGGER
X31B 41 AUX-TRIG
X31B 40 SERBUS-CLK
X31B 39 SERBUS-DAT
X31B 38 SERBUS-INT
X31B 37 SERBUS-SYNC
X31B 36 SYSRESET
X31B 35 AC-FAIL

X31B 34 BLANK
X31B 33 MARKER
X31B 32 SWEEP-STOP
X31B 31 MODCTRL-IN
X31B 30 MODCTRL-OUT
X31B 29 RESERVE
X31B 28 RES-P

R611
10R L

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

ZEICHN.-NR.



04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7308.01S
REND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			BLATT-NR. 7+ V. BL.
				ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440

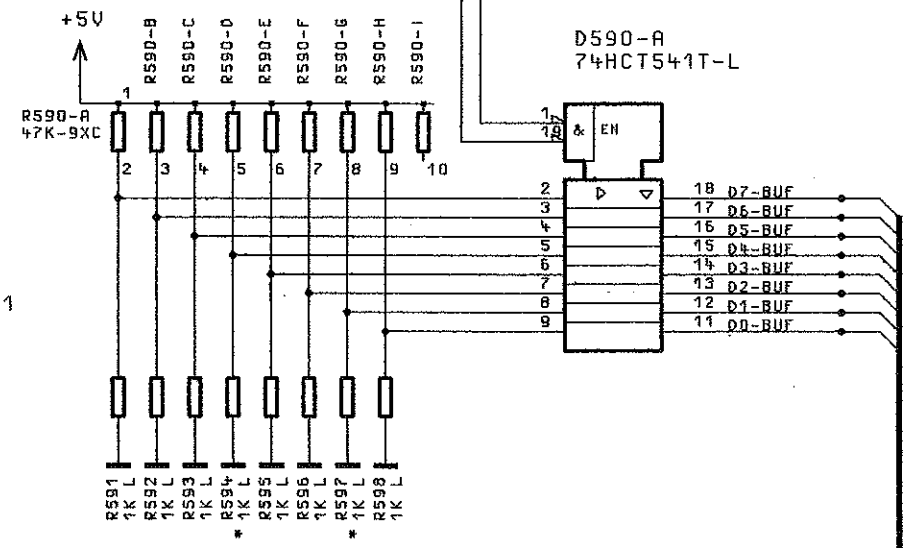
WR-REG1-HIGH

RD-PERI1 TO SHEET 7,8,9

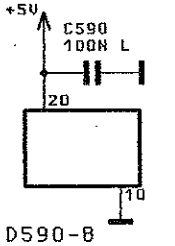
CS-REG1 TO SHEET 7,8
CS-REG2 TO SHEET 7,9
CS-REG3 TO SHEET 7,9

WR-REG2-HIGH TO SHEET 9

WR-REG4-LOW TO SHEET 11

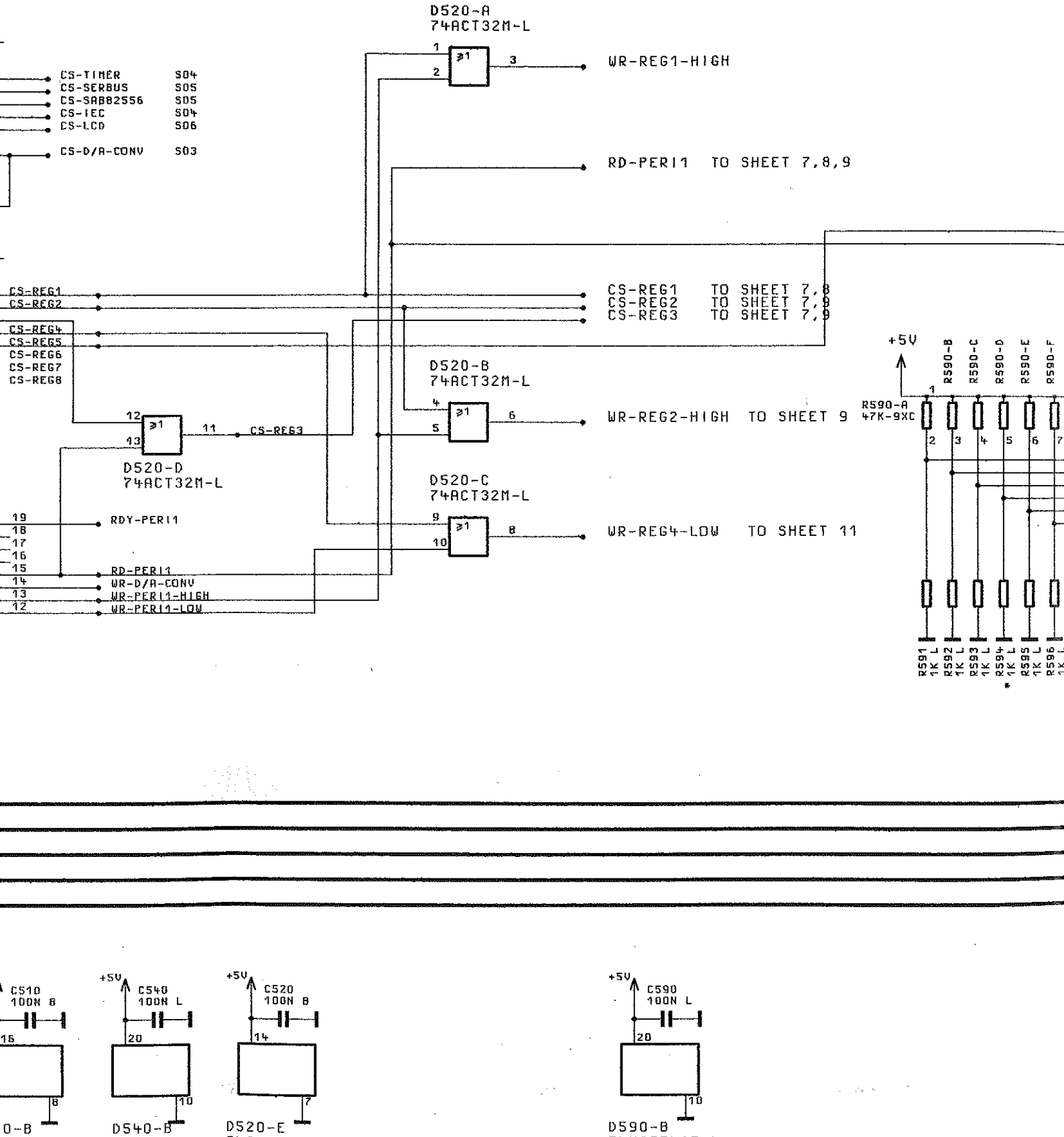


* - NOT FITTED

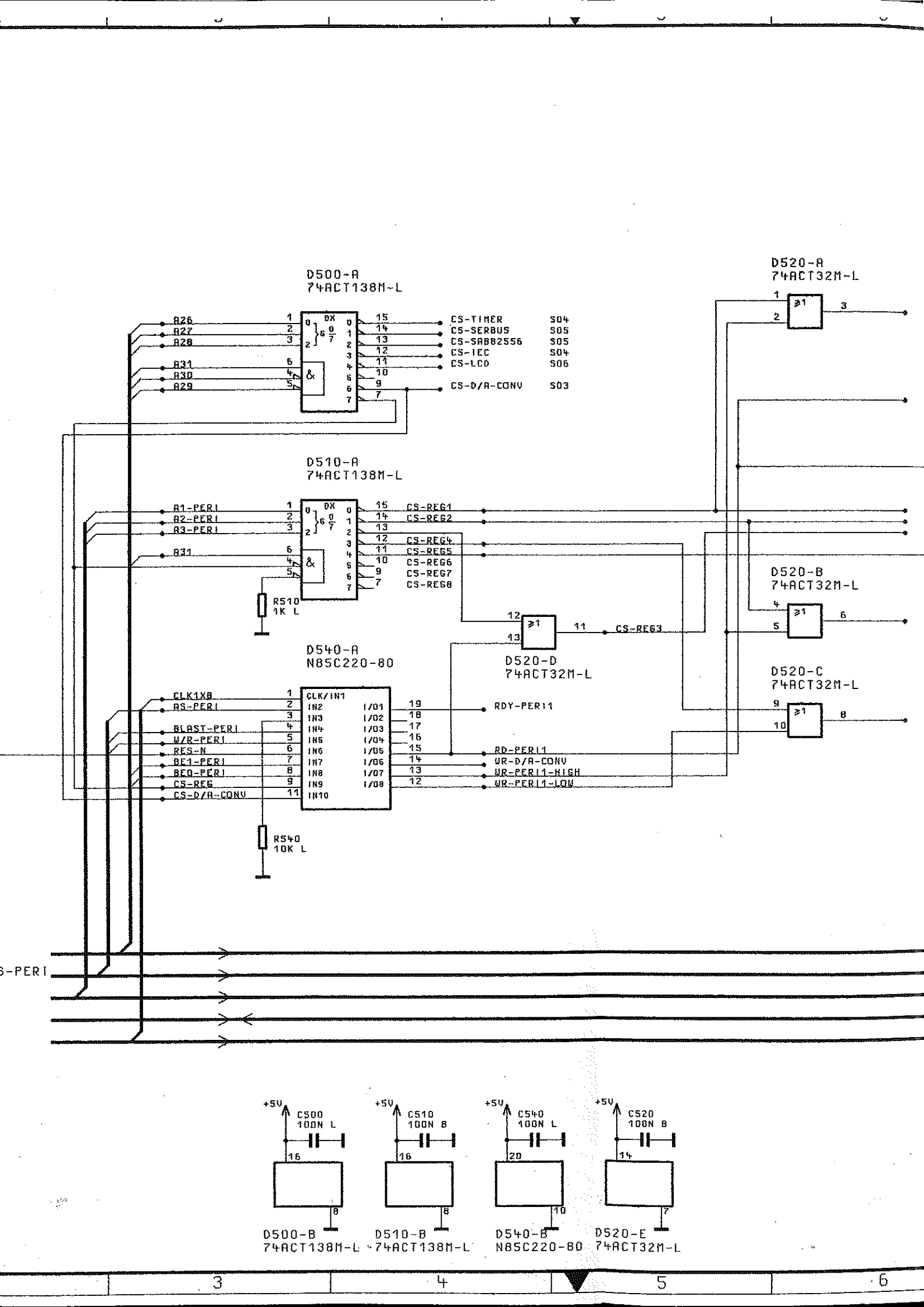


04/	48730	02.05.94	DR	1GPK	TAG	
				BEARB.		
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERRET	SMP	

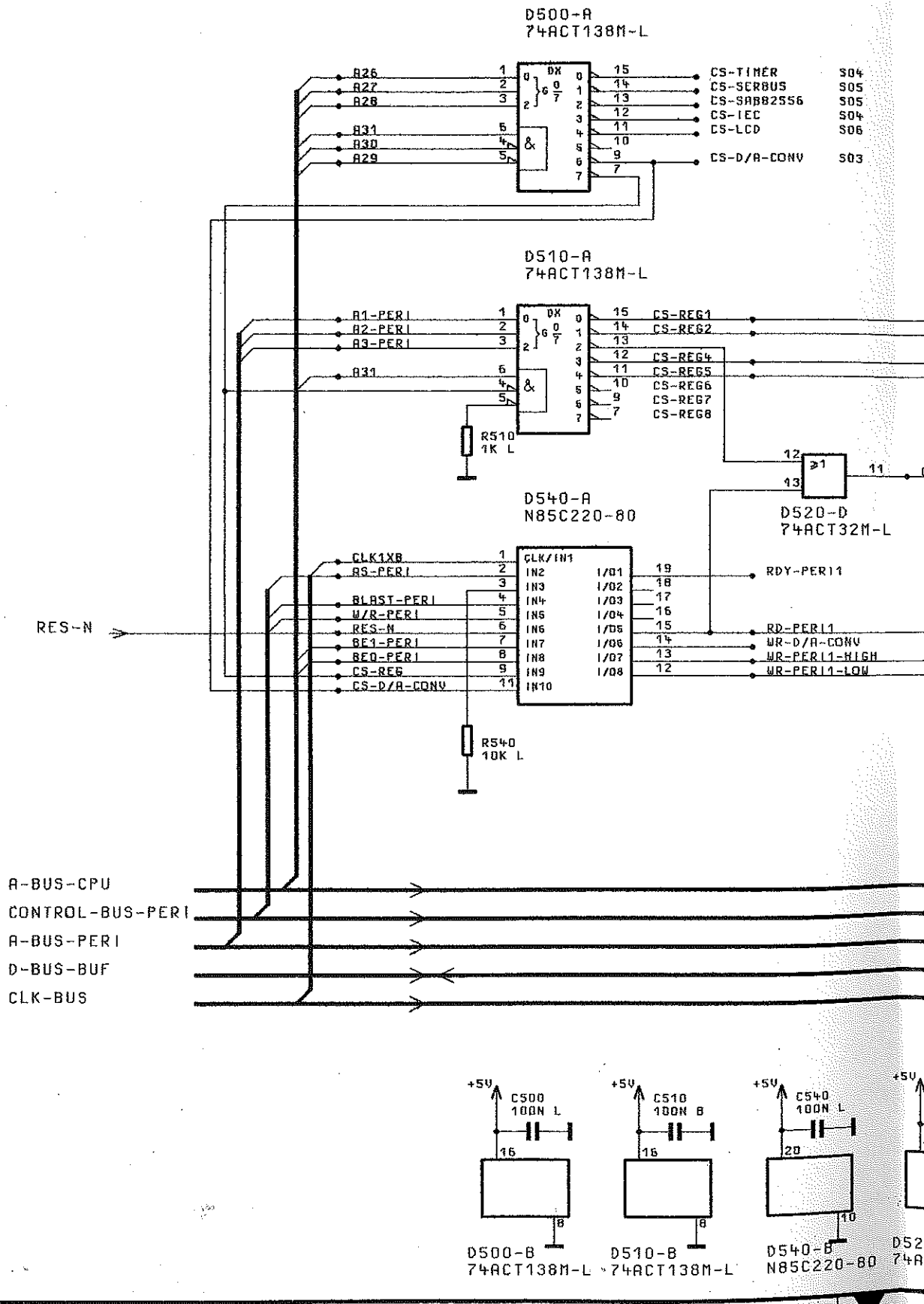
ROHDE&SCHW

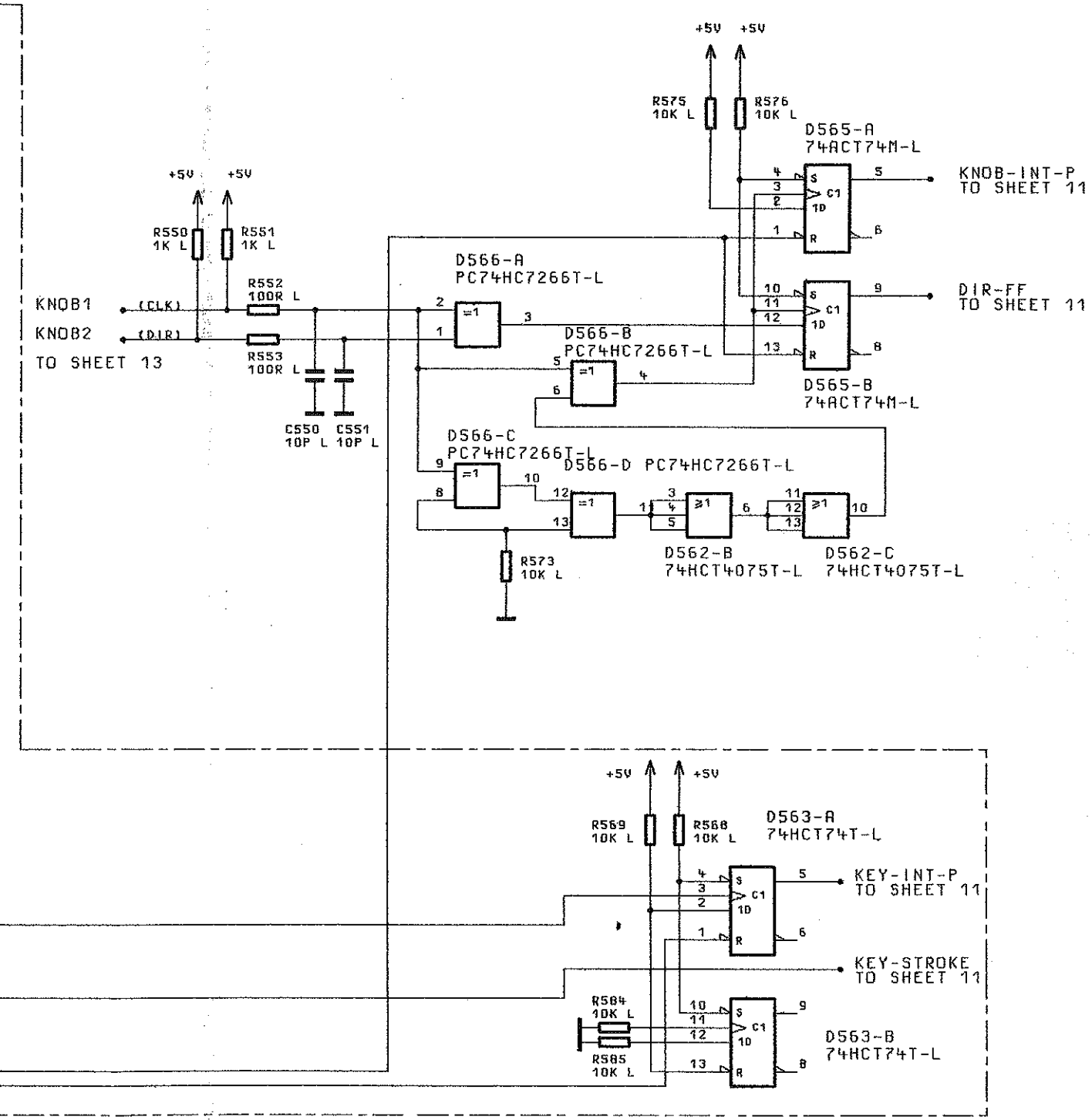

D520-A
74ACT32M-L
CS-TIMER S04
CS-SERBUS S05
CS-SAB82556 S05
CS-IEC S04
CS-LCD S06
CS-D/A-CONV S03
WR-REG1-HIGH
RD-PERI1 TO SHEET 7,8,9
CS-REG1 TO SHEET 7,8
CS-REG2 TO SHEET 7,9
CS-REG3 TO SHEET 7,9
CS-REG1
CS-REG2
CS-REG4
CS-REG5
CS-REG6
CS-REG7
CS-REG8
D520-B
74ACT32M-L
WR-REG2-HIGH TO SHEET 9
D520-D
74ACT32M-L
D520-C
74ACT32M-L
WR-REG4-LOW TO SHEET 11
RDY-PERI1
RD-PERI1
WR-D/A-CONV
WR-PERI1-HIGH
WR-PERI1-LOW
+5V
RS90-A
47K-9XC
R590-B
R590-C
R590-D
R590-E
R590-F
R591 1K L
R592 1K L
R593 1K L
R594 1K L
R595 1K L
R596 1K L

C510
100N B
C540
100N L
C520
100N B
+5V
D540-B
N85C220-80
D520-E
74ACT32M-L
C590
100N L
D590-B
74HCT541T-L


D500-A
74ACT138M-L
A26
A27
A28
A31
A30
A29
CS-TIMER S04
CS-SERBUS S05
CS-SAB82556 S05
CS-IEC S04
CS-LCD S06
CS-D/A-CONV S03
D510-A
74ACT138M-L
A1-PERI
A2-PERI
A3-PERI
A31
R510
1K L
CS-REG1
CS-REG2
CS-REG4
CS-REG5
CS-REG6
CS-REG7
CS-REG8
D520-A
74ACT32M-L
D520-B
74ACT32M-L
D520-D
74ACT32M-L
CS-REG3
D520-C
74ACT32M-L
D540-A
N85C220-80
CLK1XB
AS-PERI
BLAST-PERI
W/R-PERI
RES-N
BE1-PERI
BE0-PERI
CS-REG
CS-D/A-CONV
R540
10K L
RDY-PERI1
RD-PERI1
WR-D/A-CONV
WR-PERI1-HIGH
WR-PERI1-LOW
+5V
C500
100N L
C510
100N B
C540
100N L
C520
100N B
D500-B
74ACT138M-L
D510-B
74ACT138M-L
D540-B
N85C220-80
D520-E
74ACT32M-L

FÜR DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR





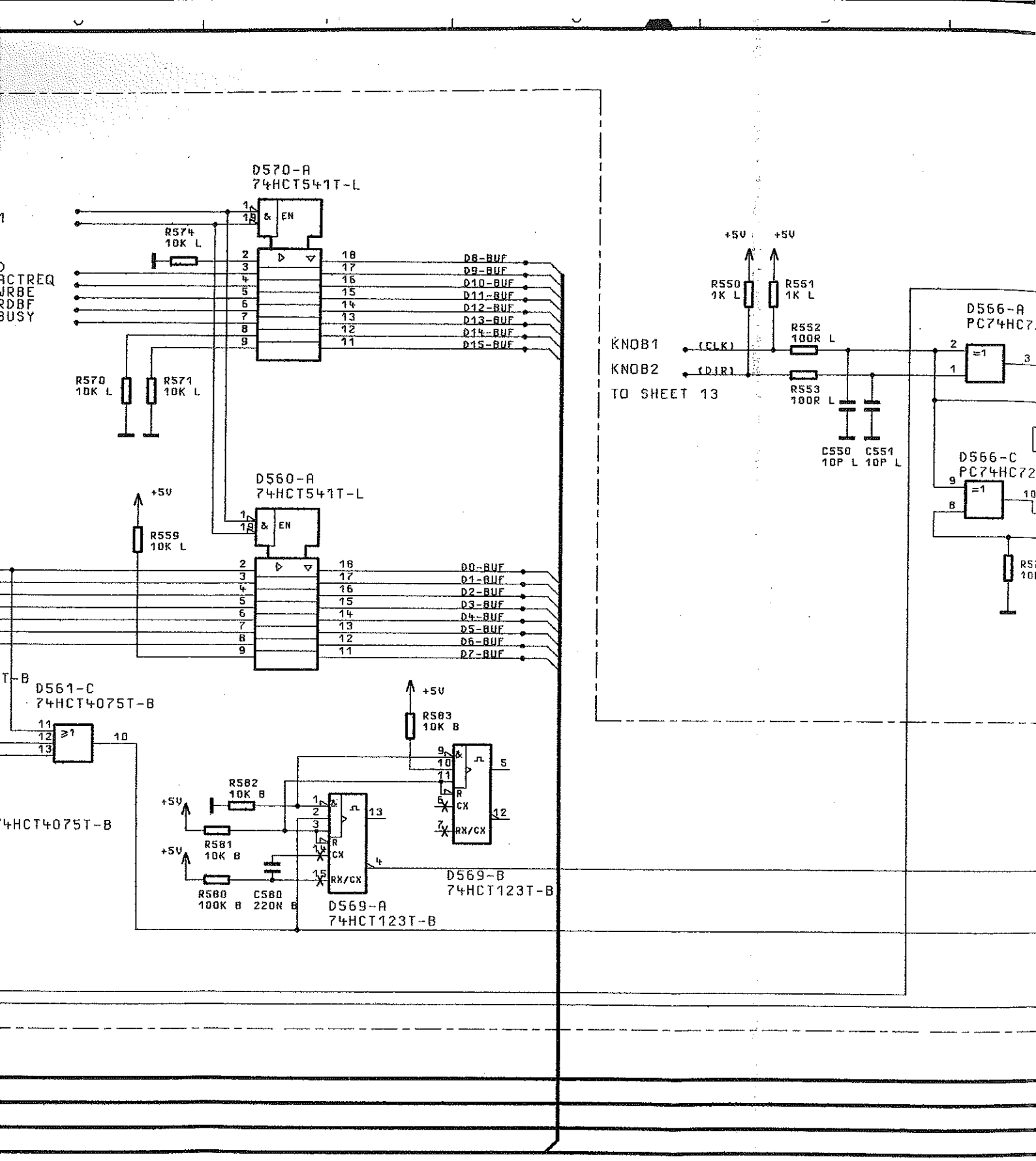
A-BUS-CPU
CONTROL-BUS-PERI
A-BUS-PERI
D-BUS-BUF
CLK-BUS

8-D
CT14T-B

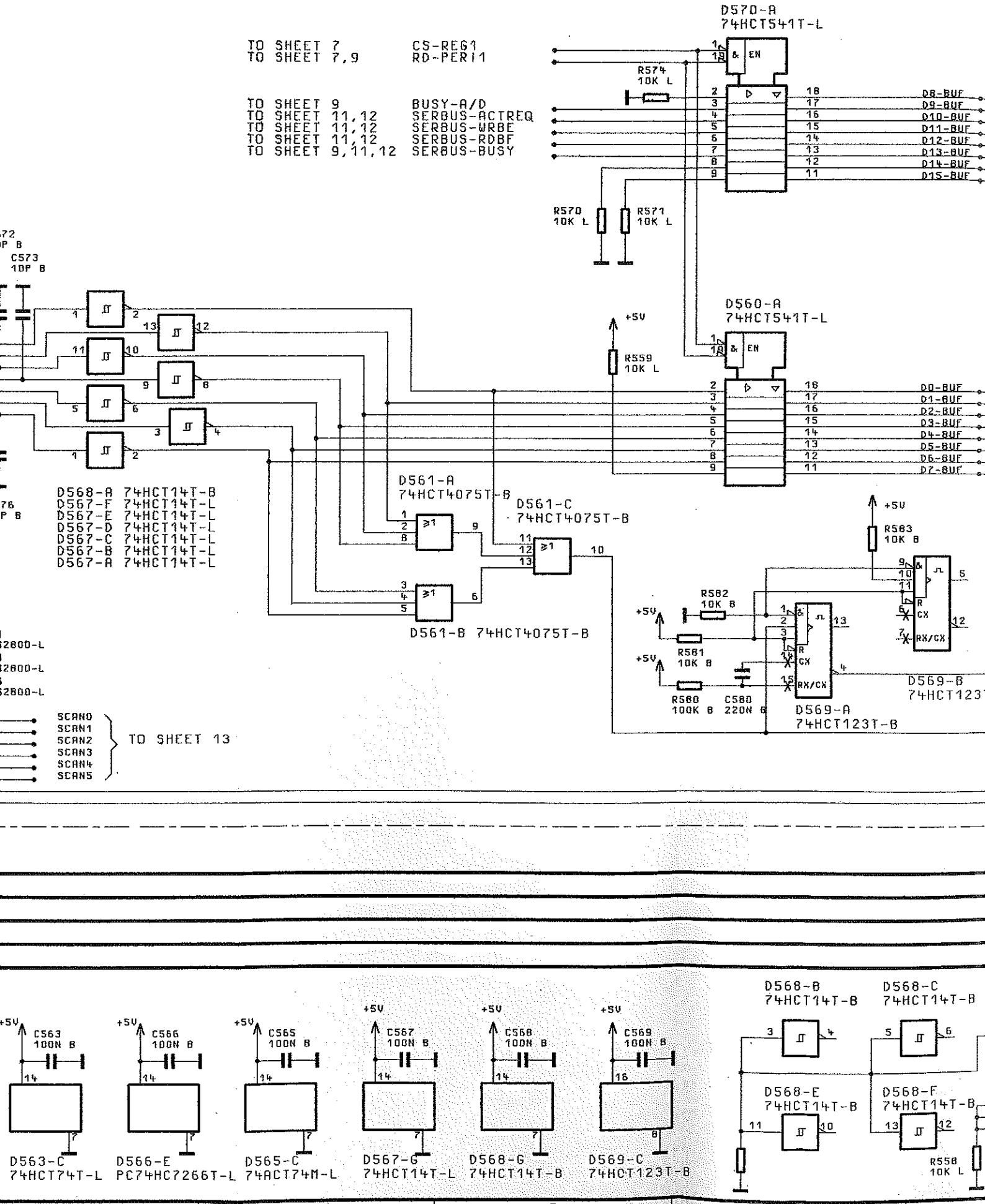
2-A
CT4075T-L

04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
							ZEICHN.-NR. 1035.7308.01S
							BLATT-NR. 8+
REND. IND.	RENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	REG.I.V.	1035.5005
						ERSTE Z.	1035.5440

ROHDE&SCHWARZ



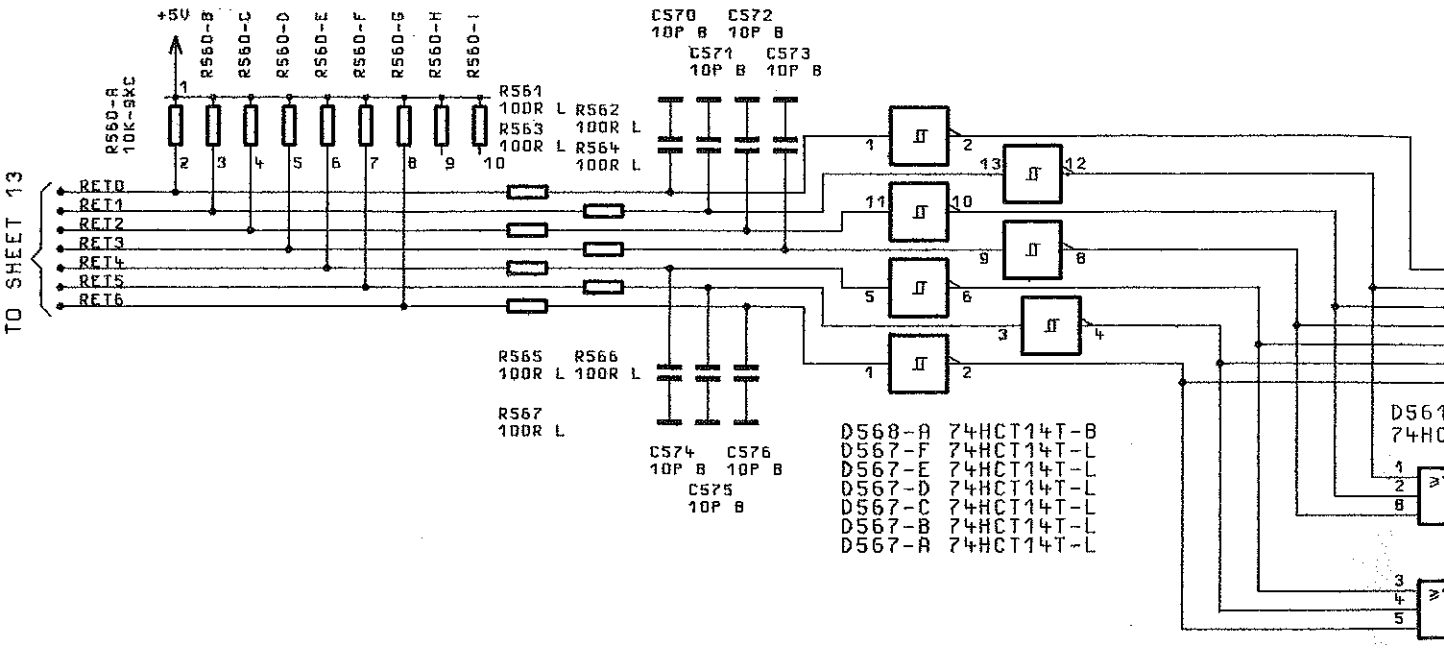
04/	48730	02.05.94	DR	1GPK	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	03.05.
REND. IND.	RENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&	
				ZU GERAET	S

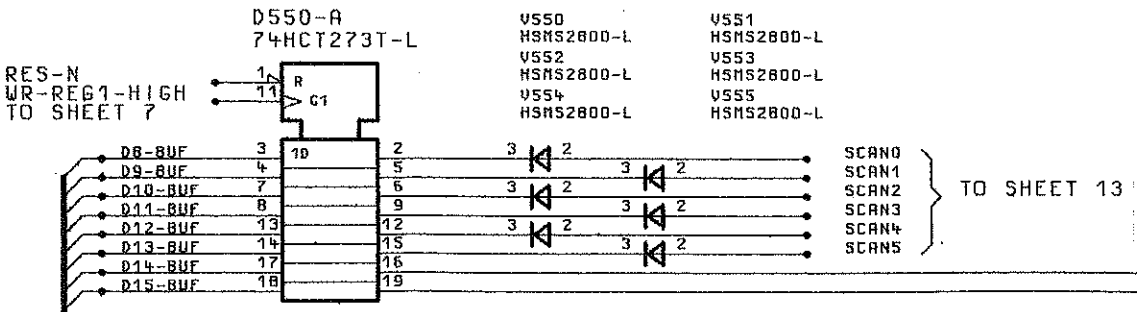

TO SHEET 7 CS-REG1
TO SHEET 7,9 RD-PERI1
TO SHEET 9 BUSY-A/D
TO SHEET 11,12 SERBUS-ACTREQ
TO SHEET 11,12 SERBUS-WRBE
TO SHEET 11,12 SERBUS-RDBF
TO SHEET 9,11,12 SERBUS-BUSY
D570-A 74HCT541T-L
D560-A 74HCT541T-L
R574 10K L
R570 10K L
R571 10K L
R559 10K L
+5V
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
C573 10P B
D568-A 74HCT14T-B
D567-F 74HCT14T-L
D567-E 74HCT14T-L
D567-D 74HCT14T-L
D567-C 74HCT14T-L
D567-B 74HCT14T-L
D567-A 74HCT14T-L
D561-A 74HCT4075T-B
D561-C 74HCT4075T-B
D561-B 74HCT4075T-B
RS83 10K B
RS82 10K B
RS81 10K B
RS80 100K B
CS80 220N B
D569-B 74HCT123
D569-A 74HCT123T-B
SCAN0
SCAN1
SCAN2
SCAN3
SCAN4
SCAN5
TO SHEET 13
C563 100N B
C566 100N B
C565 100N B
C567 100N B
C568 100N B
C569 100N B
D563-C 74HCT74T-L
D566-E PC74HC7266T-L
D565-C 74ACT74M-L
D567-G 74HCT14T-L
D568-G 74HCT14T-B
D569-C 74HCT123T-B
D568-B 74HCT14T-B
D568-C 74HCT14T-B
D568-E 74HCT14T-B
D568-F 74HCT14T-B
RS58 10K L

KEY-INTERFACE

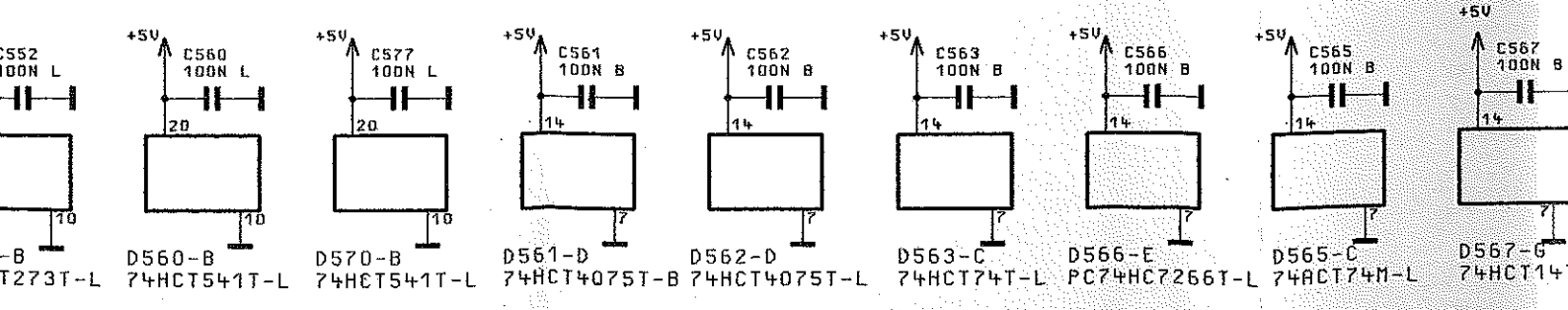
TO SHEET 7
TO SHEET 7,9

TO SHEET 9
TO SHEET 11,12
TO SHEET 11,12
TO SHEET 11,12
TO SHEET 9,11,12



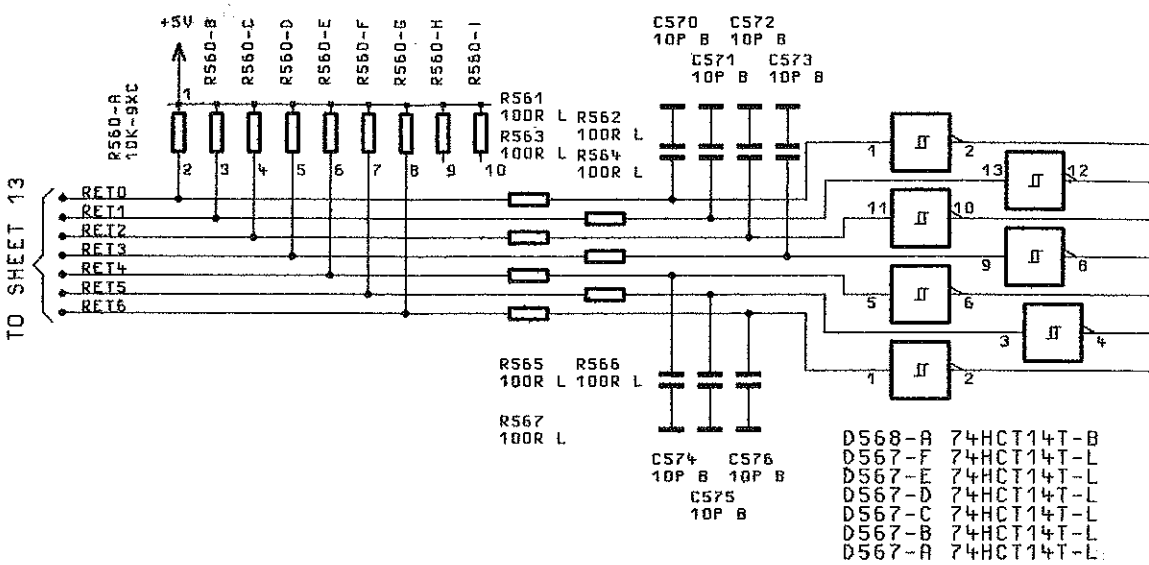


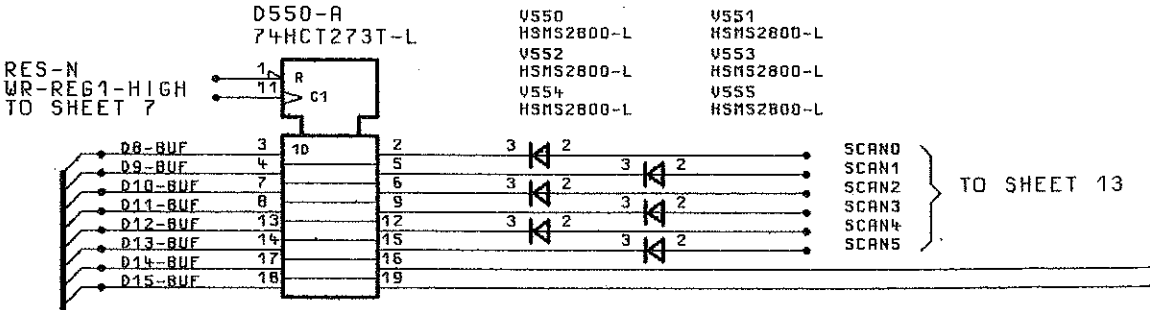
-CPU
DL-BUS-PERI
-PERI
-BUF
US



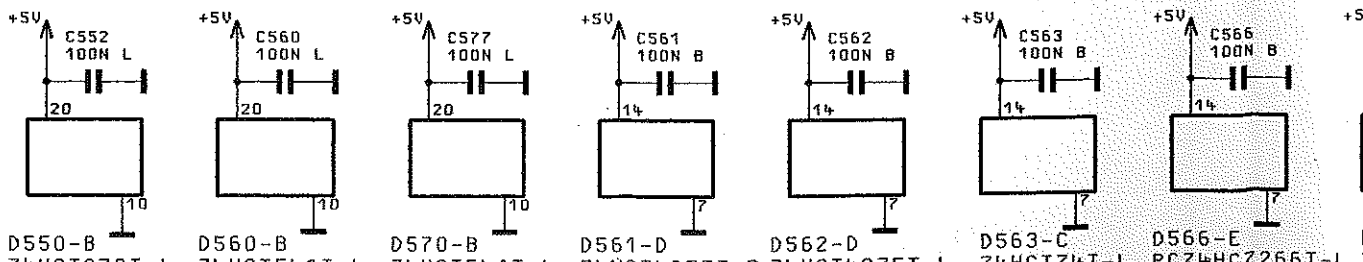
FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

KEY-INTERFACE



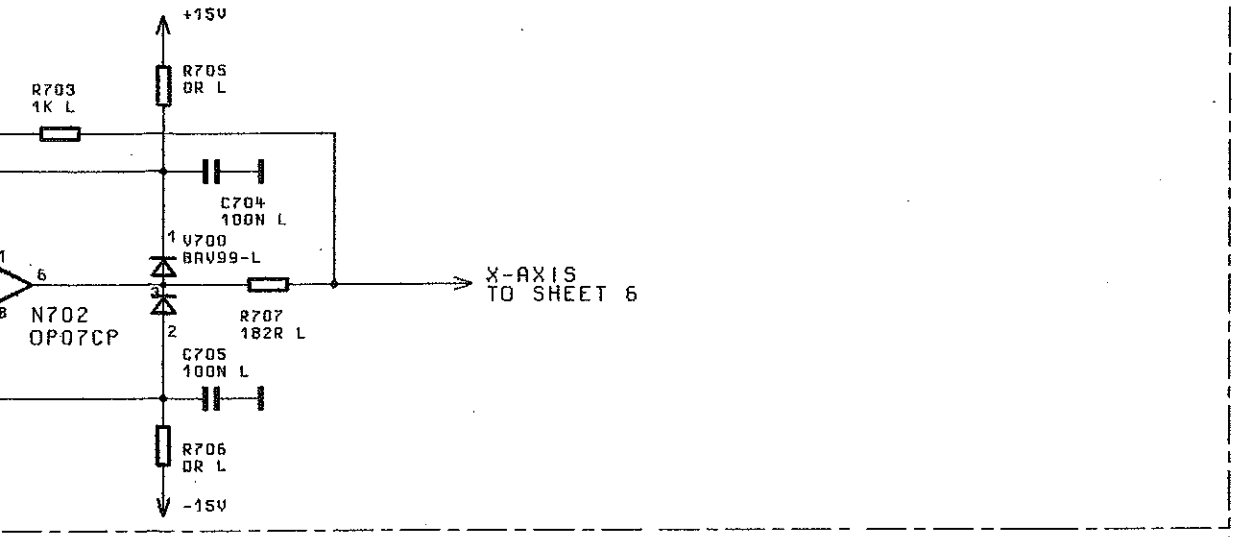


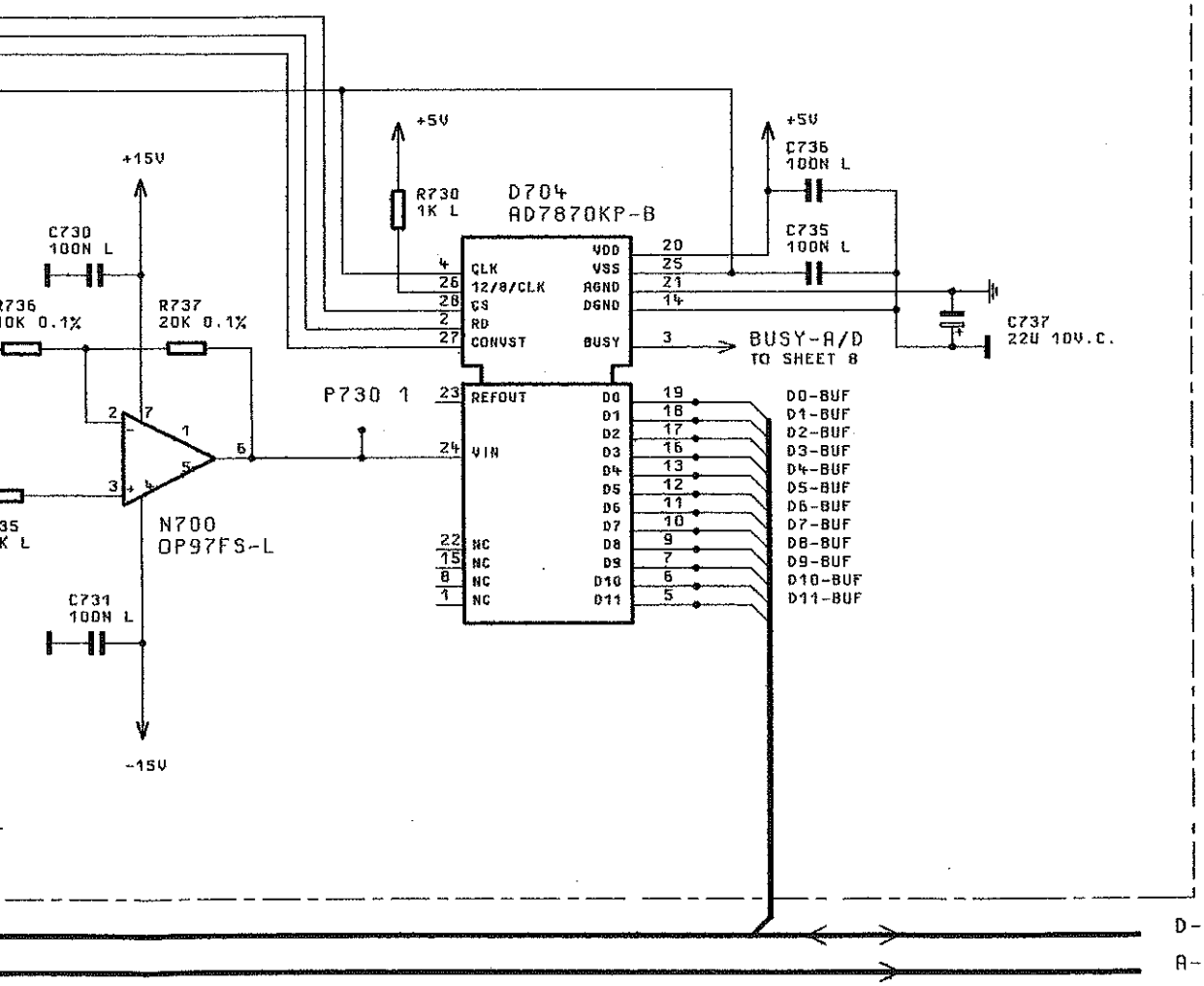
A-BUS-CPU
CONTROL-BUS-PERI
A-BUS-PERI
D-BUS-BUF
CLK-BUS



ZEICHN.-NR.

P730

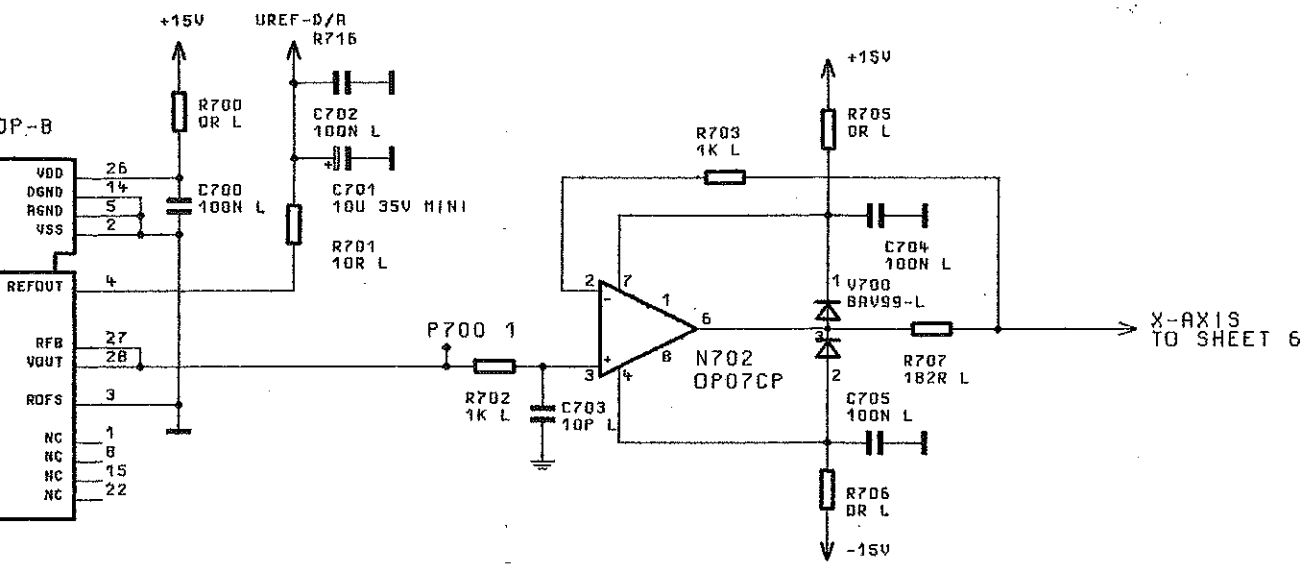


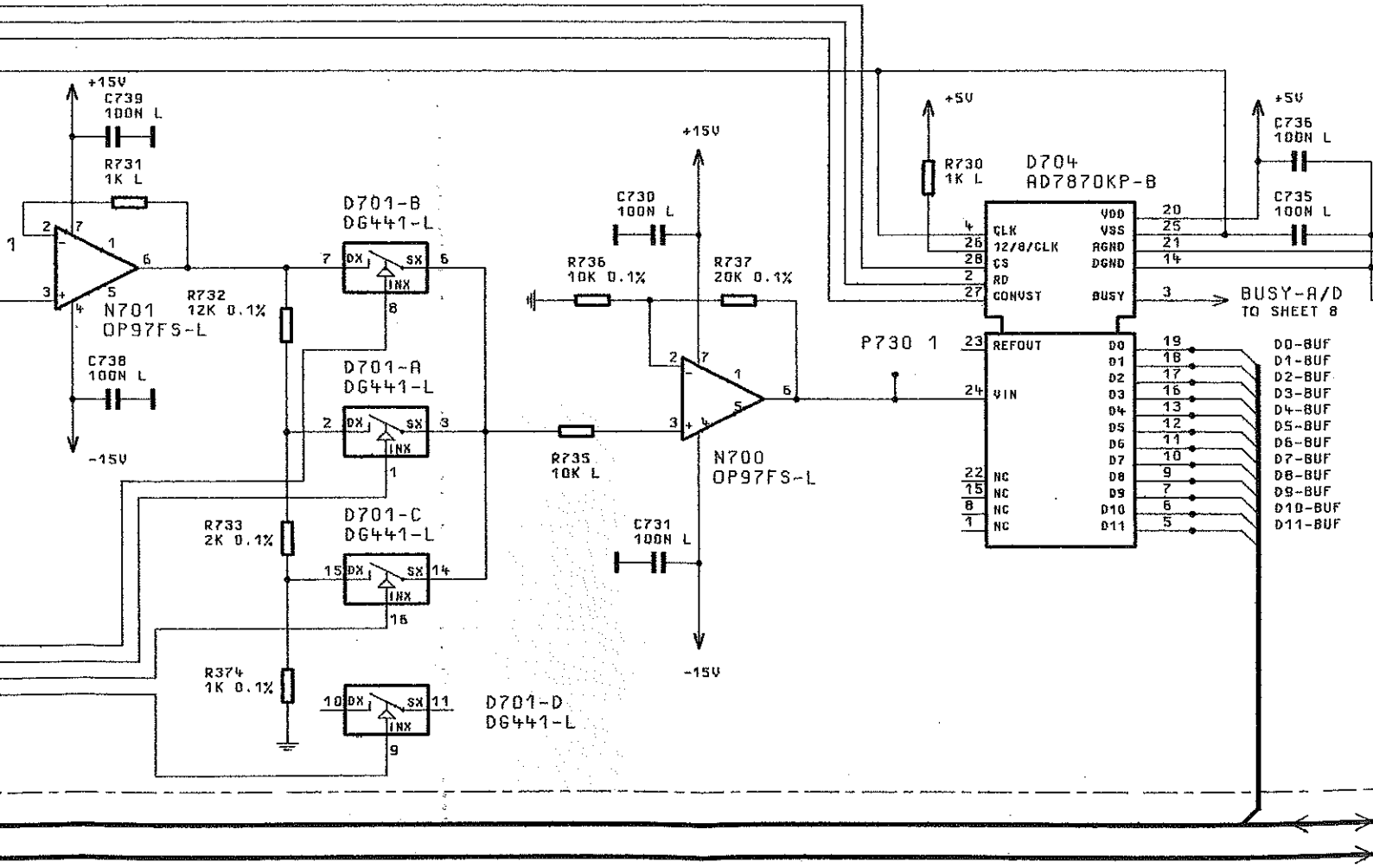


D-BUS-BUF S04

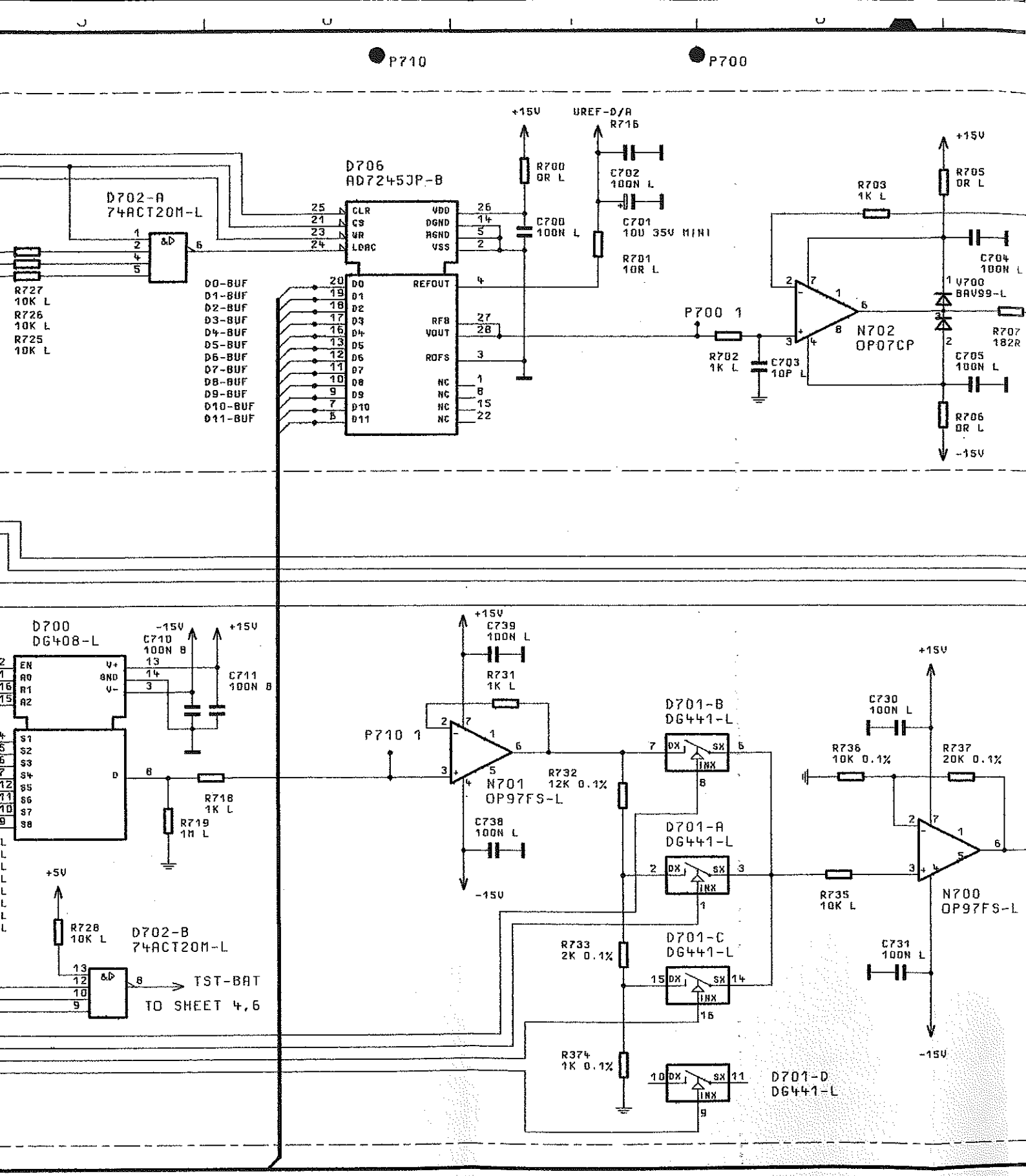
A-BUS-PERI S04

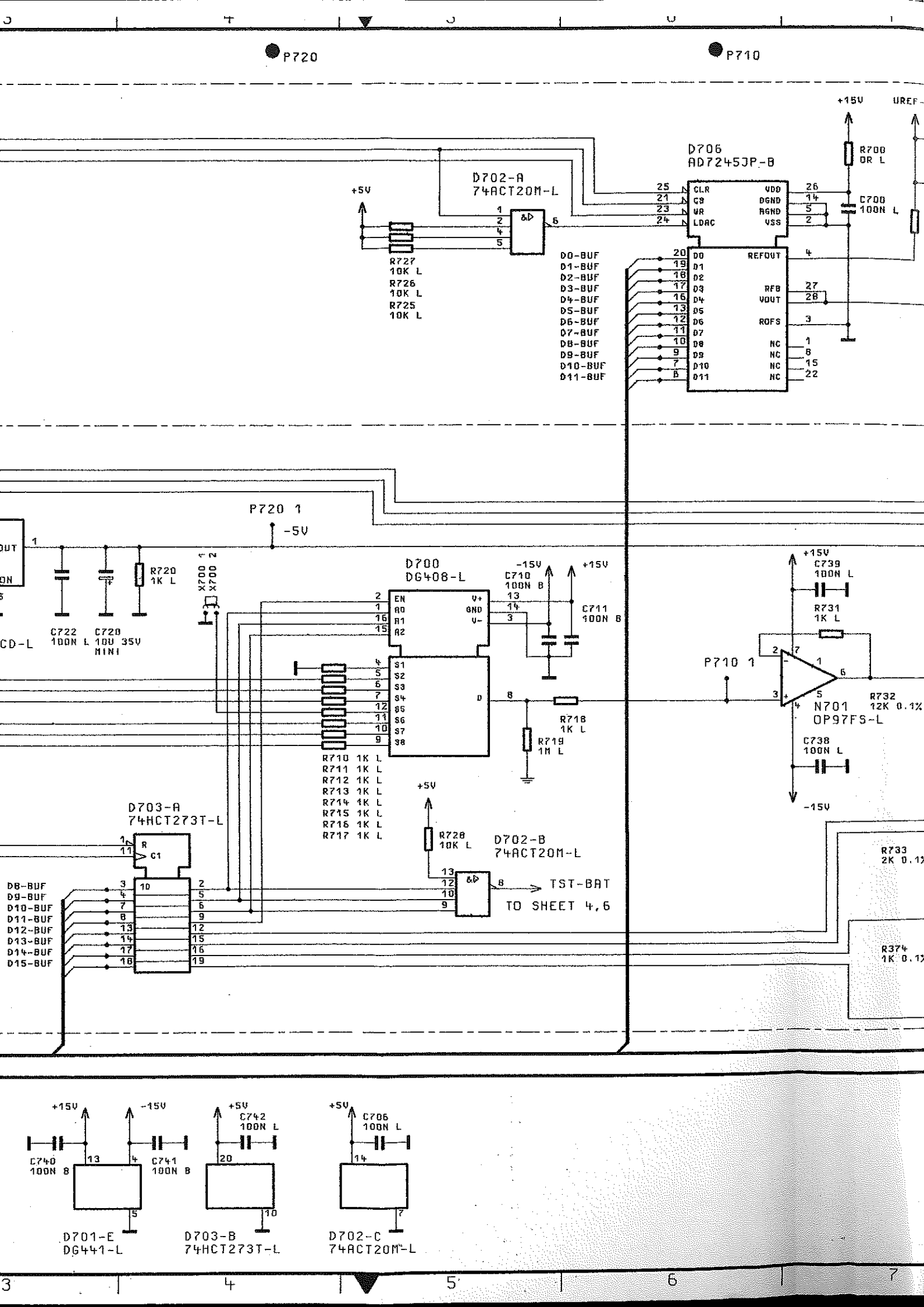
04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7308.01S
REND. IND.	RENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERRET	SMP		BLATT-NR. 9+
				REG.I.V.	1035.5005	ERSTE Z.	1035.5440





04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENE
				BEARB.		DR	
				GEPR.			
				NORM			
				PLOTT	03.05.94		
							ZEIC
/				ROHDE&SCHWARZ			
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERÄET	SMP		REG.I.


P710
P700
D702-A
74ACT20M-L
D706
AD7245JP-B
+15V
UREF-D/A
R716
R700
0R L
C702
100N L
C701
10U 35V MINI
R701
10R L
C700
100N L
R727
10K L
R726
10K L
R725
10K L
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D8-BUF
D9-BUF
D10-BUF
D11-BUF
CLR
CS
WR
LDAC
VDD
DGND
AGND
VSS
REFOUT
RFB
VOUT
ROFS
NC
P700 1
R702
1K L
C703
10P L
N702
OP07CP
R703
1K L
R705
0R L
C704
100N L
V700
BAV99-L
R707
182R
C705
100N L
R706
0R L
-15V
D700
DG408-L
C710
100N B
C711
100N B
EN
A0
A1
A2
V+
GND
V-
S1
S2
S3
S4
S5
S6
S7
S8
D
R718
1K L
R719
1M L
+5V
R728
10K L
D702-B
74ACT20M-L
TST-BAT
TO SHEET 4,6
P710 1
C739
100N L
R731
1K L
N701
OP97FS-L
R732
12K 0.1%
C738
100N L
D701-B
DG441-L
D701-A
DG441-L
D701-C
DG441-L
D701-D
DG441-L
DX
SX
INX
R733
2K 0.1%
R374
1K 0.1%
C730
100N L
R736
10K 0.1%
R737
20K 0.1%
R735
10K L
N700
OP97FS-L
C731
100N L
04/
48730
REND.
IND.


P720
P710
+15V
UREF-
D706
AD7245JP-B
R700
OR L
C700
100N L
D702-A
74ACT20M-L
+5V
R727
10K L
R726
10K L
R725
10K L
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D8-BUF
D9-BUF
D10-BUF
D11-BUF
CLR
CS
WR
LDAC
VDD
DGND
AGND
VSS
REFOUT
RFB
VOUT
ROFS
NC
P720 1
-5V
R720
1K L
X700
C722
100N L
C720
10U 35V
MINI
D700
DG408-L
-15V
C710
100N B
C711
100N B
R710 1K L
R711 1K L
R712 1K L
R713 1K L
R714 1K L
R715 1K L
R716 1K L
R717 1K L
R718
1K L
R719
1M L
C739
100N L
R731
1K L
P710 1
N701
OP97FS-L
R732
12K 0.1%
C738
100N L
R733
2K 0.1%
R374
1K 0.1%
D703-A
74HCT273T-L
R728
10K L
D702-B
74ACT20M-L
TST-BAT
TO SHEET 4,6
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
C740
100N B
C741
100N B
D701-E
DG441-L
C742
100N L
D703-B
74HCT273T-L
C706
100N L
D702-C
74ACT20M-L

P720

X-OUTPUT

RES-N
CS-D/A-CONV
WR-D/A-CONV

D702-A
74ACT20M-L

+5V

R727
10K L
R726
10K L
R725
10K L

SELF DIAGNOSE

CS-REG2
RD-PERI1
CS-REG3

-15V

D707
LM79L05ACD-L

C721
100N L

C722
100N L

C720
10U 35V
MINI

R720
1K L

P720 1
-5V

X700 1
X700 2

D700
DG408-L

C718
100N B

DIAG-15V
DIAG-5V
X-AXIS

6 VPP
UREF-D/A
6 UBATT-TST

R710 1K L
R711 1K L
R712 1K L
R713 1K L
R714 1K L
R715 1K L
R716 1K L
R717 1K L

R719
1M L

D703-A
74HCT273T-L

R-REG2-HIGH

D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF

R728
10K L

D702-B
74ACT20M-

TST

TO SHEET

D-BUS-BUF
A-BUS-PERI

+15V
-15V

C740
100N B

C741
100N B

D701-E
DG441-L

+5V

C742
100N L

D703-B
74HCT273T-L

+5V

C706
100N L

D702-C
74ACT20M-L

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

P720

X-OUTPUT

RES-N
TO SHEET 7 CS-D/A-CONV
TO SHEET 7 WR-D/A-CONV

+5V

R7
10
R7
10
R7
10

SELF DIAGNOSE

TO SHEET 7 { CS-REG2, RD-PERI1, CS-REG3

-15V

P720 1
-5V

IN OUT COMMON

C721 100N L

D707
LM79L05ACD-L

C722 100N L

C720 100U 35V MINI

R720 1K L

X700 1 2
X700

TO SHEET 6 DIAG-15V
TO SHEET 6 DIAG-5V
TO SHEET 6 X-AXIS

TO SHEET 5,6 VPP
UREF-D/A
TO SHEET 4,6 UBATT-TST

R710 1K L
R711 1K L
R712 1K L
R713 1K L
R714 1K L
R715 1K L
R716 1K L
R717 1K L

D703-A
74HCT273T-L

WR-REG2-HIGH

D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF

D-BUS-BUF
A-BUS-PERI

+15V -15V

C740 100N B
C741 100N B

D701-E
DG441-L

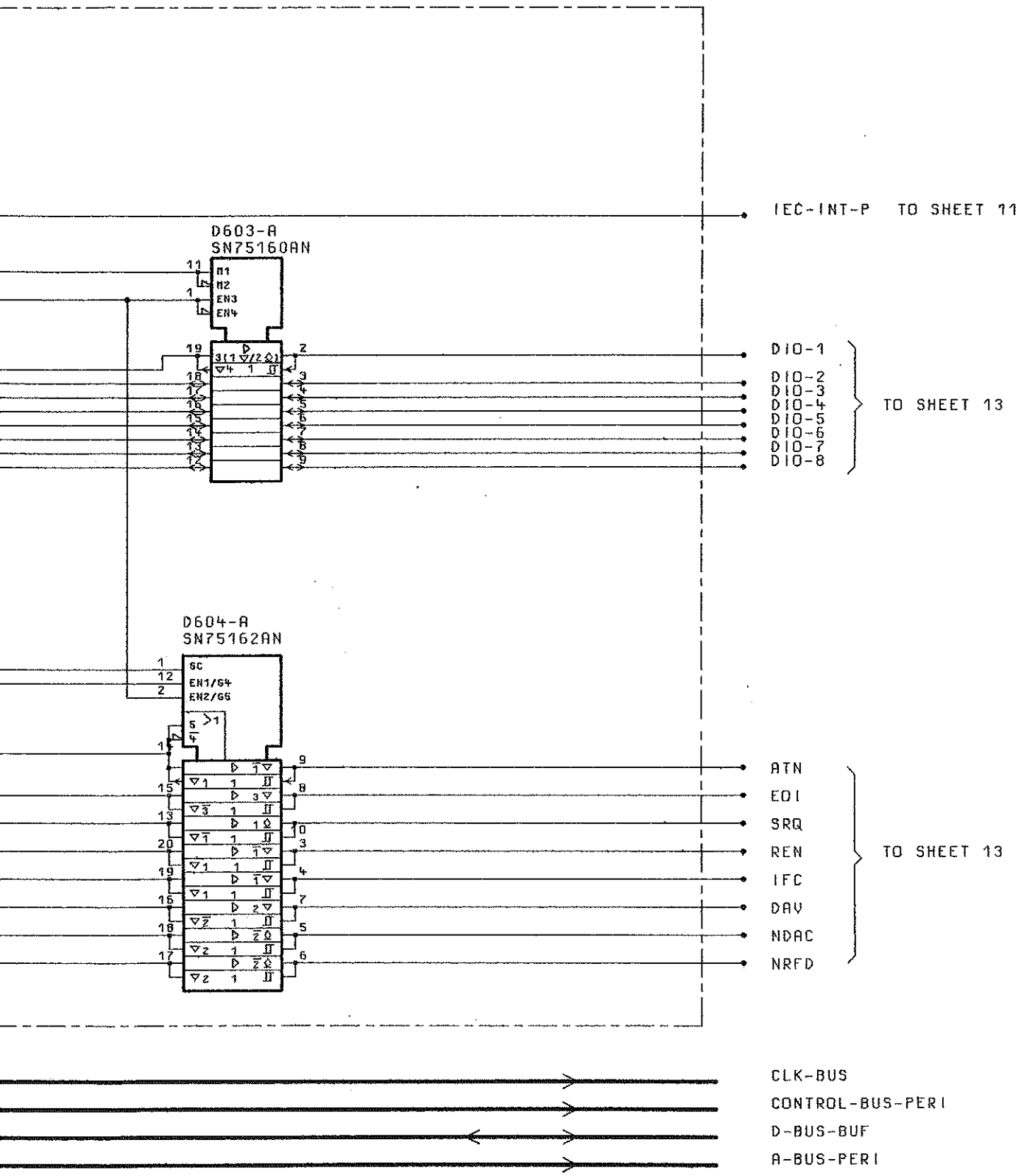
+5V
C742 100N L

D703-B
74HCT273T-L

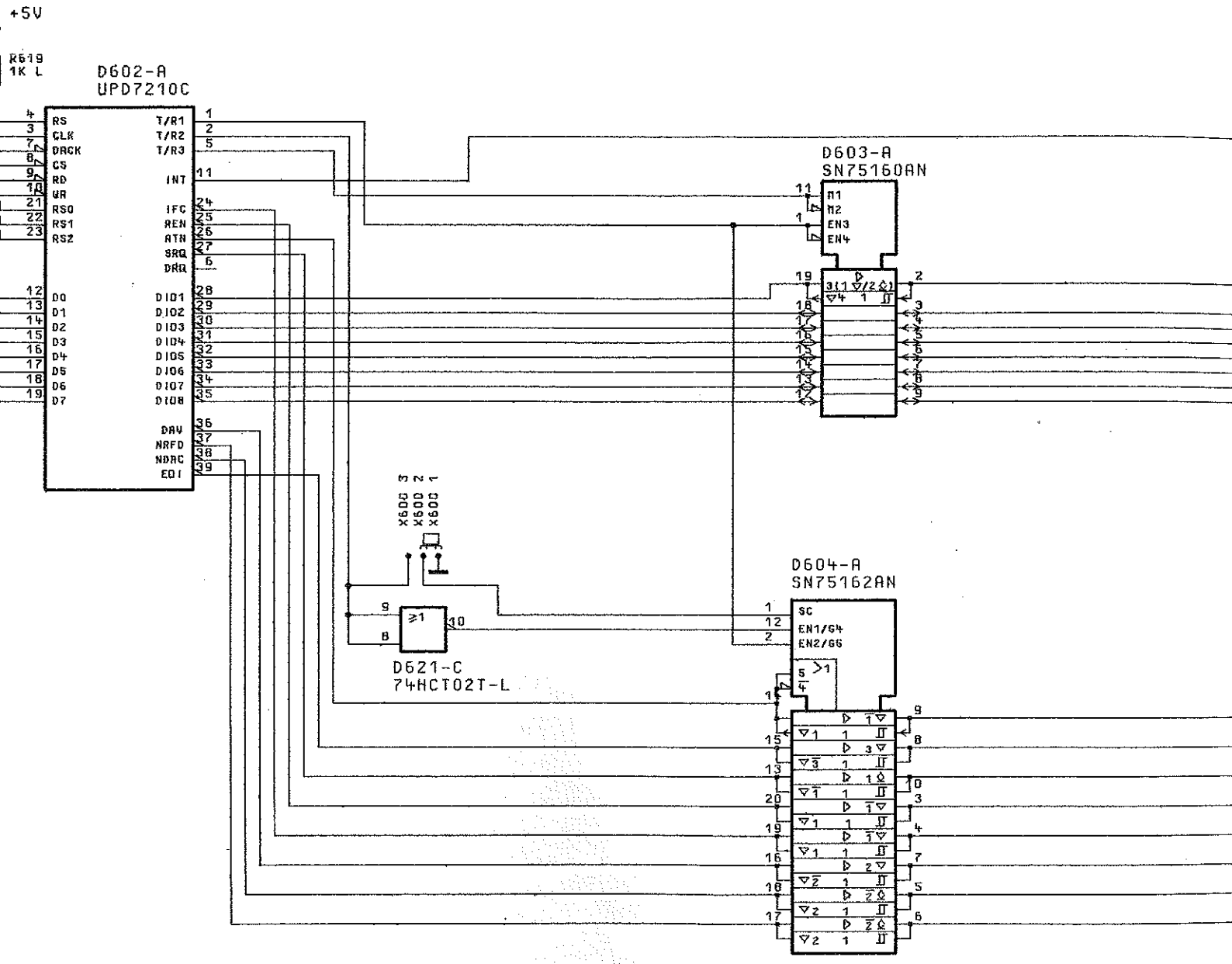
+5V
C706 100N L

D702-C
74ACT20M

ZEICHN.-NR.



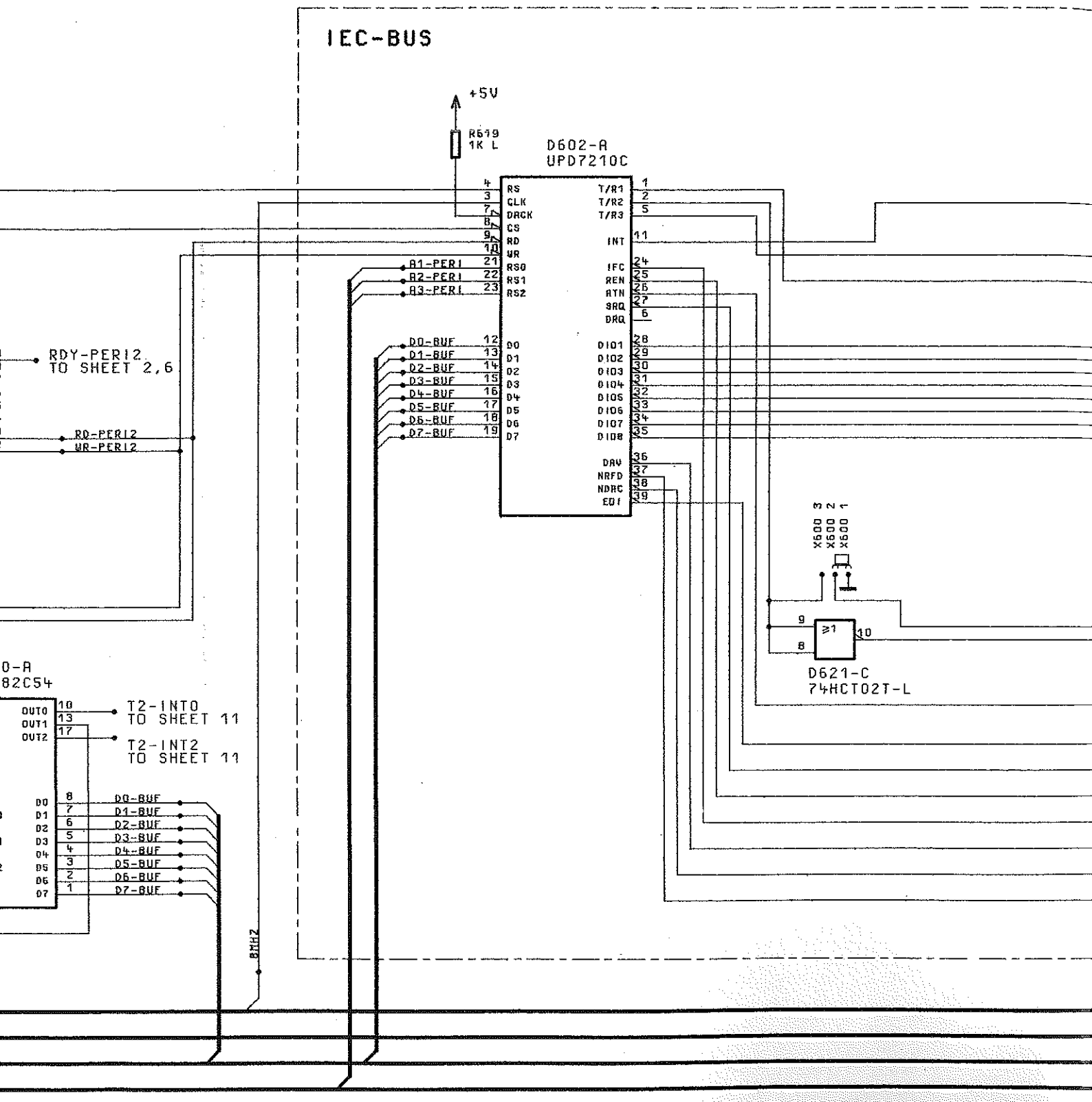
04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7308.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			BLATT-NR. 10+
				ZU GERRET	SMP		REG.I.V. 1035.5005 ERSTE Z. 1035.5440

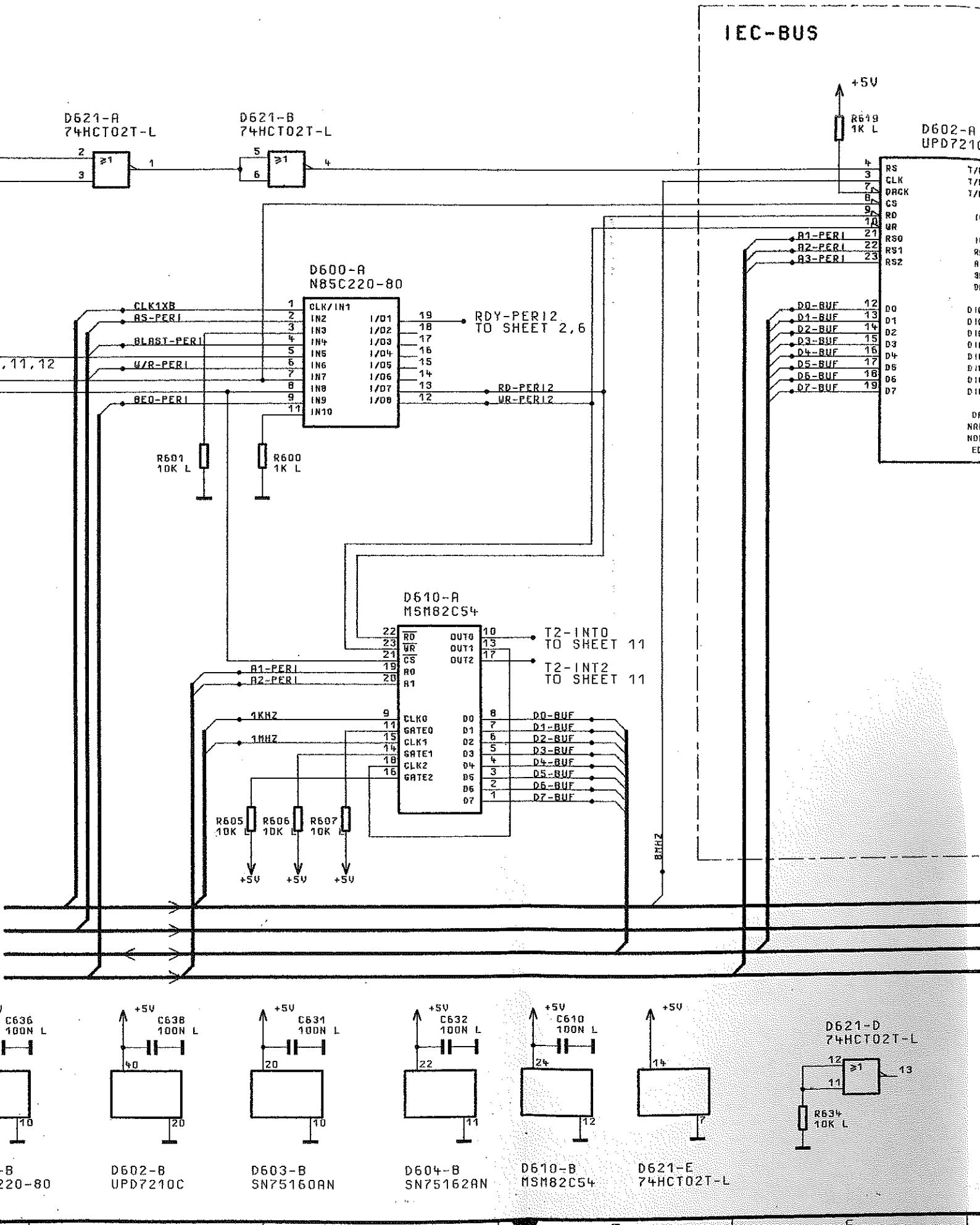


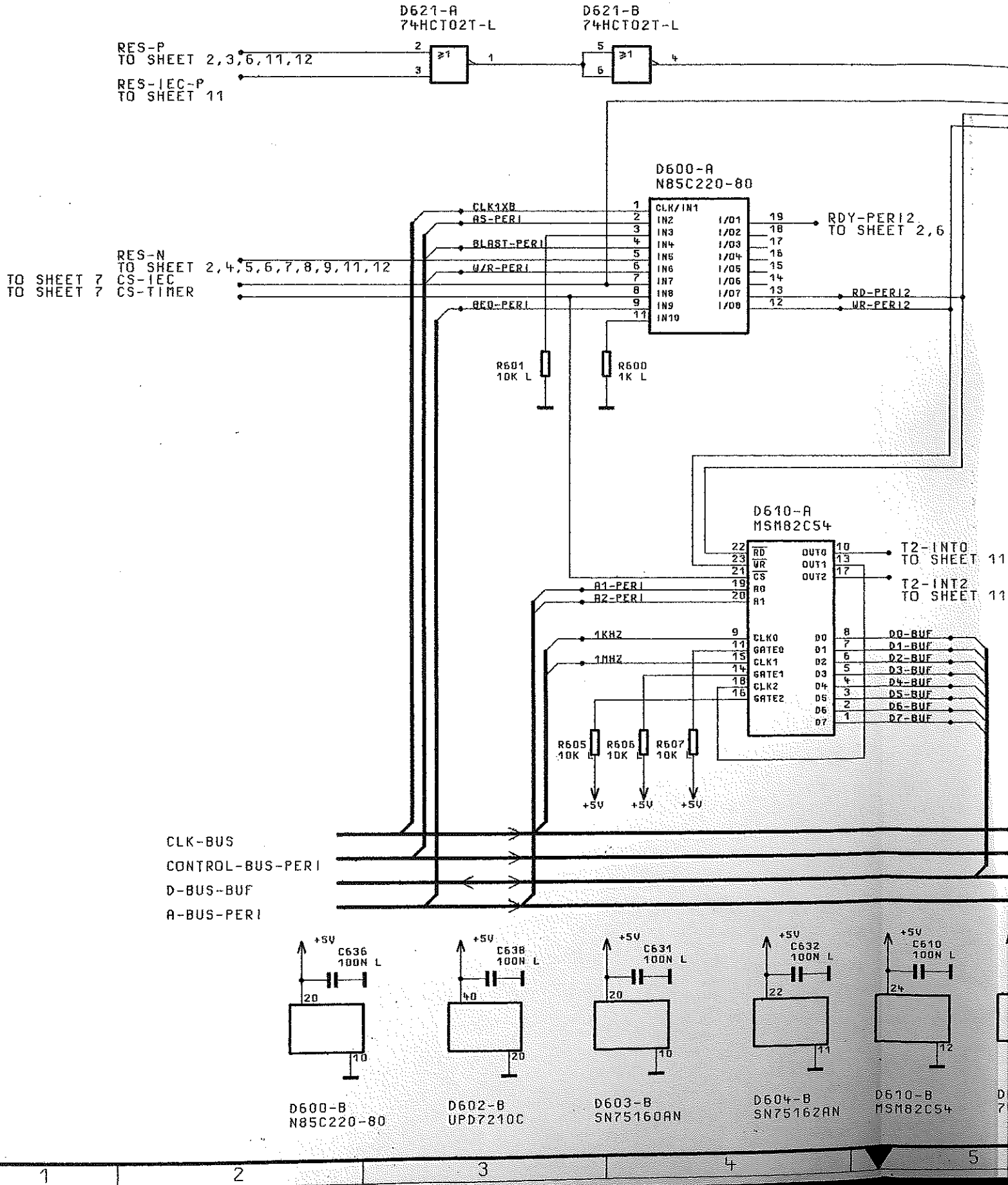
-21-D
-HCT02T-L

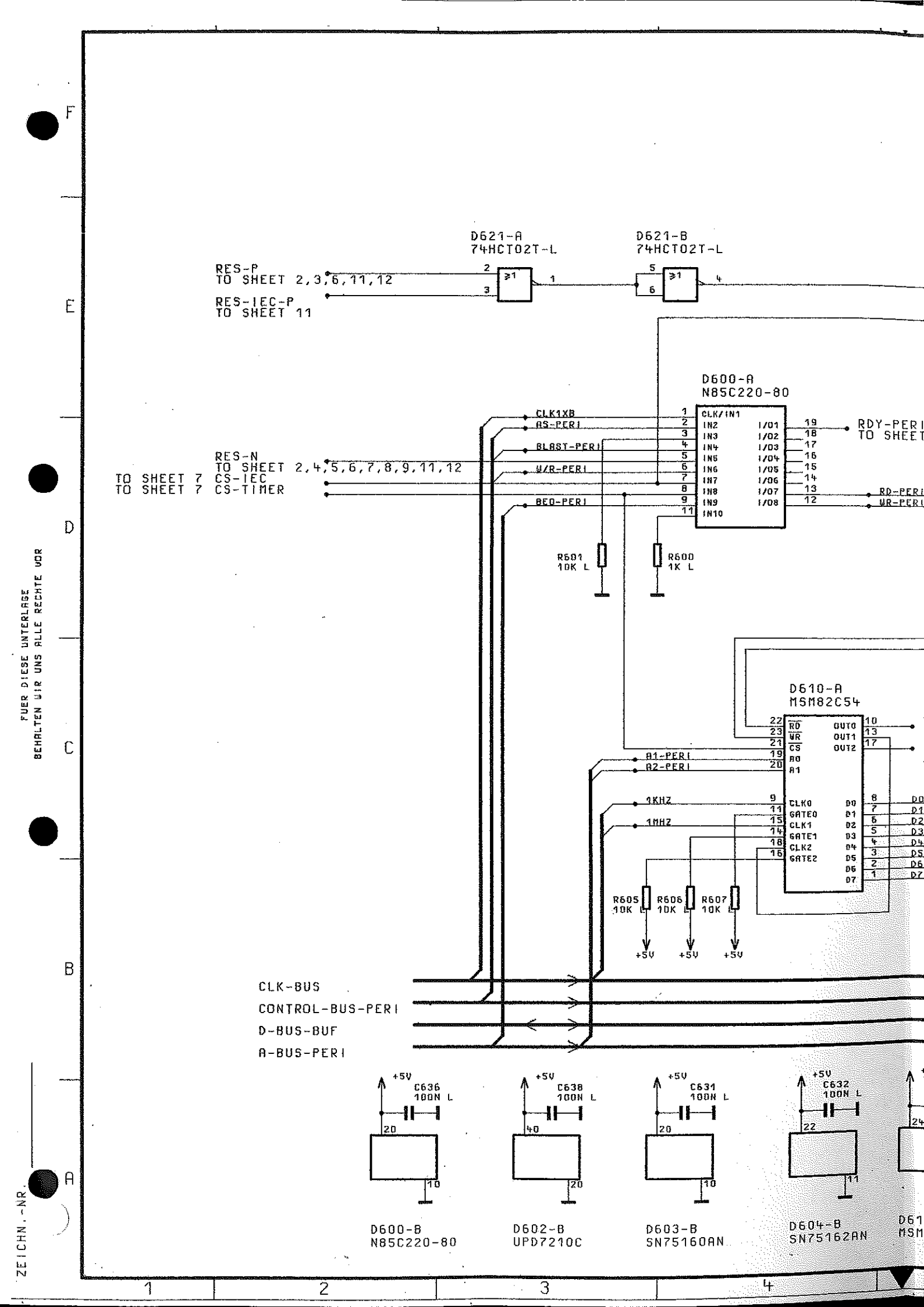
L

04/	48730	02.05.94	DR	16PK	TAG	N
				BEARB.		
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHW		
				ZU GERRET	SMP	


IEC-BUS
+5V
R619
1K L
D602-A
UPD7210C
RS
CLK
DRCK
CS
RD
WR
RS0
RS1
RS2
T/R1
T/R2
T/R3
INT
IFC
REN
ATN
SRQ
DRQ
DIO1
DIO2
DIO3
DIO4
DIO5
DIO6
DIO7
DIO8
DAV
NRFD
NDAC
EOI
A1-PERI
A2-PERI
A3-PERI
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
RDY-PERI2
TO SHEET 2,6
RD-PERI2
WR-PERI2
X600 3
X600 2
X600 1
≥1
D621-C
74HCT02T-L
T2-INT0
TO SHEET 11
T2-INT2
TO SHEET 11
8MHZ
+5V
C632
100N L
C610
100N L
R634
10K L
D621-D
74HCT02T-L
D621-E
74HCT02T-L
D610-B
MSM82C54

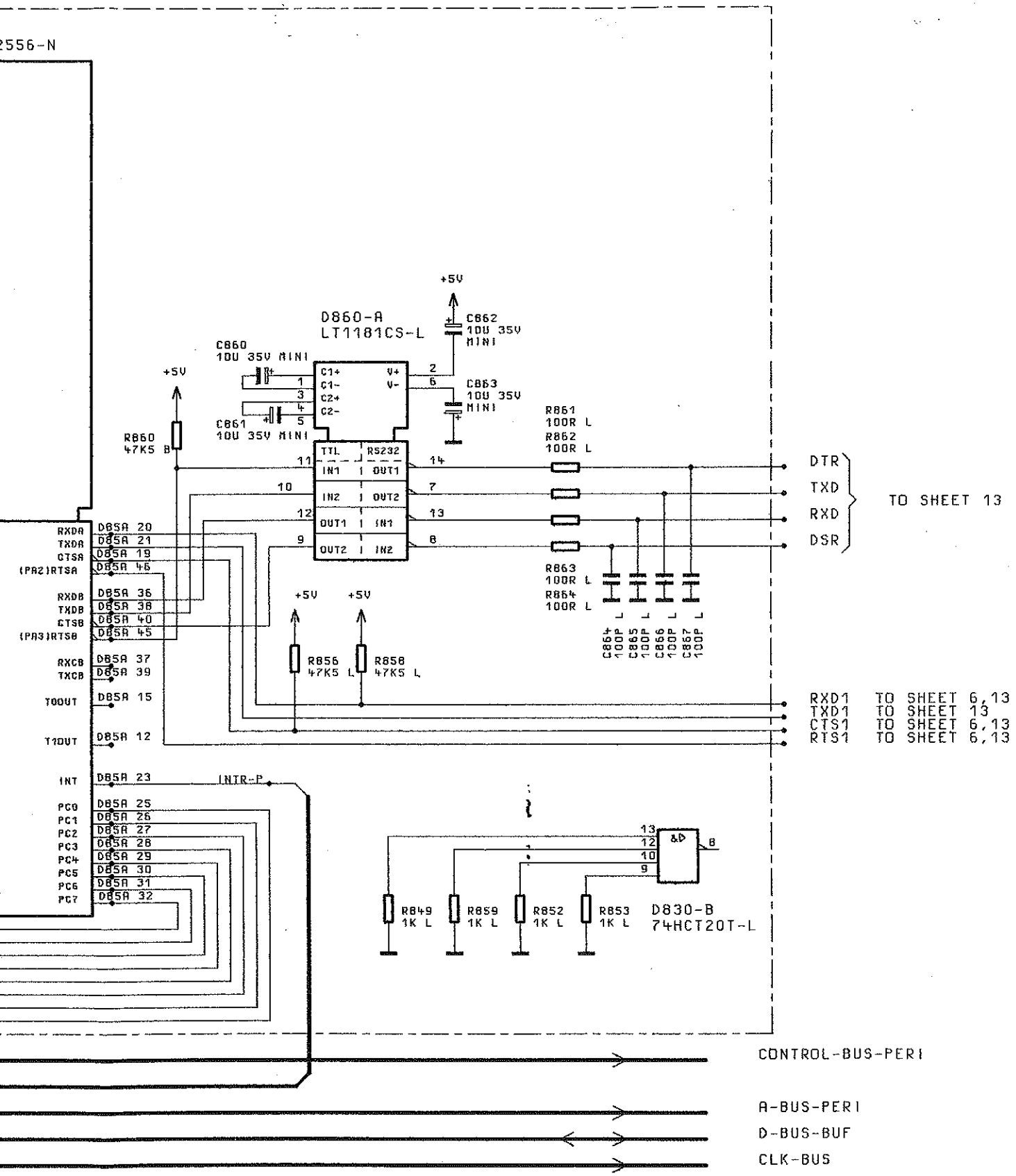

IEC-BUS
D621-A
74HCT02T-L
D621-B
74HCT02T-L
+5V
R619
1K L
D602-A
UPD7210
D600-A
N85C220-80
CLK1XB
AS-PERI
BLAST-PERI
W/R-PERI
BEO-PERI
RDY-PERI2
TO SHEET 2,6
RD-PERI2
WR-PERI2
R601
10K L
R600
1K L
A1-PERI
A2-PERI
A3-PERI
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D610-A
MSM82C54
T2-INT0
TO SHEET 11
T2-INT2
TO SHEET 11
1KHZ
1MHZ
8MHZ
R605
10K
R606
10K
R607
10K
+5V
C636
100N L
C638
100N L
C631
100N L
C632
100N L
C610
100N L
D621-D
74HCT02T-L
R634
10K L
D602-B
UPD7210C
D603-B
SN75160AN
D604-B
SN75162AN
D610-B
MSM82C54
D621-E
74HCT02T-L


D621-A
74HCT02T-L
D621-B
74HCT02T-L
RES-P
TO SHEET 2,3,6,11,12
RES-IEC-P
TO SHEET 11
D600-A
N85C220-80
CLK1XB
AS-PERI
BLAST-PERI
R/W-PERI
DEO-PERI
RDY-PERI2
TO SHEET 2,6
RD-PERI2
WR-PERI2
RES-N
TO SHEET 2,4,5,6,7,8,9,11,12
TO SHEET 7 CS-IEC
TO SHEET 7 CS-TIMER
R601
10K L
R600
1K L
D610-A
MSM82C54
T2-INT0
TO SHEET 11
T2-INT2
TO SHEET 11
A1-PERI
A2-PERI
1KHZ
1MHZ
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
R605
10K
R606
10K
R607
10K
+5V
CLK-BUS
CONTROL-BUS-PERI
D-BUS-BUF
A-BUS-PERI
C636
100N L
C638
100N L
C631
100N L
C632
100N L
C610
100N L
D600-B
N85C220-80
D602-B
UPD7210C
D603-B
SN75160AN
D604-B
SN75162AN
D610-B
MSM82C54



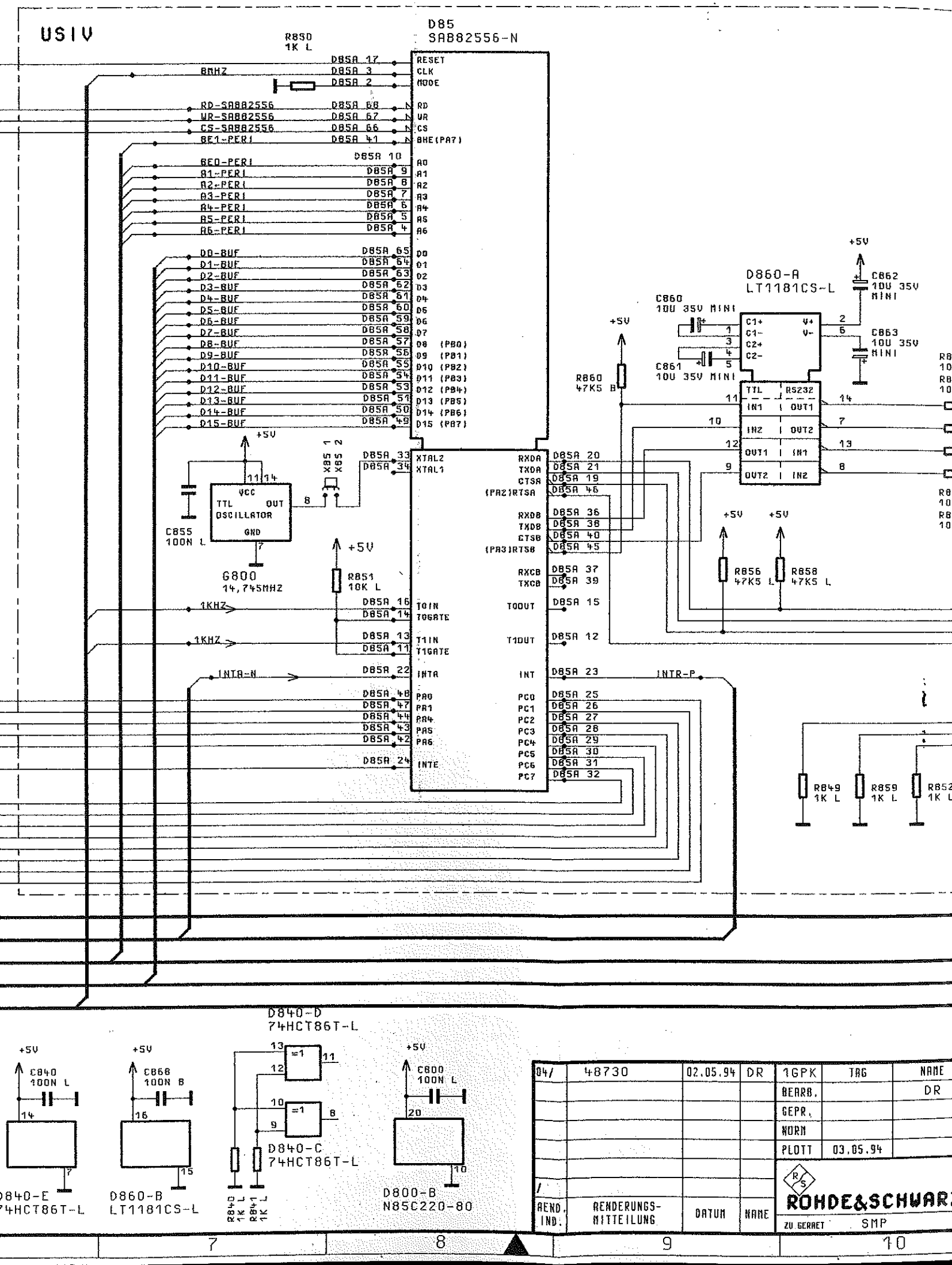
FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

ZEICHN.-NR.

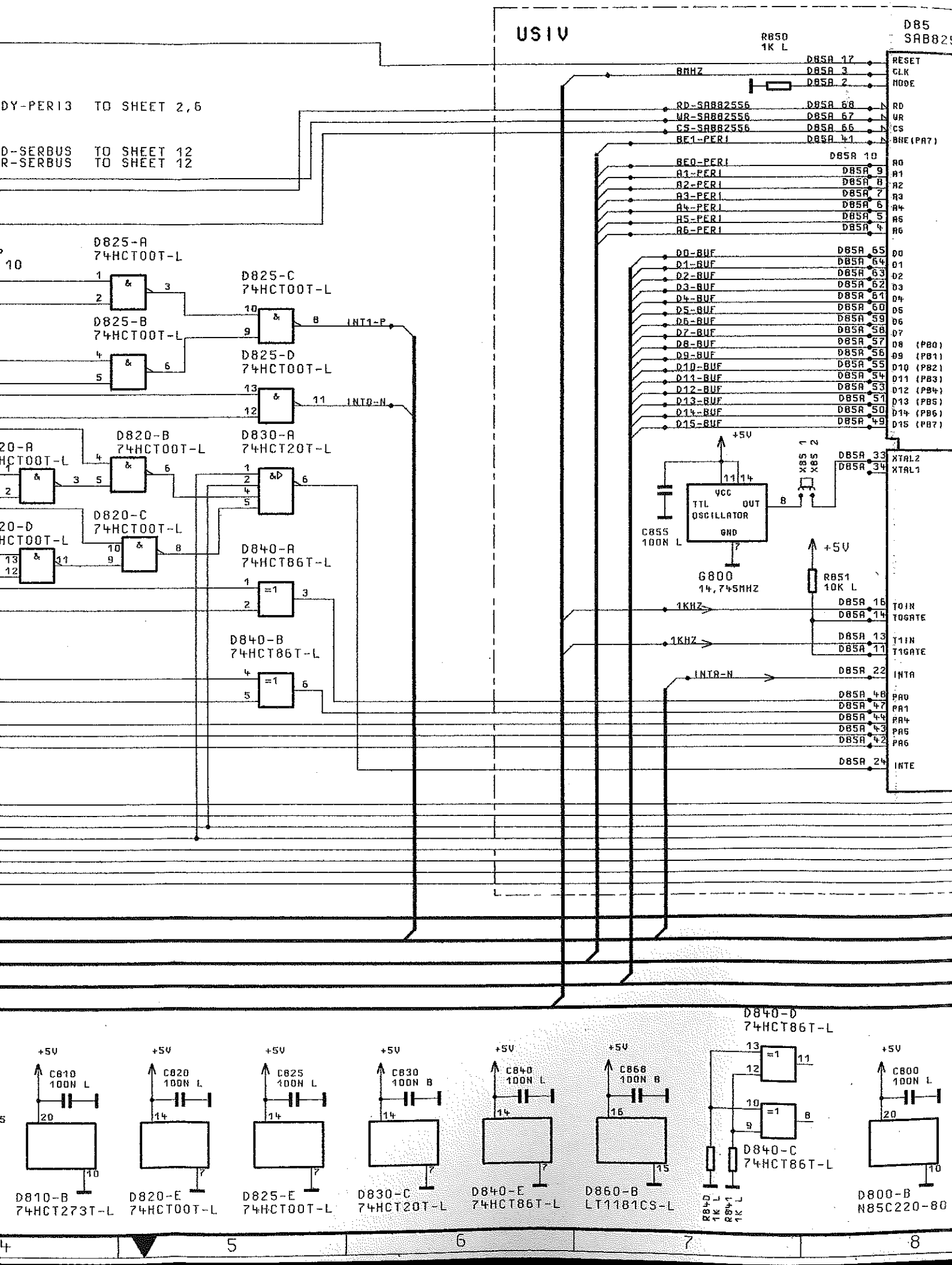


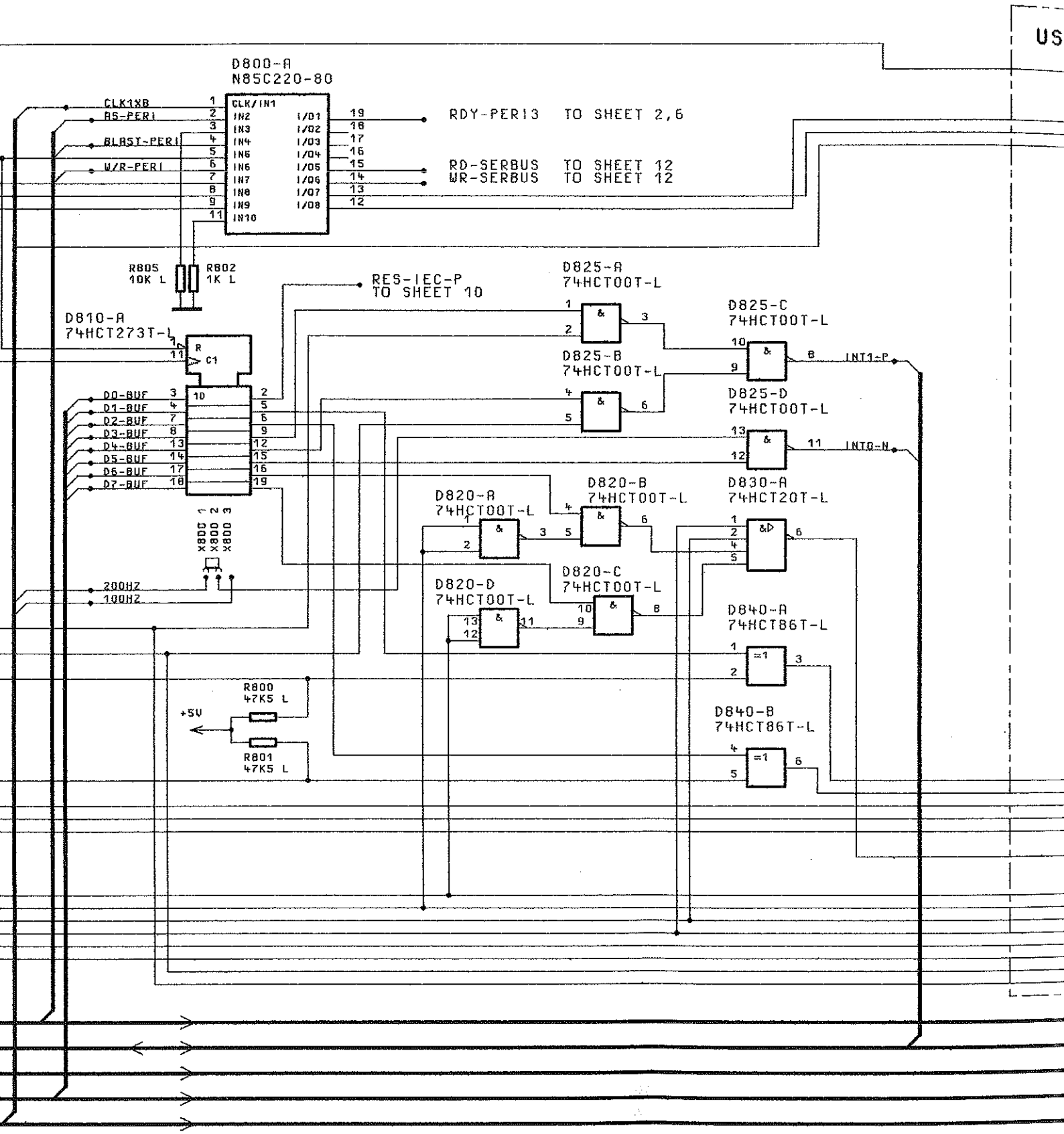
04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7308.01S
				ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440

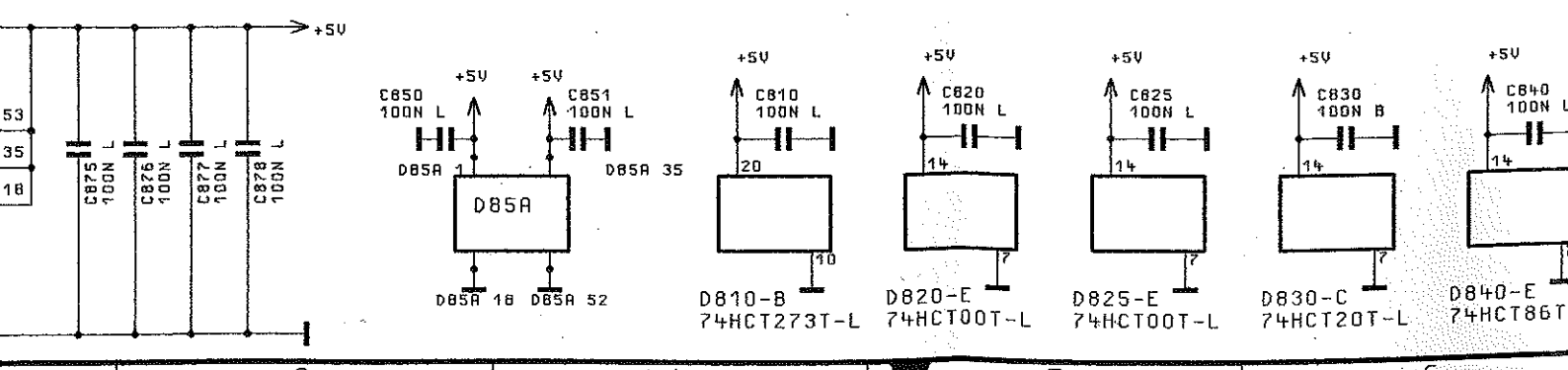
BLATT-NR. 11+ V. BL.

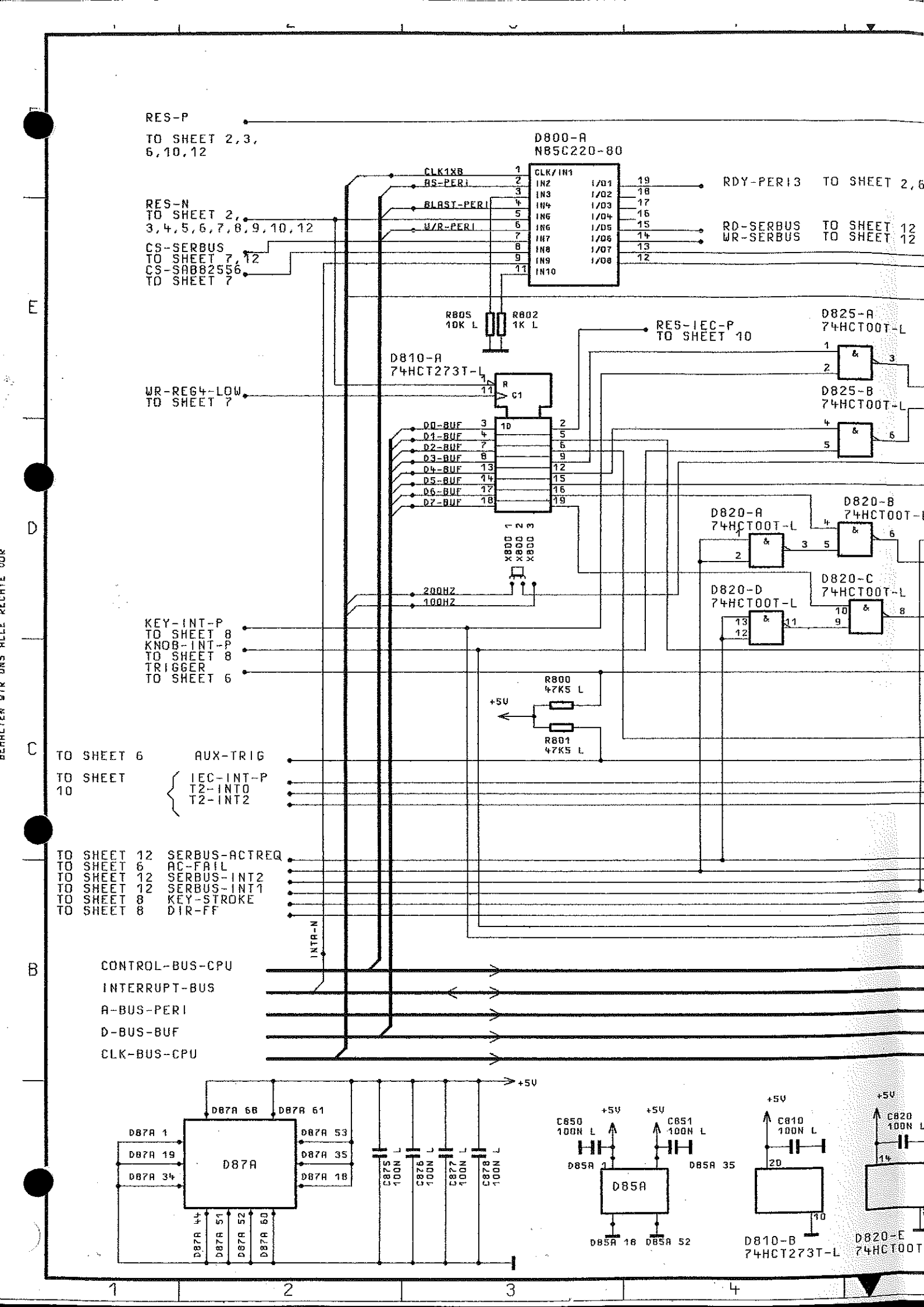


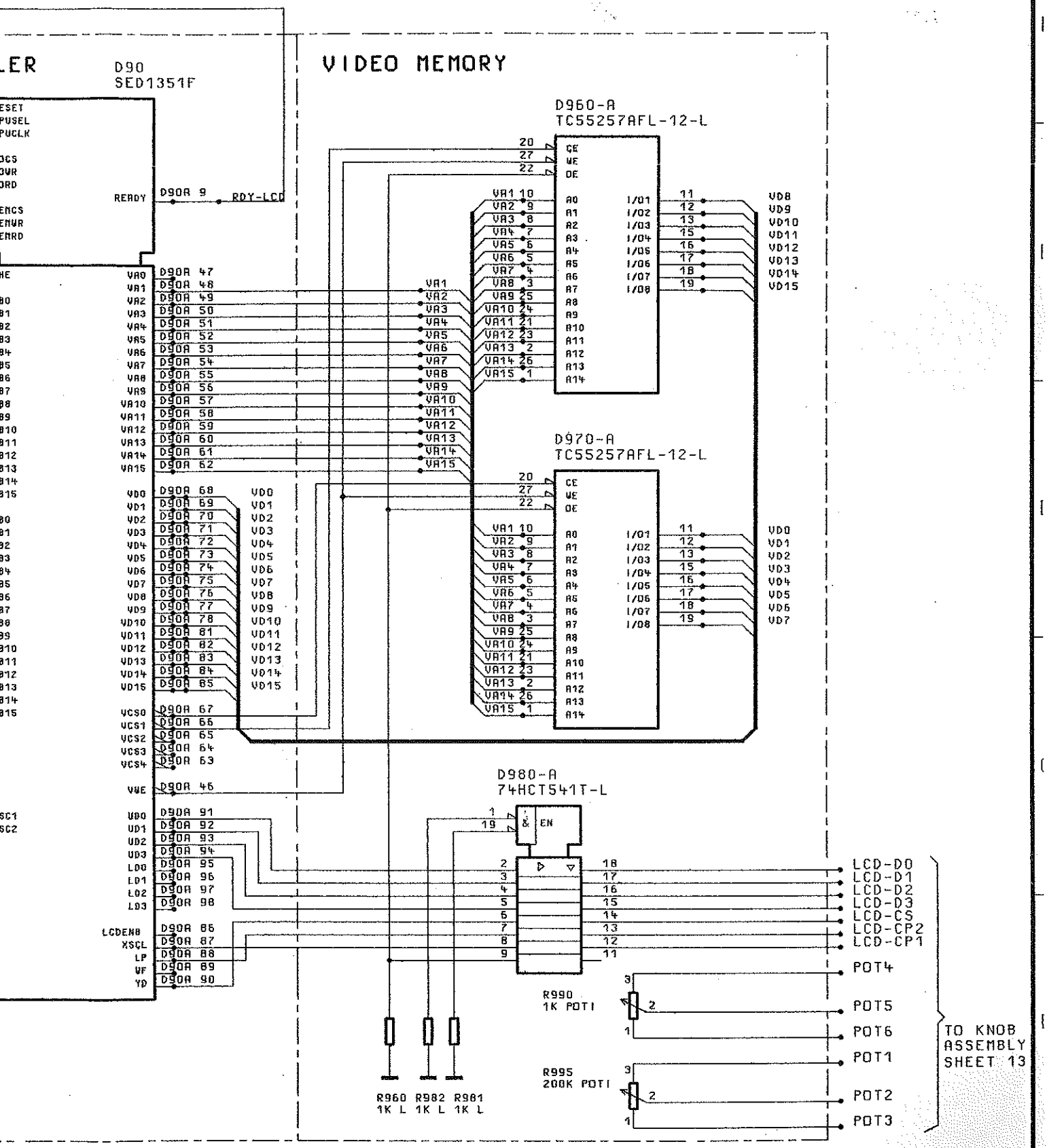
04/	48730	02.05.94	DR	1GPK	TAG	NAME
				BEARB.		DR
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ		
				ZU GERAET	SMP	


USIV
D85
SAB825
R850
1K L
8MHZ
D85A 17
D85A 3
D85A 2
RESET
CLK
MODE
DY-PERI3 TO SHEET 2,6
D-SERBUS TO SHEET 12
R-SERBUS TO SHEET 12
RD-SA882556
WR-SA882556
CS-SA882556
BE1-PERI
D85A 68
D85A 67
D85A 66
D85A 41
RD
WR
CS
BHE(PA7)
BE0-PERI
A1-PERI
A2-PERI
A3-PERI
A4-PERI
A5-PERI
A6-PERI
D85A 10
D85A 9
D85A 8
D85A 7
D85A 6
D85A 5
D85A 4
A0
A1
A2
A3
A4
A5
A6
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
D85A 65
D85A 64
D85A 63
D85A 62
D85A 61
D85A 60
D85A 59
D85A 58
D85A 57
D85A 56
D85A 55
D85A 54
D85A 53
D85A 51
D85A 50
D85A 49
D0
D1
D2
D3
D4
D5
D6
D7
D8 (PB0)
D9 (PB1)
D10 (PB2)
D11 (PB3)
D12 (PB4)
D13 (PB5)
D14 (PB6)
D15 (PB7)
D825-A
74HCT00T-L
D825-B
74HCT00T-L
D825-C
74HCT00T-L
D825-D
74HCT00T-L
INT1-P
INT0-N
D820-A
74HCT00T-L
D820-B
74HCT00T-L
D820-C
74HCT00T-L
D820-D
74HCT00T-L
D830-A
74HCT20T-L
D840-A
74HCT86T-L
D840-B
74HCT86T-L
+5V
X85 1
X85 2
D85A 33
D85A 34
XTAL2
XTAL1
VCC
TTL OSCILLATOR
OUT
GND
C855
100N L
G800
14,745MHZ
R851
10K L
1KHZ
1KHZ
INTR-N
D85A 16
D85A 14
D85A 13
D85A 11
D85A 22
D85A 46
D85A 47
D85A 44
D85A 43
D85A 42
D85A 24
T0IN
T0GATE
T1IN
T1GATE
INTR
PA0
PA1
PA4
PA5
PA6
INTE
C810
100N L
C820
100N L
C825
100N L
C830
100N B
C840
100N L
C868
100N B
C800
100N L
D840-D
74HCT86T-L
D840-C
74HCT86T-L
R840
1K L
R841
1K L
D810-B
74HCT273T-L
D820-E
74HCT00T-L
D825-E
74HCT00T-L
D830-C
74HCT20T-L
D840-E
74HCT86T-L
D860-B
LT1181CS-L
D800-B
N85C220-80

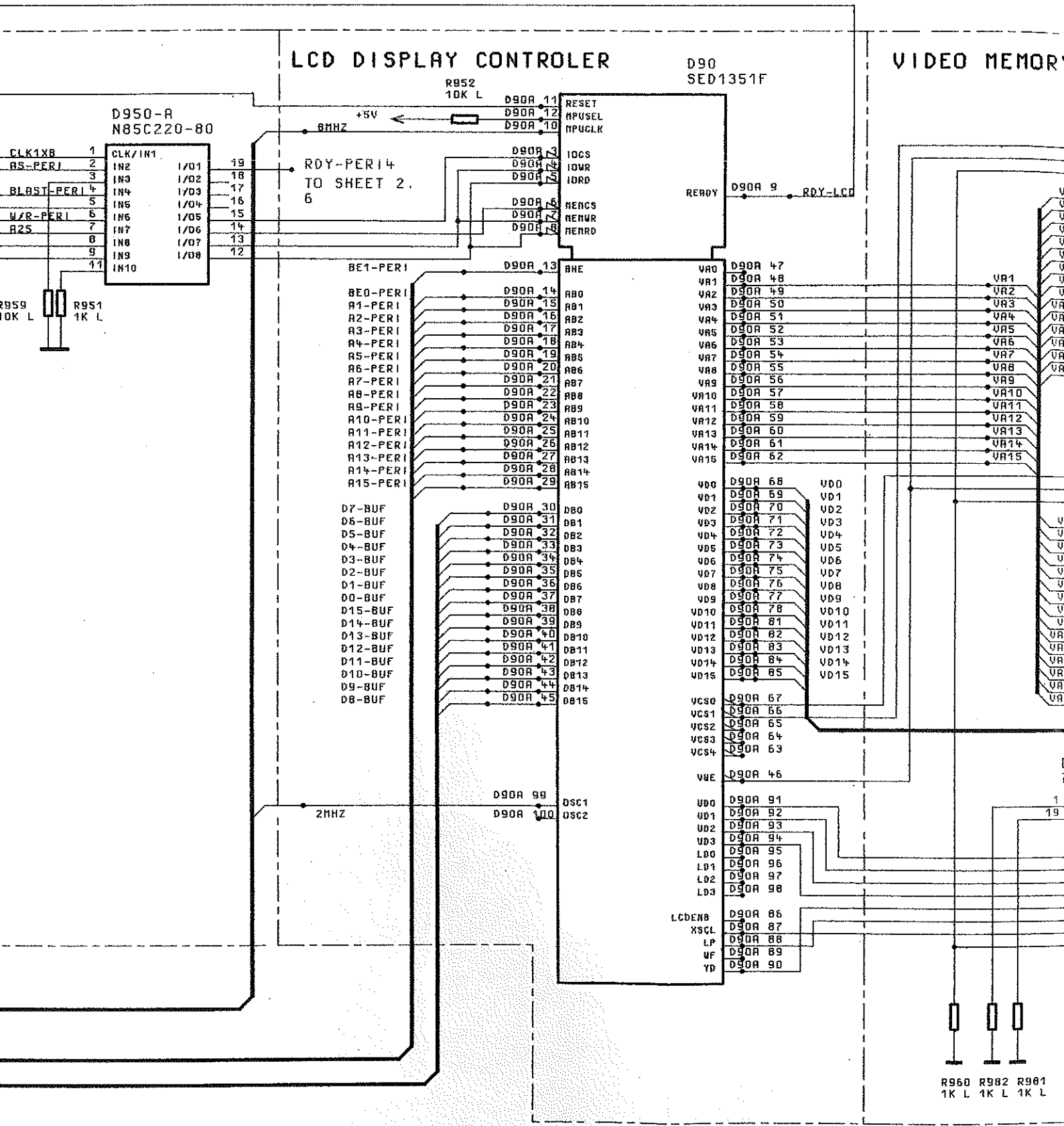

D800-A
N85C220-80
CLK1XB
RS-PERI
BLAST-PERI
W/R-PERI
RDY-PERI3 TO SHEET 2,6
RD-SERBUS TO SHEET 12
WR-SERBUS TO SHEET 12
R805 10K L
R802 1K L
RES-IEC-P TO SHEET 10
D810-A 74HCT273T-L
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
X800 1
X800 2
X800 3
200HZ
100HZ
D825-A 74HCT00T-L
D825-B 74HCT00T-L
D825-C 74HCT00T-L
D825-D 74HCT00T-L
INT1-P
INT0-N
D820-A 74HCT00T-L
D820-B 74HCT00T-L
D820-C 74HCT00T-L
D820-D 74HCT00T-L
D830-A 74HCT20T-L
D840-A 74HCT86T-L
D840-B 74HCT86T-L
R800 47K5 L
R801 47K5 L
+5V


+5V
C850 100N L
C851 100N L
D85A 1
D85A 35
D85A
D85A 18
D85A 52
C875 100N L
C876 100N L
C877 100N L
C878 100N L
C810 100N L
C820 100N L
C825 100N L
C830 100N B
C840 100N L
D810-B 74HCT273T-L
D820-E 74HCT00T-L
D825-E 74HCT00T-L
D830-C 74HCT20T-L
D840-E 74HCT86T

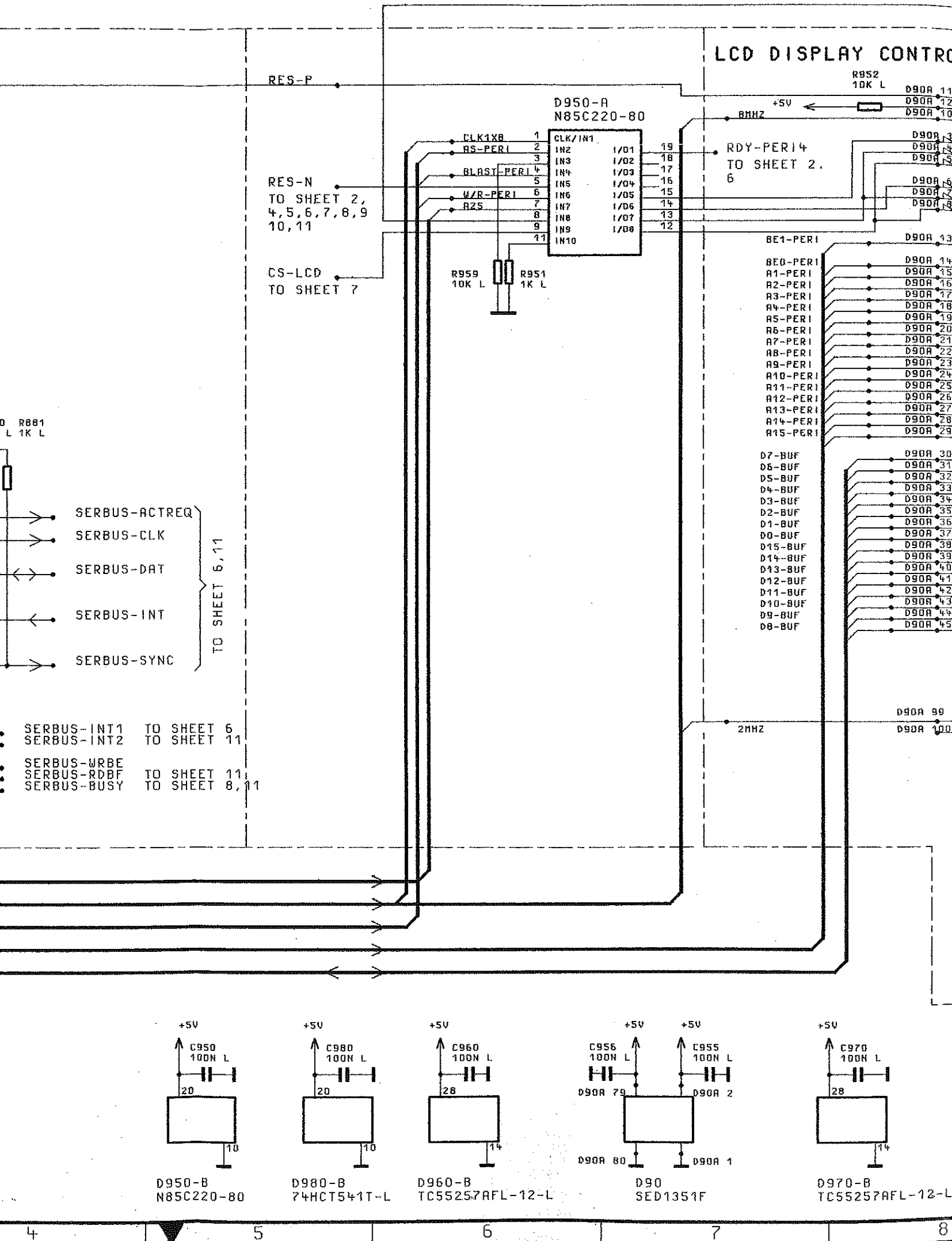

RES-P
TO SHEET 2,3, 6,10,12
D800-A
N85C220-80
CLK1XB
BS-PERI
BLAST-PERI
W/R-PERI
RDY-PERI3 TO SHEET 2,6
RD-SERBUS TO SHEET 12
WR-SERBUS TO SHEET 12
RES-N
TO SHEET 2, 3,4,5,6,7,8,9,10,12
CS-SERBUS
TO SHEET 7,12
CS-SAB82556
TO SHEET 7
R805 10K L
R802 1K L
RES-IEC-P
TO SHEET 10
D825-A
74HCT00T-L
D825-B
74HCT00T-L
D810-A
74HCT273T-L
WR-REG4-LOW
TO SHEET 7
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D820-A
74HCT00T-L
D820-B
74HCT00T-L
D820-C
74HCT00T-L
D820-D
74HCT00T-L
X800
200HZ
100HZ
KEY-INT-P
TO SHEET 8
KNOB-INT-P
TO SHEET 8
TRIGGER
TO SHEET 6
R800 47K5 L
R801 47K5 L
+5V
TO SHEET 6 AUX-TRIG
TO SHEET 10 IEC-INT-P
T2-INTO
T2-INT2
TO SHEET 12 SERBUS-ACTREQ
TO SHEET 6 AC-FAIL
TO SHEET 12 SERBUS-INT2
TO SHEET 12 SERBUS-INT1
TO SHEET 8 KEY-STROKE
TO SHEET 8 DIR-FF
INTR-N
CONTROL-BUS-CPU
INTERRUPT-BUS
A-BUS-PERI
D-BUS-BUF
CLK-BUS-CPU
D87A
D87A 68
D87A 61
D87A 1
D87A 19
D87A 34
D87A 53
D87A 35
D87A 18
D87A 44
D87A 51
D87A 52
D87A 60
C875 100N L
C876 100N L
C877 100N L
C878 100N L
C850 100N L
C851 100N L
D85A
D85A 1
D85A 35
D85A 18
D85A 52
C810 100N L
D810-B
74HCT273T-L
C820 100N L
D820-E
74HCTOOT



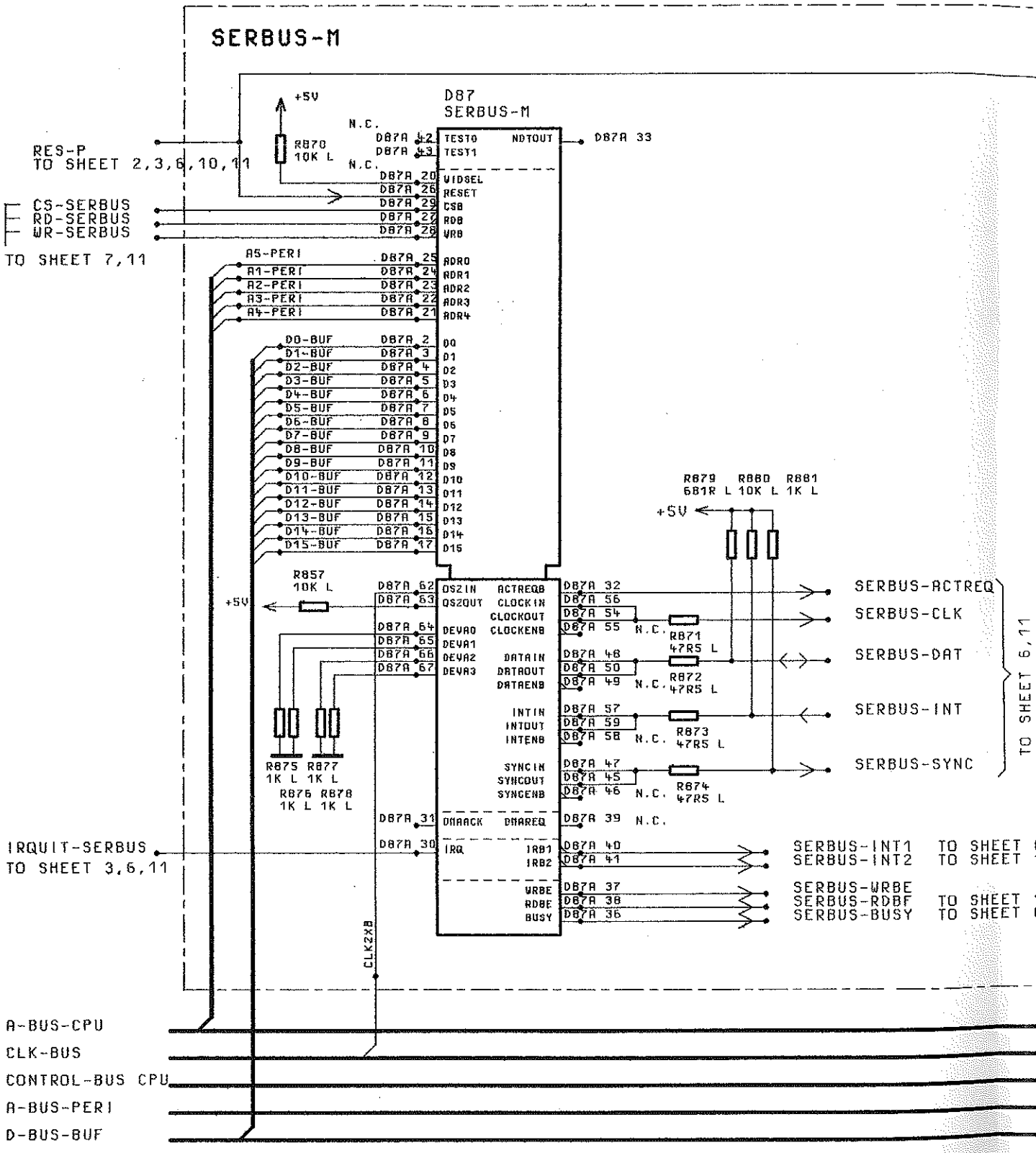
04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG	
				BEARB.		DR	RECHNER	
				GEPR.			PROCESSOR	
				NORM				
				PLOTT	03.05.94			
/							ZEICHN.-NR.	BLATT-NR.
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			1035.7308.01S	12+
				ZU GERAET	SMP		REG.I.V. 1035.5005	ERSTE Z. 1035.5440



04/	48730	02.05.94	DR	1GPK	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	03.05.94
/					
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCH	
				ZU GERAET	SMP

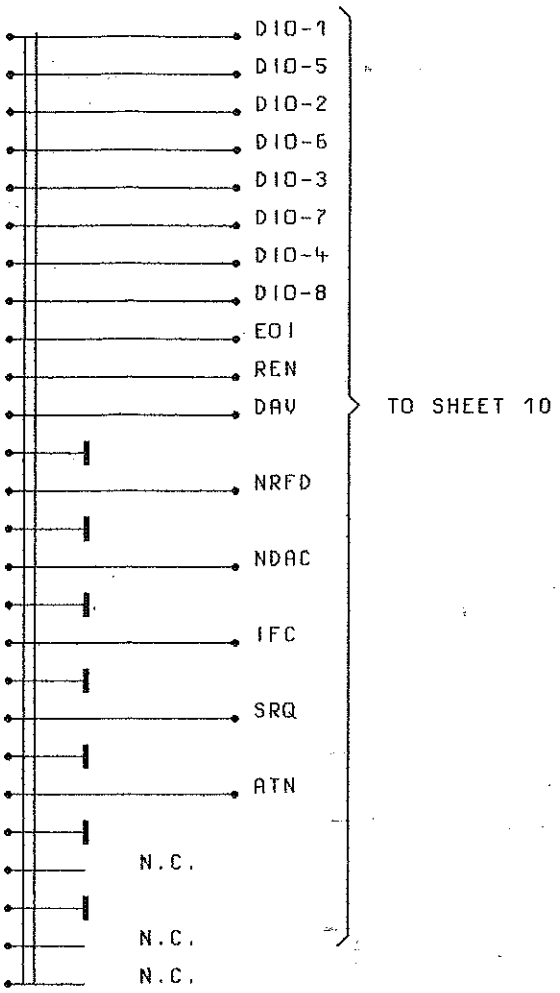

LCD DISPLAY CONTRO
RES-P
R952
10K L
+5V
8MHZ
D950-A
N85C220-80
CLK1X8
AS-PERI
BLAST-PERI
W/R-PERI
A25
CLK/IN1
IN2
IN3
IN4
IN5
IN6
IN7
IN8
IN9
IN10
I/O1
I/O2
I/O3
I/O4
I/O5
I/O6
I/O7
I/O8
RDY-PERI4
TO SHEET 2.
6
RES-N
TO SHEET 2,
4,5,6,7,8,9
10,11
CS-LCD
TO SHEET 7
R959
10K L
R951
1K L
BE1-PERI
BE0-PERI
A1-PERI
A2-PERI
A3-PERI
A4-PERI
A5-PERI
A6-PERI
A7-PERI
A8-PERI
A9-PERI
A10-PERI
A11-PERI
A12-PERI
A13-PERI
A14-PERI
A15-PERI
D7-BUF
D6-BUF
D5-BUF
D4-BUF
D3-BUF
D2-BUF
D1-BUF
D0-BUF
D15-BUF
D14-BUF
D13-BUF
D12-BUF
D11-BUF
D10-BUF
D9-BUF
D8-BUF
R881
L 1K L
SERBUS-ACTREQ
SERBUS-CLK
SERBUS-DAT
SERBUS-INT
SERBUS-SYNC
TO SHEET 6,11
SERBUS-INT1 TO SHEET 6
SERBUS-INT2 TO SHEET 11
SERBUS-WRBE
SERBUS-RDBF TO SHEET 11
SERBUS-BUSY TO SHEET 8,11
2MHZ
+5V
C950
100N L
C980
100N L
C960
100N L
C956
100N L
C955
100N L
C970
100N L
D90A 79
D90A 2
D90A 80
D90A 1
D950-B
N85C220-80
D980-B
74HCT541T-L
D960-B
TC55257AFL-12-L
D90
SED1351F
D970-B
TC55257AFL-12-L

BUS-M
D87
SERBUS-M
+5V
R870
10K L
N.C.
D87A 42
D87A 43
TEST0
TEST1
NDTOUT
D87A 33
D87A 20
D87A 26
D87A 29
D87A 27
D87A 28
VIDSEL
RESET
CSB
RDB
WRB
ADR0
ADR1
ADR2
ADR3
ADR4
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
R857
10K L
R875
1K L
R877
1K L
R876
1K L
R878
1K L
QSZIN
QSZOUT
DEVA0
DEVA1
DEVA2
DEVA3
ACTREQB
CLOCKIN
CLOCKOUT
CLOCKENB
DATAIN
DATAOUT
DATAENB
INTIN
INTOUT
INTENB
SYNCIN
SYNCOUT
SYNCENB
DMAACK
DMAREQ
IRQ
IRB1
IRB2
WRBE
RDBE
BUSY
R879
681R L
R880
10K L
R881
1K L
R871
47R5 L
R872
47R5 L
R873
47R5 L
R874
47R5 L
N.C.
SERBUS-ACTREQ
SERBUS-CLK
SERBUS-DAT
SERBUS-INT
SERBUS-SYNC
TO SHEET 6,11
SERBUS-INT1 TO SHEET 6
SERBUS-INT2 TO SHEET 11
SERBUS-WRBE
SERBUS-RDBF TO SHEET 11
SERBUS-BUSY TO SHEET 8,11
CLK2XB
RES-P
RES-N
TO SHEET 2,
4,5,6,7,8,9
10,11
CS-LCD
TO SHEET 7
CLK1XB
AS-PERI
BLAST-PERI
W/R-PERI
A25
R959
10K L
R95
1K
C950
100N L
C980
100N L
C960
100N L
20
10
14
D950-B
N85C220-80
D980-B
74HCT541T-L
D960-B
TC55257AFL-12

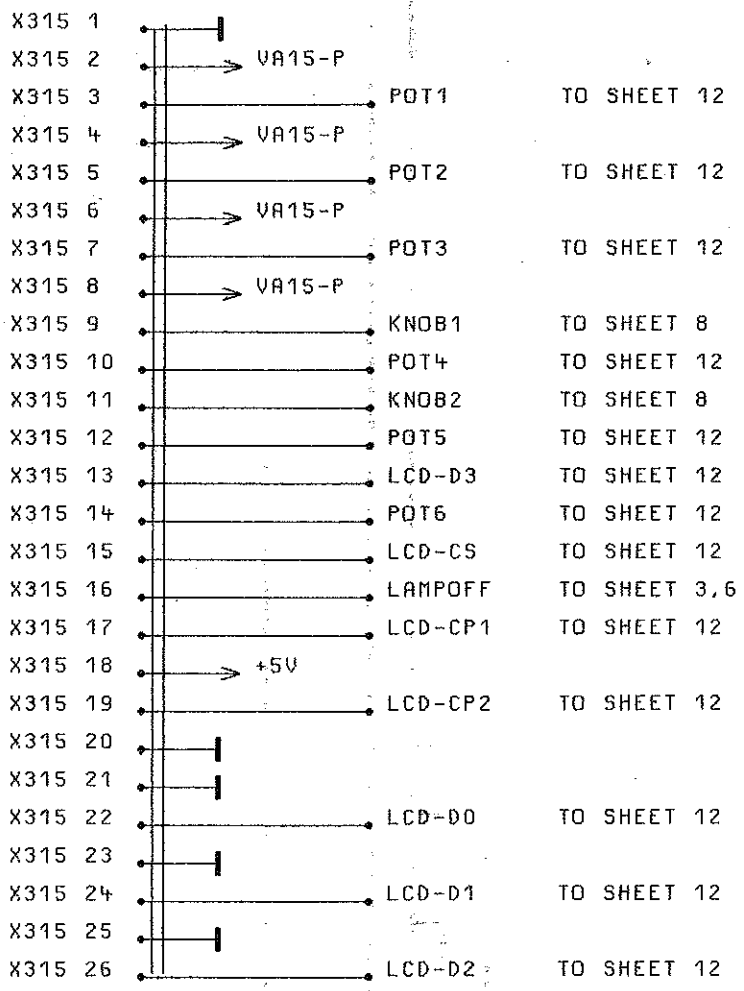


BEHALTEN WIR UNS ALLE RECHTE VOR

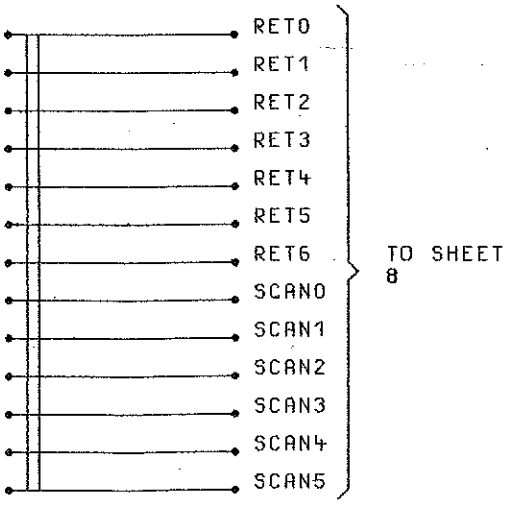
25



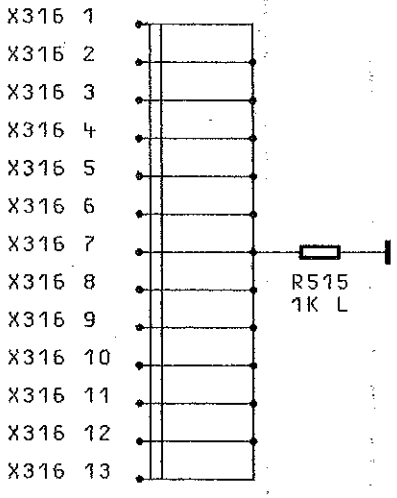
KNOB



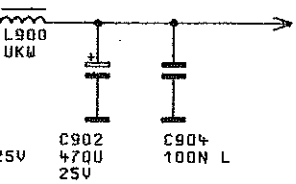
OARD

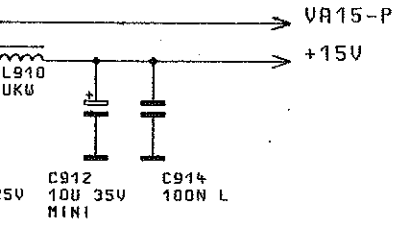


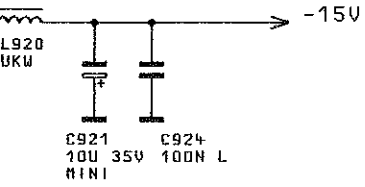
KEYBOARD



04/	48730	02.05.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7308.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			BLATT-NR. 13-
							REG.I.V. 1035.5005. ERSTE Z. 1035.5440

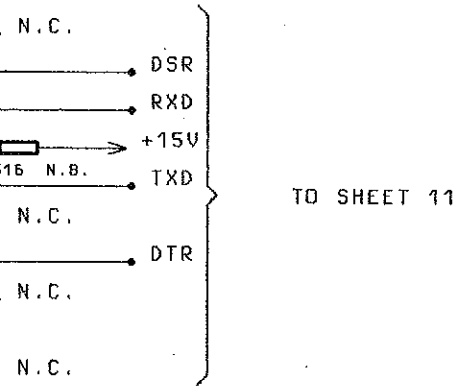




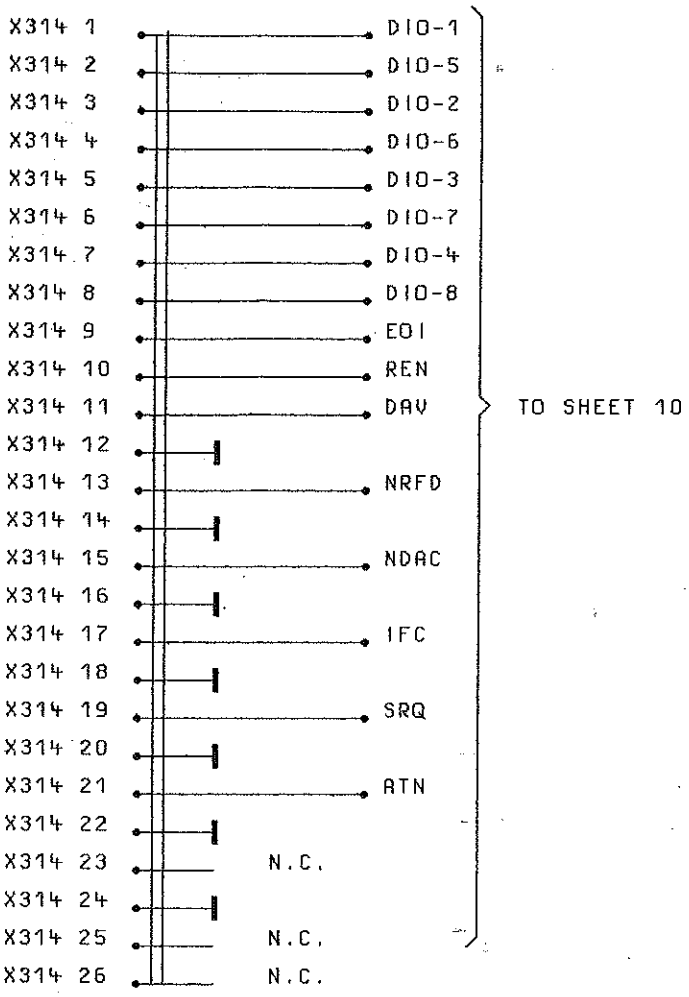


20

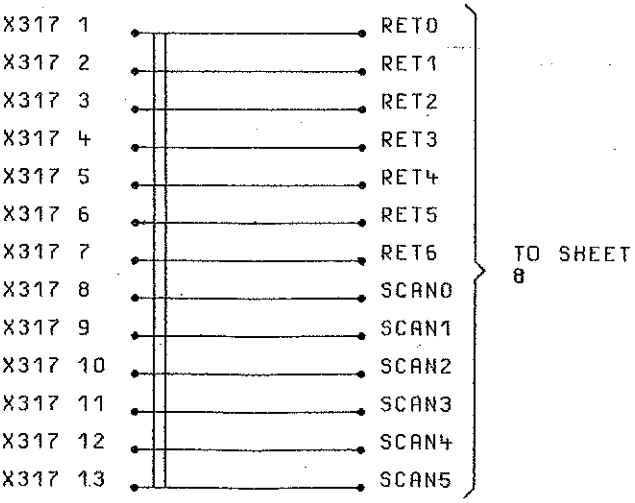
19



IEC625

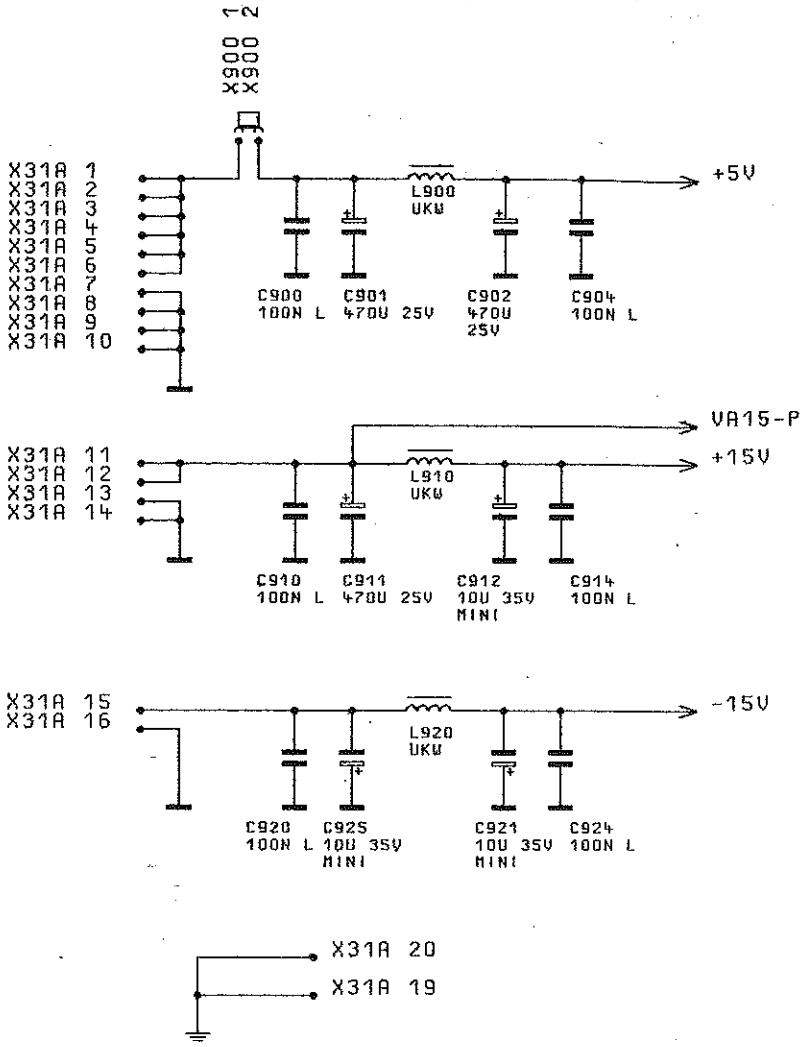


KEYBOARD

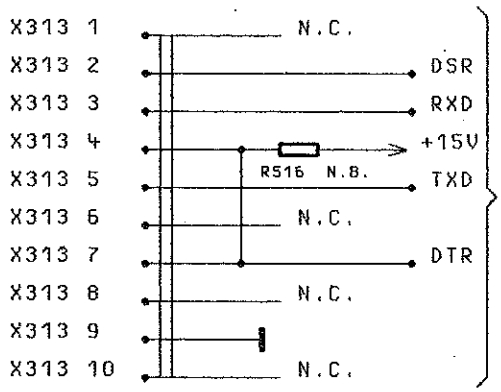


04/	48730	02.05.94	DR	1GPK	TA
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	03.05
/					
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE& ZU GERRET	

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR



RS232



TO SHEET 11

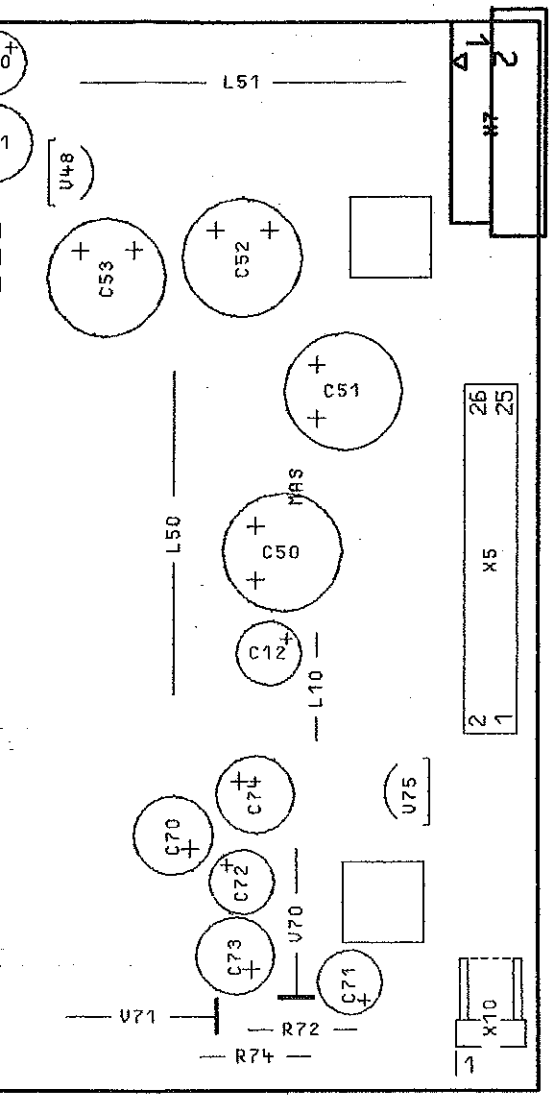
IEC6

X314 1
X314 2
X314 3
X314 4
X314 5
X314 6
X314 7
X314 8
X314 9
X314 1
X314 1
X314 1
X314 1
X314 1
X314 1
X314 1
X314 1
X314 1
X314 2
X314 2
X314 2
X314 2
X314 2
X314 2
X314 2

KEYB

X317 1
X317 2
X317 3
X317 4
X317 5
X317 6
X317 7
X317 8
X317 9
X317 1
X317 1
X317 1
X317 1

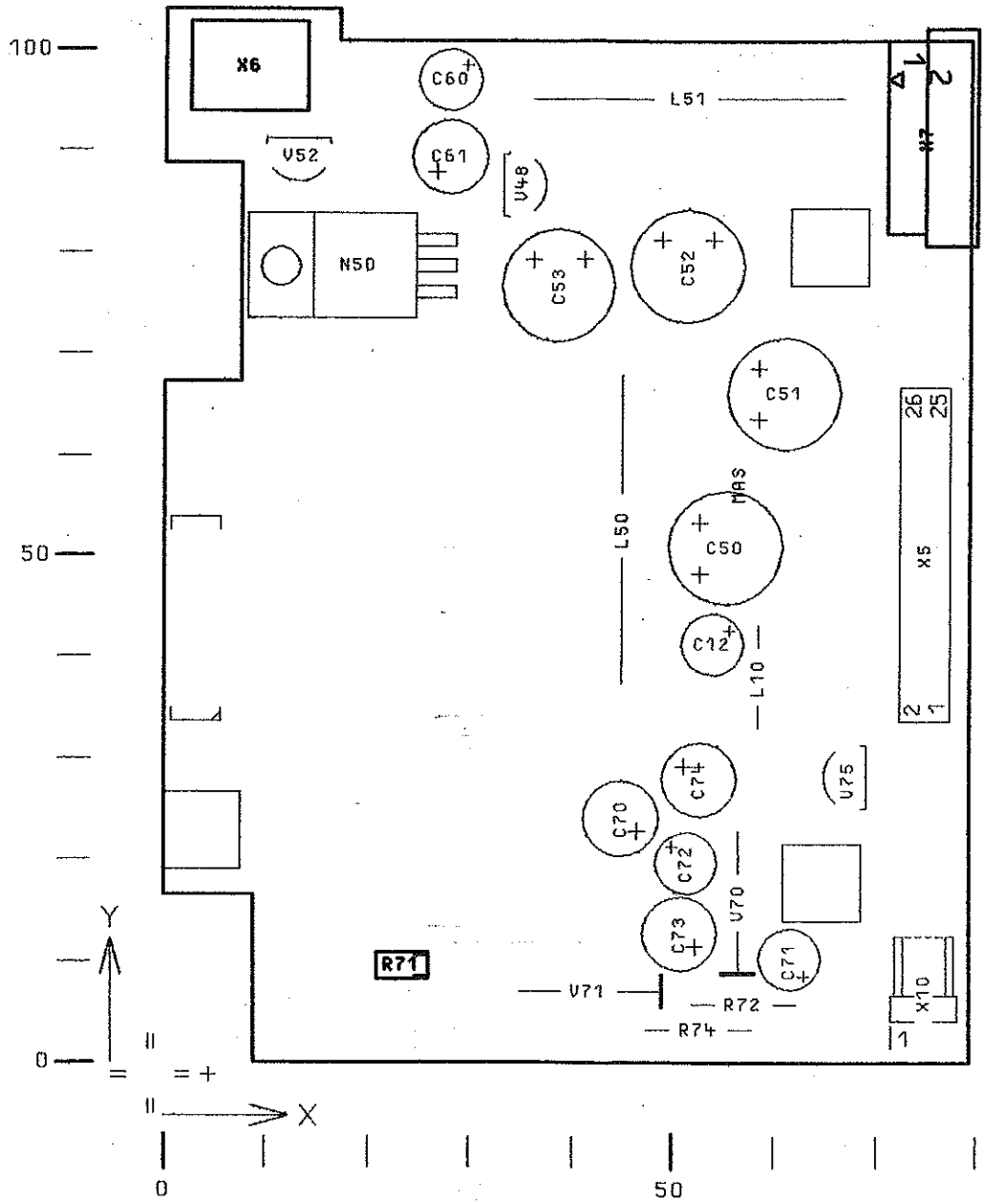
ZEICHN.-N



50

06/	48730 90	07.04.94	JN	1GPK	TAG	NAME	BENENNUNG
				BEARB.		JN	DREHGEBER
				GEPR.		DR	KNOB ASSEMBLY
				NORM			Z
				PLOTT	07.04.94		
/							ZEICHN.-NR. 1035.5592.01 ED
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			BLATT-NR. 1+ V. BL.
				ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5440

5 6 7 8



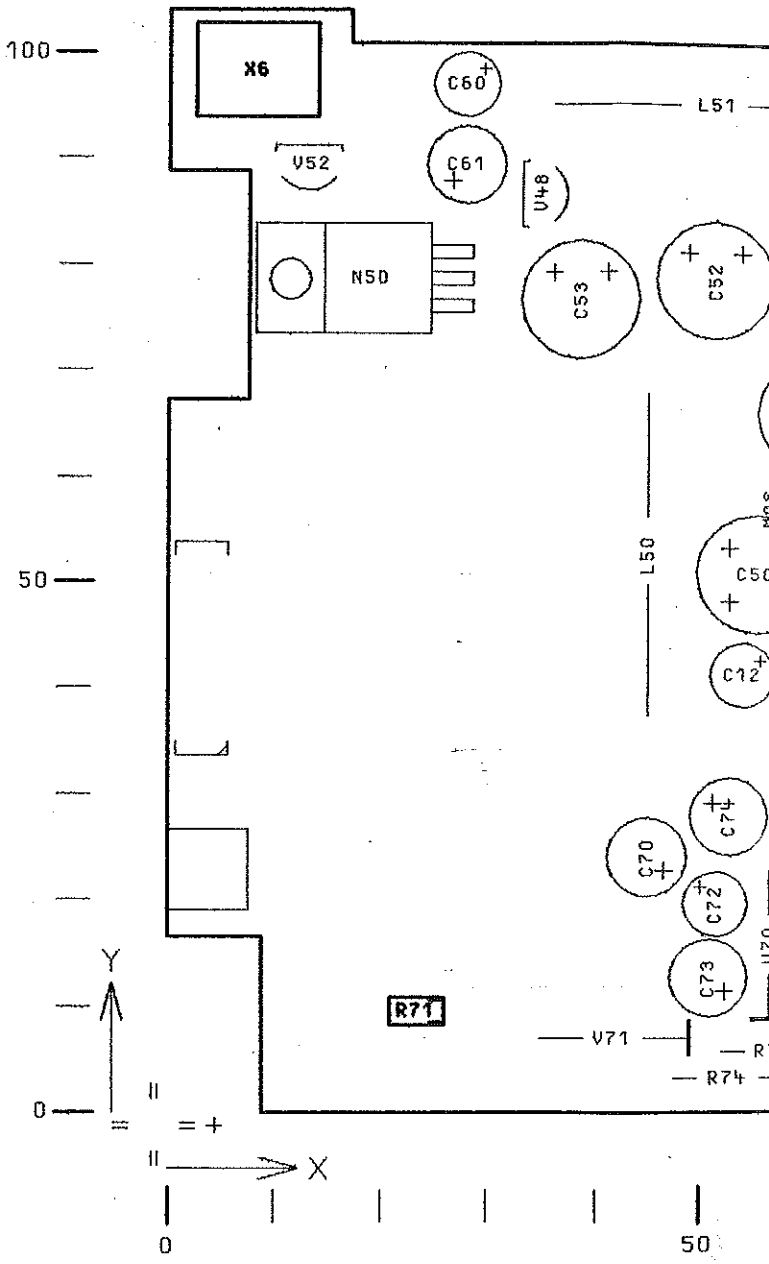
ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.

06/	48730 90	07.04.94	JN	1GPK	TAG	NAME
				BEARB.		JN
				GEPR.		DR
				NORM		
				PLOTT	07.04.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME			

ROHDE&SCHWARZ
ZU GERAET SMP



STELLUNG SEITE B

N ON SIDE B

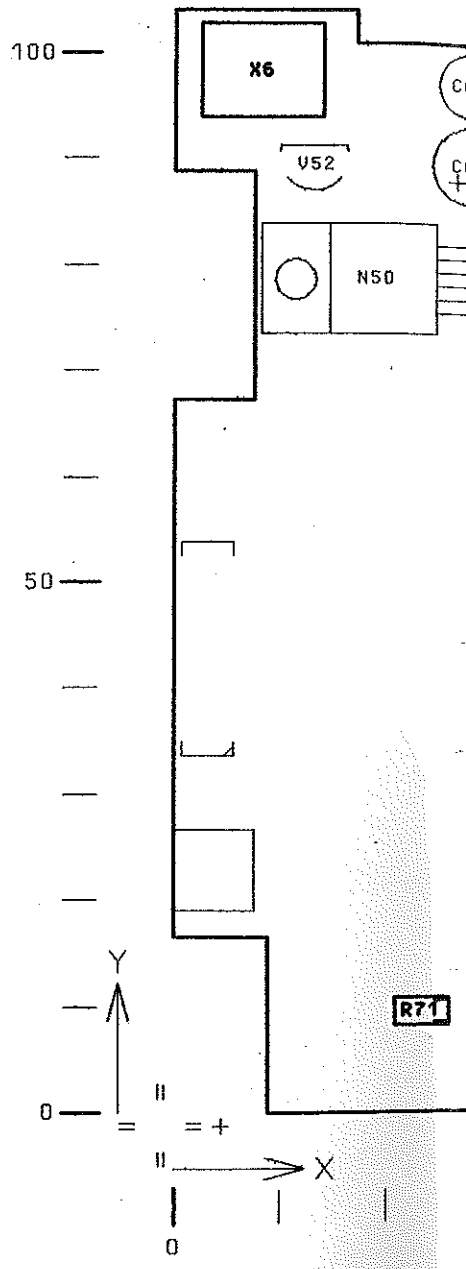
ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.

06/	48730
/	
AEND. IND.	AENDERUN MITTEILU

FUER DIESE ZEICHNUNG BEHALTEN WIR UNS ALLE RECHTE VOR. DIE WEITERGABE DIESER ZEICHNUNG IST OHNE UNSERE AUSDRUECKLICHE ERLAUBNIS UNTERSAGT. ZUWIDERHANDLUNGEN VERPFLICHTEN ZUM SCHADENERSATZ. ALLE RECHTE FUER DEN ERTEILUNG EINES PATENTES ODER GEBRAUCHSMUSTERSCHUTZES DATENSATZES ERFOLGEN
DIESE ZEICHNUNG IST E ECHNERAUSDUCK, AENDERUNGEN KOENNEN NUR DURCH AEND



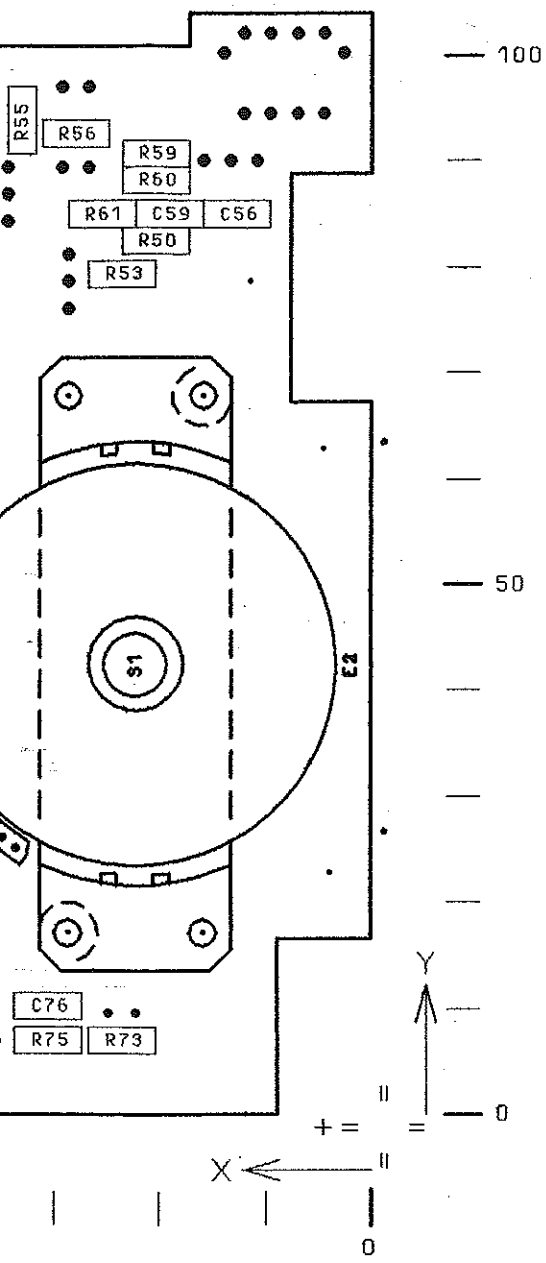
DARSTELLUNG SEITE B

VIEW ON SIDE B

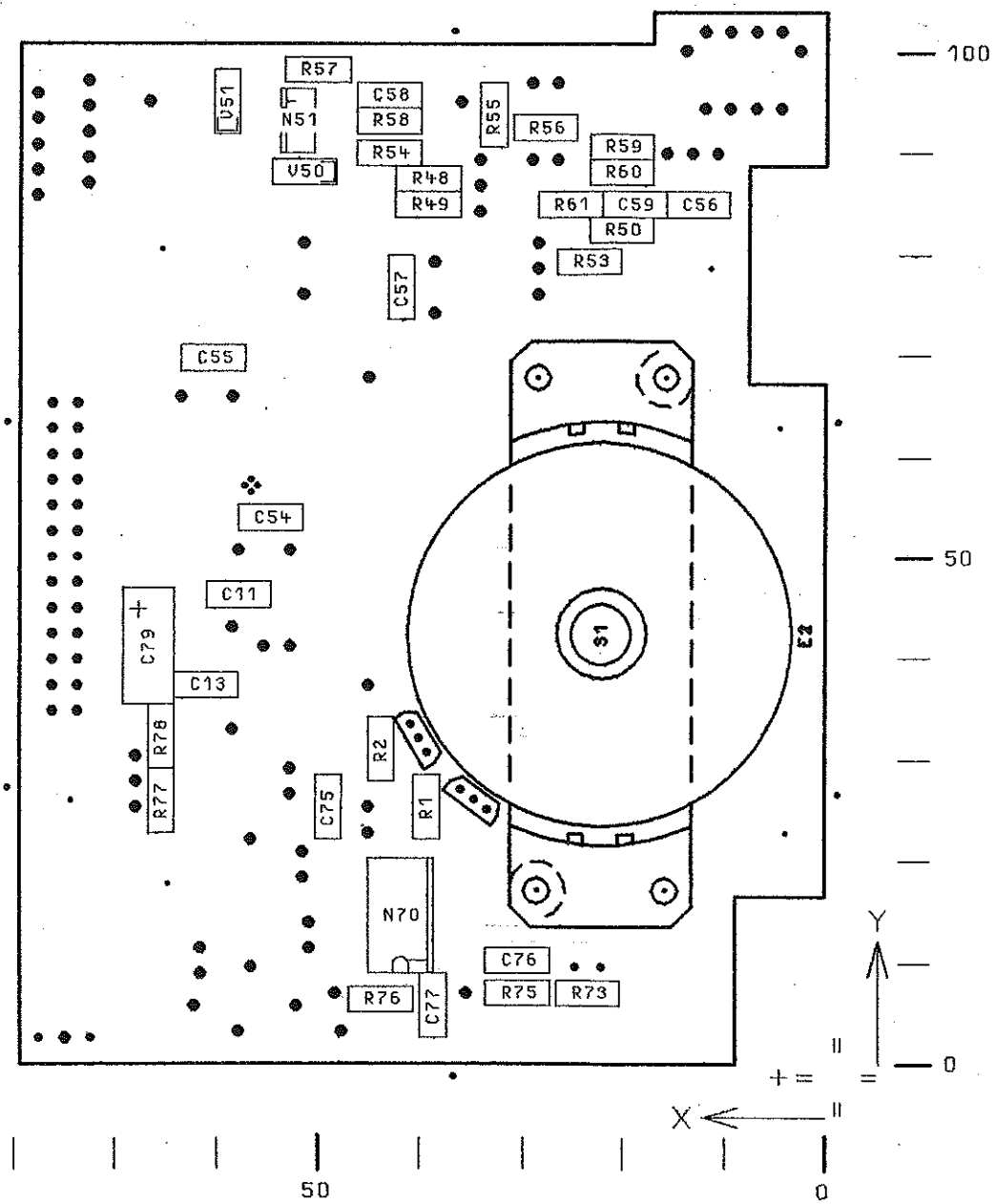
ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN
TRIMMWERTE, BAUTEILWERTE UND
NICHT BESTUECKTE BAUTEILE SIEHE

FOR BINDING INFORMATION ON MODEL
TRIMMING AND COMPONENTS VALUES A
NONFITTED COMPONENTS SEE PARTS L



06/	48730 90	07.04.94	JN	1GPK	TAG	NAME	BENENNUNG	
				BEARB.		JN	DREHGEBER	Z
				GEPR.		DR	KNOB ASSEMBLY	
				NORM				
				PLOTT	07.04.94			
/							ZEICHN.-NR.	BLATT-NR.
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			1035.5592.01 ED	2+
				ZU GERAET SMP			REG.I.V. 1035.5005	ERSTE Z. 1035.5440

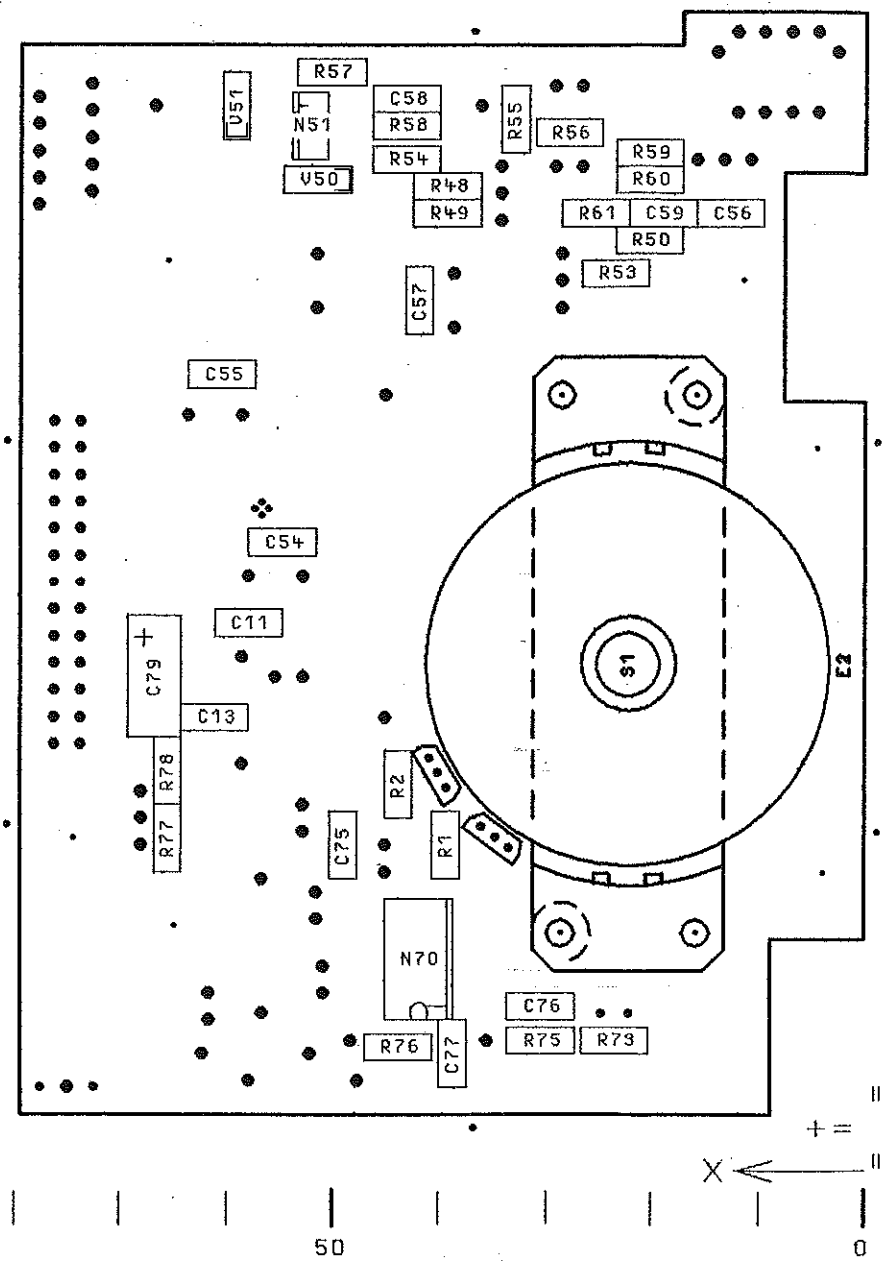


NG: EGB!
TISCH GEFAEHRDETE
E ERFORDERN EINE
RE HANDHABUNG.
ION ESD!
C SENSITIVE DEVICES
SPECIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.

06/	48730 90	07.04.94	JN	16PK	TAG	NAME	BENENNUNG
				BEARB.		JN	DRE
				GEPR.		DR	KNOB
				NORM			
				PLOTT	07.04.94		
							ZEICHN.-NR
/							
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			
				ZU GERRET	SMP		REG.I.V.



UNG SEITE A

SIDE A

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.

ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

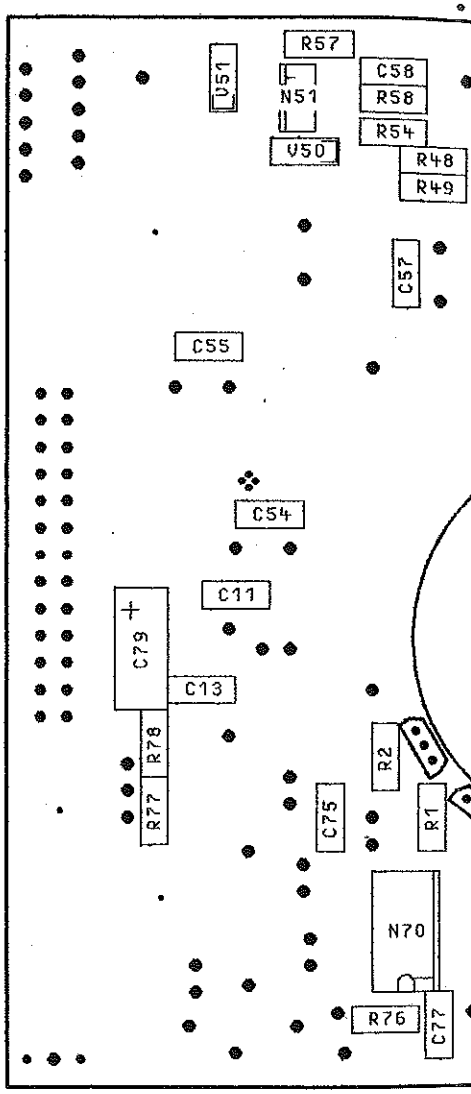
BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.

06/	48730 90	07.0
/		
AEND. IND.	AENDERUNGS-MITTEILUNG	DA

2 3 4 5

DIESE ZEICHNUNG IST E... ER DIESE ZEICHNUNG BEHALTEN WIR UNS ALLE RECHTE VO... ES DATENSATZES ERFOLGEN
...CHNERAUSDUCK, AENDERUNGEN KOENNEN NUR DURCH AENDE...



50

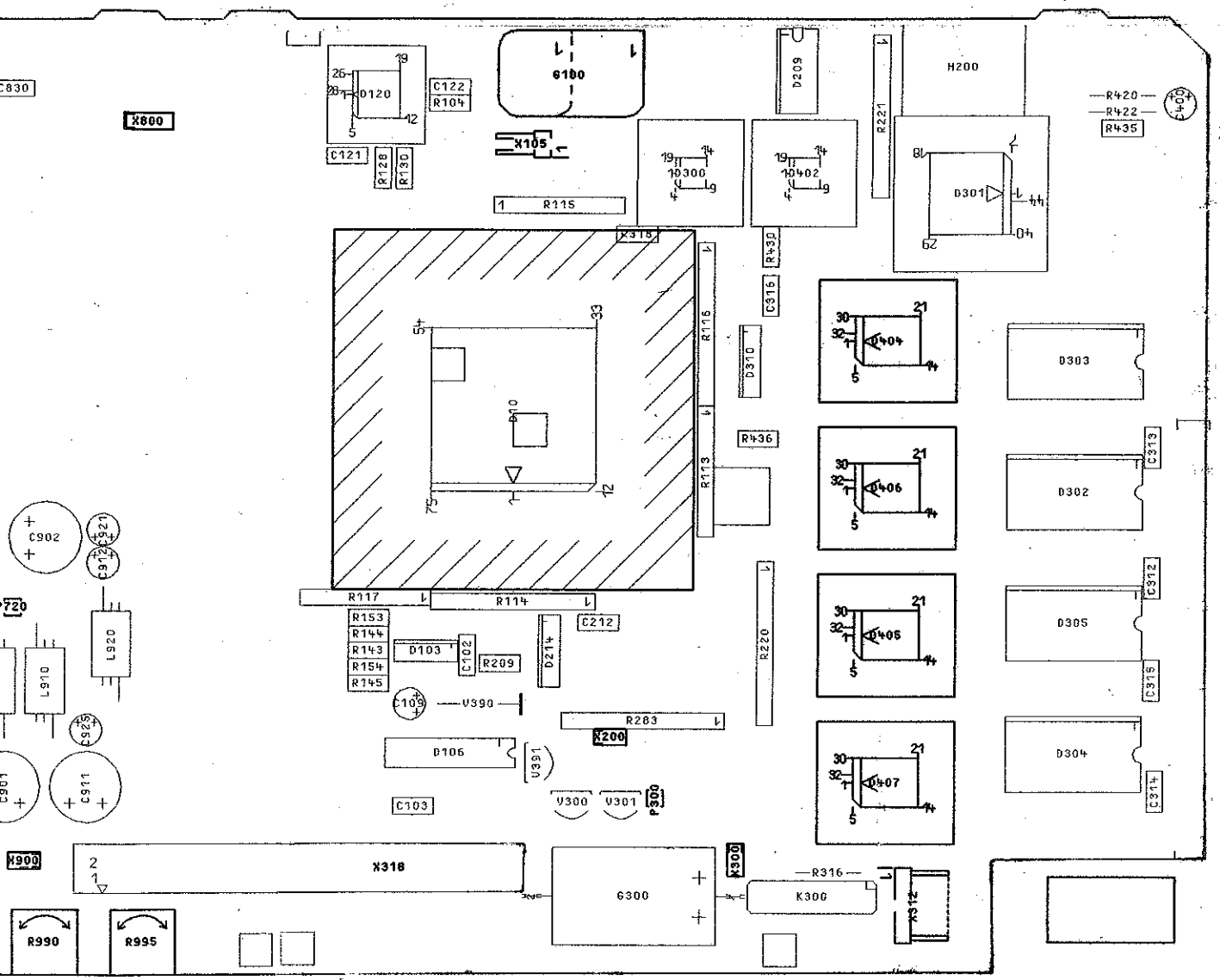
DARSTELLUNG SEITE A

VIEW ON SIDE A

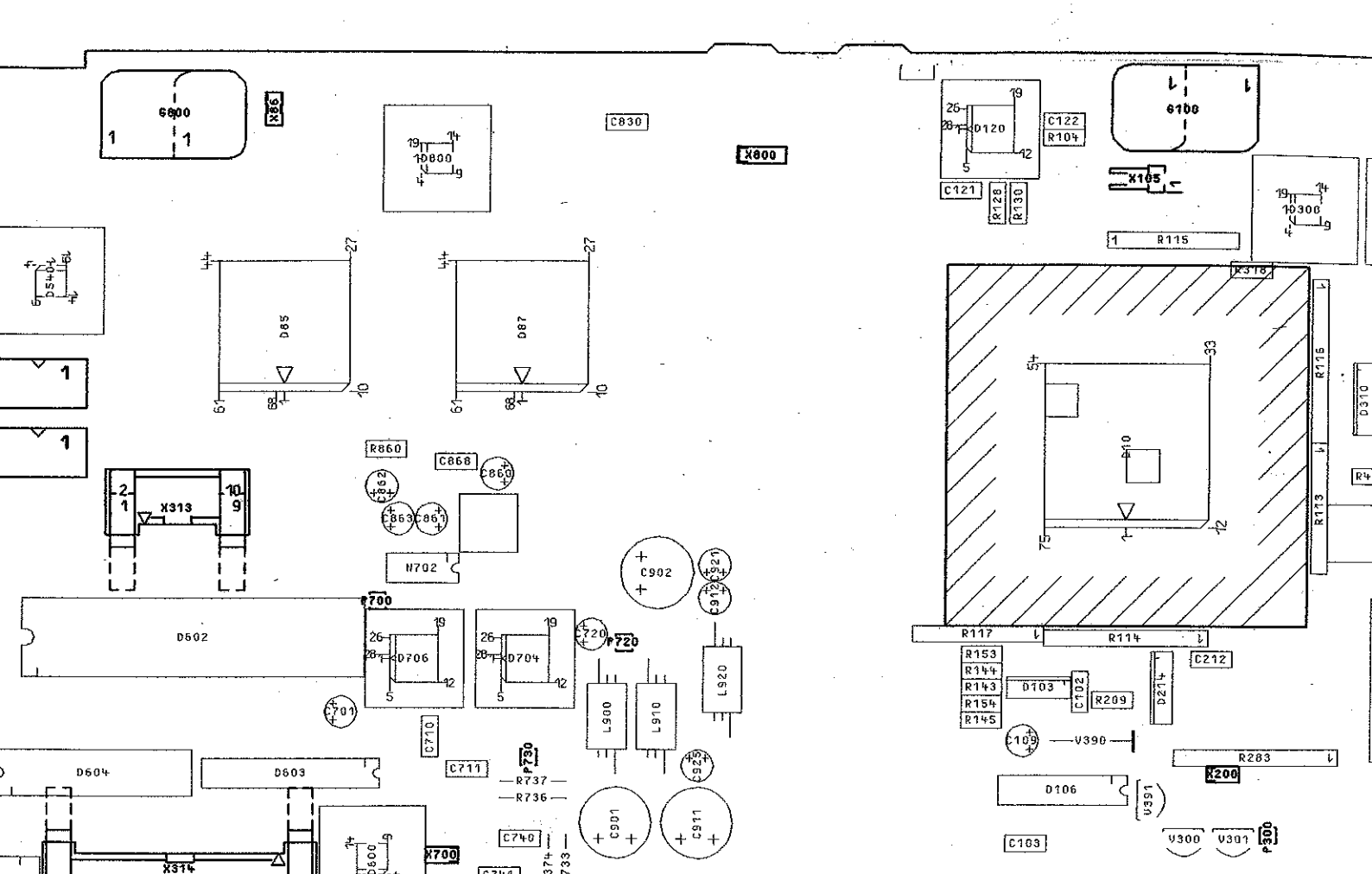
ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE S...

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AN... NONFITTED COMPONENTS SEE PARTS LI...



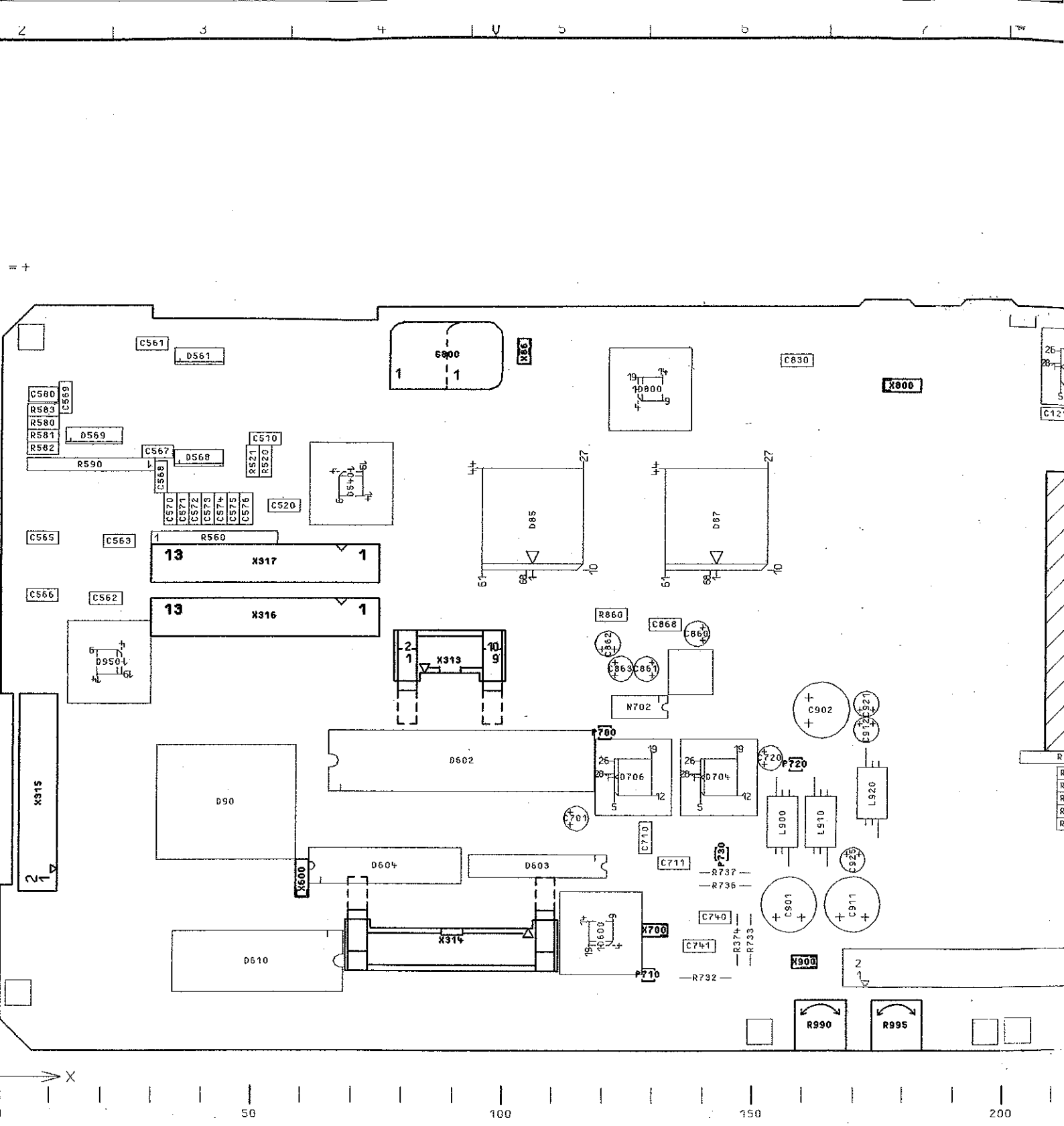
					TAG	NAME	BENENNUNG	
04				BEARB.		DR	RECHNER	Z
				GEPR.		DR	PROCESSOR	
				NORM				
				PLOTT	25.09.92			
				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7308.01	ED
				ZU SERIE:	SMP		REG.L.V. 1035.5005	ERSTE Z.
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME				BLATT-NR. 1+ V. BL.	



BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.

REND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME
04			



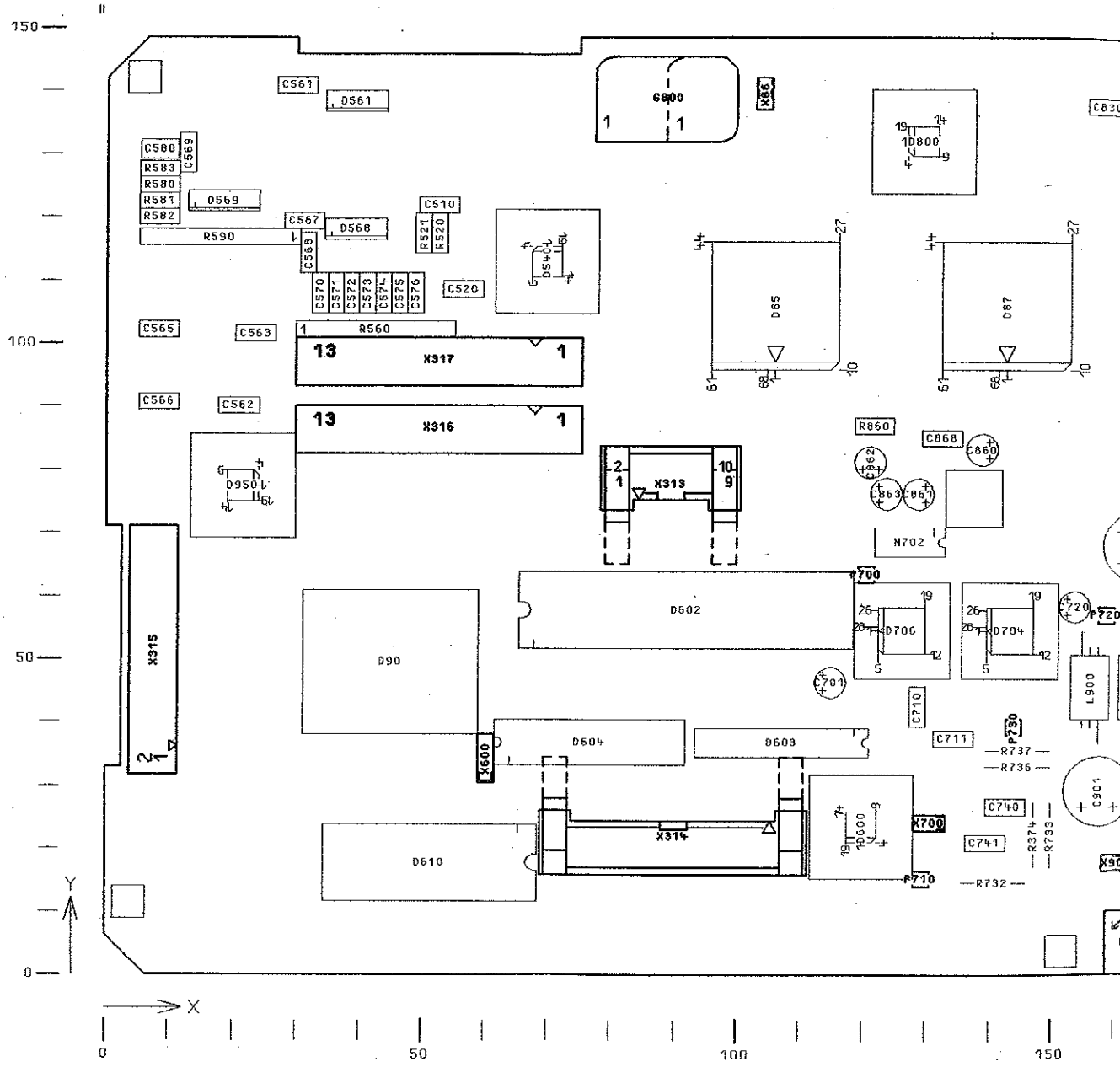
SEITE B

B

ACHTUNG: ESD!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.



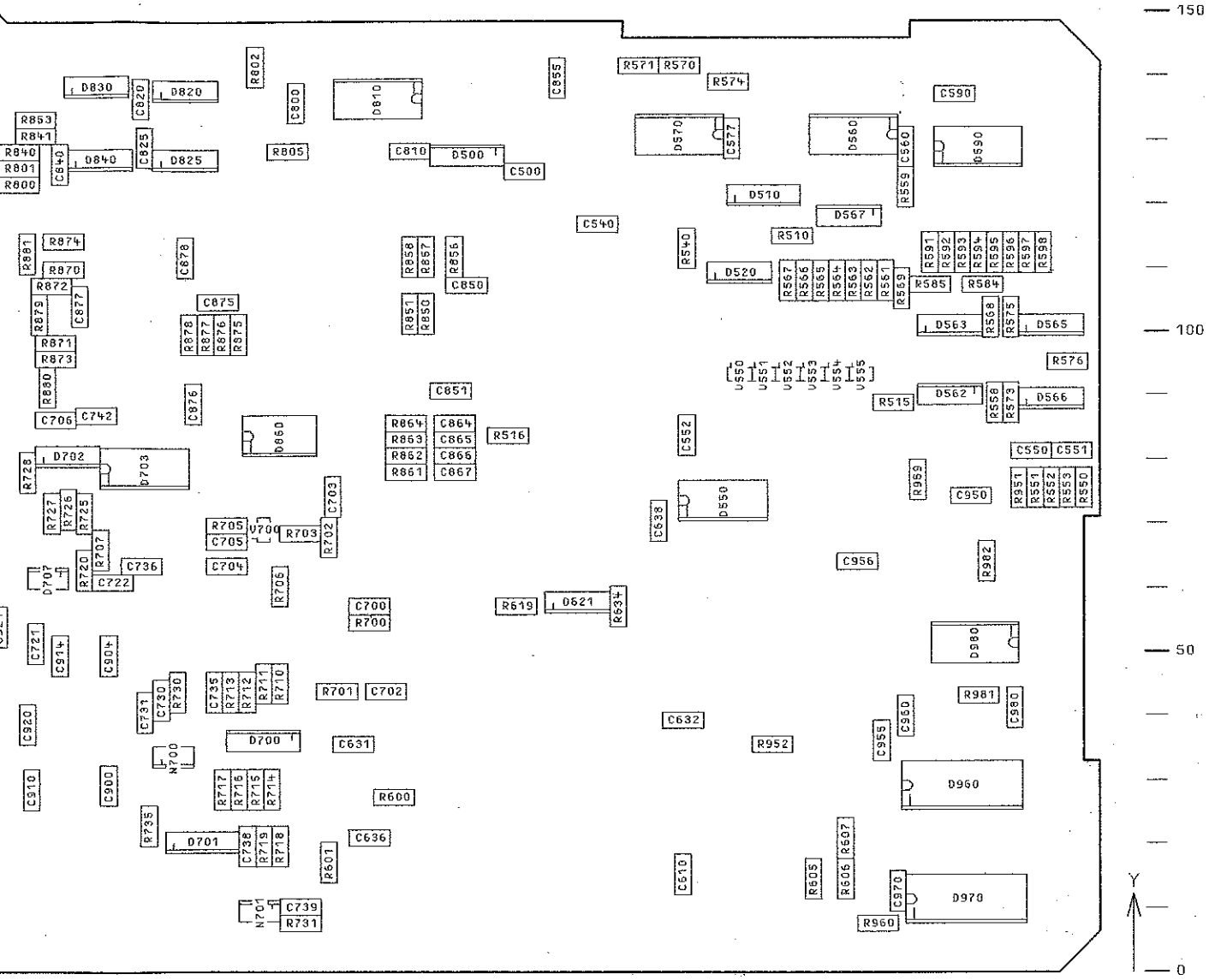
FUER DIESE ZEICHNUNG BEHALTEN WIR UNS ALLE RECHTE VOR.
DIESE ZEICHNUNG IST EIN RECHNERAUSDRUCK, RENDERUNGEN KOENNEN NUR DURCH RENDERN DES DATENSATZES ERFOLGEN

DARSTELLUNG SEITE B
VIEW ON SIDE B

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

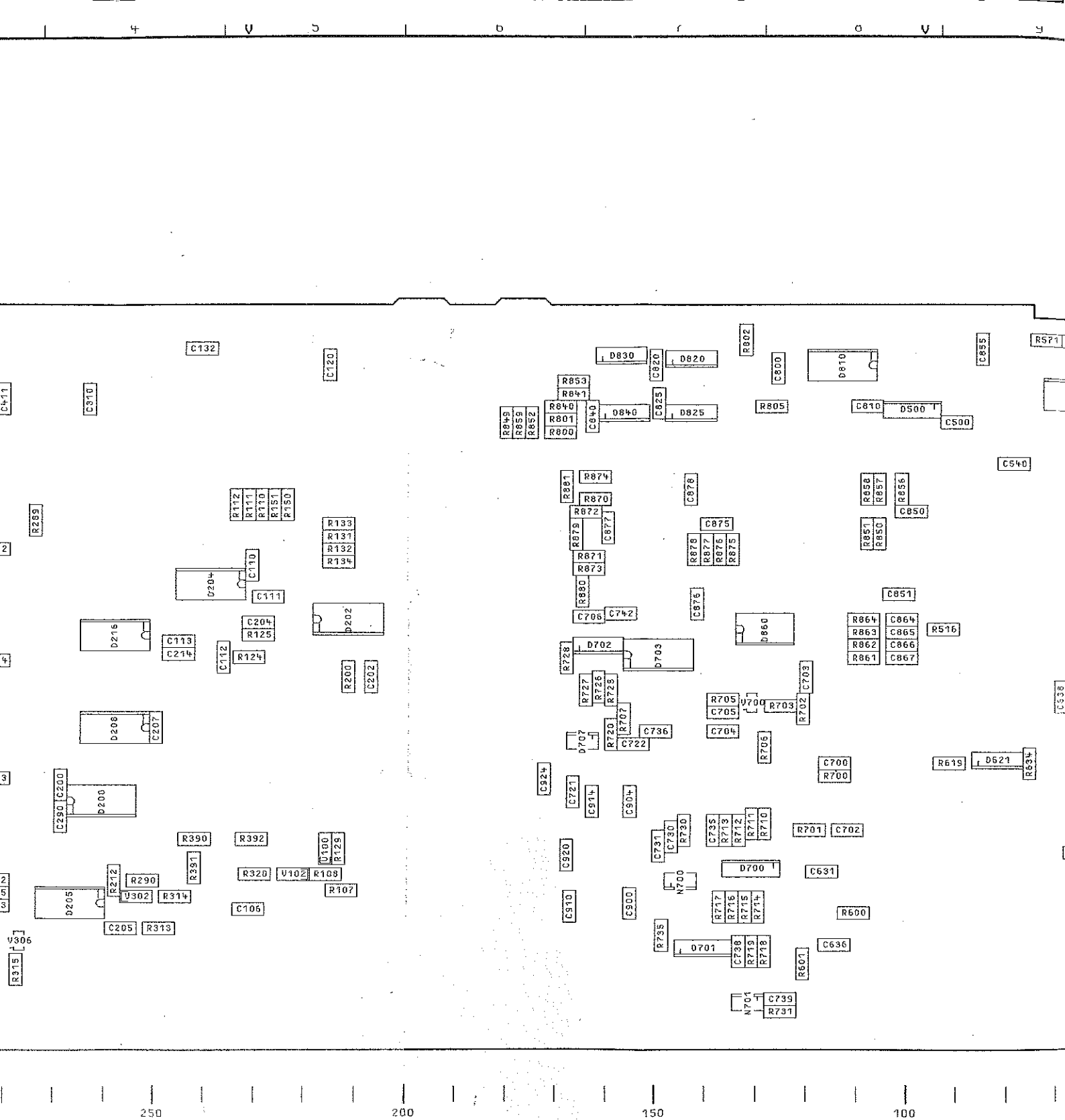
BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.



					TAG	NAME	BENENNUNG	
04				BEARB.		DR	RECHNER	Z
				GEPR.		DR	PROCESSOR	
				NORM				
				PLOTT	25.09.92			
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7308.01	ED
				ZU GERÄT SMP			REG.-NR. 1035.5005	ERSTE Z.

BLATT-NR. 2+

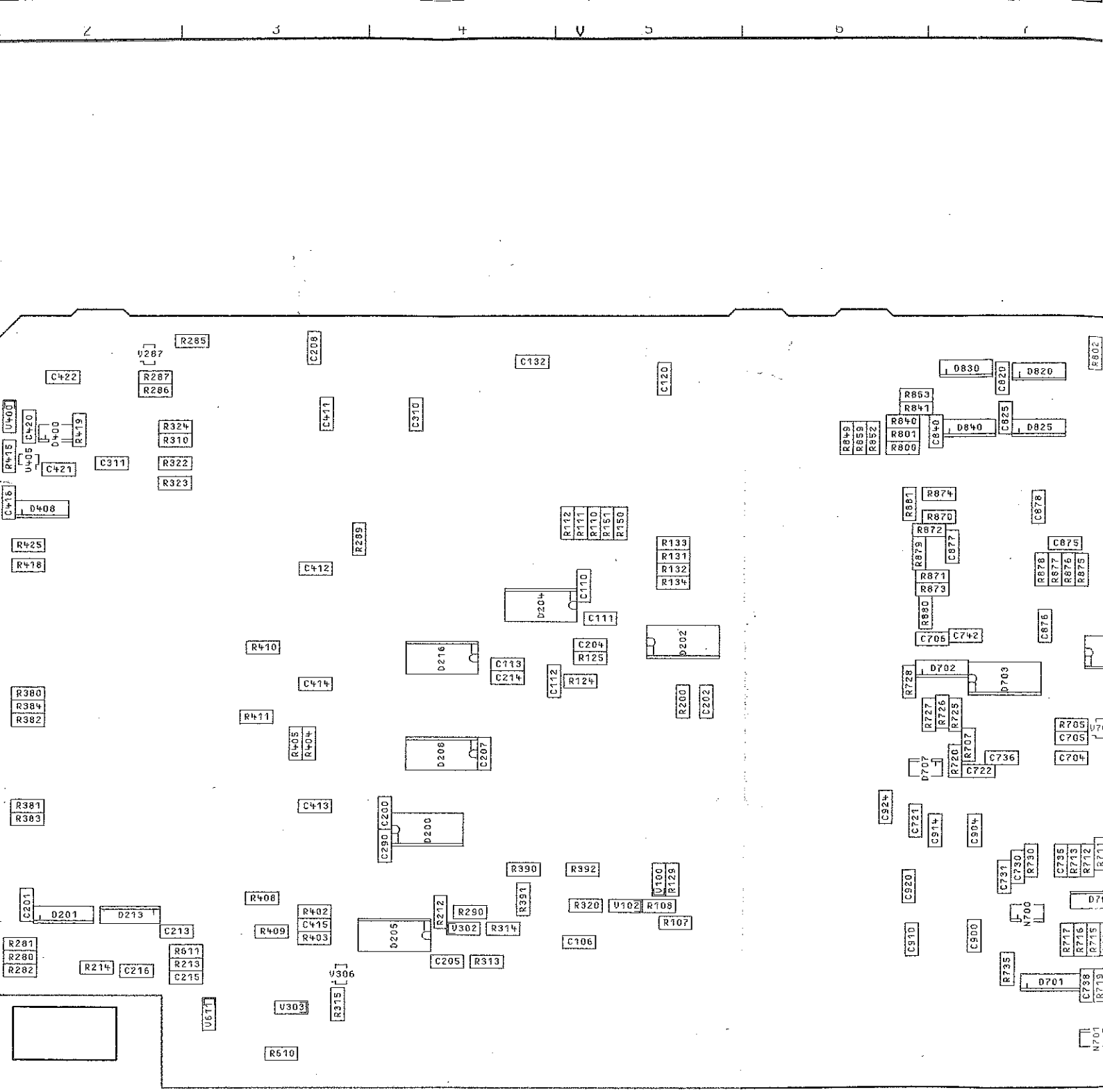


EGB!
H GEFAEHRDETE
RFORDERN EINE
HANDHABUNG.
N ESD!
ENSITIVE DEVICES
CIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN,
TRIMMWERTE, BAUTEILWERTE UND
NICHT BESTUECKTE BAUTEILE SIEHE 5A.

FOR BINDING INFORMATION ON MODELS,
TRIMMING AND COMPONENTS VALUES AND
NONFITTED COMPONENTS SEE PARTS LIST.

04		
AEND. IND.	AENDERUNGS-MITTEILUNG	DA



X

0 300 250 200 150

LLUNG SEITE A

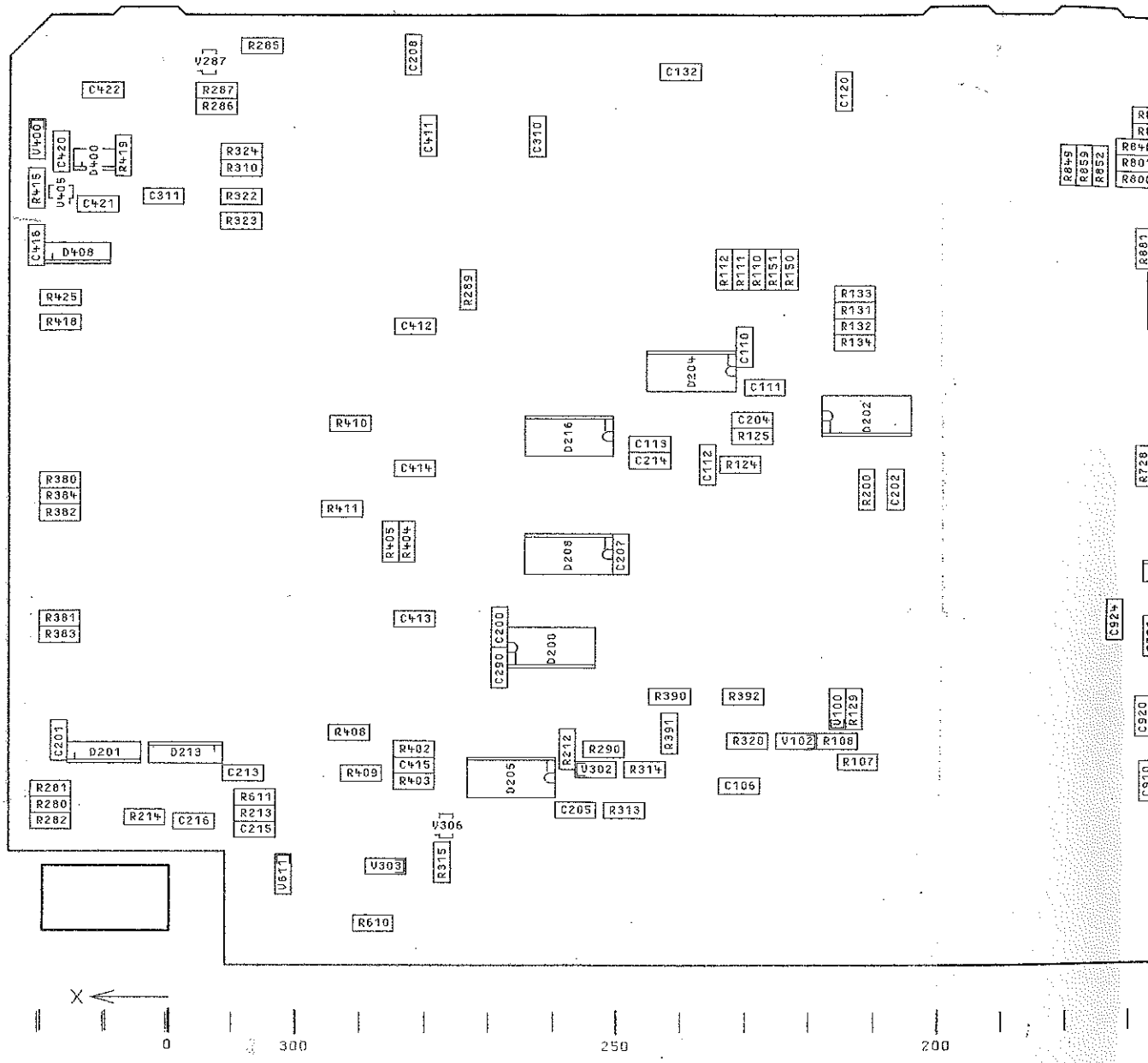
SIDE A

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.

ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

BINDEHOE ANGABEN UEBER VARIANTEN, TRIMMUERTE, BAUTEILUERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.



FUER DIESE ZEICHNUNG BEHALTEN WIR UNS ALLE RECHTE VOR.
DIESE ZEICHNUNG IST EIN RECHNERAUSDRUCK, RENDERUNGEN KOENNEN NUR DURCH RENDERN DES DATENSATZES ERFOLGEN

DARSTELLUNG SEITE A

VIEW ON SIDE A

ACHTUNG: ESD!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.