AUSTRON MODEL 1210D SERIES PORTABLE CRYSTAL CLOCK

OPERATION AND MAINTENANCE

AUSTRON INC.

WARRANTY

AUSTRON, Inc., of Austin, Texas, warrants, for one year after delivery, to the original purchaser of any product manufactured by AUSTRON, that same shall be free of defects in material and workmanship. Obligation under this warranty shall be limited to repair or replacement, at AUSTRON's discretion, of any product or part thereof which has been returned by the original purchaser with transportation prepaid, and upon examination by AUSTRON, is found to be defective. AUSTRON assumes no responsibility for loss or damage to equipment being returned for repair or replacement under the terms of this warranty.

For this warranty to be effective, the purchaser agrees that the equipment will be properly installed and maintained. Equipment which, upon examination by AUSTRON, requires repair or replacement of parts thereof as a result of improper installation, misuse, unauthorized alterations or repairs, or user negligence, such repairs or replacement of parts thereof will be made at cost.

AUSTRON makes no representation or warranty of any kind, either expressed or implied, with respect to equipment operation and procedures. Any action that the user may take in reliance upon the operation or accuracy of this equipment shall be taken solely upon the user's own responsibility and risk.

AUSTRON shall not be liable for consequential damages to purchaser, user, or any others resulting from the possession or use of this equipment.

Prior to return of a product under terms of this warranty, AUSTRON, Inc., Austin, Texas, is to be notified. Notification is to include the Model Number and Serial Number of the product and full details of the problem.

AUSTRON INC. -

AUSTRON MODEL 1210D PORTABLE CRYSTAL CLOCK

TABLE OF CONTENTS

Section		<u>Title</u>	Page
1.0	GENERAL D	ESCRIPTION	1-1
	1.1	SCOPE OF SECTION	1-1
	1.2	PURPOSE OF EQUIPMENT	1-1
	1.3	SPECIFICATIONS OF EQUIPMENT	1-2
	1.4	CONTROLS, INDICATORS, AND CONNECTORS .	1-4
2.0	INSTALLAT	ION	2-1
	2.1	SCOPE OF SECTION	2-1
	2.2	UNPACKING AND INSPECTION	2-1
	2.3	OPERATIONAL INSTALLATION	2-2
	2.4	PREPARATION FOR RESHIPMENT	2-2
3.0	OPERATION	INSTRUCTIONS	3-1
	3.1	SCOPE OF SECTION	3-1
	3.2	SET-UP AND TURN-ON PROCEDURES	3-1
	3.3	TIME SYNCHRONIZATION	3-2
	3.4	TIME DISPLAY SYNCHRONIZATION	3-4
4.0	FUNCTIONA	L DESCRIPTION	4-1
	4.1	SCOPE OF SECTION	4-1
	4.2	GENERAL CIRCUIT DESCRIPTION	4-2
	4.3	+15 VOLT REGULATOR	4-4
	4.4	+5 VOLT DC REGULATOR	4-5
	4.5	SINE CONVERTER	. 4-6
	4.6	OUTPUT AMPLIFIERS	. 4-7
	4.7	TIME SYNC	. 4-8
	4.8	DIVIDER CARD	. 4-9
	4.9	PULSE AMPLIFIER CARD	. 4-10
	4.10	TOD CONTROL AND DISPLAY	. 4-11
	4.11	INTERNAL OSCILLATOR	. 4-13

AUSTRON MODEL 1210D PORTABLE CRYSTAL CLOCK

TABLE OF CONTENTS (Cont'd.)

	Section			Tit	<u>le</u>										Page
	5.0	MAINTENAN	CE						•						5-1
		5.1	SCOPE OF	F SECTI	ON .										5-1
		5.2	TROUBLE	ANALYS	SIS GU	IDES			•				•		5-1
		5.3	GENERAL	MAINTE	NANCE	· .		•	•	•	•	•	-	•	5-4
	6.0	REPLACEAB	LE PARTS										•		6-1
		6.1	SCOPE OF	F SECTI	ON.				•			•	•		6-1
·		6.2	ORDERING	G REPLA	CEMEN	T PA	RTS	•	•	•	•	•	•	•	6-1
	7.0	OPTIONAL	ASSEMBLII	ES .									•	•	7-1
		7.1	SCOPE OF	F SECTI	ON .				•						7-1
		7.2	LIST OF	OPTION	IAL AS	SEMBI	LIES	·	•		•	•			7-1
		7.3	MODEL 12	210D-01	PORT	ABLE	CRY	STA	ΛL	CI	OC	K	•		7-3
		7.4	MODEL 12	210D-02	PORT	ABLE	CRY	STA	ΛĽ	CI	OC.	K	•		7-4
		7.5	MODEL 12	210D-03	PORT	ABLE	CRY	STA	ΛĽ	CI	OC	K	•		7-8

AUSTRON MODEL 1210D PORTABLE CRYSTAL CLOCK

LIST OF ILLUSTRATIONS

Section	<u>Title</u> <u>Figu</u>	re No
1.0	FRONT AND REAR PANEL CONTROLS (12496783) 1	-1
4.0	BLOCK DIAGRAM 1210D CRYSTAL CLOCK (12496793) 4	-1
	SCHEMATIC, 1210D CRYSTAL CLOCK (12396154) 4	-2
	PCB ASSY., BRIDGE-A7 (10394664) 4	-3
	TOP VIEW, 1210D CRYSTAL CLOCK (12496787) 4	-4
	BOTTOM VIEW, 1210D CRYSTAL CLOCK (12496786) 4	- 5
	PCB ASSY., DC FILTER-All (10396200) 4	-6
	PCB ASSY., OUTPUT FILTER-A3 (10396735) 4	-7
	SCHEMATIC, +15 VDC REGULATOR-A17 (12396784) 4	-8
	PCB ASSY., +15 VDC REGULATOR-A17 (10396197) 4	-9
	SCHEMATIC, +5 VDC REGULATOR (12394684) 4	-10
	PCB ASSY., +5 VDC REGULATOR (10394607)	-11
	SCHEMATIC, SINE CONVERTER (12396096)	l - 12
	PCB ASSY., SINE CONVERTER (10396025)	l - 13
	SCHEMATIC, OUTPUT AMPLIFIER (12396098) 4	1-14
	PCB ASSY., OUTPUT AMPLIFIER (10396031)	1- 15
	SCHEMATIC, TIME SYNC (12394686)	!- 16
	PCB ASSY., TIME SYNC (10394618)	
	SCHEMATIC, DIVIDER (12394679)	1-18
	PCB ASSY., DIVIDER (10394604)	
	SCHEMATIC, PULSE AMPLIFIER (12394695)	
	PCB ASSY., PULSE AMPLIFIER (10394595)	
	SCHEMATIC, TOD CONTROL/DISPLAY (12396189)	
	PCB ASSY., TOD CONTROL (10396188)	
		4-24

AUSTRON INC.

AUSTRON MODEL 1210D PORTABLE CRYSTAL CLOCK

LIST OF ILLUSTRATIONS (Cont'd.)

Section	<u>Title</u>	Figure	No.
7.0	FRONT PANEL (00996192)	. 7-1	
	SCHEMATIC, 1210D-01 CRYSTAL CLOCK (12396570)	. 7-2	
	BLOCK DIAGRAM, 1210D-01 CRYSTAL CLOCK (12496555) .	. 7-3	
	PCB ASSY., FINAL FILTER (10397355)	. 7-4	
	SCHEMATIC, 100 KHZ OUTPUT AMPLIFIER (12397357)	. 7-5	
	PCB ASSY., 100 KHZ OUTPUT AMPLIFIER (10397359)	. 7-6	
	SCHEMATIC, 10, 5, 1, MHZ OUTPUT AMPLIFIER (12397364) 7-7	
	PCB ASSY., 10, 5, 1, MHZ OUTPUT AMPLIFIER (10397342) 7-8	
	BLOCK DIAGRAM, 1210D-02 CRYSTAL CLOCK (12496556) .	. 7-9	
	SCHEMATIC, 1210D-02 CRYSTAL CLOCK (12396571)	. 7-1	0
	SCHEMATIC, 60 HZ SINE CONVERTER (12396518)	. 7-1	1
	PCB ASSY., 60 HZ SINE CONVERTER (10396517)	. 7-1	2
	BLOCK DIAGRAM, 1210D-03 CRYSTAL CLOCK (12496625) .	. 7-1	3
	SCHEMATIC, 1210D-03 CRYSTAL CLOCK (12396624)	. 7-1	4
	SCHEMATIC, DIVIDER (12396615)	. 7-1	5
	PCB ASSY., DIVIDER (10396614)	. 7-1	6

AUSTRON MODEL 1210D PORTABLE CRYSTAL CLOCK

1.0 GENERAL DESCRIPTION

1.1 SCOPE OF SECTION

1.1.1 This section introduces the AUSTRON Model 1210D Portable Crystal Clock. Provided here are descriptions of the equipment, operating controls, and indicators (see figure 1-1).

1.2 PURPOSE OF EQUIPMENT

- 1.2.1 The AUSTRON Model 1210D Clock is a compact self-contained time reference providing stable time outputs at several pulse frequencies. The timing of the output pulse can be varied over a wide range by means of a digital phase shifter, which has a resolution of 0.2 microseconds. In addition to the pulse output, the model 1210D has sinusoidal outputs. Thus, the model 1210D clock can be used to transfer time from one geographical location to another. A digital time display is also provided.
- 1.2.2 The case is approximately 8 1/2 inches high, 8 1/2 inches wide and 14 inches long, which facilitates portability. This case size permits upright storage under the type of passenger seat found on most commercial aircraft.
- 1.2.3 The AUSTRON Model 1210D Clock operates from a source of 115/230 V ac , or from 24 V dc to 28 V dc, \pm 2 V dc. An internal battery pack will operate the clock for a period in excess of eight hours.
- 1.2.4 The AUSTRON Model 1210D utilizes high quality silicon semiconductors and integrated circuit assemblies. All resistors, capacitors, and hardware are chosen for high reliability, and where practical, to meet military requirements.

1.3 SPECIFICATIONS OF EQUIPMENT

1.3.1 The AUSTRON Model 1210D Portable Crystal Clock consists of an AUSTRON Model 1150 Crystal Oscillator, a series of dividers, a digital phase shifter, and output amplifiers which are powered by a supply (which has a voltage regulator and standby batteries). The following paragraphs provide specific information on the model 1210D.

1.3.1.1 Physical Specifications

Height:

8 1/2 inches (216 mm)

Width:

8 1/2 inches (216 mm)

Length:

15 inches (381 mm)

Weight:

22 pounds

1.3.1.2 Electrical Specifications

Input AC Voltage:

115/230 V ac + 10%

48 - 420 Hz

Input DC Voltage:

24 - 28 V dc + 2 V dc

Standby:

Internal Nicad battery pack

provides at least eight

hours of operation.

Battery Recharge:

14 to 16 hours.

1.3.1.3 Operating Specifications

Sinusoidal Outputs

Frequency:

5 MHz and 1 MHz

Level:

1 VRMS -20 +50% into 50 ohms

Harmonic Distortion:

30 dB from rated output

Spurious Signals:

60 dB from rated output

Pulse Outputs

Rate:

1 PPS

Amplitude:

+ 5 V peak minimum into

50 ohms

Width:

5 - 25 microseconds at the

50% amplitude points

Jitter:

Less than 50 nanoseconds

1.3.1.3 (Cont'd.)

**Frequency Change with

Temperature:

Less than \pm 2 X 10^{-10} over a temperature range of 0°C

to 35°C.

Operating Temperature

Range:

 0°C to $+50^{\circ}\text{C}$ (35°C on

High Charge).

Storage Temperature

Range:

-40°C to +85°C.

^{**}Performance of the timing outputs under various ambient conditions can be computed from the following relationship: $\Delta\tau = 8.64 \ \frac{\Delta F}{F} \ T + 4.32 \text{KT}^2; \text{ where}$

 $[\]Delta \tau = \text{time gained or lost (microseconds)}$

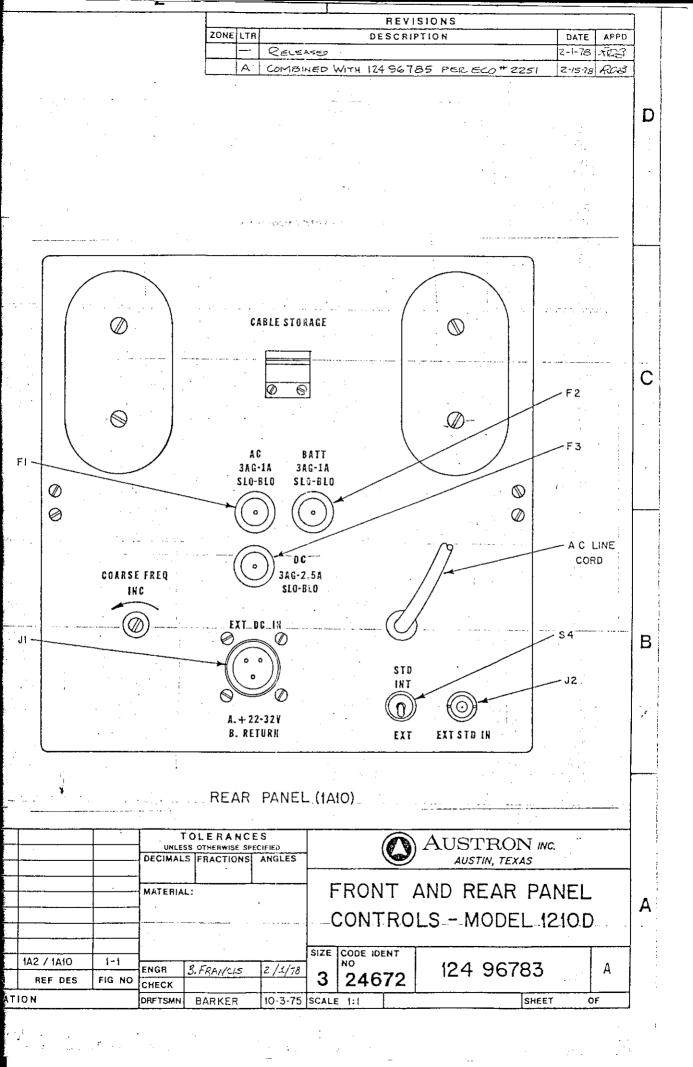
 $[\]frac{\Delta F}{F}$ = frequency offset in parts in 10^{-10}

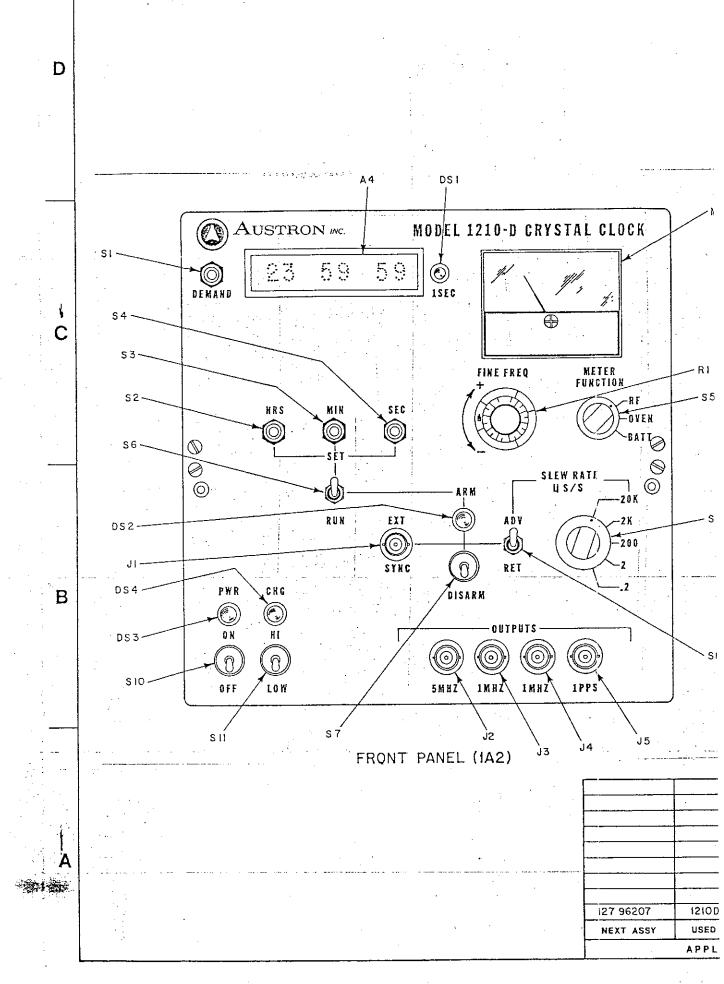
T = time in days

 $k = drift rate of the oscillator expressed in parts <math>10^{-10}$

1.4.1 Figure 1-1 shows all panel controls, indicators, and connectors for the model 1210D. Specific information is provided in the following paragraphs.

REFERENCE S10	4.1.1 Front Panel DESCRIPTION Power ON/OFF	FUNCTION Disconnects both ac and battery power from clock. (Note: Placing the power switch in the "off" position will stop the clock, even if it was operating from its internal battery power source.)
DS3	Power Lamp	Indicates ac operation.
S11	Charge HI/LOW Switch	Selects the rate at which the internal batteries are to be charged.
DS4	Charge Lamp	Indicates the internal batteries are charging at the high rate.
Jl	EXTernal SYNC Connector	BNC connector accepts an external synchronizing pulse.
J2	5 MHz Output Connector	BNC connector provides 5 MHz sinusoidal output.
J3	l MHz Output Connector	BNC connector provides 1 MHz sinusoidal output.
J4	1 MHz Output Connector	BNC connector provides 1 MHz sinusoidal output.





REFERENCE J5	DESCRIPTION 1 PPS Output Connector	FUNCTION BNC connector provides one pulse- per-second output.
S5 and M1	METER FUNCTION Switch and Meter	Three-position rotary switch selects, one of the following functions for display on meter: RF - The oscillator bias of the internal frequency standard OVEN - Oven of the internal frequency standard BATT - Voltage of the internal battery pack
R1	FINE FREQuency Control	Ten-turn precision potentiometer to accurately adjust the frequency of the internal oscillator. This control may be calibrated by dividing the electrical tuning range of the oscillator by 10 ³ .
5 8	ADVance/RETard Slew Rate	Two-position toggle switch which advances or retards the timing pulse at a rate selected by the SLEW RATE switch, S9.
s9	SLEW RATE Switch	Five-position rotary switch which selects the rate (in microseconds/second) which the digital phase shifter slews.

1.4.1.1 REFERENCE S7	(Cont'd.) DESCRIPTION ARM/DISARM Switch	FUNCTION Toggle switch which enables the clock sync and time setting functions.
DS2	ARM Lamp	This lamp being lit indicates that the clock is ARMED and the timing of its output pulse may be changed, or the HRS, MIN, or SEC display may be set by the appropriate pushbutton controls.
S6	SET/RUN Switch	Toggle switch which halts the clock register and allows up-dating in the SET position, or allows the clock register to accumulate time in the RUN position.
Sl	DEMAND Switch	Pushbutton switch which forces the clock register to display time— for about eight seconds when the ac input is absent. (Note that— the time display is continuous when the ac input is present.)
DS1	1 SECond Indicator	Indicator which flashes once per second when the clock register is accumulating time.
S2	HRS SET Switch	Pushbutton switch which advances the hours clock register when the clock is in the SET mode.

	1.4.1.1	(Cont'd.)	
REFERENCE	,	DESCRIPTION	FUNCTION
S 3	,	MIN SET Switch	Pushbutton switch which advances
			the minutes clock register when
			the clock is in the SET mode.
S4		SEC SET Switch	Pushbutton switch which advances
			the seconds clock register when
			the clock is in the SET mode.
A4		Time Display	Indicates the time-of-day in
		Assembly	hours, minutes, and seconds.
	1 4 7 0		
DEEDDEMAN		Rear Panel	
REFERENCE		DESCRIPTION COLDER	FUNCTION
		COARSE FREQuency	Access hole to allow for adjust-
		Adjustment	ment of the frequency at the
			internal frequency standard.
F1		AC Fuse	Type 3AG - 1A SLO-BLO fuse
			provides ac power supply
			protection.
F2		Battery Pack Fuse	Type 3AG - 1A SLO-BLO fuse
			provides protection for the
			internal battery pack.
F3		DC Fuse	Type 3AG - 2.5A SLO-BLO fuse
			provides protection for the DC
			standby input.
Ј2		EXTernal STanDard	BNC connector accepts external
52		INput	5 MHz sine wave input with an
			amplitude of at least 1 V RMS
			into a 50 ohm load.

1.4.1.2 (Cont'd.)

REFERENCE

S4

INTernal/EXTernal
STanDard Switch

DESCRIPTION

FUNCTION

Toggle switch selects operation from either internal frequency standard, or from an external 5 MHz frequency standard.

2.0 INSTALLATION

- 2.1 SCOPE OF SECTION
- 2.1.1 This section describes the steps required to prepare the AUSTRON Model 1210D Portable Crystal Clock for operation or reshipment to another location. Included in this section are instructions for unpacking, inspection, and shipping, along with lists of fundamental electrical requirements and accessories.
 - 2.2 UNPACKING AND INSPECTION
- 2.2.1 Initial Inspection -- Immediately report any equipment damage to the carrier making delivery and to AUSTRON, Inc. Before applying power to the unit, visually inspect internal components and circuit boards by removing the top cover. Examine exterior and interior parts carefully for scratches, dents, damaged printed circuit boards, etc., which might indicate improper handling.
- 2.2.2 Circuit Boards -- Exercise care when removing or installing printed circuit boards. The recommended installation procedure is to align the board contacts with the connector, and then carefully insert the board as far as it will go.

NOTE: COMPONENT SIDE IS TOWARDS THE FRONT.

CAUTION: ALWAYS TURN POWER OFF BEFORE REMOVING OR INSTALLING ANY PRINTED CIRCUITS OR INTEGRATED CIRCUITS.

- 2.3.1 Accessories -- The following accessories should be received with the model 1210D:
 - a) One MS type mating dc power connector.
 - b) One cable clamp for MS type connector.
 - c) Two sets of spare fuses.
 - d) Two technical manuals.
 - e) One pcb extender card.
- 2.3.2 Power Connections -- This unit operates on 115 V ac or 230 V ac ± 10%, 48-420 Hz. Before connecting the ac power cable to the source, verify that the 115/230 volt selector switch (located on the top of the power transformer) is in the required position (115 V or 230 V), and that fuse Fl is 1 amp SLO-BLO 3 AG for 115 V operation of 0.5 amp SLO-BLO 3 AG for 230 V operation.
- 2.3.3 Cable Connections -- With the MS type connector supplied, fabricate a cable to supply the dc standby power. The connections are as follows:

PIN	DESCRIPTION
A	24 - 28 V dc + 2 V dc
В	Return
С	No Connection

2.4 PREPARATION FOR RESHIPMENT

2.4.1 Turn power off and disconnect all external cables. Check to see that all mounted components are in place and secure, and that all printed circuit boards are snugly inserted in their respective connectors.

CAUTION: IF SHIPMENT OR STORAGE DURATION OF GREATER
THAN THIRTY (30) DAYS OR STORAGE TEMPERATURES
OUTSIDE THE RANGE OF MINUS FORTY DEGREES (-40°)
CENTIGRADE TO PLUS FIFTY DEGREES (+50°) CENTIGRADE
IS ANTICIPATED, THE NICAD BATTERIES SHOULD BE
REMOVED BY A QUALIFIED TECHNICIAN.

- 2.4.2 Use the original packing material for shipping, if possible. If not, enclose the unit in a suitable water and vapor proof plastic bag. Projections and sharp edges should be padded with a cushioning material. Heat seal or tape the plastic bag to ensure a moisture proof enclosure. When sealing the bag, keep the trapped air volume to a practical minimum.
- 2.4.3 Place the unit in a interior box of such dimensions that will not allow movement. Place the interior box in a shipping container of such dimensions that will allow for at least two inches of rubberized hair (or similar material) on all sides of the interior box.

3.0 OPERATION INSTRUCTIONS

- 3.1 SCOPE OF SECTION
- 3.1.1 This section provides instructions for operating the AUSTRON Model 1210D Portable Crystal Clock. Included are general descriptions of set-up, check out, and adjustment.
 - 3.2 SET-UP AND TURN-ON PROCEDURES
- 3.2.1 After installing the model 1210D per section 2.3, place the power switch (1A2S10) in the ON position (up). Verify that the PWR lamp (1A2DS3) is lighted, indicating ac operation. The digital time display should light.
- 3.2.2 Position the METER FUNCTION switch (1A2S5) as indicated, and verify the following:
 - a) RF a reading above 30.
 - b) OVEN an initial reading below 15. This reading will increase between 35 and 65 as the oscillator oven stabilizes.
- 3.2.3 Position the METER FUNCTION switch in the BATT position, and disconnect the ac and dc power cords. Note that the PWR lamp (1A2DS3) is off, and there should be no significant change in the meter reading. Reinsert the ac power cord, and verify that the PWR lamp is on. Place the CHARGE rate switch (1A2S11) in the HI position. Verify that the CHG lamp (1A2DS4) is on, and that the meter shows a slight movement up-scale. Place the CHG switch in the LOW position.

- 3.2.4 At this point in the operating procedure a 3 hour delay is necessary in order to allow the internal oscillator time to warm-up. In the interim, the following procedure may be performed to verify the complete operation of the clock controls and outputs:
- 3.2.4.1 Check the 5 MHz and 1 MHz sine outputs and the 1 PPS pulse output with an oscilloscope. These outputs should conform to the specifications listed in paragraph 1.3.
- 3.2.4.2 Place the ARM/DISARM switch (1A2S7) in the ARM (up) position. Verify that the ARM lamp (1A2DS2) lights. Place the ARM/DISARM switch in the DISARM (down) position. Verify that the ARM lamp extinguishes.
- 3.2.4.3 Arm the clock and place the clock SET/RUN switch (1A2S6) in the SET position. The digital display should hold the time being display. Place the clock SET/RUN switch in the RUN position. Verify that the seconds increment. Disarm the clock by placing the ARM/DISARM switch in the DISARM position.
- 3.2.4.4 Verify the function of the EXT SYNC in the following manner. ARM the clock and apply a 1 PPS (+3V for 1 microsecond or greater) pulse to the EXT SYNC input (1A2J1). The ARM lamp should extinguish indicating that synchronization has occurred. Verify the synchronization by comparing the synchronizing pulse and clock output pulse. The pulses should be within 0.4 microseconds of each other.
- 3.2.5 Approximately 3 hours after turning the Model 1210D on, the frequency of the internal oscillator should be within the adjustment range of the front panel frequency control. Using a meter standard and the FINE FREQ control (1A2R1), adjust the internal oscillator of the model 1210D on frequency.

3.3 TIME SYNCHRONIZATION

3.3.1 The model 1210D clock may be synchronized with a master clock automatically or manually.

- 3.2.4 At this point in the operating procedure a 3 hour delay is necessary in order to allow the internal oscillator time to warm-up. In the interim, the following procedure may be performed to verify the complete operation of the clock controls and outputs:
- 3.2.4.1 Check the 5 MHz and 1 MHz sine outputs and the 1 PPS pulse output with an oscilloscope. These outputs should conform to the specifications listed in paragraph 1.3.
- 3.2.4.2 Place the ARM/DISARM switch (1A2S7) in the ARM (up) position. Verify that the ARM lamp (1A2DS2) lights. Place the ARM/DISARM switch in the DISARM (down) position. Verify that the ARM lamp extinguishes.
- 3.2.4.3 Arm the clock and place the clock SET/RUN switch (1A2S6) in the SET position. The digital display should hold the time being display. Place the clock SET/RUN switch in the RUN position. Verify that the seconds increment. Disarm the clock by placing the ARM/DISARM switch in the DISARM position.
- 3.2.4.4 Verify the function of the EXT SYNC in the following manner. ARM the clock and apply a 1 PPS (+3V for 1 microsecond or greater) pulse to the EXT SYNC input (1A2J1). The ARM lamp should extinguish indicating that synchronization has occurred. Verify the synchronization by comparing the synchronizing pulse and clock output pulse. The pulses should be within 0.4 microseconds of each other.
- 3.2.5 Approximately 3 hours after turning the Model 1210D on, the frequency of the internal oscillator should be within the adjustment range of the front panel frequency control. Using a meter standard and the FINE FREQ control (1A2R1), adjust the internal oscillator of the model 1210D on frequency.

3.3 TIME SYNCHRONIZATION

3.3.1 The model 1210D clock may be synchronized with a master clock automatically or manually.

- 3.3.1.1 <u>Automatic Synchronization</u> -- Automatic synchronization is accomplished by placing the ARM switch in the ARM (up) position. The red ARM lamp, located immediately above the ARM/DISARM switch, will light indicating that the clock is ready to receive a timing command. Next, connect the master timing pulse to the EXT SYNC BNC connector located on the front panel of the model 1210D clock. When the master timing pulse arrives, the clock will automatically synchronize to within 0.4 microsecond timing pulse. The ARM lamp will go out, indicating synchronization has taken place.
- 3.3.1.2 <u>Manual Synchronization</u> -- Manual synchronization is accomplished by using an oscilloscope, with a triggered sweep, as a comparator and aligning the clock with the master clock pulse as follows:
 - 1) Trigger the sweep of the oscilloscope with the external master timing pulse.
 - 2) Connect the 1 PPS output from the model 1210D to the vertical input of the oscilloscope. Set the vertical gain to 1 volt per division and the sweep rate to 0.1 second per division. The model 1210D clock output pulse should appear on the base line.
 - 3) ARM the clock, and select a high slew rate by means of the SLEW RATE switch (1A2S9). The slew rates, shown on the righthand side of the selector switch, are expressed in microseconds per second.
 - 4) Advance or retard the model 1210D clock output pulse at the rate selected, by means of the SLEW RATE ADV/RET switch (1A2S8). As the time difference decreases, select slower slew rates and faster sweep rates until the model 1210D is synchronized.
 - 5) Disarm the clock.

- 3.4.1 Alignment of the clock display to the master clock is done as follows:
 - 1) Position the SET/RUN switch (1A2S6) to the SET position.
 - 2) ARM the clock.
 - 3) Using the HRS, MIN, or SEC pushbutton switches (1A2S2 1A2S4), advance the display to several seconds ahead of the desired display time.
 - 4) When real time is in coincidence with the digital display, place the SET/RUN switch in the RUN position.
 - 5) Disarm the clock.

3.5 BATTERY CHARGING

- 3.5.1 Due to the difficulty in determining the state of charge of the Nicad batteries, the following procedure is recommened for recharging the cells:
 - 1) Place the CHG switch on the HI position.
 - 2) Compute the charging time using the following information:
 - a) The clock draws approximately 420 ma when operating from the internal batteries.
 - b) The batteries have a 7 ampere hour capacity.
 - c) The HI charge rate is approximately 750 ma.
 - d) Replace 1-1/2 times the ampere hours used.
- 3.5.2 The most important thing is to not allow the heat to build up during the charge cycle. Therefore, at frequent intervals during a prolonged charge cycle, the batteries should be tested with the back of the hand. If the batteries should become uncomfortable to the touch, the charge rate switch should be set to LOW until the batteries have cooled. The charge may then be resumed at the HI rate until the computed charge time is completed.

(Cont'd.)

3.5.2

NOTE:

repeated shallow discharge/charge cycles the batteries should be discharged to 1 volt per cell and then recharged 14 hours. This will restore the batteries to their maximum capacity.

Nickel cadmium batteries have a memory. After

AUSTRON MODEL 1210D PORTABLE CRYSTAL CLOCK

4.0 FUNCTIONAL DESCRIPTION

4.1 SCOPE OF SECTION

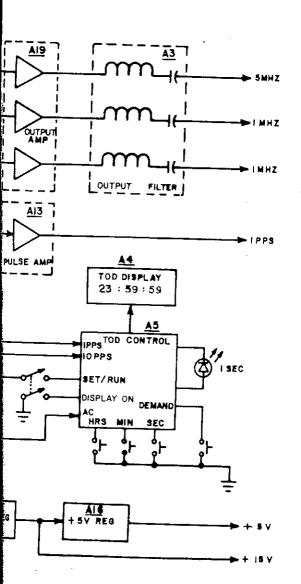
4.1.1 This section provides a functional analysis of the Model 1210D Portable Crystal Clock. The clock consists of the circuits shown in the diagrams in Figures 4-1 and 4-2. Each circuit will be discussed separately in this section.



REVISIONS

ZONE LTR DESCRIPTION DATE APPD

- RELEASED 10-9-75-126

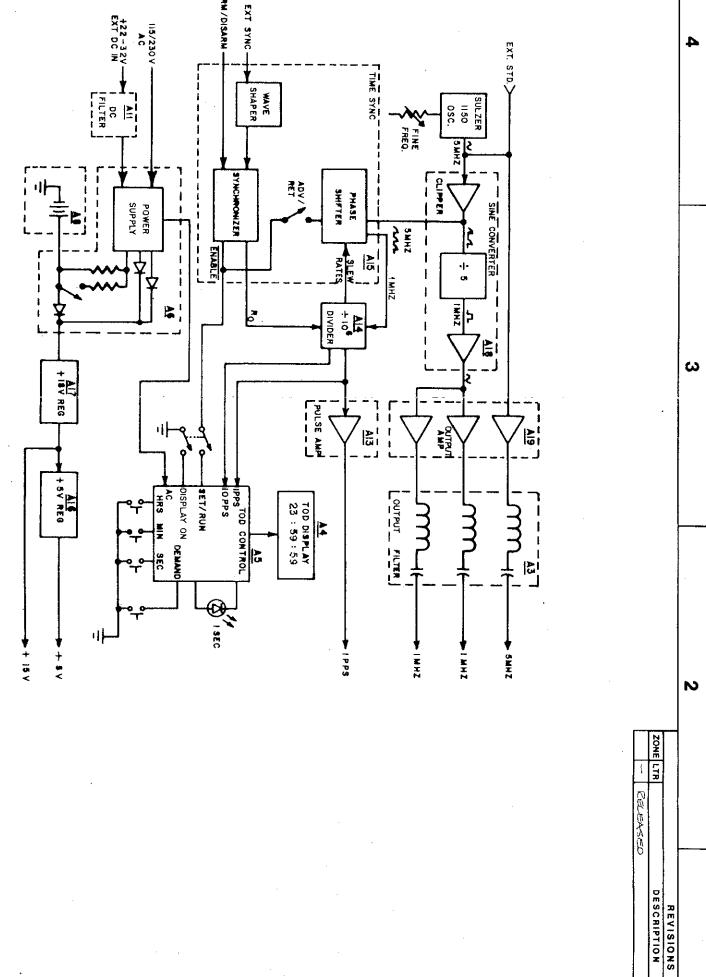


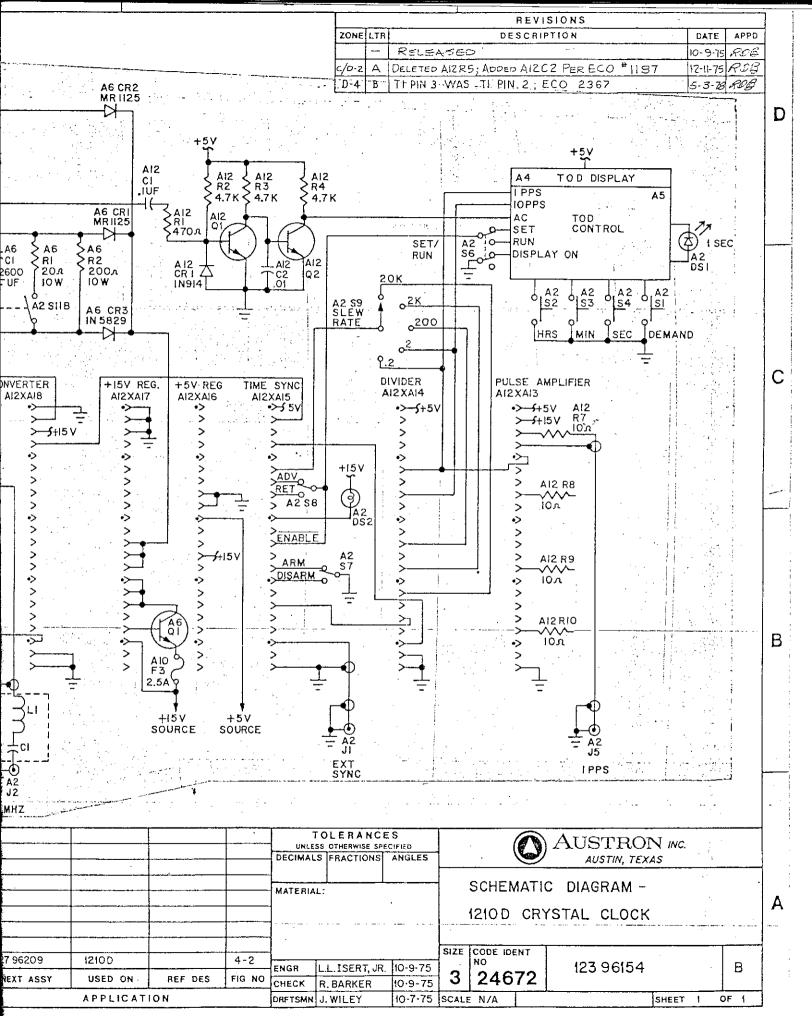
												
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				MATERIA	_ <u></u> L:			BLO	CK	DIAGRAM	-	
							1	2101) C	RYSTAL C	LOCK	
96207	1210 D	·	4-1				SIZE	CODE ID	ENT			 [
XT ASSY	USED ON	REF DES	FIG NO		L.L. ISERT, JF R. BARKER	10-9-75	3	246	72	124 967	93	
	APPLICATI	ON		1	K. WIGGINTO						louses :	
İ											SHEET	OF 1

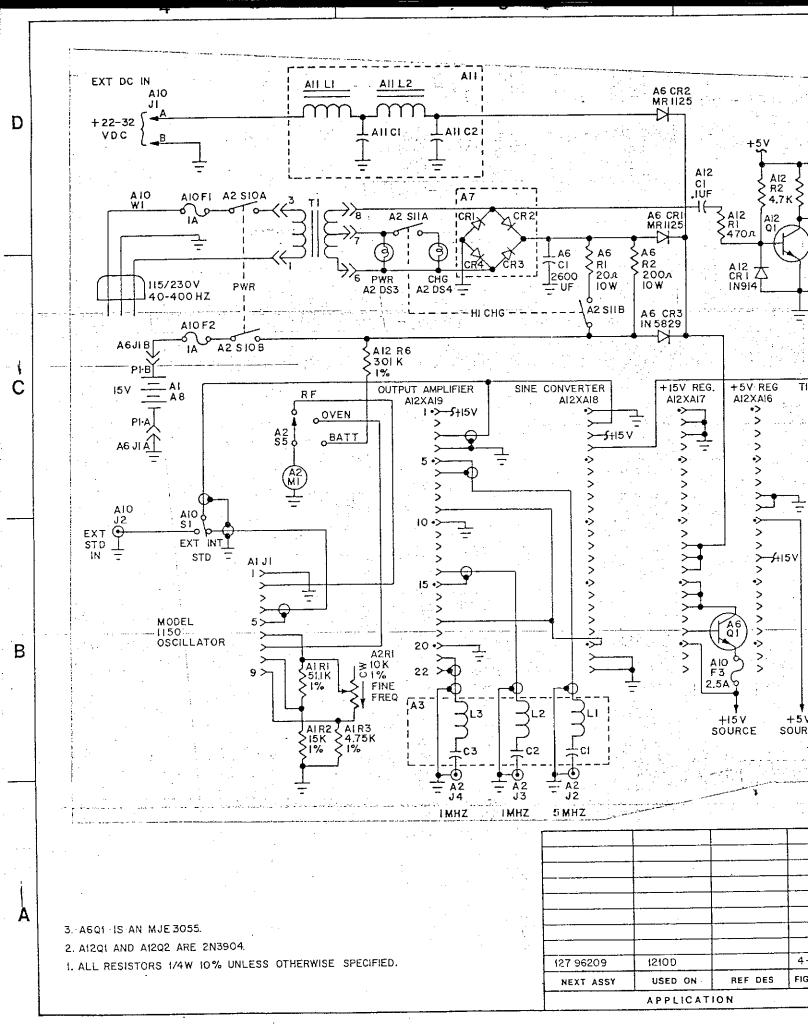
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В



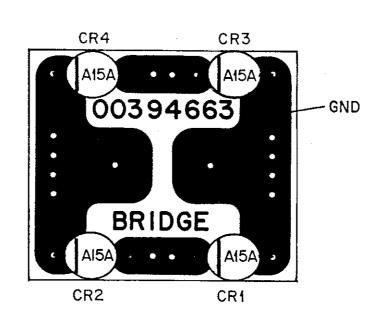




4.2 GENERAL CIRCUIT DESCRIPTION

- 4.2.1 The clock's overall functions are illustrated in Figure 4-1. A 5 MHz signal from either the internal AUSTRON Model 1150 Oscillator or an external standard is used as the timing for the clock. This signal is shaped and divided by the Sine Converter in order to supply the 5 MHz and 1 MHz sine outputs.
- 4.2.2 After conditioning by the Sine Converter, the 5 MHz signal is applied to the Time Sync circuits. These circuits convert the 5 MHz signal to a 1 MHz signal, which is in turn applied to the divider circuits to obtain the 1 PPS output. The 1 MHz generated in the Time Sync circuits may be phase shifted manually in accordance with the SLEW RATE and ADV/RET switches in order to slew the occurrence of the 1 PPS.
- 4.2.3 The output of the divider is used to generate time of day signals in the TOD Control and TOD Display circuits. An $\overline{\text{ENABLE}}$ signal generated in the Time Sync circuits allows the TOD Display to be set.
- 4.2.4 The power supply circuits are shown in detail on the chassis schematic Figure 4-2. AC power is obtained through the power transformer, Tl, and rectified by the bridge consisting of la7CR1 la7CR4.
- 4.2.5 Note that the bridge is located on a separate assembly, A7, which is shown in Figure 4-3. The output of the bridge is routed through either lA6Rl and lA6R2, or lA6R2 to supply charging current for the Battery Pack, A8. lA6CRl, lA6CR2, and lA6CR3 are connected as an "OR" circuit, so that either the ac power, EXT DC or batteries may supply the power to the +15 volt regulator, Al7.
- 4.2.6 1A12Q1 and 1A12Q2 are used to detect ac power. The resultant TTL level signal is used in the TOD Control circuits.

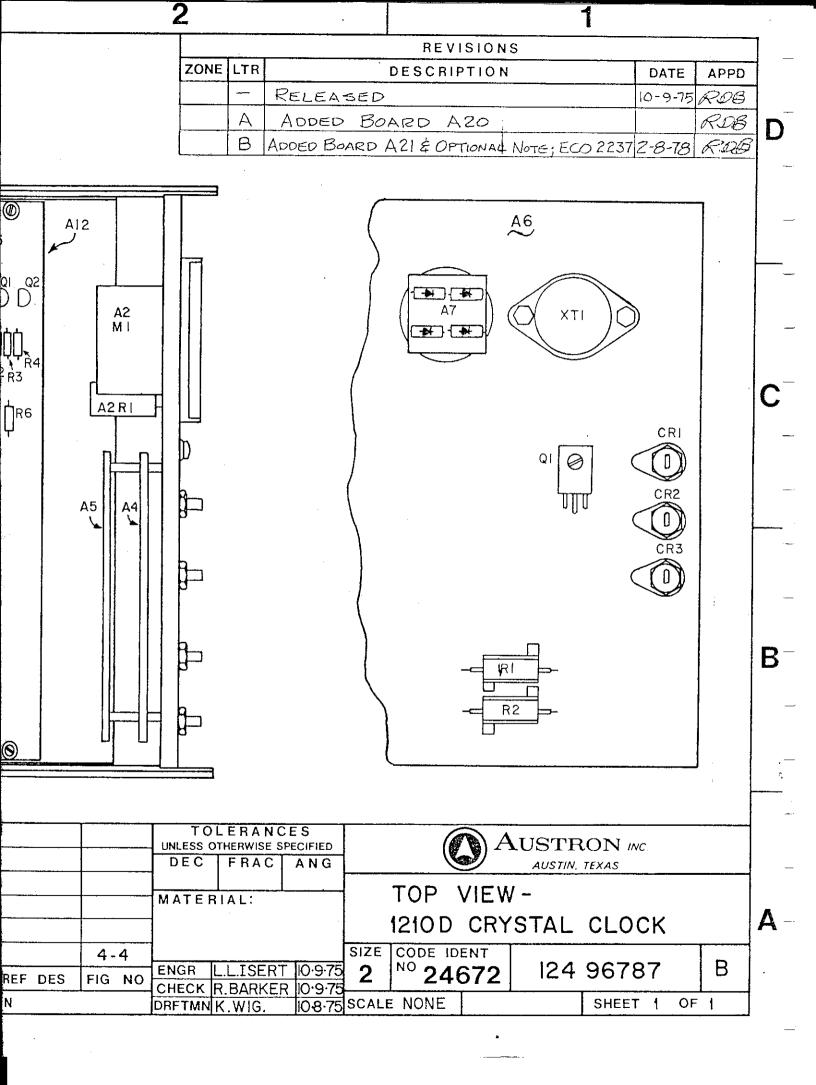
APPLIC	ATION		REVISIONS		
NEXT ASSY USED ON			DESCRIPTION	DATE	APPROVED
221 94648	1210 C	A	REVISED PER ECO *978		ROB
110 96211	1210D	B	REDRAWN TO UPGRADE FORMAT; ECO 2220	2-10-78	
		C	ADDED NOTE & PER ECO 3127	12-13-79	ROB

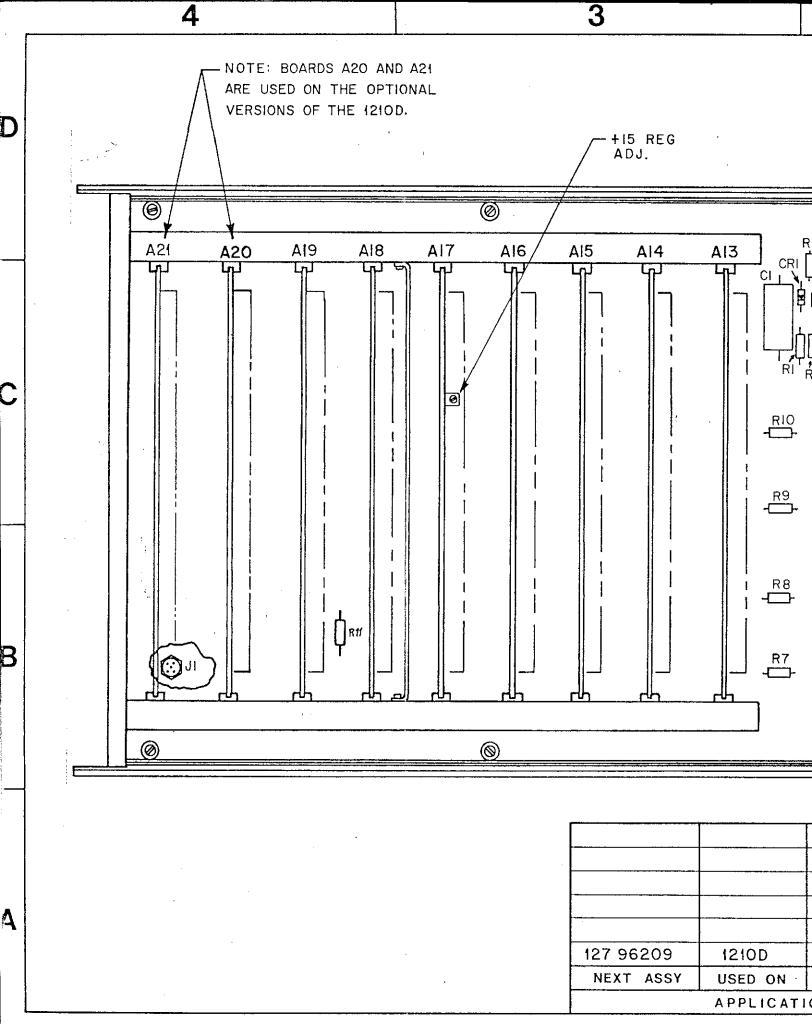


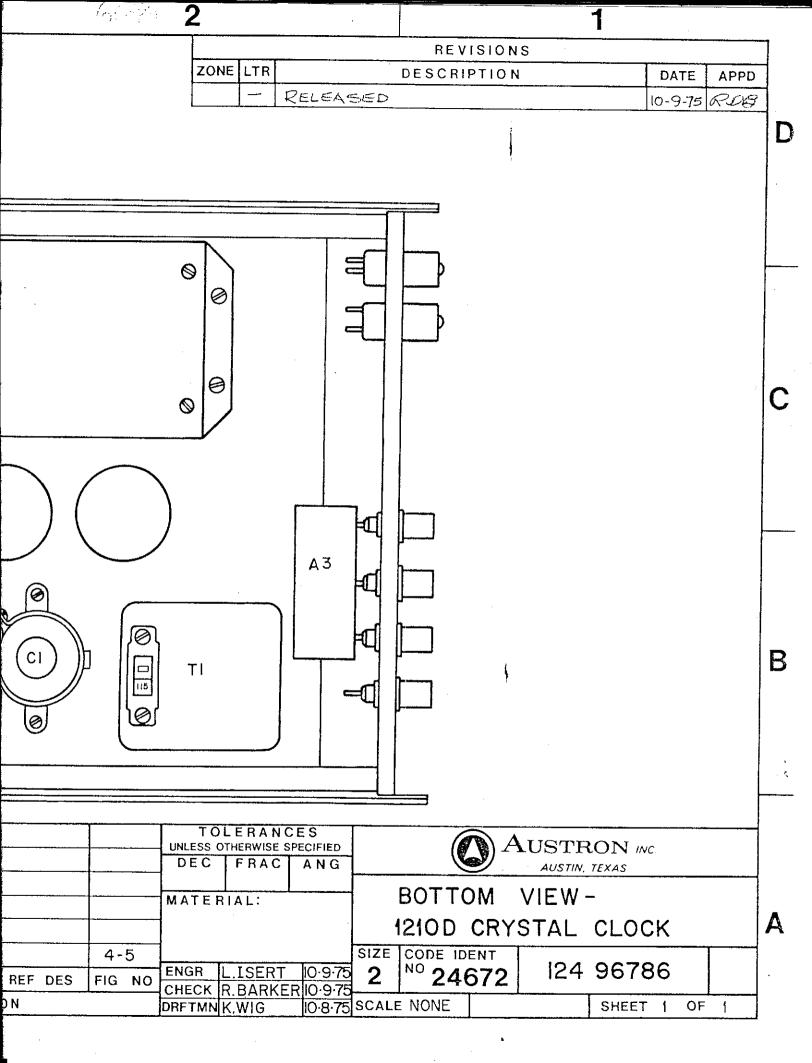
1. INSTALL COMPONENTS ON FOIL SIDE NOTES:

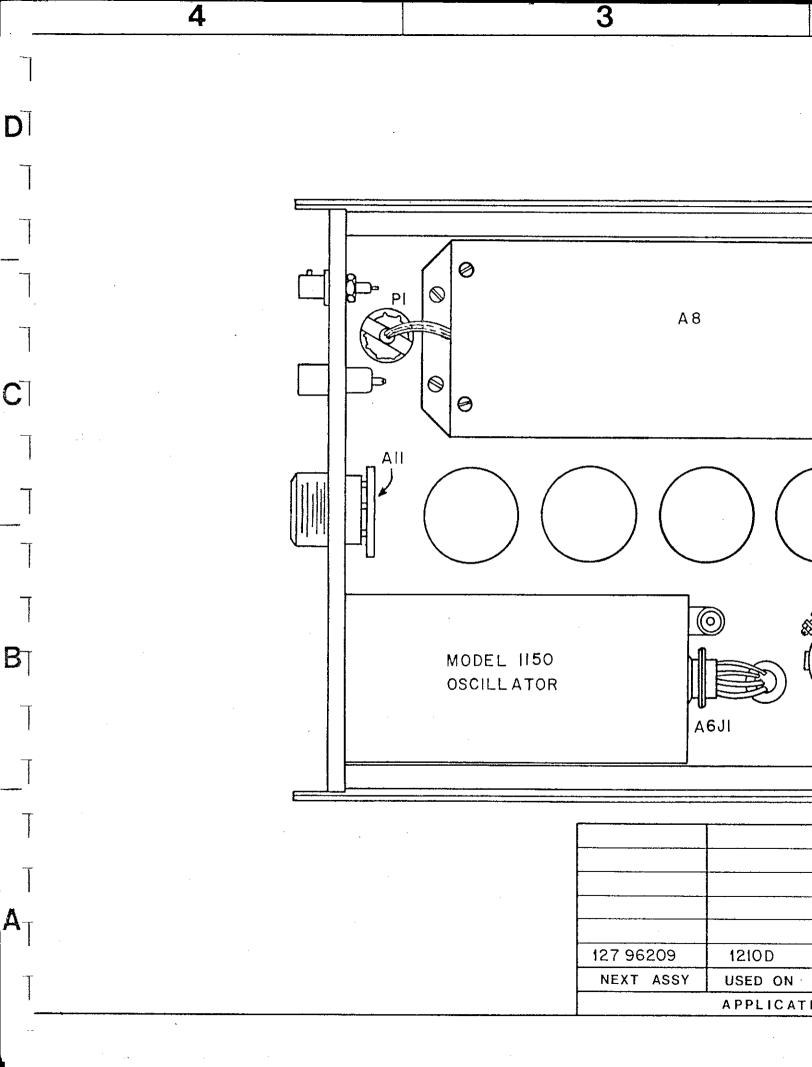
					F	\us	TR	RON INC.	AU. TEX	STIN, (AS
		ENGINEER CHECKED DRAFTSMAN	L.L.I. R.D.B. K.WIG	9-5-75 9-5-75 9-5-75		C BO BRIDGI		ASSY-		
(1210D) A7	4-3				SIZE	CODE II	DENT	107.040	^ ^	
REF DES	FIG NO			. .	1	NO.24	672	103 946	64	C
					SCAL	E 2:I		SHEET	1 (of 1

- 4.2.7 There are other circuits that have been packaged off the main printed circuit cards. Among these is the Output Filters assembly, A3. These filters have been mounted on a small card in the immediate proximity of the output connectors in order to further assure signal purity.
- 4.2.8 1A6Ql is the series pass transistor for the +15V regulator. 1A12R7-R10 are current limiting resistors used to protect the Pulse Amplifiers' output transistors against short circuiting.

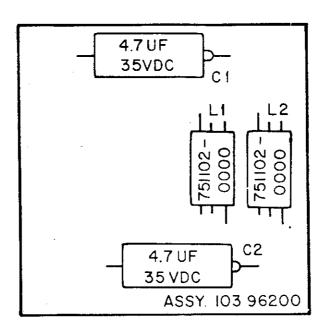






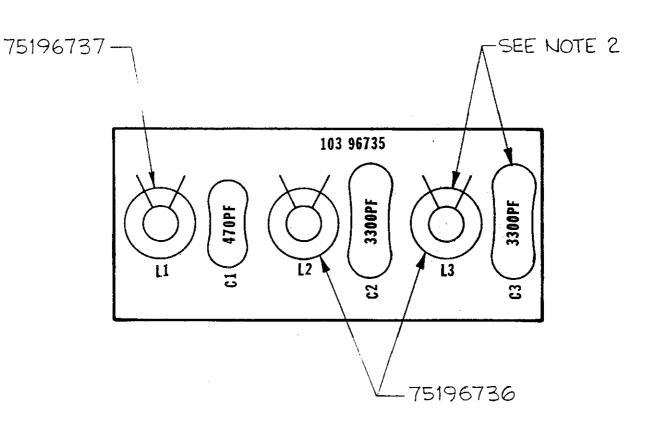


APPLIC	CATION		REVISIONS		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
NEXT ASSY	USED ON	LTR	DESCRIPTION	DATE	APPROVED
109 96 195	1210-D	_	RELEASED	9.29.7	ROB
		Α	REVISED TITLE BLOCK TO INCLUDE REF DES É FIG. NA : SCALE WAS !!!! PED FCO 2223	2-13-78	ROB



			AUSTRON	J INC. AUSTIN, TEXAS
		ENGINEER 7-22- CHECKED 9-27- DRAFTSMAN BARKER 9-20-	D C EUTED	_
All REF DES	4-6 FIG NO		SIZE CODE IDENT	3 96200 A
	• • • • • • • • • • • • • • • • • • • •		SCALE 2:1	SHEET OF

APPLIC	ATION		REVISIONS						
NEXT ASSY	USED ON	LTR	DESCRIPTION	DATE	APPROVED				
109 96193	1210 · D	_	RELEASED	9.29.75	RDB				
10996193-2	1210-D-02	Α	REVISED TITLE BLOCK TO INCLUDE REF DES É FIG NO: SCALE WAS 1:1; PER ECO 2222	2-13-78	ROB				
		B	ADDED PART NO.S & NOTE 2 PER ECO. 3257	2-28-80	Rat				



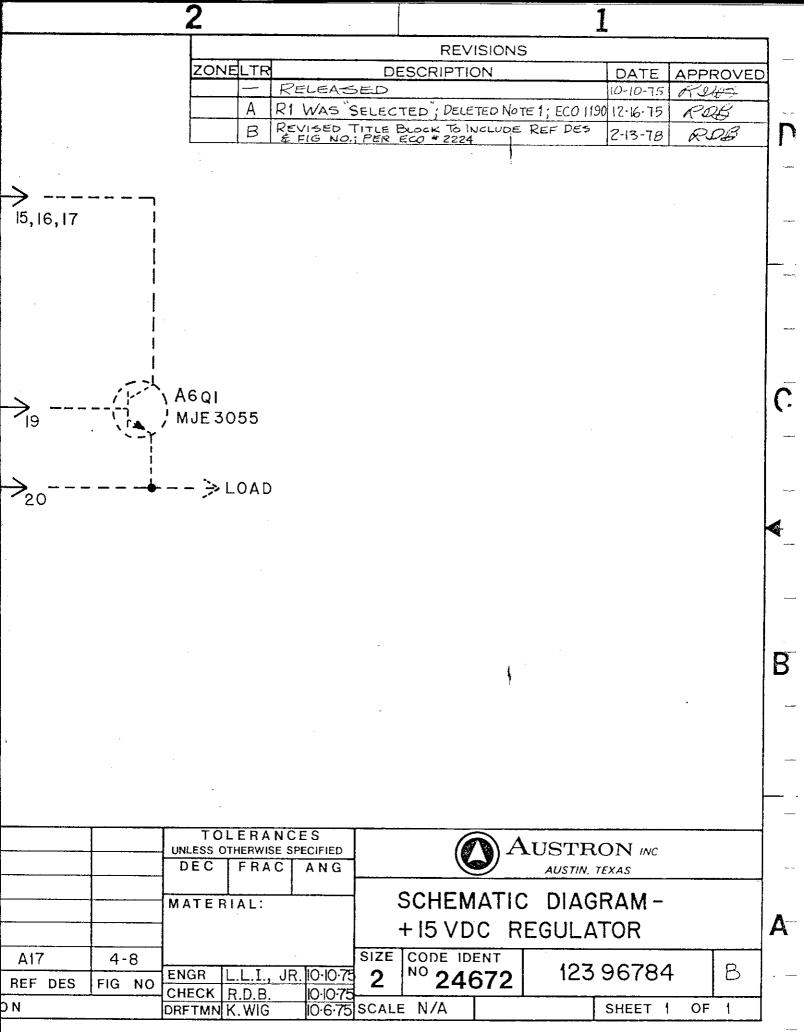
- *2. C3 AND L3 NOT USED ON 10396735-1.
 - 1. MOUNT CHOKES WITH NYLON KOD.

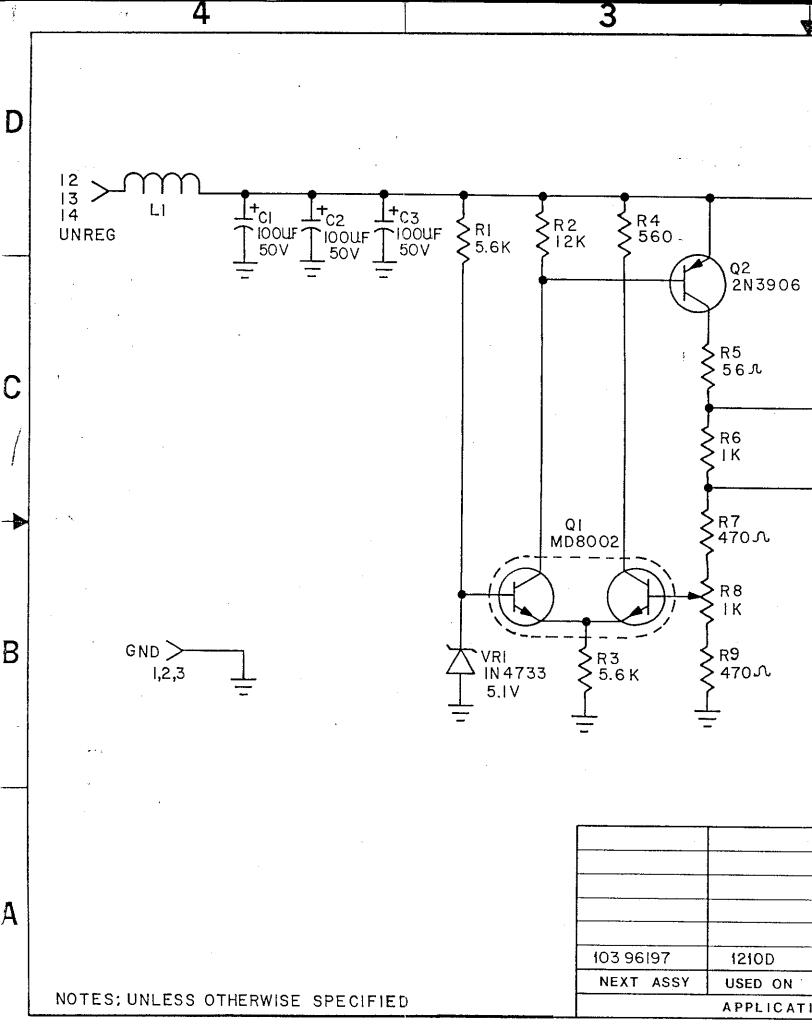
NOTES:

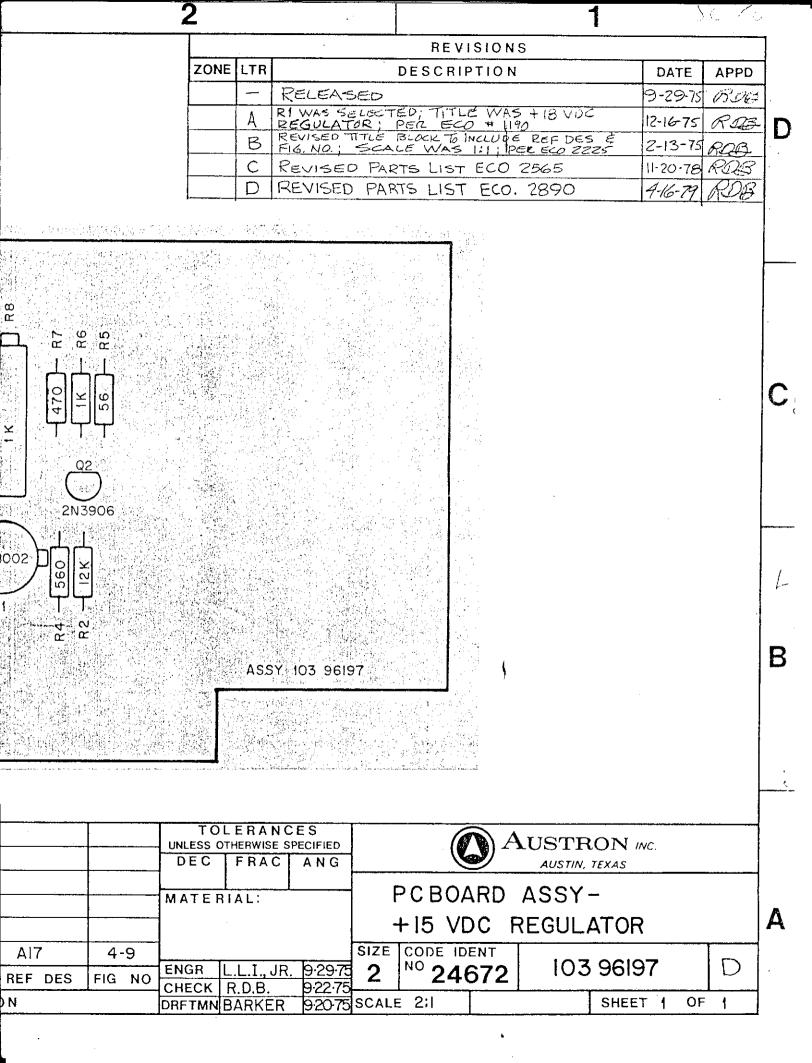
			AUSTRON INC. AUSTIN, TEXAS
		ENGINEER 2 3/ 7 ==-7. CHECKED 1 2 2-27. DRAFTSMAN BARKER 9-20-7	
A3 REF DES	4-7 FIG NO		1 CODE IDENT 103 96735 - * B
			SCALE 2:1 SHEET 1 OF 1

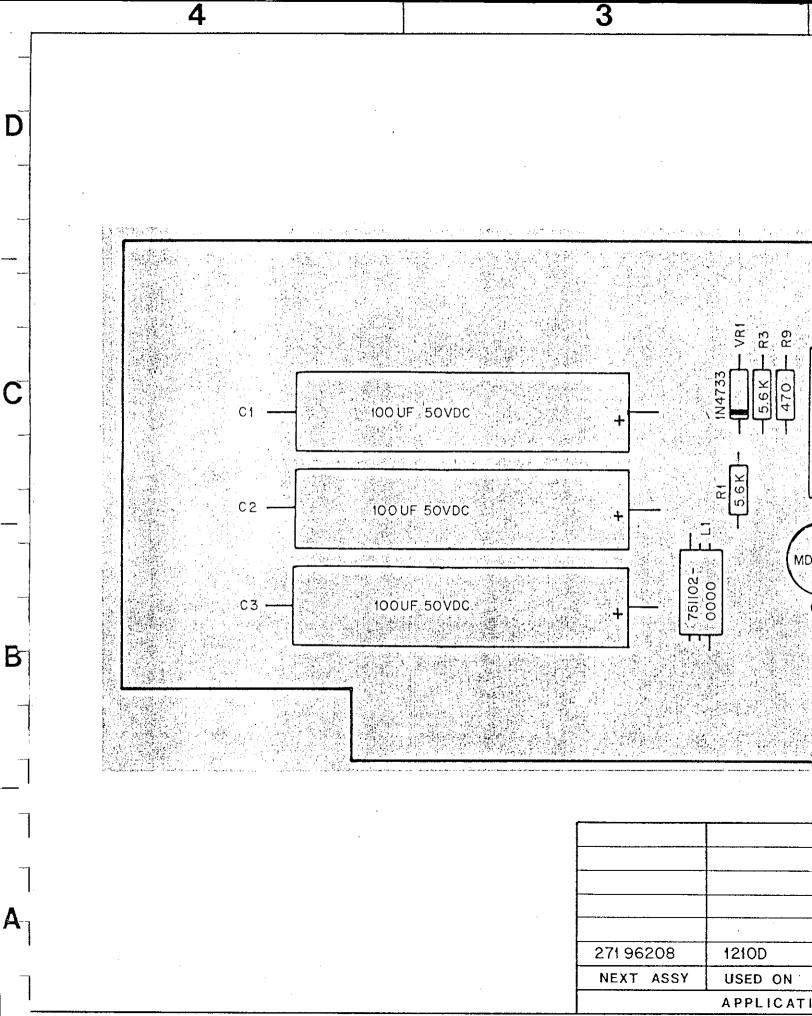
- 4.3 +15 V REGULATOR
- 4.3.1 The 15 Volt Regulator (Al7) is a series regulator that operates in the following manner:
- 4.3.2 The inductor L1 and capacitors C1, D2 and C3 form a decoupling network. The error voltage between diode VR1 and resistor R8 is amplified by a differential amplifier, Q1 and then by transistor Q2. Transistor Q2 drives a series pass transistor, located on the chassis. When the output of the regulator increases above the set point, the collector current thru R2 decreases, reducing the collector current of Q2 and decreasing the drive to the series pass transistor, thereby decreasing the output voltage of the regulator. When the regulator output decreases below the set point, the collector currents and drive currents increase, thereby increasing the output voltage of the regulator.

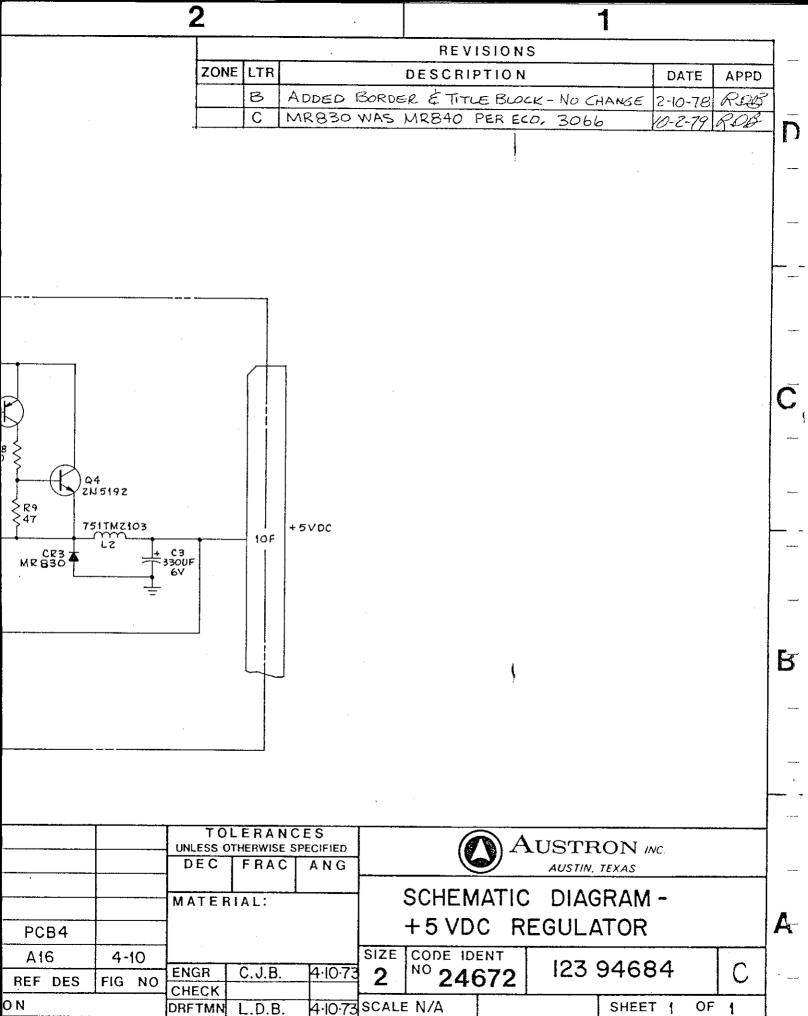
- 4.3 +15 V REGULATOR
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- 4.3.2 The inductor L1 and capacitors C1, D2 and C3 form a decoupling network. The error voltage between diode VR1 and resistor R8 is amplified by a differential amplifier, Q1 and then by transistor Q2. Transistor Q2 drives a series pass transistor, located on the chassis. When the output of the regulator increases above the set point, the collector current thru R2 decreases, reducing the collector current of Q2 and decreasing the drive to the series pass transistor, thereby decreasing the output voltage of the regulator. When the regulator output decreases below the set point, the collector currents and drive currents increase, thereby increasing the output voltage of the regulator.

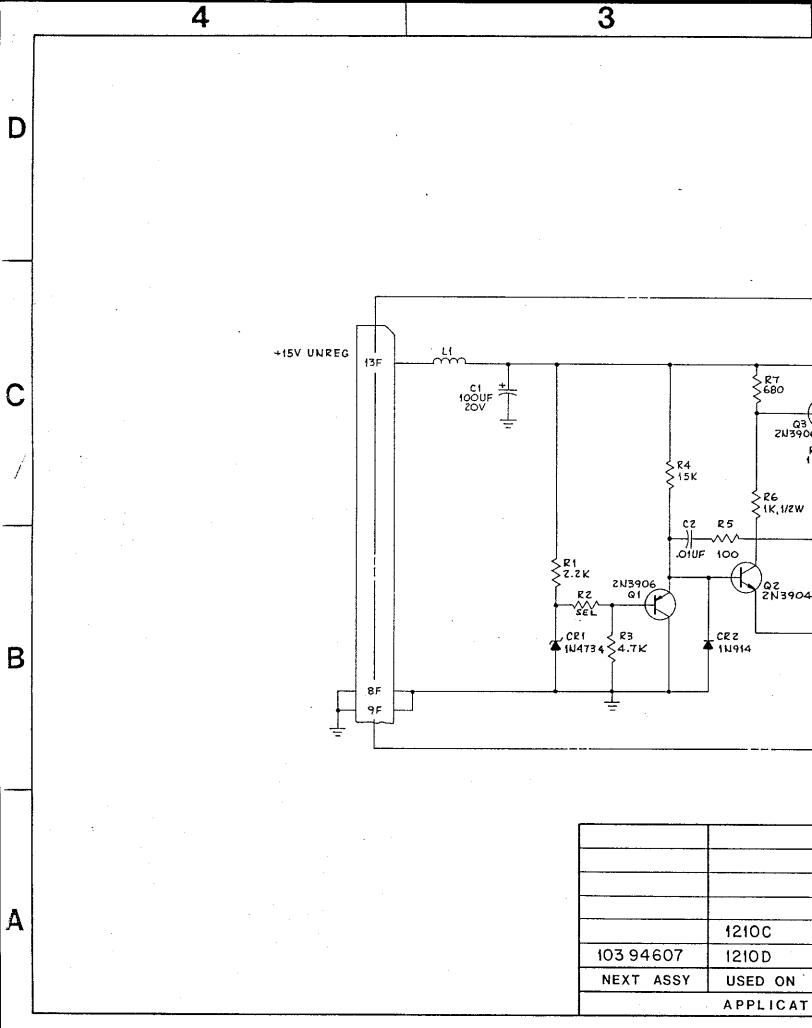


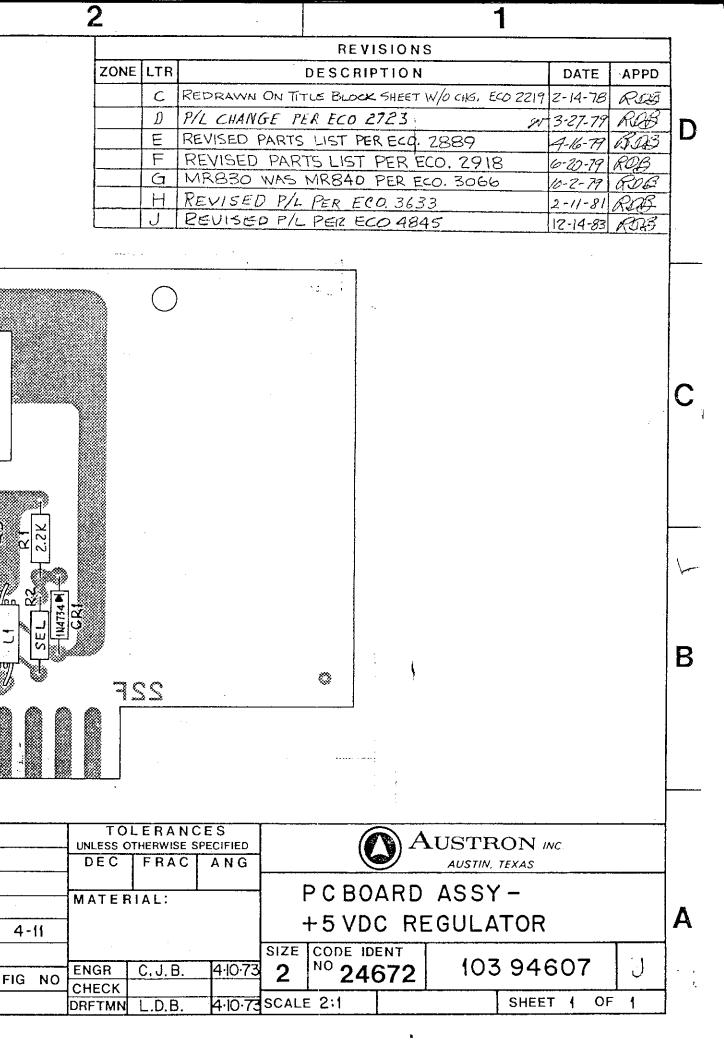










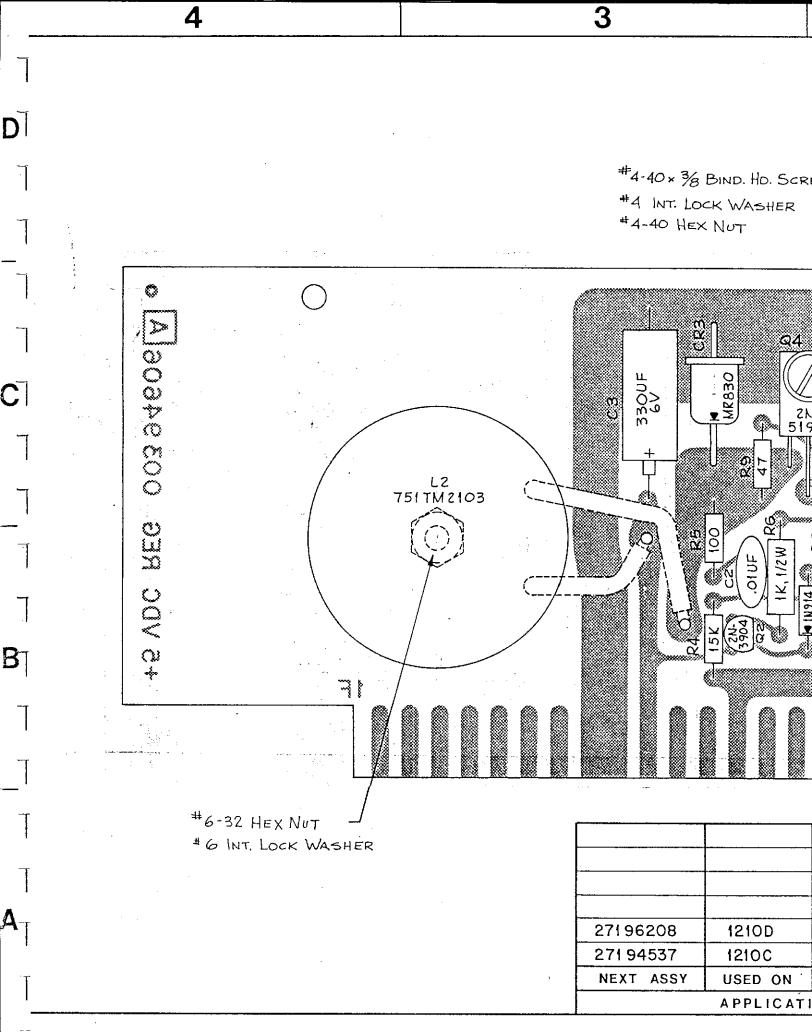


A16

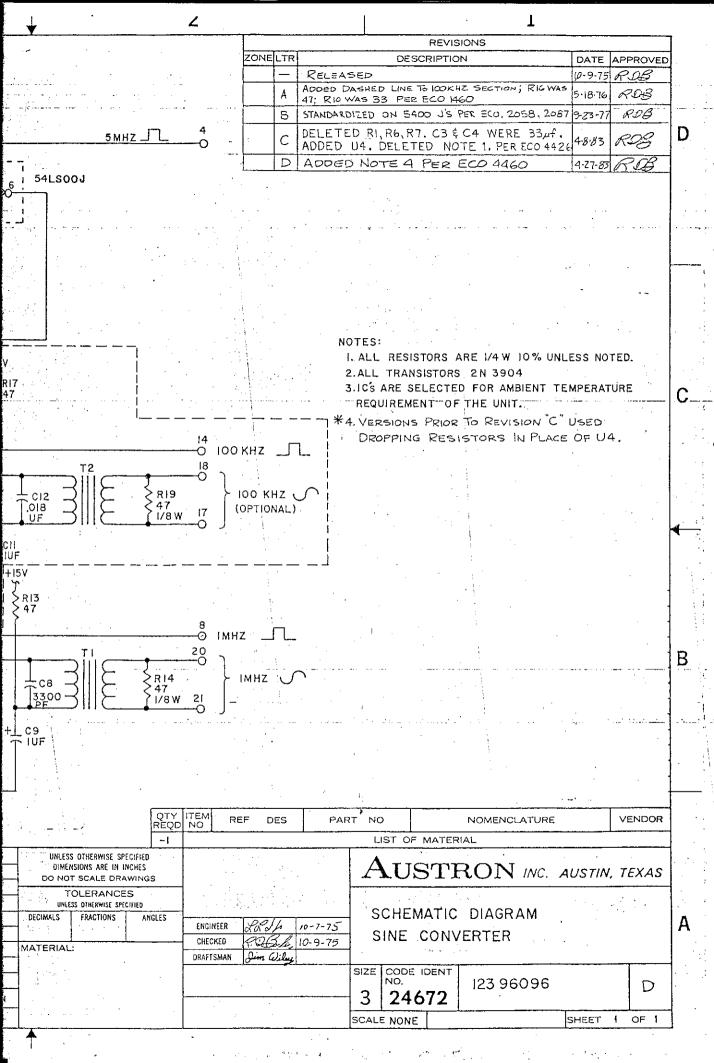
PCB4

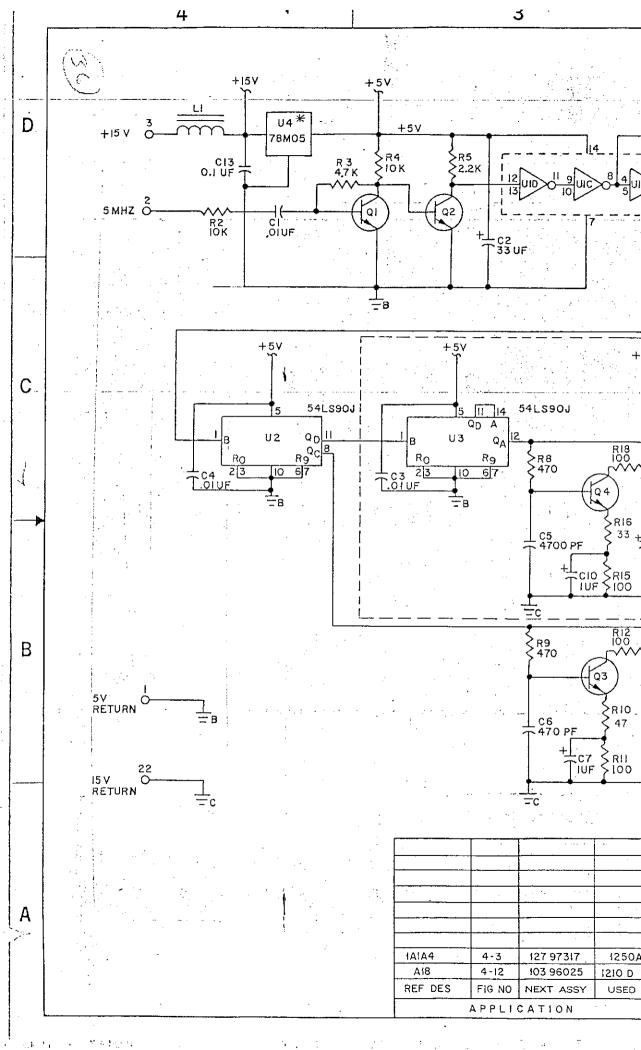
REF DES

N



- 4.5 SINE CONVERTER
- 4.5.1 The Sine Converter assembly (Al8) contains a 5 MHz clipper, 5 MHz buffer gates, a divide by 5 stage, a decade divider and two sine converter, buffer amplifiers.
- 4.5.2 The 5 MHz sinewave from the internal oscillator is supplied through isolating resistor R2 and coupling capacitor C1 to the input of a two-stage direct-coupled amplifier made up of Q1 and Q2. The output of this amplifier is a 5 MHz pulse which has the proper characteristics to drive U1B, C, D, and 5 MHz buffer gate. The output of U1B is a 5 MHz signal which is supplied to U2.
- 4.5.3 U2 is a divide-by-five stage which produces a 1 MHz output from the 5 MHz input. The 1 MHz output from U2 is fed through a low pass filter R9, C6, to the base of Q3, a collector tuned RF amplifier. The resulting 1 MHz sinewave at the secondary of R1 is used to drive a 1 MHz output buffer.
- 4.5.4 U3 is a decade divider which produces a 100 kHz output from a 1 MHz input supplied from U2. The 100 kHz signal is fed through a low pass filter R8 and C5 to the base of Q4, a collector tuned output amplifier. The 100 kHz signal present at the secondary of T2 is used to drive a 100 kHz output buffer.

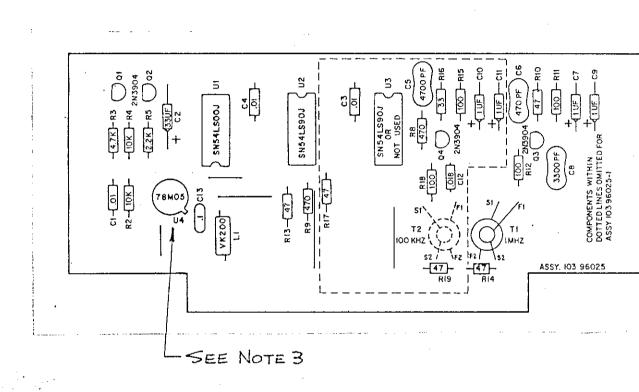




-7				_							
ļ	REVISIONS										
	ZONE	LTR		DATE	APPROVED						
			RELEASED	10-7-75	SOK.						
		Α	REVISED TO ADO -1 PER ECO # 1191	12.31.75	RAB						
		В	ADDED DASH LINES & OPTIONAL" PER ECO 1459	5-18-76	RDB						
		С	ADDED SEF DESIGNATORS TO TI ETZ PER ECO 1856		ROB						
		D	STANDARDIZED ON 5400 J'S PER ECO. 2058, 2087	9-23-77							
		Ε	CORRECTED TYPE No. OF UZ & U3 PER ECO 2234	2-13-78	ROB						
		F	REVISED PARTS LIST PER ECO. #2810	4-16-79	ROB						
		G	REVISED SILKSCREEN PER ECO. # 3088	9.24.79	ROG						
		H	REVISED PARTS LIST PER ECO # 3069	10-6-80							
		J	ADDED NOTE 2 PER ECO. 3874	9-1-81	ROB						
		K	REVISED PARTS LIST PER ECO. 4224	10-19-82	ROB						
		L	DELETED RI,R6,R7. C3 ¢ C4 WERE 33 UF. ADDED U4 ¢ C13 PER ECO 4426	4-8-83	ROB.						
		M	ADDED 601100-0104 TO P/L PER ECO 4437	4-14-83	ROR						
		N	ADDED NOTE 3 PER ECO 4460	4-27-83	ROB						
1		P	ADDED NOTE 4 PER ECO 4881	12-21-83	ROB						

ITEM NO	REF	DES	S PART NO			NOMENCLATURE			VENDOR		-		
				,, , ,- ,	LIST C	F MA	TERIAL				<u>. </u>		-}
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES DO NOT SCALE DRAWING				P	AUSTRON INC AUSTIN TEXAS								
TOLERANCES UNLESS OTHERWISE SPECIFIED ENGINEER ROLL 10-9-75 PC BOARD ASSY -						_							
DEC	FRACT	ANG	CHECKED DRAFTSMAN	BARKE	10-9-75 P10-7-75	SINE CONVERTER A					A		
MATERIAL:				SIZE 2		672	103	96025	*	PinP			
					·	SCAL	E [;]			SHEET	1 (OF (]

B



4. ON TRANSFORMERS TI & T2. THE RED WIRES ARE FOR SI & FI. THE GREEN WIRES ARE FOR S2 & F2.

B

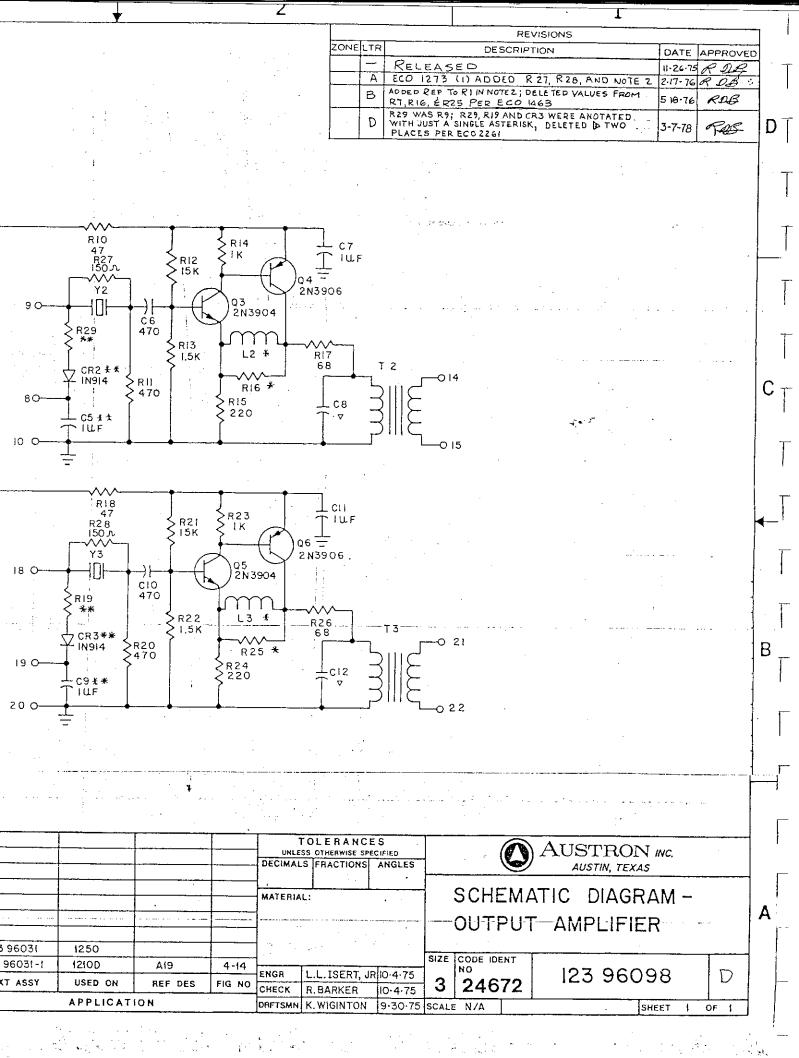
3. VERSIONS PRIOR TO REVISION L" USED DROPPING RESISTORS IN PLACE OF U4.

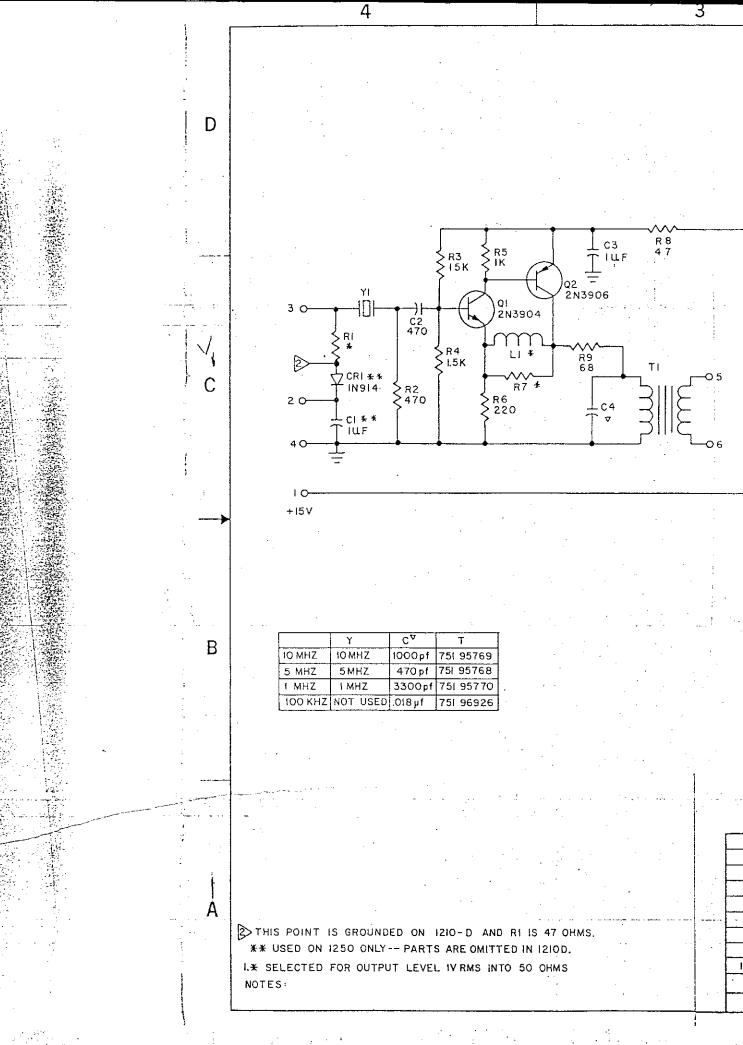
2. WHEN INSTALLING T2, CONNECT "F1" LEAD WITHOUT TRIMMING EXCESS LENGTH.

* 1. FOR 10396025-1 DO NOT INSTALL COMPONENTS IN THE AREA WITHIN THE DOTTED LINES.

A18		271 96208-1	1210D-01					
1A1A4	4-4	254 95205 * 3	1250A:					
A18	4-13	271 96208	1210D					
REF DES	FIG NO	NEXT ASSY	USED ON					
Α	APPLICATION							

- 4.6 OUTPUT AMPLIFIERS
- 4.6.1 The Output Amplifier assembly (A19) contains three similar amplifiers which operate in an analogous manner. This description will consider the 5 MHz amplifier.
- 4.6.2 Crystal Yl is an input filter with R2 as its load. The transistors Ql and Q2 form a two-stage common emitter, collector tuned amplifier with negative feedback. The feedback ratio is determined by R7 and R6. These amplifiers have been designed to allow a minimum level change for a maximum load change.

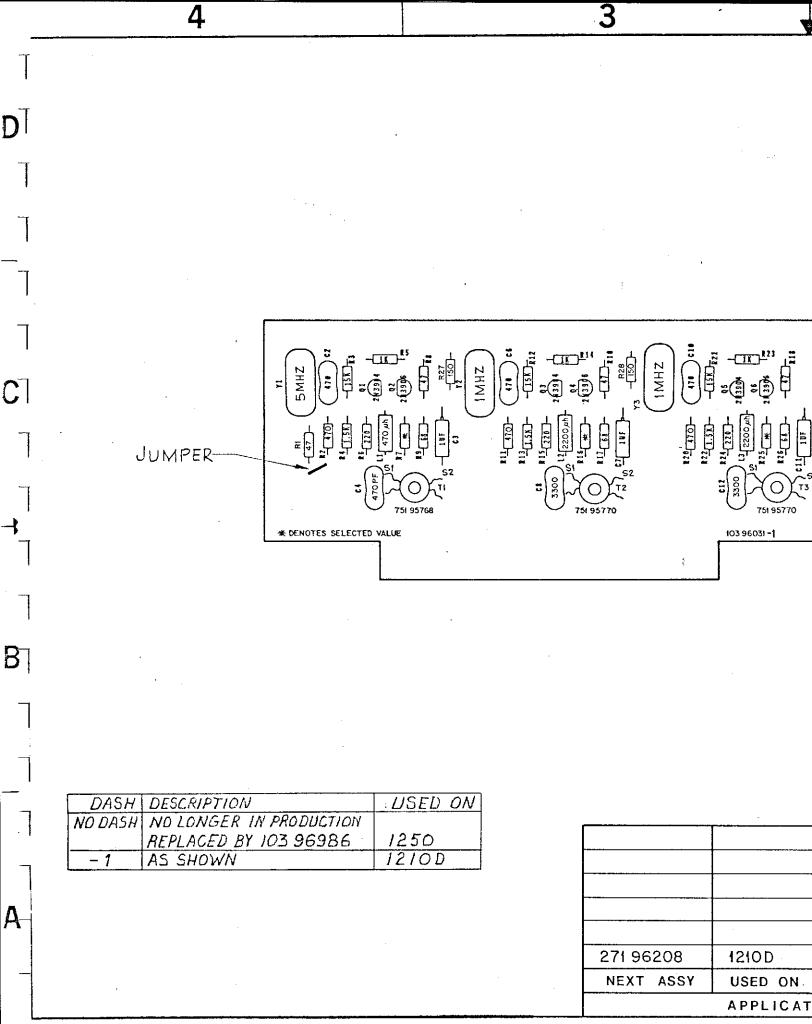




REVISIONS ZONELTR DESCRIPTION DATE APPROVED ROB RELEASED 10-7-75 ECO 1273 (1) REVISED EXTENSIVELY (2) 2/16/76 DELETED -1 REINSTATED DASH 1 AND ADDED 5-6-76 RDB DASH NO. DESCRIP BLOCK PER ECO 1368 ADDED VALUES TO Y1, Y2 & Y3; ADDED S1 & S2 To T1, T2 & T3; PER ECO * 1855 ROB 3-16-77

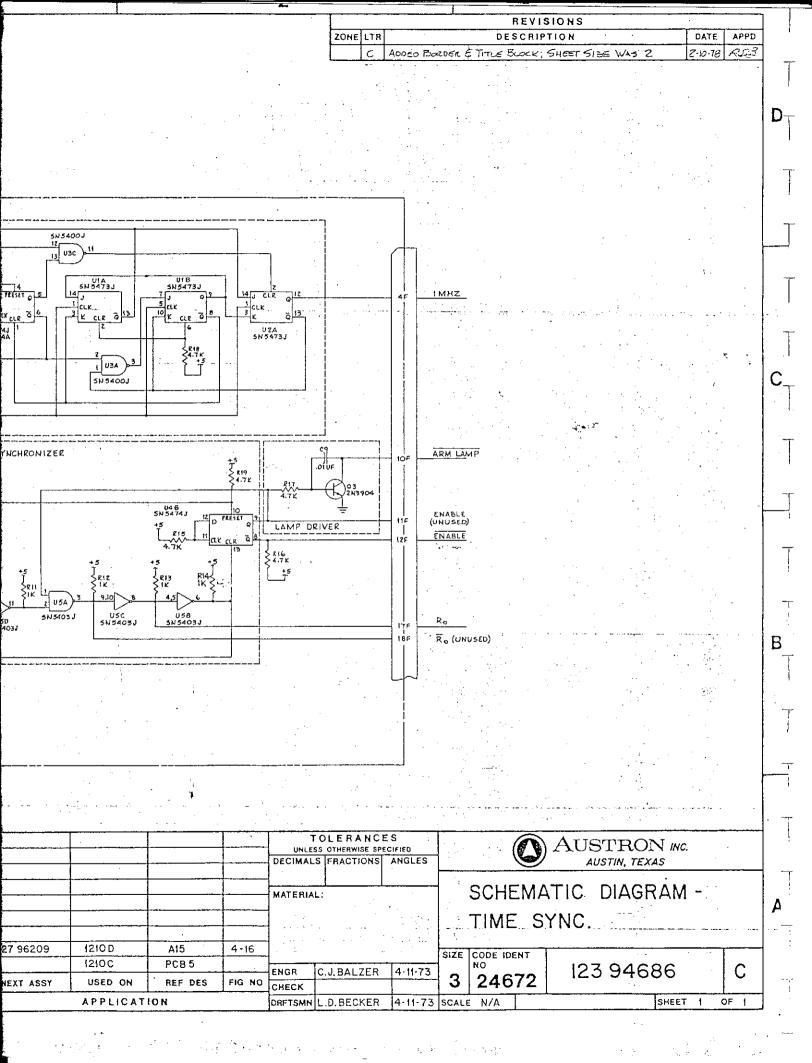
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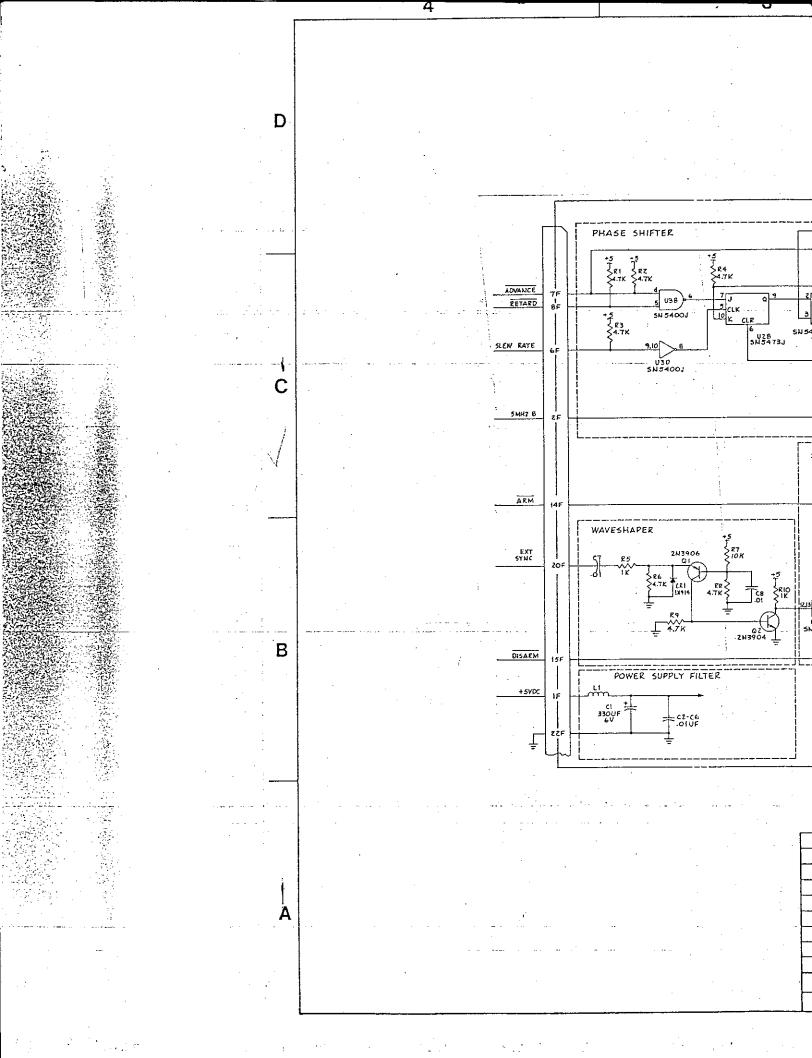
			TOLERANCES UNLESS OTHERWISE SPECIFIED			AUSTRON INC.						
	ļ		DEC	FRAC	ANG		- 1	9 /	AUSTIN,	TEXAS		
			MATER	HAL:		PCBOARD ASSY-					_	
)UTPU	IT A	MPLIF	IER		Δ
A19	4-	15				SIZE	CODE ID		107	00071.8		7
REF DES	FIG	NO		<u>L.ISERT</u> R.BARKE	10·9·75	• 4	^{NO} 24	672	103	96031 *	С	_]·
ОИ				R.BARKE			E 1:1			SHEET O	F (7



4.7 TIME SYNC

- 4.7.1 The Time Sync assembly (Al5) contains a digital phase shifter and circuits which allow the clock to be synchronized by an external signal.
- 4.7.2 The digital phase shifter is made up of Ul, U2, U3, and the A section of U4. The phase shifter accepts a 5 MHz signal from the 1 MHz/5 MHz Output card and normally divides it by 5. On command however, the divider can be forced to divide by 4 or 6, thus "adding" or "subtracting" cycles at a rate selected by the setting of S6. The rate commands are supplied through U3D. The direction command is supplied through U3B.
- 4.7.3 The external signal, EXT SYNC, (used to synchronize the clock to an external source) is first conditioned and converted to TTL levels by the wave shaper circuits that include Q1 and Q2.
- 4.7.4 The synchronizer circuits consist of U4B and U5. The purpose of these circuits is to generate the R0 signal and the ENABLE whenever U4B-9 has been preset high by ARM. R0 is used on the divider card to reset the decade divider chain, thus synchronizing all clock outputs. U5B clears U4B, forcing the clock to be re-armed before another R0 can be generated. Thus, the clock is automatically disarmed when synchronizing from an external source.
- 4.7.5 The ENABLE signal is used to enable the ADV/RET switch and the SET/RUN switch. DISARM must be manually generated by the ARM/DISARM switch after arming the clock to use either the slewing or clock setting functions.
- 4.7.6 The lamp driver, Q3, turns on the ARM lamp whenever U4B is in the armed state.



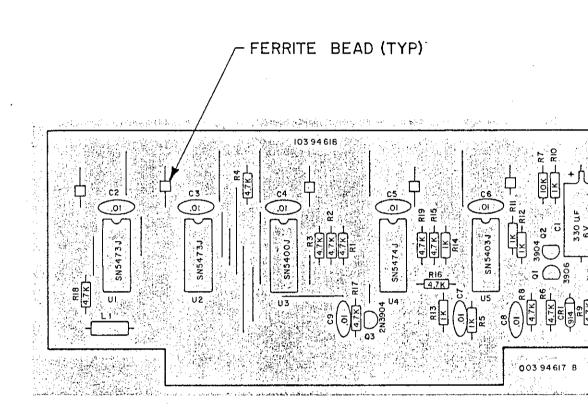


	REVISIONS									
ZONE	LTR	DATE	APPD							
	E	REDRAWN TO UPGRADED STOS PER ECO # 2075	10-26-77	RDB						
	F	REVISED PARTS LIST PER ECO. 2892	4-16-79	ROB						

В

		1	LERANC THERWISE S			(6	$\overline{\mathfrak{D}}A$	USTR	ON INC.	
		DEC	FRAC	ANG]	/4		AUSTIN,	TEXAS	···
		MATER	RIAL:		1			ASSY	-	
A15	4-17	1			TIME SYNC.					
A15	4-17	<u></u>		- I		CODE ID		103	94618	
REF DES	FIG NO	ENGR CHECK	ISERT BARKER	10·26·7	2	NO 24	672	103	34010	
ON		DRFTMN				≣ 1:1			SHEET (O	F 1





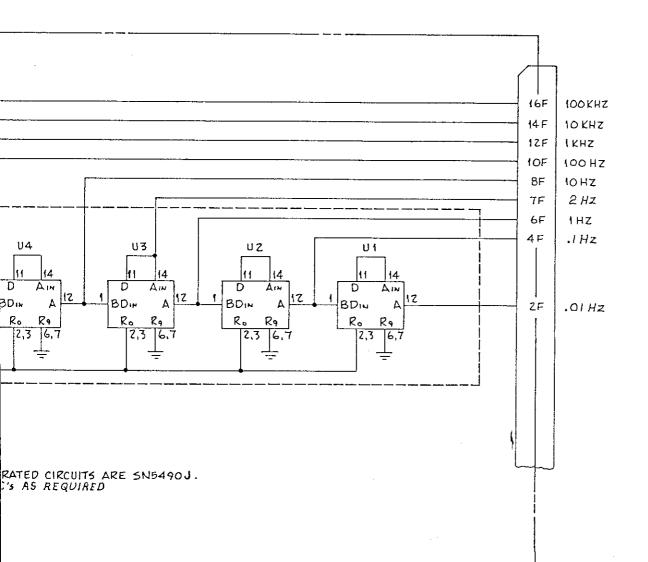
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	271 96208 -1	1210D-01
	271 96208	1210D
	NEXT ASSY	USED ON
		APPLICAT

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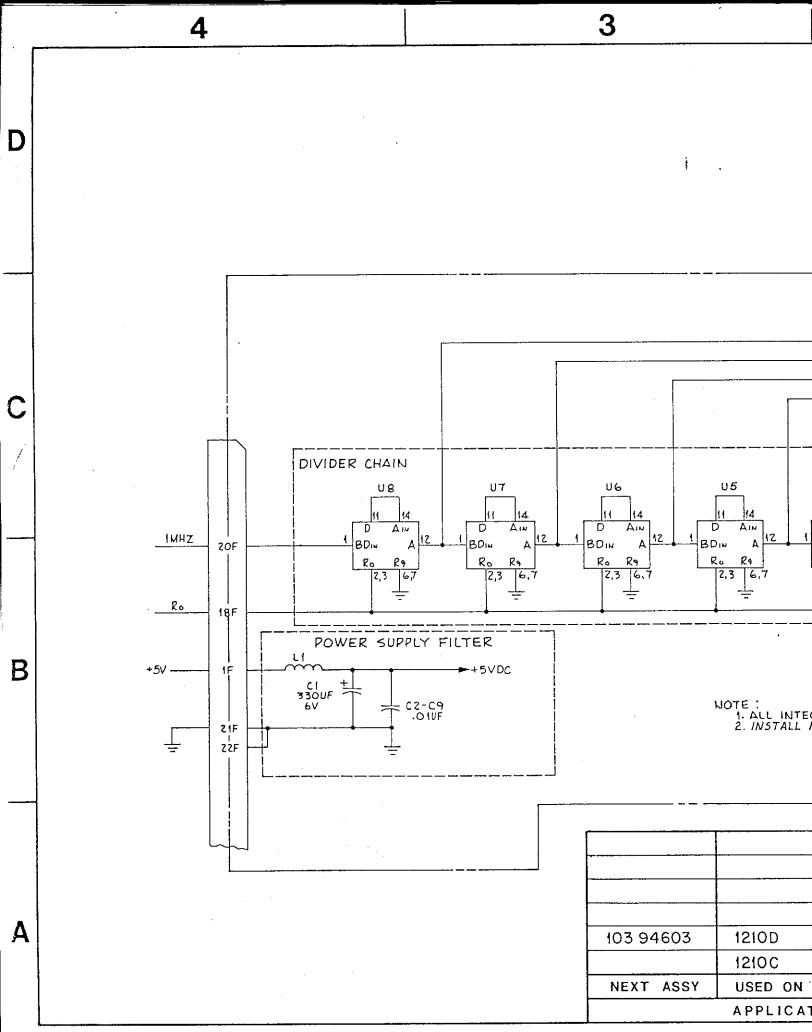
- 4.8 DIVIDER CARD
- 4.8.1 The Divider assembly (Al4) contains provisions for eight cascade decade dividers.
- 4.8.2 The 1 MHz TTL output from the Time Sync card is divided to 1 Hz by means of six cascade decade dividers. The resulting 1 Hz and other intermediate rates are used by the Clock Driver and the Phase Shifter cards.
- 4.8.3 The RO signal originates on the Time Sync card, and is used to reset all dividers when synchronizing the clock to an external source.

B

REVISIONS										
ZONE	LTR	DESCRIPTION	DATE	APPD						
	В	ADDED BORDER & TITLE BLOCK-NO CHANG	E 2-10-78	ROB						



				LERANC THERWISE S					USTRON INC					
			DEC	FRAC	ANG				AUSTIN.	TEXAS]	
			MATER	IAL:			SCHEM		DIAG	GRAM -	-			
A14	4-1	В				[DIVIDE	R					1	
PCB6						SIZE	CODE ID		107	04070	`	<u> </u>	1	
REF DES	FIG	NO	ENGR CHECK	C.J.B.	4.4.73	2	NO 246	672	123	94679	J	В	-	
ON			DRFTMN	L.D.B.	4.4.73	SCALE	N/A			SHEET	∮ OF	1	1	

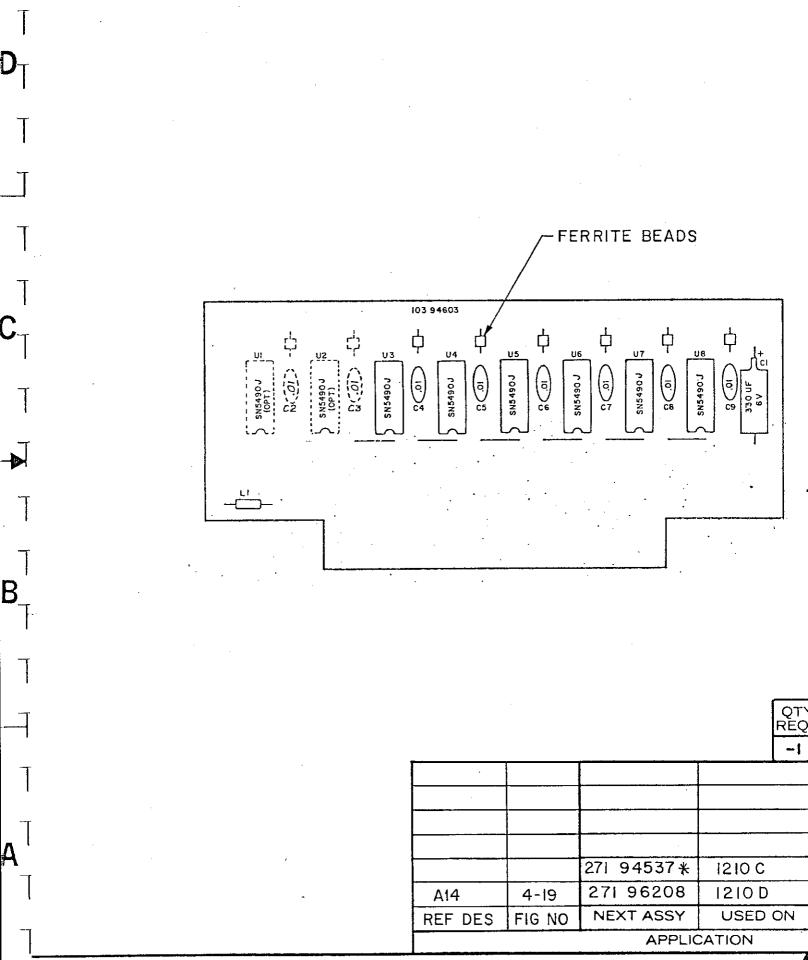


REVISIONS										
ZONE	LTR	DESCRIPTION	DATE	APPROVED						
	В	ECO 974; DELETED SOCKETS; REDRAWN	12/29/3	ROB						
	С	DELETED JUMPERS, BEADS, É.OI CAPS (CZECZ) ASSOCIATED WITH UI & UZ PER ECO 1465	5-18-76	ROB						
	D	ECO 2088: SN 5490J WAS SN 5490 N	10-7-77	RDB						
	E	ADD C2+C3, JUMPERS +TABLE ECO 2274	3-17-78	ROB						

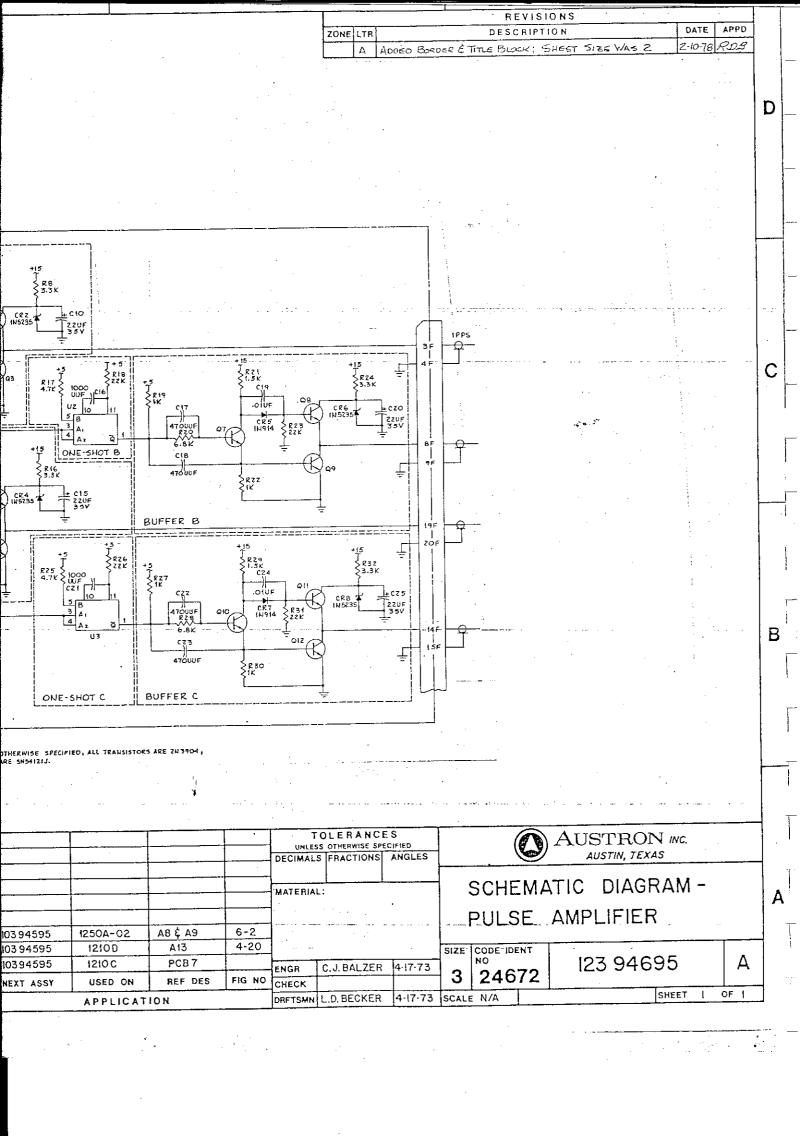
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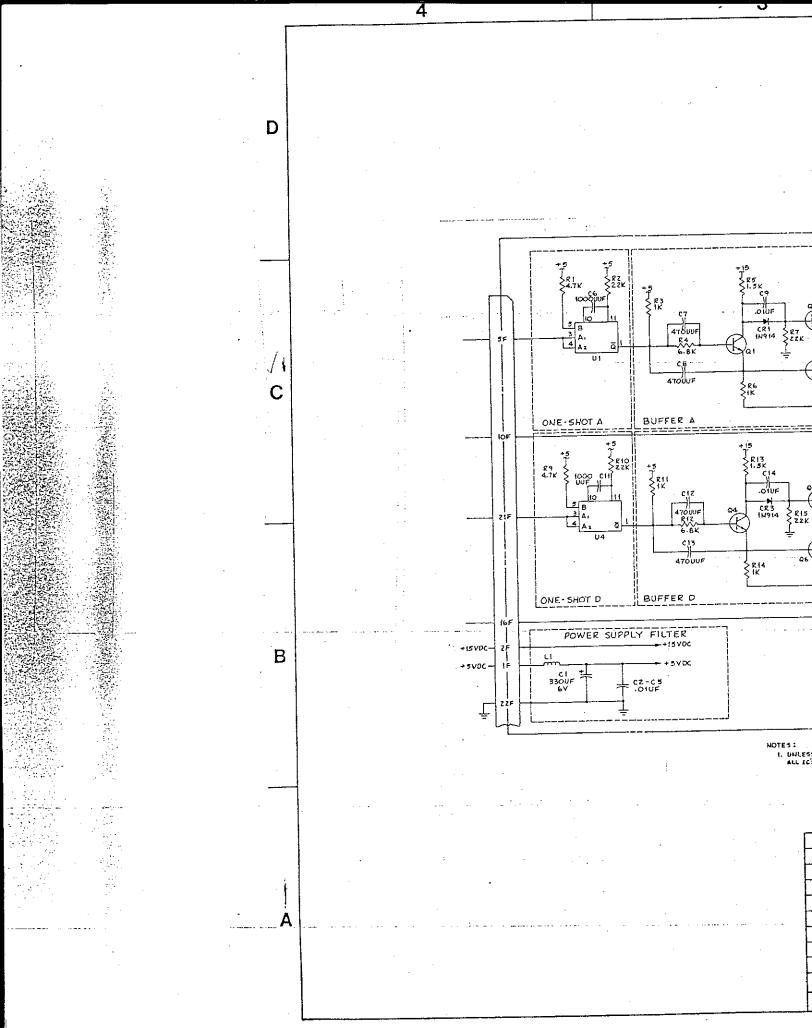
	103 94603	C4 THRU C9 AND U3 THRU U8 INSTALLED
*	103 94603-1	C3 AND U2 ADDED
*	103 94603-2	C2 AND C3, UI AND U2 ADDED

						<u> </u>							1
TEM NO	REF	DES	PART 1	NOMENCLATURE							VENDOR		
				LIST C	F MA	TERIAL							
VLESS OTHERWISE SPECIFIED IMENSIONS ARE IN INCHES DO NOT SCALE DRAWING					AUSTRON INC AUSTIN								
TOLERANCES UNLESS OTHERWISE SPECIFIED			ENGINEER RATA	12-75	PC BOARD ASSY, DIVIDER								
DEC	FRACT	ANG	CHECKED Report									F	
ATERIAL:			7		SIZE 2	CODE IC		103	946	503	*	Ε	
					SCAL	E [/]			SH	HEET	(OF	



- 4.9 PULSE AMPLIFIER CARD
- 4.9.1 The Pulse Amplifier card (Al3) contains four identical pulse shapers and line drivers.
- 4.9.2 Ul is a one-shot multivibrator which accepts a TTL signal from the Divider card. The output of the one-shot is a pulse of fixed duration, determined by R2 and C6. Q1, Q2, and Q3 form a line driver capable of delivering a 5 volt pulse to a 50 ohm load.
- 4.9.3 Only the required circuits will be connected in order to conserve power. Normally only one circuit will be used for 1 PPS.



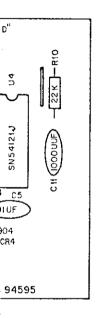


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		REVISIONS		
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	Α	REDRAWN TO ADD DASH NOS. PERECO 1184		
<u> </u>	В	ECO 1192 : ADDED NOTE 2	1-6-76	gar
	C	CRZ,4,6 & 8 TYPE No. WAS 1N5235; ECO #1857	3-15-77	ROB
	D	703 SN 54121 J WAS 703 SN 54121 N PER ECO 2090	10-14-77	RDB
	Ε	REVISED PARTS LIST PER ECO. 2891	4-16-79	RDB
	F	ADDED	12-17-79	ROB
	G	REVISED PL PER ECO. 3556	2-11-81	ROB

D

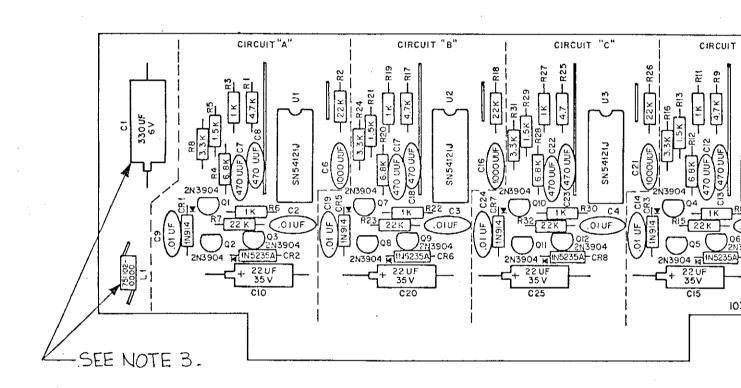
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ITEM NO	REF	DES		PART	NO		1	NOM	ENCL	ATURE		VE	NDOR	<u> </u>
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	ERANC THERWISE S FRACT	T T	ENGINEER CHECKED	RG	F 12·10-75	1	-			SSY	_			
			DRAFTSMAN		R 12:10:75								ı	"
MATER	IAL:			······································		SIZE 2	COD NO			103	94595	*	G	
						SCAL	E (:				SHEET	1 (OF !]

*	FOR PART NO.	USE CIRCUIT
	103 94595	ALL
:	103 94595-1	Ϋ́Α"
	103 94595-2	"A & B"
	103 94595-3	"A', "Β' έ C"

SEE NOTE 2



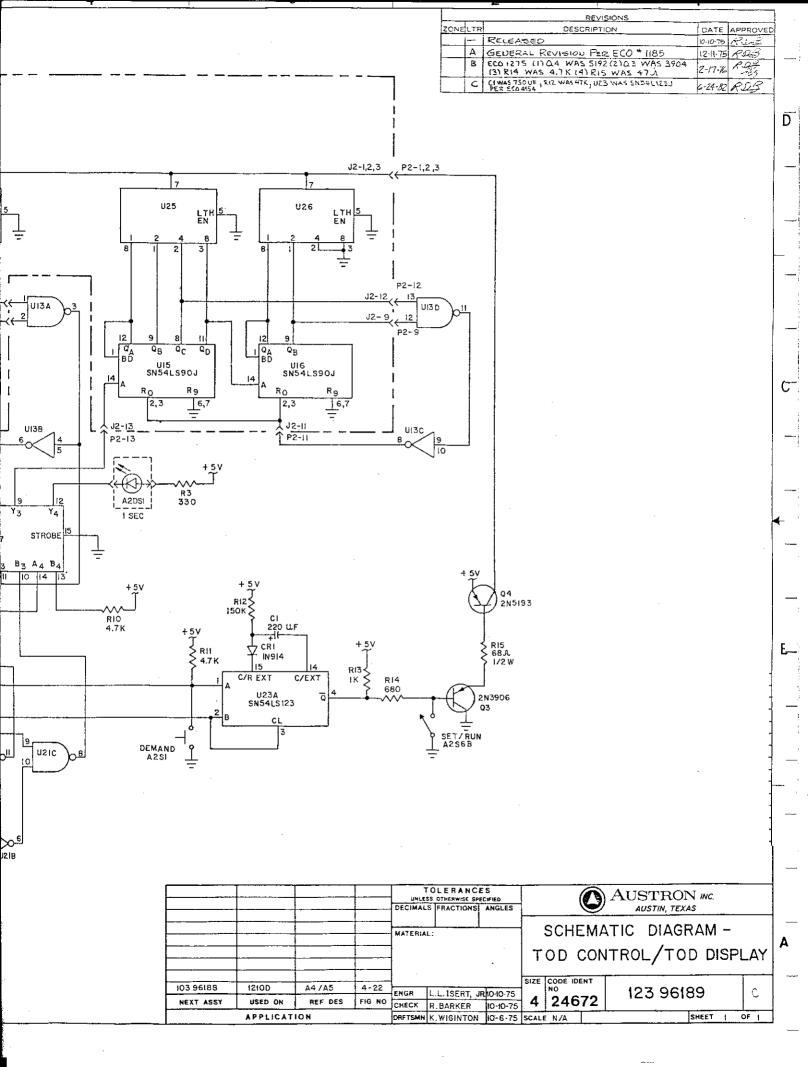
- 3. INSTALL LI AND CI FOR ALL PART NUMBERS.
- 2. NO DASH VERSION USES 703SN7412IN INSTEAD OF 703SN5412IJ.
- A A IDENTIFY ASSEMBLY WITH APPROPAGE RIATE PART NUMBER PER CHART ABOVE.

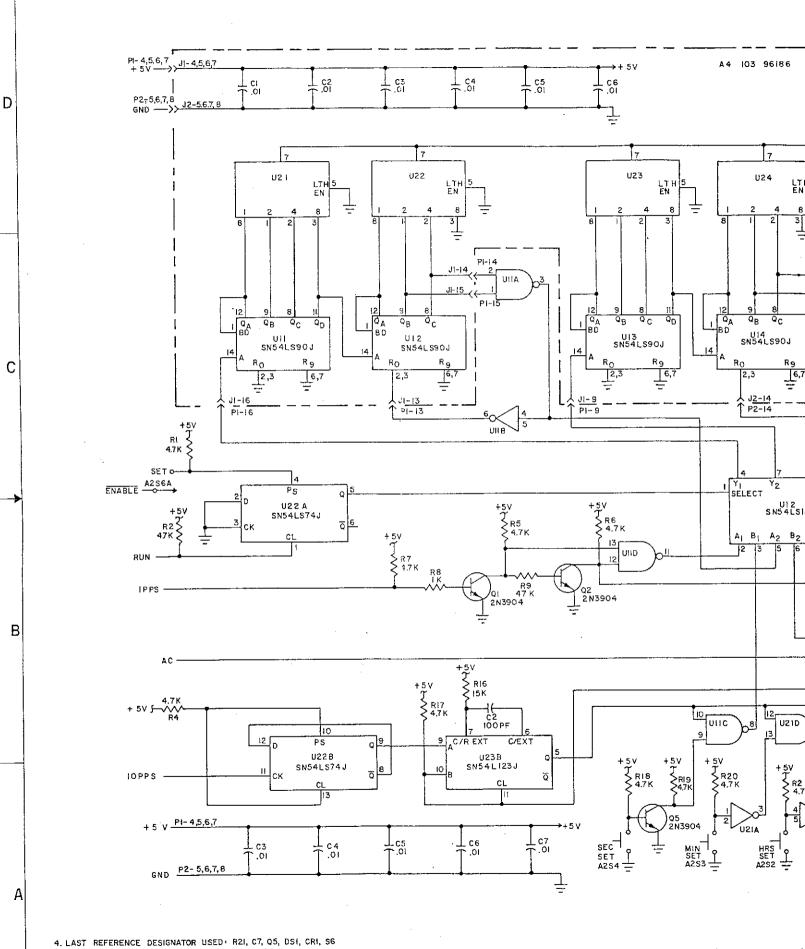
NOTES:

			REQL
			-1
A8 ¢ A9	,6-3	25497307-2	1250A-02
A13	4-21	271 96208	1210D
REF DES	FIG NO	NEXT ASSY	USED ON
		APPLIC	CATION

- 4.10.1 The Model 1210D TOD Display, which is composed of assemblies A4, A5 and various controls which are mounted on the front panel, is covered by a single description and schematic.
- 4.10.2 The model 1210D A4 assembly contains six dividers, Ull through Ul6, and six decoder/driver/displays, U21 through U26. The A5 assembly contains a quad 2-line to 1-line data selector, Ul2; a one shot, U23; a flip flop, U22; and various gates.
- In the RUN mode, a 1 PPS signal from the clock divider is processed by the sliver generator, composed of A5Q1, A5Q2, and A5UllD, and applied to the Al input of the data selector, A5Ul2. The Yl output of A5U12 drives the units seconds divider, A4U11, which is decoded and displayed by A4U21. The OD output of A4U11 drives the tens of seconds divider A4Ul2, which is decoded and displayed by A4U22. When a six is present at the outputs of A4U12, it is detected by gate A5UllA which furnishes a pulse to the reset gate A5UllB and to the A2 input of the data selector, A5Ul2. The Y2 output of A5Ul2 drives the units minutes and tens of minutes dividers and decoder/displays in similar manner. Gate A5U13A functions like gate A5U11A by furnishing a pulse both to the reset gate A513B and to the A3 input of the data selector, A5U12. The Y3 output of A5U12 drives the units hours divider A4U15, whose output is decoded and displayed by A4U25. output of A4U15 drives the tens of hours divider A4U16 whose output is decoded and displayed by A4U26. The units hours and tens of hours dividers A4U15 & 16, are cascaded together to form a divide by twentyfour stage: the QC output of A4U15 and the QB output of A4U16 are detected by gate A513D and applied to the reset gate A513C, which then reset both A5Ul5 and A5Ul6. The A4 input of the data selector A5Ul2 receives a 1 PPS signal from A5Q2, and the Y4 output drives the 1 SEC lamp A2DS1.

- 4.10.4 In the SET mode an ENABLE will be present at A2S6A from the clock arm circuit. When switch A2S6A is placed in the SET position (up), the Q output of A5U22A goes high. This is connected to the select input of the data selector A5U12 and selects the B inputs, thereby isolating the seconds, minutes and hours counters. The divider A5U22B receives a 10 PPS signal from the clock divider and divides it by two. The resulting 5 PPS is applied to the one-shot A5U23B, whose time constant is approximately one psecond. The one psecond 5 PPS signal is present at gates A5U11C, A5U21D, and A5U21C, which are controlled by switches A2S4, A2S3, and A2S2 respectively. These switches allow the 5 PPS rate to be applied to the seconds, minutes, or hours counter, as selected.
- 4.10.5 The one-shot A5U23A is used to enable the clock display when an ac signal is present to retrigger the one-shot or when the demand button is pushed during standby operation. The time constant for A5U23A is approximately 8 seconds. Switch A2S6B will turn on display regardless of the state of A5U23A.





^{3.} ALL GATES AND INVERTERS ARE SN54LSOOJ 2. ALL RESISTORS ARE 1/8 W, 10 % COMP

NOTES: UNLESS OTHERWISE SPECIFIED

8

I. ALL COMPONENTS ARE PART OF A5 103 96188



		REVISIONS		
ZONE	LTR	DESCRIPTION	DATE	APPD
	_	RELEASED	9-29-75	ROB
	Α	REVISED SILKSCREEN PER ECO#1186	12-16-75	RIB
	В	Q3 WAS 3904; Q4 WAS 5192; R14 WAS 4.7K; R15 WAS 47; PER ECO# 1275	2-17-76	ROB
	C	REVISED PARTS LIST PER ECO. 2888	4-16-79	200
	D	C1 WAS 750UF, R12 WAS 47K, U23 WAS SN 54 L 123 J PER ECD 4154	6-24-82	
	E	ADDED TO THE PL #65110-0473 EC04879	12-21-83	RIB

В

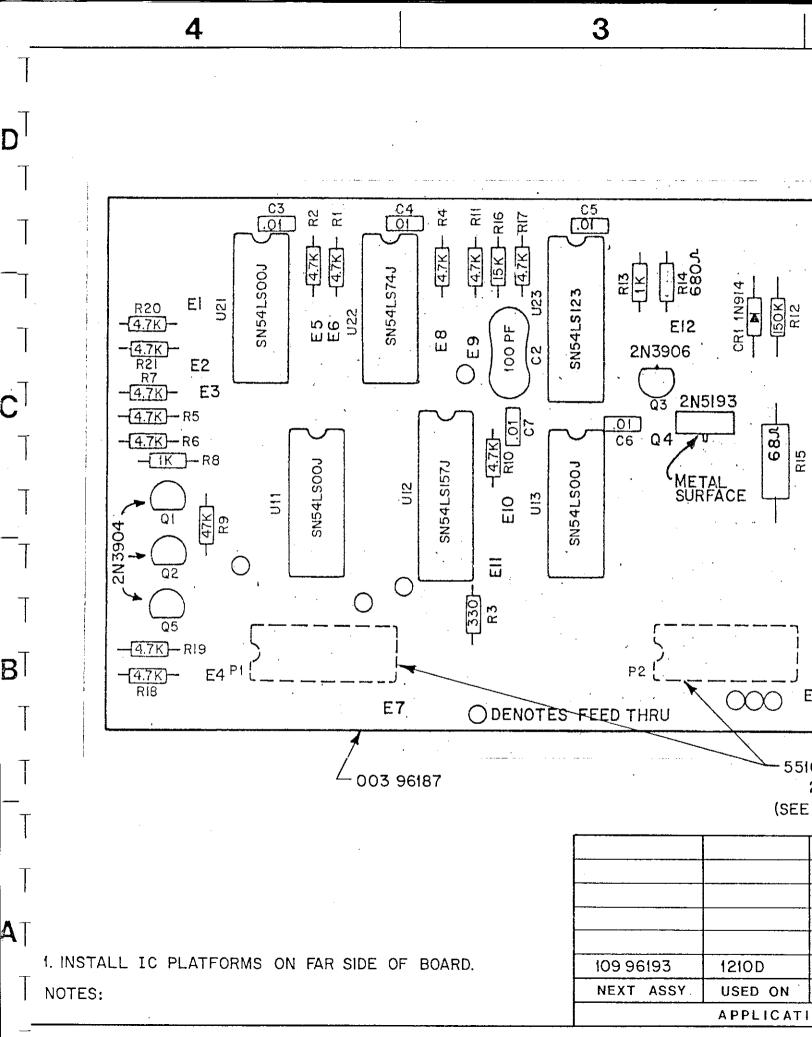
C1 220 UF

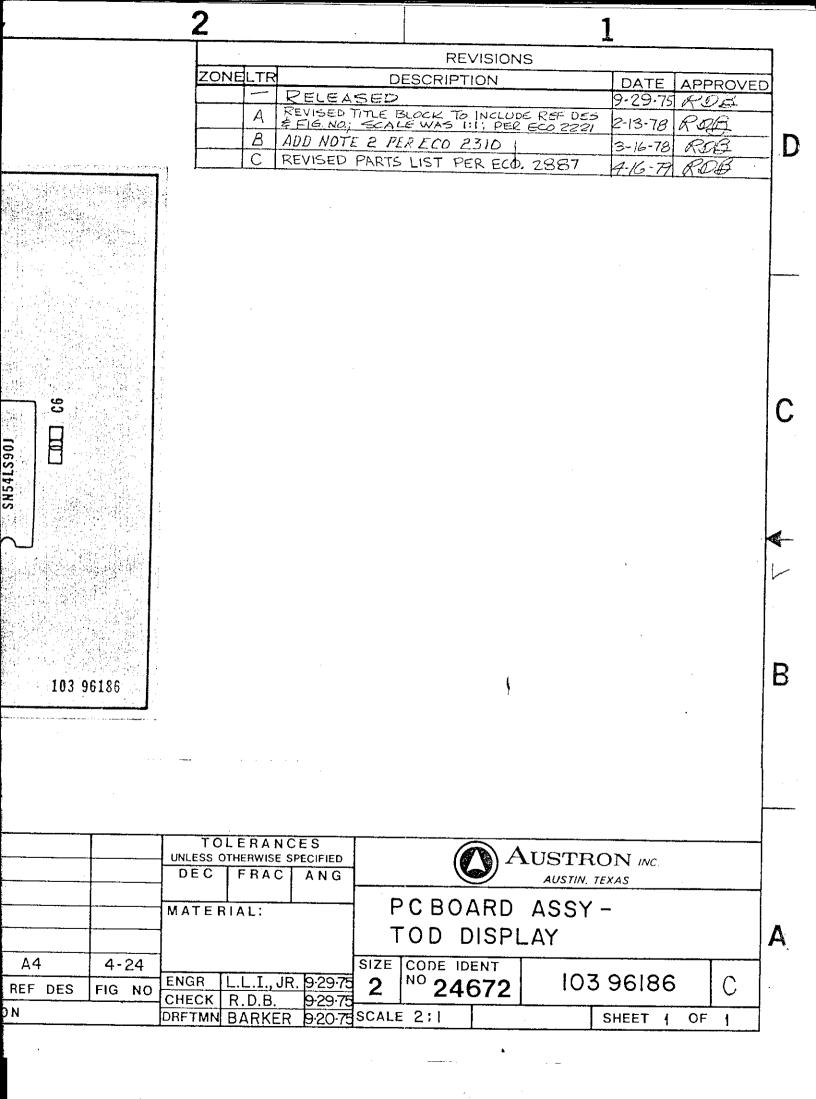
103 96188

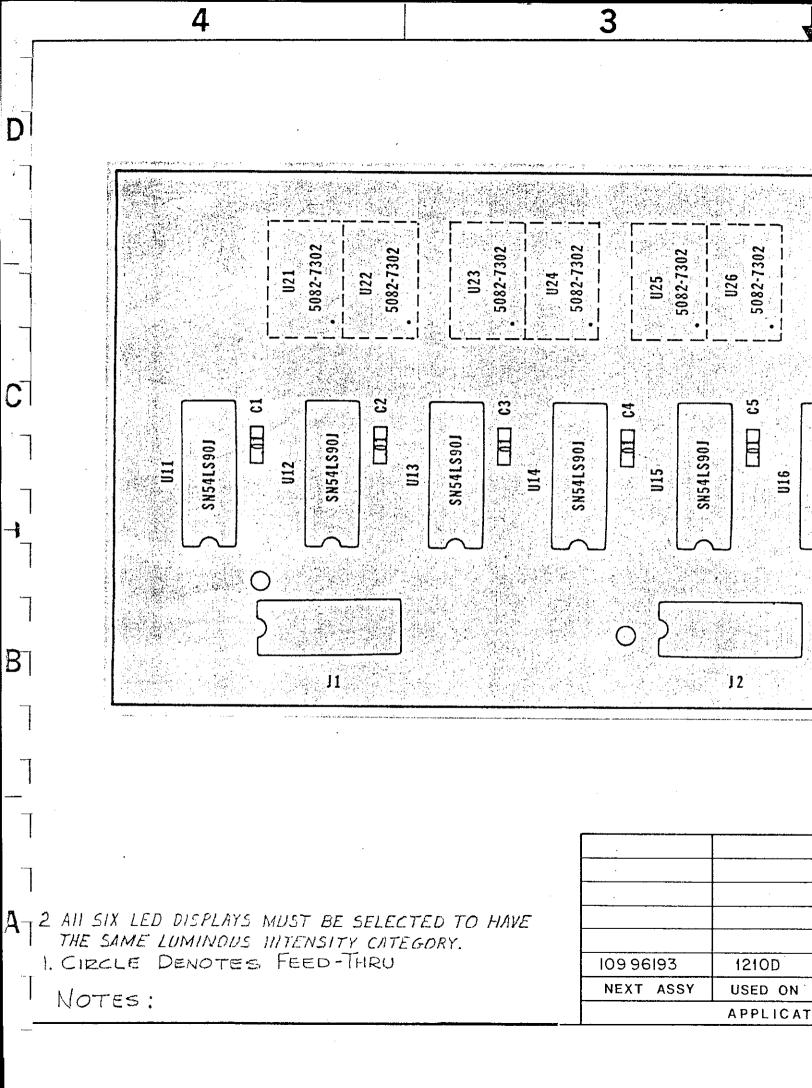
16-0013 ₋ REQD NOTE 1)

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			LERANC THERWISE S				$\mathbf{\hat{j}}^A$	USTR				-
		MATER	RIAL:			PC BOA		ASSY TROL	_	·		A
A5 REF DES	4-23 FIG NO		L.ISERT R.BARKER	9.29.75	2	NO 24		103	96188		E	
N		DRFTMN	R.BARKEF	R 92075	SCALE	2:1			SHEET !	OF	1]







4.11 INTERNAL OSCILLATOR

4.11.1 The AUSTRON Sulzer Model 1150 oscillator (A9) employs a high quality 5 MHz, fifth-overtone, high-temperature bake-out crystal unit which exhibits exceptional retrace and long-term aging characteristics. The crystal is mounted in another proportionally controlled oven. This arrangement assures almost complete freedom from frequency shifts due to environmental temperature changes. A high-gain AGC system is used to keep the crystal drive level constant, assuring excellent long-term aging.

NOTE: The AUSTRON Model 1150 is a <u>sealed unit</u>, and as such, is <u>not</u> field repairable. If this assembly is found to be in need of repair, return it to AUSTRON, Inc. (see section 6.0).

5.0 MAINTENANCE

5.1 SCOPE OF SECTION

- 5.1.1 This maintenance section of this manual provides the technician with a general approach to maintaining the model 1210D. Included are trouble analysis guides and general maintenance procedures.
 - 5.2 TROUBLE ANALYSIS GUIDES
 - 5.2.1 Specific information is provided in the following:

SYMPTOM

1) (PWR Lamp) 1A2DS3 fails
to light.

PROBABLE CAUSE

- 1. AC power not available.
- 2. Power switch not ON.
- 3. Power cord not connected.
- 4. 230 V selected when using 115 V.
- 5. Lamp is burned out.
- 2) Charge lamp fails to light (1A2DS4) when line lamp is on.
- 1. Lamp burned out.
- 2. Switch 1A2S11 open.
- 3) BATT circuit test reads high.
- 1. Blown 1A10F2.
- 2. Open 1A2S10.
- 3. Battery pack open.
- 4) OVEN circuit test reads low.
- 1. Oscillator is in warm-up stage.
- 2. Supply voltage to oscillator is low.
- 3. Defective oscillator.
- 5) RF circuit test reads low.
- 1. Check output of oscillator; should be 1 V RMS, unloaded.

	5.2.1 (Cont'd.)		
	SYMPTOM	PRO	OBABLE CAUSE
6)	All meter functions not	1.	Bad power supplies.
	correct.	2.	Meter lA2Ml is bad.
		3.	Switch 1A2S5 is bad.
7)	ARM lamp will not light	1.	Switch 1A2S7 is bad.
	after placing switch 1A2S7	2.	ARM lamp 1A2DS2 is burned out.
	in the ARM (up) position.	3.	Logic on TIME SYNC pcb (Al5)
			not functional.
0)	a		
8)	Cannot set TOD display.		Clock is not ARMed.
			ARM circuit not functional.
		3.	SET/RUN switch (1A2S6) not in SET
			position.
		4.	10 PPS input not present on TOD
			gating circuitry (A5).
		5)	TOD control pcb/(A5) not functional.
9)	1 SEC lamp (1A2DS1) does	1)	SET/RUN switch (1A2S6) not put into
	not flash each second.	•	RUN position while clock was ARMed.
		2)	Lamp (1A2DS1) is burned out.
			1 PPS not available from clock
			logic.
10)	TOD display not incrementing	1)	Display not functioning because
	time.		ac not prsent. Press DEMAND
			pushbutton.
		2)	l PPS from clock logic not
			available.
		3)	Clock not placed in the RUN mode
			with the clock ARMed.
		4)	TOD CONTROL pcb not functional.

5) Divider on TOD DISPLAY pcb not

functional.

	5.2.1 (Cont'd.)	
	SYMPTOM	PROBABLE CAUSE
11)	Clock output cannot be	1. Clock not ARMed.
	slewed.	2. Slew rates not available from
		divider chain.
		3. Switch 1A2S9 open.
		4. Switch 1A2S8 open.
		5. TIME SYNC pcb (Al5) not functional.
12)	Clock will not sync.	1. Clock not ARMed.
		2. External SYNC pulse not available.
		at connector 1A2J1.
		3. TIME SYNC pcb (Al5) not functional.
13)	Sine Wave output not present.	1. Check associated buffer card.
		2. Check associated output filter.
14)	Pulse output not present.	1. Check PULSE AMPLIFIER pcb.
		2. Check DIVIDER pcb.
15)	No clock outputs are	1. OSC switched to EXT. STD. input.
	functional.	2. Power supply is bad.
16)	Cannot adjust coarse	1. Tuning tool broken.
	frequency.	2. Defective oscillator.
17)	Batteries will not operate	1. Not charged for 16 hours.
	unit for 8 hours.	2. High charging circuit not
		delivering 500 to 750 mA.
		3. Replace batteries.

- 5.3 GENERAL MAINTENANCE
- 5.3.1 Check capacity of Nicad batteries every six months. Deep discharge and recharge for 16 hours. When batteries do not give required standby capacity replace them. (See paragraph 1.3.1.2.)
 - 5.3.2 No other periodic maintenance is required.

6.0 REPLACEABLE PARTS

6.1 SCOPE OF SECTION

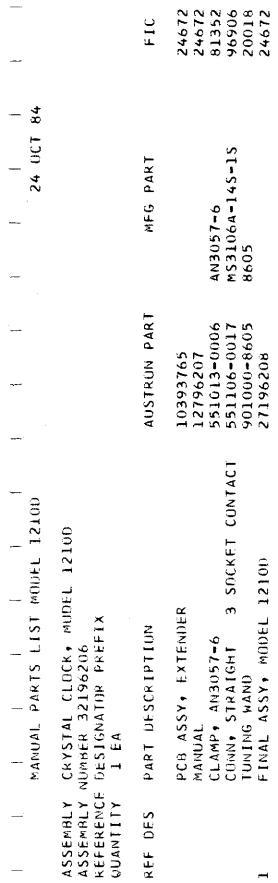
- 6.1.1 The following is a list of replaceable parts which includes the reference designator, description, and AUSTRON Part Number. For convenience in ordering from local suppliers, the manufacturer's part number and the manufacturer's Federal Identification Code (FIC) are also given where applicable.
 - 6.2 ORDERING REPLACEMENT PARTS
- 6.2.1 To order replacement parts from AUSTRON, Inc. address order to:

Austron Inc. P.O. Box 14766 Exit 248, N. IH-35 Austin, Texas 78761

Specify for each part; 1) model and serial number of the instrument from the rear panel label, 2) complete circuit reference designator, 3) AUSTRON part number, 4) description.

- 6.2.2 To order parts not listed, add a complete description of function of part and location.
- 6.2.3 Part numbers as shown will change occasionally as vendor items are re-evaluated or as improved components become available. The equivalent part currently used in production at the time orders are received will be shipped. Where manufacturer's part number or FIC is missing, any reputable manufacturer's part of the appropriate value, indicated in the description, may be used.

6.2.4 The circuit reference designator includes the reference designator prefix in the page heading plus the reference designator for the individual part. If, for example, the reference designator prefix for a circuit board assembly is 1A4A7 and the desired component is capacitor Cl, the complete circuit reference designator would be 1A4A7Cl.



ASSY, MODEL 1210D FINAL ASSEMBLY ASSEMBLY

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ASSEMBLY KEFEKENCE QUANTITY	ASSEMBLY NUMBER 27196208 KEFEKENCE DESIGNATOR PREFIX 1 QUANTITY 1 EA			
REF DES	PART DESCRIPTION	AUSTRON PART	MFG PART	FIC
A1	NUT USED			
A2	1	10996193		24672
43	SY, OUTPUT FI	10396735		24672
77	A S	10396186		24672
45	ASSY 1.0.0. CON	∞		24672
46	SSY (A6)	11096211		24672
47	BOARD ASSY	10394664		24672
48	PACK ASSY	12096212		24672
ФФ	XTLUSC, 1150 (70-85 DEG)	30296818		24672
410	SSY	10996195		24672
A11	E.R.	10396200		24672
~		10396769		24672
m	ASSY-PULSE	10394595-1		24672
√t.	PC BUARD ASSY, DIVIDER	10396614		24672
2		10394618		24672
2	ASSY. +5 V	10394607		24672
_	•	10396197		24672
മ	•	10396025-1		24672
œ	PCB ASSY OUTPUT AMPLIFIER	10396031-1		24672
_	\sim	00794304		24672
2	COVER-INSTRUMENT	00796209-1		24672
3	COVER-INSTRUMENT	00796209-2		24672
4	ANGULAR	01096753		24672
RP5	*171x1/4x1/2 SPACER ROUND	520830-0005	\sim	06540
Q.	0.72	520830-0005	28-	06540
7	(1/2 SPACER	520830-0005	28-S	06540
သ	(1/2	520830-0005	28-5517	06540
	WASHER, PRELOAD	02096741		24672
MP10	\sim	02096741		467
MP]]	SHER, P	02096741		467
MP12	ASHER, PREL			2.
<u>.</u>	Y MOON Y	520205-0005	10R4-1501B	14519
MP15	SHOCK MOUNT BUSHING	520205-0005	10R4-1501B	14519

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	ASSEMBLY ASSEMBLY N REFERENCE GUANTIFY	FINAL ASSY, MUNDBER 27196208 E DESIGNATOR PRE I EA	FINAL ASSY, MÜDEL 1210D HUMBER 27196208 DESIGNATOR PREFIX 1 1 EA	12100		10)	(CONT)							
	REF DES	PART DES	PART DESCRIPTION			AUS	AUSTRON PART	ART	Ξ	MFG PART		Ī	FIC	
	MP16	SHOCK MC	MOUNT BUSHING	NG NG		520	520205-0005	05	1084-1501B	501H		14;	14519	
	MP17	SHOCK MC	MOUNT BUSHING	NG		520	520205-0005	0.5	1084-15018	5018		14;	14519	
	MP18	SHUCK DR	DRIVE MASHER	œ		520	520205-0010	10	10R41500	00		149	14519	
	MP19	SHOCK DR	DRIVE WASHER	o∡		520	520205-0010	10	10R41500	0(145	14519	
	MP20	SHOCK DR	DRIVE WASHER	œ		520	520205-0010	10	10R41500	20		141	61541	
	MP21	SHOCK DR	DRIVE WASHER	œ		520	520205-0010	10	10K41500	00		145	14519	
	MP22	BUMPER	RUBBER	W/HARDWARE	Ť.	520	520210-2198	86	2198			83330	330	
	MP23	BUMPER	RUBBER W/	W/HARDWARE	H	520	520210-2198	86	2198			83330	330	
	MP24	BUMPER	RUBBER W/	W/HARDWARE	щ	520	520210-2198	86	2198			833	3330	
	NP25	BUMPER	RUBBER WA	W/HARDWARE	更	520	20210-2198	86	2198			833	3330	

FIC

MFG PART

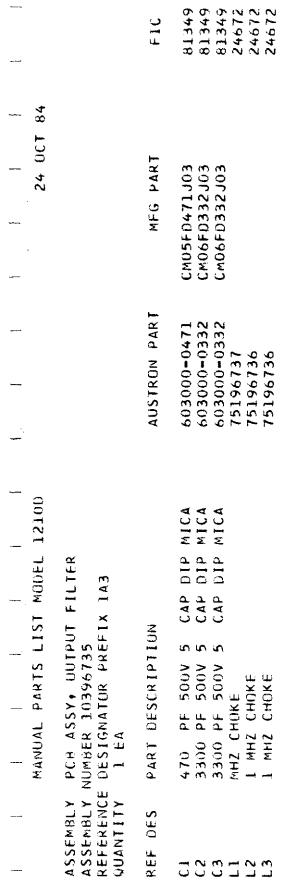
AUSTRON PART

ASSEMBLY PANEL ASSY, FRONT ASSEMBLY NUMBER 10996193 REFERENCE DESIGNATOR PREFIX 1AZ QUANTITY 1 EA

PART DESCRIPTION

REF DES

DS1	1.2 MCD 20MA TI	3/4 LED RED	555600-4403	HLMP-0102	28480
0\$2	LAMP, RED 28V		555008-0002	BP62-RC8-1762	03797
053	LAMP. GRN 28V		555008-0004	BP62-6CR-1762	03797
0.54			555008-0005	ACB-1	03797
٦ ا	CUNNECTOR BNC		551100-7935	KC79+35	11636
75	CUNNECTOR BNC		551100-7935	KC79-35	11636
13	CONNECTOR BNC		551100-7935	KC79-35	11636
40	CONNECTOR BNC		551100-7935	KC79-35	11636
J 5	OR BNC		551100-7935	KC79-35	11636
SI	. PSHBIN.	SPST-NO	553012-0001	30-1	81073
\$2	_	SPST-NO	553012-0001	30-1	81073
S 3	SHBTN.	SPST-NO	553012-0001	30-1	81073
54	TCH+ PSHBIN+	SPST-NO	553012-0001	30-1	81018
55	SWITCH, RUTARY		553905-3601	50CD3601-2-AJN	81013
98	SW, TUGGLE DPDT		553010-0006	MST-205N	95146
57	SW+ TOGGLE SPDT	CENTER OFF	553304-0006	7105SY2WE	09353
58	SW, TOGGLE SPDT	TER	553304-0006	7105SY20E	09353
89	SWITCH+ RUTARY		553905-3601	50CD3601-2-AJN	81073
810	T066LE		553010-0006	MS1-205N	95146
511	Sw. TOGGLE DPDT		553010-0006	MS1-205N	95146
M]	METER, 0-100UA		557000-0020	1S-DUA-100	32171
ж Р]	FILTER POLAROID		02096755		24672
MP2	DIAL KNUB TURNS	COUNTING	505010-0462	RD462	75042
X —	10 K 2 M 5	RES VAR H.X.	659275-0103	534-103	02111



ASSEMBLY PCB ASSY, TOD DISPLAY ASSEMBLY NUMBER 10396186 REFERENCE DESIGNATOR PREFIX 1A4 QUANTITY 1 EA

REF DES	PART DESCRIPTION	AUSTRON PART	MFG PART	FIC
C.1		601100-0103	CY15C103M	71590
C2	.01 UF 50 V 20 CAP CERAMIC	601100-0103	CY15C103M	71590
C3	UF 50 V 20 CAP	601100-0103	CY15C103M	71590
C4	.01 UF 50 V 20 CAP CERAMIC	601100-0103	CY15C103M	71590
C5	UF 50 V 20	601100-0103	CY15C103M	71590
90	OI UF 50 V 20 CAP CERAMIC	601100-0103	CY15C103M	71590
01.1	IC DECADE COUNTER	703SN54LS90J	SN54LS90J	01295
U12		7035N54LS90J	SN54LS90J	01295
013	IC DECADE COUNTER	7035N54LS90J	SN54LS90J	01295
014	IC DECADE COUNTER	703SN54LS90J	SN54L590J	01295
U15		703SN54LS90J	SN54L.S90J	01295
016	1C DECADE COUNTER	7035N54L590J	SN54LS90J	01295
021	LED NUM. DISPLAY 0.3IN LHDP W/LATCH	555600-7302	5082-7302	28480
U22	NUM. DISPLAY 0.	555600-7302	5082-7302	28480
023	LED NUM. DISPLAY 0.3IN LHDP W/LATCH	555600-7302	5082-7302	28480
1124	LED NUM. DISPLAY 0.3IN LHDP W/LATCH	555600-7302	5082-7302	28480
025	LED NUM. DISPLAY 0.3IN LHDP W/LATCH	555600-7302	5082-7302	28480
026	LED NUM. DISPLAY 0.3IN LHDP W/LATCH	555600-7302	5082-7302	28480
Хì	SUCKET, 1.C. 16-PIN	551026-0860	1-380860-1	04618

04618

1-380860-1

551026-0860

SUCKET. I.C. 16-PIN

		 12100	1		
ASSEMBLY ASSEMBLY REFERENCE QUANTITY	PCB ASSY T.G.D. CONTROL NUMBER 10396188 E DESIGNATOR PREFIX 1A5 1 EA				
REF DES	PAKT DESCRIPTION		AUSTRON PART	MFG PAKT	FIC
C1	20 UF 10V 10 CA		608021-0227	CS138C227K	81349
2	00 PF 500V 5 CAP C	MICA	603000-0101	\sim	279
C3	01 UF 50 V 20 CAP C	MIC	601100-0103	5010	50
C4	01 UF 50 V 20 CAP (MIC	601100-0103	5010	
C5	.01 UF 50 V 20 CAP CERAMIC	MIC	601100-0103	5010	15
90	01 UF 50 V 20 CAP CER	MIC	10	CY15C103M	
C.7	01 UF 50 V 20 CAP	MIC	1100-010	CY15C103K	
CR1	5 PRV 010	SIG	7011N914	1N914	(1)
٦ ا	LATFORM 16		551016-0013	616-461	-
P2	TFORM 16 DIP		551016-0013	616-461	91506
5	31W 10+92 XSTR		7022N3904	2N3904	8
0.2	T0-92 XS	VS SH	7022N3904	2n3904	81349
63	31W TO-92 XSTR		90	2N3906	81349
6.4	W CASE 77-1 XSTR		225	2N5193	\rightarrow
0.5	*31W TU-92 XSTR NP		2N390	2N3904	81349
₹	.7 K 1/8W 10 RES FXD	COMP	110-047	172	81349
82	.7 K 1/8W 10 RES FXD	COMP	51110-047	172	_
€ 3	OHM 1/8W 10 KES FXD	COMP	51110-033	RC056F331K	,
۸ ۲	.7 K 1/8W 10 RES FXD	COMP	51110-0		81349
85	.7 K 1/8W 10 RES FXD	COMP	110-041	-	81349
86	.7 K 1/8W 10 RES FXD	СОМР	51110-047	RC056F472K	81349
R7	K 1/8W 10 RES FXD	COMP	651110-0472	RC056F472K	81349
8 X	K 1/8W 10 RES FXD	СОМР	51110-010	RC056F102K	81349
<u>გ</u>	7 K 1/8W 10 RES FXD	COMP	110-047	KC056F473K	33
۲10	/8M 10 RES FXD	COMP	110-047	56F47	81349
k11	.7 K 1/8W 10 RES FXD	COMP	651110-0472	C056F47	81349
k12	50 K 178M 10 KES FXU	COMP	51110-011	05GF15	81349
R13	K 1/8W 10 RES FXD	COMP	1110-010	56	81349
K14	80 OHM 1/8W 10 RES FXL		51110-06	C056F	81349
R15	ES FXD COMP 68	UHM 1/2W 05%	1101-068	C206F68	81349
R16	5 X 1/8% 10 RES FXU	СОмр	1110-	C056F1	81349
Ř17	1/8W 10 RES FXD	Š	40-	56F4	
	.7 K 1/8w 10 RES FXD	СОМР	651110-0472	C056F472	81349

ASSEMBLY PCB ASSY 1.0.D. CONTRUL ASSEMBLY NUMBER 10396188
REFERENCE DESIGNATOR PREFIX 1A5 QUANTITY 1 EA

(CONT)

REF DES	PART DESCRIPTION	AUSTRON PART	MFG PART	51.C
R19	4.7 K 1/8W 10 RES FXD COMP	651110-0472	RC056F472K	81349
R20	4.7 K 1/8W 10 RES FXD COMP	651110-0472	RC056F472K	81349
R21	4.7 K 1/8W 10 RES FXD COMP	651110-0472	RC056F472K	81349
U11	IC QUADR 2-INP NAND GATF	703SN54LS00J	SN54LS00J	01295
012	IC QUADR 2 TO 1 LINE DATA SEL/MUX	7035N54L5157	SN54LS157J	01295
013	IC QUADR 2-INP NAND GATE	7035N54LS00J	SN54LS00J	01295
021	IC QUADE 2-INP NAND GATE	7035N54L500J	SN54LS00J	01295
U2 <i>2</i>	IC DUAL D-TYPE FLIP-FLUP	7035N54LS74J	SN54LS74J	01295
023	I.C.	7035N54L123		

- Common of the	MANUAL		T MODEL	1210		~ ~				1	- 48		
ASSEMBLY PLATE ASSY (A6) ASSEMBLY NUMBER 11096211 REFERENCE DESIGNATOR PRE	PLATE ANUMBER DESIGNA	PLATE ASSY (A6) UMBER 11096211 DESIGNATOR PREFIX 1A6 1 EA	X 1A6										
REF DES	PART DE	PART DESCRIPTION				AUSTI	AUSTRON PART	ıRT	W.	MFG PART		11	FIC
CR1 CR2 CR3 J1 Ø1 R1	2600 UF 50 500VR 12A 500VR 12A DIGDE HUT C CUNNECTUR 5 TRANSISTUR 20 UHM 10	√ ≪ ○ ○	OV 10 CAP FLECTRUL DIO S REC DIO S REC CARRIER 5 PIN W/ LOCK CLIP W 3 RES FXD W.W.	FLECTRULYTIC 110 S REC 00-4 110 S REC 00-4 LOCK CLIP FXD W.W.	Y11C D0-4 D0-4	701M 701M 7011F 5513C 702M 65201	602036-0268 701MK1125 701MK1125 7011N5829 551300-0007 702MJE3055 652011-0200	8 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	36D262G050AB2A MR1125 MR1125 1N5829 126-216 MJE3055 RE65G20R0 RE65G20R0	050AB2A 050AB2A 050AB2A		300001088	56289 04713 04713 04713 13511 04713 81349
11 XT1	TRANSFORMER SUCKET:	4	3	ER SULDER TYPE	FYPE	12097247 551024-0	12097247 551024-0013	κή	77-M1P-8	æ		246	24672 74868

ASSEMBLY PC BOARD ASSY-BRIDGE ASSEMBLY NUMBER 10394664 REFERENCE DESIGNATOR PREFIX 1A7 QUANTITY I EA

FIC	03508	03508	03508
MFG PART	A15A	Alsa	A15A
AUSTRUN PART	701A15A	701A15A	TOLAISA
	DIO S RECT	DIO S RECT	DIG S RECT
DESCRIPTION	5.A.	5.A	5A
PART	100VR	100VR	100VR
REF DES	CR1	CR3	CR4

Samuel Comment		-	24 OCT 84	
ASSEMBLY ASSEMBLY REFERENCE QUANTITY	BATTERY PACK ASSY NUMBER 12096212 E DESIGNATUR PREFIX 1A8 1 EA			
REF DES	PART DESCRIPTION	AUSTRON PART	MFG PART	FIC
67.1	٧ .	570900-0015	7.USCL	31741
BT2	'* NICAD 1.25V 7.	5100-006015	7.0SCL	
BT3	'+ NICAD 1.25V 7.5AMP.	570900-0015		31741
BT4	'* NICAD 1.25V 7.5AMP.	570900-0015	\Box	31741
ឆ្នារីទ	', NICAD 1.25V 7.	570900-0015	$\overline{}$	31741
BT6	'+ NICAD 1.25V 7.	570900-0015	7.0SCL	31741
817	' NICAD 1.25V 7.	570900-0015	35	~
818	' NICAD 1.25V 7.	570900-0015	7.USCL	31741
\sim	' NICAD 1.25V 7.	570900-0015	7.05CL	31741
_	. NICAD 1.25V 7.	570900-0015	7.0SCL	31741
Ŕ111	' NICAD 1.25V 7.	570900-0015	7.0SCL	31741
	BATIERY, NICAD 1.25V 7.5AMP.HR.	570900-0015	7.0SCL	31741
MP]	COVER HATTERY. BOTTOM	00796732		46
AP.2	COVER PATTERY, TOP	~		24672
MP3	SPACER BATTERY BUX			24672
₩ D 4	SPACER BATTERY BOX	_		24672
アドン	OR, BATTERY,	_		24672
m.P.6	UR, BATTERY,	02096215		467
MP7	UR, HATTERY,	Ξ		24672
& & &	OR, BATTERY,	02096216		24672
300	OR. BATTERY, NAR	02096216		24672
MP10	INSULATOR, BATTERY, NARROW	20862		24672
P.1	CONN+ 5 PIN	551300-0013	126-223	74868

ASSEMBLY XILUSC, 1150 (70-85 DEG) ASSEMBLY NUMBER 30296818 REFERENCE DESIGNATOR PREFIX 1A9 OUANTITY 1 EA

REF DES

PART DESCRIPTION

THIS OSCILLATOR IS A SEALED UNIT RETURN TO FACTORY FOR REPAIR

AUSTRUN PART

MFG PART

FIC

enement Services		 PARTS	1	HODE	 - 1.2	100			-	water		184	-
ASSEMBLY PANEL ASSY, REAK ASSEMBLY NUMBER 10996195 REFERENCE DESIGNATOR PREFIX 1 QUANTITY 1 EA	PANEL ASSY, REAK NUMBER 10996195 E DESIGNATOR PREFI 1 EA	.SSY, R 099619 TOR PR	EAK 5 EFIX	1 A 10	-								
REF DES	PART DESCRIPTION	SCRIPT	ION					AUSTRON PART	DART	MFG	G PART		FIC
F1		346	1 4				вго	552002-0010	-0010	313001			75915
F.2		34G	٦ ۲			SLO	8L 0	552002	-0010	313001			75915
F3	FUSE 3	346 2	2.5 AI	AMP	1257	SLO	BLO	552001.	-0026	31302.5			75915
JJ	CONN. B	BOX MOUNT		3-PIN		CONTACT	ACT	551102	-0016	MS3102A-14S-1P	-14S-1P		90696
75	CUNNECTOR	OK BNC						551100	-7935	KC79-35			11636
5.1	S#+ TOGGLE SPOT (CEK #7101)	GLE SP	DT (C6K #	1011	_		553010-	.0015	U115Y20			95146
м]	CORD PO	MER BE	LDEN	1723	S-61			570075	-0005	172398			70903
XF1	HOLDER, FUSE	FUSE						507003-2012	-2012	342012			75915
XF2	HOLDER+FUSE	FUSE						507003-2012	.2012	342012			75915
XF3	HULDER, FUSE	FUSE						507003-2012	-2015	342012			75915

T T T		IX 1A11	
PUB ASSY, DC FILTER	10396200	DESIGNATOR PREFIX	
	NUMBER		1 EA
A V V L I I I I I	ASSEMBLY	REFERENCE	QUANTITY

F1C	81349 81349 02114 02114
MFG PART	CS138F475K CS138F475K VK20010/3B VK20010/3B
AUSTRON PART	608017-0475 608017-0475 751102-0000 751102-0000
NOIL	10 CAP TANT 10 CAP TANT CHUKE CHÜKE
PART DESCRIPTION	4.7 UF 35V 4.7 UF 35V WIDEBAND WIDEBAND
REF DES	5353

	MANUAL	1 PARTS	r LIST		d012		-		
ASSEMBLY ASSEMBLY KEFEKENCE QUANTITY	PCB ASSY, I NUMBER 10396 E DESIGNATOR I EA	ASSV. INTER R 10396769 SNATOR PREF	NTEK(769 PREF I	EKCDNNECT 9 EFIX 1A12					
REF DES	PART DE	ESCRIPT	NOI I	7			AUSTRUN PART	MFG PART	FIC
C1	10F	2067 10	_	CAP MYLAR			604192-0027	192910492	56289
. 23	AP C	RA AXL	X7R	•	1000	10%	205-	CK128X103K	-
CR1	α			D10 S S1			7011N914	1N914	111
5	3	10-92		XSTR NPNS	SH		7022N3904	2v3904	****
0.5	3	10-92		XSTR NPNS	S		7022N3904	2N3904	\exists
R1	RES FXD	XD CUMP	^	_		_	651102-047	RC076F471K	81349
R2	u.	D COMF	^	4 · 4	`		651102-0	RC076F472K	-
e E	u.	o comp	•	4.7 K	1/41	~	651102-047	KC07GF472K	(1)
44	i.L	COMF	•		`		651102-047	RC076F472K	(2)
なぶ	ı.	D COMF	^		11/4W	7.0	651102-0	RC07GF470K	(1)
K 6	\mathbf{x}	1/8k	-	RES FXD FI	ΣΞ		53001-3	C74301K1%	40
K7	缸	D COMF	٥	10 OH	_		65	KC07GF100K	<u> </u>
КЯ	iL.	D COMF	•	10 UHW 1			651102	RC076F100K	81349
R9	ii.	O COMF	•	10 OHM	~	10%	651102-0	RC07GF100K	8
R10	ũ.	Ü COMF	_	10 OHM	_		651102	RC076F100K	8
_	Œ.	O CUMF		56 OHM	_		651102	RC076F560K	81349
_	•	Spin C	DUAL	KEADUUT	Od d	TAB	551005	-451	Ţ
_	•	SPIN C	DUAL	READOUT	PC	TAB	551009	-45]	1,
	•	SPIN 0	DUAL	READOUT	م ک	TΔB	551009	00-6007-044-451-0018	17
,	•	SPIN D	DOAL	READOUT	ΡC	IAb	551009	00-6007-044-451-0018	11769
_		Z	DUAL	KEADOUT	D d	TAB	551009	00-6007-044-451-0018	11769
		Z	TVIN	READOUT	PC	TAB	551009-	5]	11769
XA19		22PIN D	DUAL	. READOUT	PC	TAB	551009	6007-044-451-	11769
\sim		z	DUAL	KEADOUT		<	51009	-6007-644-451	~
n.	CUNN 2	_	UAI.	KEADOUT	PC		551009-0008	00-6007-044-451-0018	-

ASSEMBLY PC BOARD ASSY-PULSE AMPLIFIER
ASSEMBLY NUMBER 10394595
RFFERENCE DESIGNATOR PREFIX 1A13
QUANTITY I EA

QUANTITY	I E	A) 				
REF DES	PART	DESC	SCRIPTION	z O			AUSTRON PART	MFG PART	FIC
C1	330	UF	-	O CAP	TANT		608013-0337	CS1388337K	81349
C2	•01			O C	CERAMI	()	010	16-510	929
C3	•01			Ç	CERAMI	()	1004-010	TG-S10	~~
C4	10	L.		0	CERAMI	<i>c</i> 1	1004-010	16-510	628
C5		UF.		O CAF	CERAMÍ	. >	1004-010	TG-S10	528
93	1000	PF 1	000	AΡ	DIP MIC			CM05FA102J03	34
C7	470	ш,	00V 5	a	MIC.			9	134
CB CB	470	44.1	000	A P	ت	٨	3000-047	CM05FD471J03	13
63	.01			=	CERAMI	. 1		16-510	528
010	22	(4)	57 11	AP				CS138F226K	134
C11	1000	PF 1		Aρ	4 I C			CM05FA102J03	134
C12	014		500V 5	0	DIP MICA		603000-0471	CM05FD471J03	34
C13	470		æ	CAP	4 I C		603000-0411	9	134
C14	•01		>	CAF	CERAMI		601004-0103	TG-S10	528
C15	22			CAP	TANT		608017-0226	CS138F226K	134
616	1000		001	a	٩IC		603000-0102	9	134
C17	470		5007 5	AΡ	DIP MICA		603000-0411	CMG5FD471J03	134
C18	470	PF 5	S	CAP	١IC		603000-0411	9	134
C19	.01	UF	>	CAF	CERAMI	, ,	601004-0103	16-510	528
C20	22	UF 3		44			608017-0226	CS138F226K	134
C21	1000	PF 1	_	AF	#F		603000-0102	9	134
C22	470	ш	5007 5	~	DIP MICA		603000-0471	CM05F0471J03	134
C23	470	PF 5	S.	ب	<u>~</u>		-047	2	134
C24	•01	UF	1007		CERAM		\odot	TG-S10	328
C25		UF 3	5V 10	CAP	ANT		2	CS13BF226K	34
CRI		>		[.]	0 S S		7011N914	11011	34
CR2	\mathbf{x}	500mm	MW 10	<u> </u>	N32 S 0	2-00	7011N5235A	IN5235A	08288
CR3	5 P	>		OI	0 8 8		7011N914	1N914	34
CR4	30	300k	0 M) ÜI	7 5 0	2- 00	1N52	IN5235A	28
CRS	75 PR	>		<u> </u>	S		11N91	4[6N]	134
CR6	æ	\$00₩	30 ₩₩	10 (2 S	00 - 7	11N52	52	828
CR7	۵.	>		10	S		110914	$\vec{}$	134
CR8	6.8 <	S00MW	≥ 1C	io (0 S ZEN	2-90	7011N5235A	1N5235A	∞

		TET MODEL	 	010		 	24 OCT 84	NAME OF THE PROPERTY OF THE PR
ASSEMBLY ASSEMBLY REFERENCE QUANTITY	PC BUARD ASSY NUMBER 1039459 DESIGNATUR PR I EA	PULSE FIX 1A	AMPLIFI 13	₩ ₩		(CONT)		
REF DES	PART DESCRIPTION	S. C.				AUSTRON PART	MFG PART	FIC
<u>, , , , , , , , , , , , , , , , , , , </u>	WIDEBAND	CHOKE				751102-0000	VK20010/38	02114
0.1	1W T0-9	-	NPNS	SH		22N	904	134
0.2	.31W TU-	Ŝ	NPNS	SH		N	2N3904	34
63	.31W T0-9	XSTR	SNGN	SH		22N3	2N3904	134
5 0	.31W TO	SI	SUJZ	SH		022 _{N3}	2N3904	30
65	•31w T0	S	NPNS	SH		22N390	2N3904	81349
90	3 TE .	XSTR	SNAN	SH		22N	2N3904	4
0.1	•31× TC	Sī	SNGZ			022N3	2N3904	134
80	.31w TÜ	S	NPNS			22N3	2N3904	134
60	.31w TO	SI	いといる	SH		22N3	2N3904	134
_	•31M TO	S	SZGZ			(74	2N3904	81349
(, ,	.31w TO	S	SNdN			55N390	2n3904	134
012	.31w TO	STR	SNGN	SH		22N3904	_4	134
Rl	ES FXD	٠	≻	`	3€	1102-047	4	134
۲ 2	ES FXD	22	×	M+ /	%0	1102-022	\sim	13
m ×	ES F		×	3		1102-010	\mathcal{L}	134
K 4	ES FXD	6.8		14E	% 0	1102-068	32	34
RS	ES FXD	1.5		/ 4 w	% 0	1102-015	5	34
R6	ES FXD		¥	Z + /	%	1102-010	9	
κ ₇	ES FXD	22				2-052	(C)	34
	ES FXD	ε.		7.4%		1102-033	<u>~</u>	34
o,	() X	4.7	×	* + *	- 39 01	02-047	0200	134
. بـ	ES FXU	22	¥	Z + \		51102-022	725	134
, ہے	ES FXD	-	**	₹	36	51102-010	C076F10	81349
	ES EXU	8.9	~ ~		9	51102-068	76F68	81349
,	ES FXD	1.5	_		% C	51102-015	C076F15	81349
-	ES FXD		¥	₹ †	* C	51102-010	C076F10	81349
	ES FXD C	25			₩	51102-022	C076F223	81349
	S FXD	£.6		1/4w	340	51102-033	C076F332	81349
	ES FXD COM			1/4W]	₩	1102-047	C076F472	81349
	S FXD COM	22	¥	1/4W	عو	51102-02	C076F223	81349
R19	ES FXD		¥]/4w]	3¢	1102-010	076F1	81349
N	ES FXD COM	8.9	_	1/4m]	3.01 3.01	1102-068	C076F682	81349

(CON) ASSEMBLY PC BOARD ASSY-PULSE AMPLIFIER ASSEMBLY NUMBER 10394595
REFERENCE DESIGNATUR PREFIX 1A13
QUANTITY 1 EA

REF DES	DES	PAR	I DE	PARI DESCRIPTION	NOI					AUSTRUN PART	ART	MFG PART	FIC
R23		RES			Ţ	ič.	¥	1/4×	10%	651102-015	52	RC076F152K	81349
R22		RES			, 	- -	¥	1/4W	201	651102-0102	02	RC076F102K	81349
R23		RES			~		¥	3/4W	10%	651102-0223	23	RC076F223K	81349
R24		KES	FXÜ	COMP	m	3,3	¥	1/4%	10%	651102-0332	32	RC076F332K	81349
R25		RES			4		¥	1/4W	301	651102-0472	72	RC076F472K	81349
R26		RES			2		¥	1/4W	201	651102-0223	23	RC076F223K	81349
K27		KES			,i		¥	1/4w	10%	651102-0102	0.2	RC076F102K	81349
¥28		RES			9		¥	1/4W	10%	651102-0682	82	RC076F682K	81349
R29		KES			1	1.5	×	1/42	701	651102-0152	52	RC076F152K	81349
R30		RES	1			_	¥	3/4W	¥01	651102-0102	0.2	RC076F102K	81349
R31		RES	FXC	COMP	rr)	6	¥	1/4W	10%	651102-0332	32	RC076F332K	81349
R32		KES	FXD	COMP	2	22	¥	1/4W	%01	651102-0223	23	RC076F223K	81349
<u>.</u>		ر اد	MONO!		MULTIV	18R	ATOR			7035N54121J	1,1	SN54121J	01295
70		IC	40NO	MUNUSTABLE	MULTIVIBRATOR	188	ATOR			7035N54121J	17	SN54121J	01295
03		ľC	KONO	MÜNUSTABLE	MOLTIV	IHK	ATOR			7035N54121J	ľJ	SN54121J	01295
40		C	MOND	MONDSTABLE	MULTIV	15%	IVIERATOR			703SN54121J	1,1	SN54121J	01295

 	MANUAL PARTS LIST MODEL 1210D	PART	_ S L.1	ST MC	DEL	121(_ <u>ac</u>	-	~~~		<u>-</u>		 	- 48			
ASSEMBLY PC BOARD ASSY, DIVIDER ASSEMBLY NUMBER 10396614-1 REFERENCE DESIGNATOR PREFIX 1A14 QUANIITY I EA	PC BOARD ASSY, DIVIDER NUMBER 10396614-1 EDESIGNATOR PREFIX 1A14 1 EA	RD AS 10396 ATOR	5Y, 614- PREF	DIVIC 1 IX IA)ER												
REF DES	PART DESCRIPTION	ESCR I	PTIU	Ž					AUSTRON PART	N PART		MFG	PART		FIC	()	
CJ	330 UI	UF 6V		10 CAP TANT	TANT				608013-033	-0337	CS13	HB3371	¥		81349	6+	
C2	CAP CE	CERA AXL	L X7R	· ~	0	UF]	1001	10%	601205-0103	-0103	CK12	CK128X103K	¥		81349	6.	
C3	CAP CERA	RA AX	1		01		1001	10%	601205-0103	-0103	CK12	8X103	¥		81349	o,	
C4	CAP CERA	RA AXL			.01	GF.	1001	707	601205-0103	-0103	CK12	CK128X103K	¥		81349	6,	
C5	CAP CERA	RA AXI	L X7R		.01		1001	10%	601205-0103	-0103	CK12	CK12BX103K	¥		81349	6	
<u>بــ</u>	WIDEBAND	ON		CHUKE	ш				751102-0000	-0000	VK20	VK20010/3B	90		02114	14	
nı	IC DUAL	L DECADE	ADE	COUNTER	FR				703SN54LS390	4LS390	SN54	SN54LS390J	- 0		01295	35	
0.2	IC DUAL	L DECADE		COUNTER	<u>ب</u> 1				7035N54L5390	4LS390	SN54	SN54LS390J	7		01295	35	40
U3	IC DUAL		DECADE	COUNTER	ER				7035N54LS390	4LS390	SN54	SN54LS390J	-		01295	5	
04	IC DUAL		ADE	DECADE COUNTER	E.R.				703SN54L5390	4LS390	SN54	SN54LS390J	7		01295	35	
05	IC DIV.	DIVIDE BY TWELVE COUNTER	<u>×</u>	ELVE	COUN	TER			703SN54LS92	4LS92J	SN541	N54LS92J			01295	ŭ	_

ASSEMBLY PCB ASSY, TIME SYNC ASSEMBLY NUMMER 10394618 REFERENCE DESIGNATOR PREFIX IA15 QUANTITY 1 EA

REF DES	PART	DESCRIPTION	110 _N			AUSTRUN PART	MFG PART	FIC
C1	0	4	10 CAP TA	L		801	S138	20
C.2	_	UF 100V	20 CAP C	ERAMI		1004-010	510	56289
63	_	7	20 CAP C	ERAMI		4-010	G-S	\tilde{z}
£.4	_	_	20 CAP C	ERAMI		601004-0103	9	Ž
C5	-4		20 CAP C	ERAMI		004-010	5	Ž
92		~	20 CAP C	ERAMI		601004-0103	T6-510	Ň
<u>C7</u>	.01	щ. П	20 CAP C	ERAM		601004-0103		Ň
ر د	_	, 	20 CAP C	ERAMI		01004-	2	2
6 <u>)</u>	3		20 CAP C	ERAMI		01004-010	16-510	N
ָראַן:	a a	>	010	S 516		7011N914	1N914	3
	IDEB		Ī			51102-0	0	7
<u>ن</u> ا	.3	10-92		PS		022N390	2N3906	34
20	Ę.		STR	SZ		022N3	2N3904	34
63	. 3 L	ċ	STR	Z	I	022N390	2N3904	34
R 1	u. Vi	_	٠	~	Z T	* 651102-047	GF472	34
#2 -	الله الا	_	1.4	7	4W 10	\$ 651102-047	GF472	34
න ය	S	_	•	7	4w 10	% 651102-047	GF472	34
7 ×	S	_	4.7	_	4₩ 10	\$ 651102-047	GF472	3.4
œ «	S	•	7		4W 10	x 651102-010	GF102	2
R6	S	_	4.7		4× 10	x 651102-047	GF472	. W
K7	S.	_	10	, ~	4M 10	x 651102-010	GF103	34
88	S	_	4.7		4W 10	x 651102-047	GF472	3.
~	S	_	٠	-	4W 10	x 651102-047	GF472	34
RIO	S.	XD COMP		/ ×	4W 10	£ 651	6F1	34
	S.	_	-		4₩ 10	8 651102-010	GF102	34
	S.	·	,(4× 10	651102-010	GF102	4
	S	•	~		4× 10	51102-010	GF 102	134
	خة ح	_			4w 10	651102-010	GF102	134
	S.	0	•		0 t ×	651102-047	65472	134
	S	Ŷ	4.7		0T <u>*</u>	102-047	GF472	134
_	i.	-	•		01 *	651102-047	GF472	134
_	S	O	4.7		10	102-047	C076F472	134
	SF	O		,	01	651102-047	07664	134

			1210b 24 OCT 84	
ASSEMBLY ASSEMBLY T REFERENCE QUANTITY	ASSEMBLY PCB ASSY, TIME SYNC ASSEMBLY NUMBER 10394618 REFERENCE DESIGNATOR PREFIX 1A15 QUANTITY 1 EA	(CONT)		
REF DES	PART DESCRIPTION	AUSTROM PART	MFG PART	FIC
n 1		703SN5473J	SN5473J	01295
7.5	IC DUAL J-K FLIP-FLOP W/CLEAR	703SN5473J	SN5473J	01295
63	IC QUADR 2-INP NAND GATE	703SN5400J	SN5400J	01295
174	IC DUAL D-TYPE FLIP-FLOP	703SN5474J	SN5474J	01295
05	IC GUADR 2-INP NAND GATE W/OCOLL OT	703SN5403J	SN5403J	01295

ASSEMBLY PC BOARD ASSY. +5 VDC REGULATUR ASSEMBLY NUMBER 10394607 REFERENCE DESIGNATOR PREFIX IA16 QUANTITY I EA

REF DES	PART DESCRIPTION		AUSTRON PART	MFG PART	FIC
C.1	100 UF 20V 10	CAP TANT	608016-0107	CS13BE107K	81349
C 5	UF 100V 20) CAP CERAMIC	601004-0103	TG-S10	56289
C3	UF 6V 10	CAP TANT	608013-0337	CS1388337K	81349
CR1	5.6 V 1 W 10	010 S ZEN 59	7011N4734	1N4734	08288
CR2	75 PRV	010 \$ 816	7011N914	1M914	81349
CR3	50 VR 3A	DIO S RECT 60	701MR830	MR830	04713
L.1	WIDEBAND	CHUKE	751102-0000	VK20010/38	02114
1.2	TRANSFURMER		751310-2103	TM-2103	24765
01	₩ T0-92	KSTR PNPS SH	7022N39U6	2N3906	81349
20	0.31W TO-92	XSTR NPNS SH	7022N3904	2N3904	81349
63	~	(STR PNPS SH	7022N3906	2N3906	81349
40	ASE 77-1	KSTR NPNS AP	7022N5192	2N5192	81349
K.	RES FXD COMP	2.2 K 1/4W 1(10% 651102-0222	RC076F222K	81349
۲ 2	SELECTED1/4W 10 H	KES FXD COMP	651102-SEL		
£3	RES FXD COMP	4.7 K 1/4% 1(10% 651102-0472	RC076F472K	81349
4	ES FXD	15 K 1/4W 10	10% 651102-0153	RC076F153K	81349
み		100 OHM 1/4W 10	10% 651102-0101	RC076F101K	81349
8 6	RES FXD COMP	1 K 1/2W 10	10% 651103-0102	RC20GF102K	81349
×7	RES FXD COMP	0 OHM 1/4W	10% 651102-0681	RC076F681K	81349
88	RES FXU COMP	OHM 1/4W	10% 651102-0100	RC076F100K	81349
8 8	RES FXD COMP	47 OHM 1/4W 10	10% 651102-0470	RC076F470K	81349

	MANUAL PARTS LIST]	24 OCT 84	
ASSEMBLY ASSEMBLY N REFERENCE QUANTITY	PCB ASSY• +15 VDC NUMBER 10396197 : DESIGNATOR PREFIX I EA	C REGULATOR X 1A17			
REF DES	PART DESCRIPTION		AUSTRON PART	MFG PART	FIC
CI	UF 50V 10	ELEC	602039-0035	39D107G050EJ4	56289
C2	UF 50V 10	FLEC	602039-0035	39D107G050EJ4	56289
C3	UF 50V 10 C	CAP ELEC MIN	602039-0035	39D1076050EJ4	56289
[]	WIDEBAND	CHOKE	751102-0000	VK20010/38	02114
	TRANSISTOR DUAL		702MD8002	MD8002	04713
	10-92	XSTR PNPS SH	7022N3906	2N3906	81349
	RES FXD COMP	X 1/48	10% 651102-0562	RC076F562K	81349
	RES FXD COMP	X 1/4%	10% 651102-0123	RC076F123K	81349
₽3	RES FXD COMP			RC076F562K	81349
	9	OHM 1/4W		RC076F561K	81349
	<u>_</u>	OHM 1/4W	10% 651102-0560	RC07GF560K	81349
	9		10% 651102-0102	RC07GF102K	81349
	O COMP	470 UHM 1/4W	10% 651102-0471	RC076F471K	81349
œ	3/4W 10	RES VAR CERMET	659015-0102	89WR1K	73138
£3	لللا		10% 651102-0471	RC076F471K	81349
VR]	5.1 v 1 w 10	DIO S ZEN 59	7011N4733	1N4733	08288

ASSEMBLY PCB ASSY, SINE CUNVERTER ASSEMBLY NUMBER 10396025-1 KEFERENCE DESIGNATOR PREFIX 1A18 QUANTITY 1 EA

REF DES	PART DESCRIPTION	AUSTRON PART	MFG PART	FIC
Č1	P CERA AXL XIR	x 601205-0103	CK12BX103K	81349
C2	3 UF 10V 10 CAP TANT	608014-0336	CS138C336K	81349
C3	AP CERA AXL X7R .01 UF 100V	% 601205-0103	CK128X103K	81349
C4	CERA AXL X7R		CK12BX103K	81349
65	OT USED			
Ç6	PF 500V 5	603000-0471	CM05F0471J03	81349
C.7	UF 35V 10	608017-0105	CS138F105K	81349
CB	5000 5	603000-0332	CM06FU332J03	81349
63	UF 35V 10 CAP	608017-0105	CS138F105K	81349
L.1	IDEBAND	751102-0000	VK20010/38	02114
01	31W TO-92 XSTR NPNS	7022N3904	2N3904	81349
0.5	.31W TU-92 XSTR NPNS	7022N3904	2n3904	81349
63	NA TOTOS XSIR NONS	7022N3904	2N3904	81349
R.1				
K2	FXD COMP 10 K 1/4W	~	RC076F103K	81349
R3	5 FXD COMP 4.7 K 1/4W	•	RC076F472K	81349
R4	S FXD C	•	RC076F103K	81349
π. 20	5 FXD COMP 2.2 K 1/4W	x 651102-0222	RC076F222K	81349
R6	_			
R7	-			
X &	T USEO			
R9	S FXD COMP 470 OHM 1/4W		RC07GF471K	81349
$\overline{}$	S FXD COMP 47 OHM 1/4W		RC076F470K	81349
RII	S FXD COMP 100 OHM 1		RC07GF101K	81349
	S FXU CGMP 100 DHM 1/4W		RC07GF101K	81349
_	ES FXU COMP 47 OHM 1/4W		RC076F470K	81349
_	UHM 1/8W	651110-0470	RC05GF470K	81349
T.1	ORMER, 1	75195770		24612
10	QUADR 2	703SN54LS00J	SN54LS00J	01295
77	DECADE	7035N54LS90J	SN54LS90J	01295
		703MC78M05CG	MC78M05C6	04713

	(1	1 1	24 OCT 84) ! !
ASSEMBLY ASSEMBLY REFERENCE QUANTITY	r PCB ASSY OUTPUT A F NUMBER 10396031-1 CE DESIGNATOR PREFIX F 1 EA	AMPLIFIER X 1A19			
REF DES	PART DESCRIPTION		AUSTRON PART	MFG PART	FIC
55	USED	2	7 P		; ;
<u>ر</u> ان د	470 PF 500V 5	CAP TANT	603000-0471	CMOSFD4 / 1 JOS (<1 38 F 1 0 5 K	81349
40	PF 500V 5		603000+0471	CMOSF0471J03	81349
C 50	USED	; ;	: :		
م د	500√ 5 26 × 20	CAP DIP MICA	603000-0471	CM05FD471J03	81349
ي ر	<u>י</u> ע	CAF LANI	\$010 - 030807	COLSBF105K	81349
6 5 5	USED		2660=000600	CHOOLEGISTAGS	~+
C10	PF 500V 5		603000-0471	Ç	~
C11	UF 35V 10	CAP TANT	608017-0105	¥	81349
C12	īζ	CAP DIP MICA	603000-0332	CM06FD332J03	81349
L1	HO	CHOKE	751104-0471	4-7	90696
L2	HN	CHOKE	751104-0222	1	90696
L3	I	HOKE	751104-0222	7-5	90696
01	10-92	STR NPNS	7022N3904	2N3904	81349
42	10-92	STR PNPS	7022N3906	2N3906	81349
63	10-65	STR NPNS	7022n3904	2N3904	81349
⊅	T0-92	STR PNPS	7022N3906	2N3906	81349
0 Z C	0.31% TO=92	ACTA DADA ON	7022N3904	2N3904	81349
R.1	COMP	M4/1 MH0 74	651102-0470	RC076F470K	81349
R2	9	0 OHM 1/4%		RC076F471K	81349
R3	RES FXD COMP	ۍ ح		RC076F153K	81349
Κ 4	9	₹ 7		RC076F152K	81349
ኢ	FXD	1 K 1/4w 10%		RC076F102K	81349
¥6	FXD COMP	220 OHM 1/4W		RC076F221K	81349
×~	ECTED1/4W 10		651105		
88	FXU	**************************************	651102	RC076F470K	81349
ک و	ES FXD C	¥ † /	651102-0	RC07GF680K	81349
K10	ES FXD C	UHM 1/4W	651102	RC076F470K	81349
R11	RES FXD COMP	/T WH	65110	RC076F471K	к1349
R12	ES FXD	K 1/4w	651102	RC07GF153K	81349

ASSEMBLY ASSEMBLY REFERENCE QUANTITY	PCB ASSY OUTPUT AMPLIFIER NUMBER 10396031-1 E DESIGNATOR PREFIX 1A19 1 EA	(CON1)		
REF DES	PART DESCRIPTION	AUSTRON PART	MFG PART	FIC
_	COMP 1.5 K 1/4w	1x 651102-0152	RC07GF152K	81349
	COMP 1 K	3 651102-010S	RC076F102K	81349
_	COMP 220 UHM 1/4W	1x 651102-0221	RC076F221K	81349
R16	D1/4W 10 RES FXD COM	651102-SEL		
-	CUMP 68 0HM 1/	3€	RC07GF680K	81349
$\overline{}$	COMP 47 OHM 1/4W	3.6	RC076F470K	81349
Ţ	NUT USED			
3	COMP 470 OHM 1/4W		RC076F471K	81349
\sim	COMP 15 K 1/4W	8 651102-0153	RC07GF153K	81349
\sim	COMP 1.5 K 1/4W	_	RC07GF152K	81349
7	COMP 1 K	_	RC076F102K	81349
\sim	COMP 220 OHM	۰ę	RC076F221K	81349
\sim	01/4W 10 RES FXD COM	651102-SEL		
~	COMP 68 DHM	— عود	RC076F680K	81349
~	COMP 150 OHM	_	RC07GF151K	81349
2	COMP 150 OHM	_	RC07GF151K	81349
	, 5MHZ OUTPU	75195768		24672
	_	75195770		24672
	INHZ OUTPUT	75195770		24672
∔ >	000000	752B5000000	4051122-C	74306
	ZHW 000000'	752A1000000	4051122	74306
	ZHM 0000000	752A1000000	4051122	74306

7.0 OPTIONAL ASSEMBLIES

- 7.1 SCOPE OF SECTION
- 7.1.1 This section lists the special options available and describes the changes to the AUSTRON Model 1210D Portable Crystal Clock for each of these options.
 - 7.2 LIST OF OPTIONAL ASSEMBLIES
- 7.2.1 The following is a list of the different options available, and a description of each:

Model Number	Description
1210D-01	10 MHz, 5 MHz, 1 MHz and 100 kHz sinusoidal outputs; 1 PPS
1210D-02	pulse output. 5 MHz, 1 MHz, and 60 Hz

- sinusoidal outputs; 1 PPS and 1 PP10S pulse outputs.
- 1210D-03

 10 MHz, 5 MHz, and 1 MHz
 sinusoidal outputs; 100 PPS,
 1 PPS and 1 PPM pulse outputs.

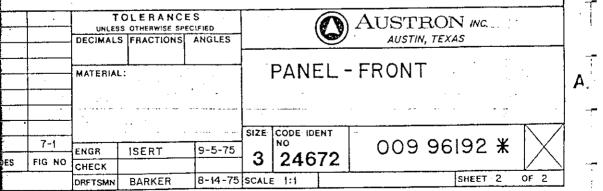
- 7.2.2 Figure 7-1 may be referenced to show the location of the outputs on the front panel. A chart is included to illustrate the descriptive differences between each unit. Notice also that the reference designators for the additional connectors are included in figure 7-1.
- 7.2.3 The following sections discuss the <u>difference</u> between each optional assembly and the previously discussed model 1210D assembly. Chassis circuit diagrams, circuit descriptions, schematics and parts list are included, where applicable, for the variations.

7.3.1 The model 1210D-01 Portable Crystal Clock has 10 MHz, 5 MHz, 1 MHz, and 100 kHz sinusoidal outputs as well as a 1 PPS output. These outputs retain the specifications detailed in section 1.3. This version is the same as the model 1210D with the addition of a 100 kHz Output Amplifier (A20). The Output Amplifier (A19) has been replaced with the 10 MHz, 5 MHz, 1 MHz Output Amplifier (AUSTRON Part Number 10397342). The Sine Converter (A18) has the optional 100 kHz section installed. The Final Filter (A3) has been replaced with one having four sections tuned to the appropriate frequencies.

 		TEVISIONS LTR DESCRIPTION DATE APPO		
		· - REVISIONS		
ZONE	LTR	DESCRIPTION	DATE	GPPA
· ·	Н	REVISED -2 SILKSCREEN PER ECO# 3179	11-14-79	RIB

NOTE: "J" NUMBERS SHOWN IN PARENTHESIS ARE FOR REFERENCE ONLY AND ARE NOT ON THE SILKSCREEN.

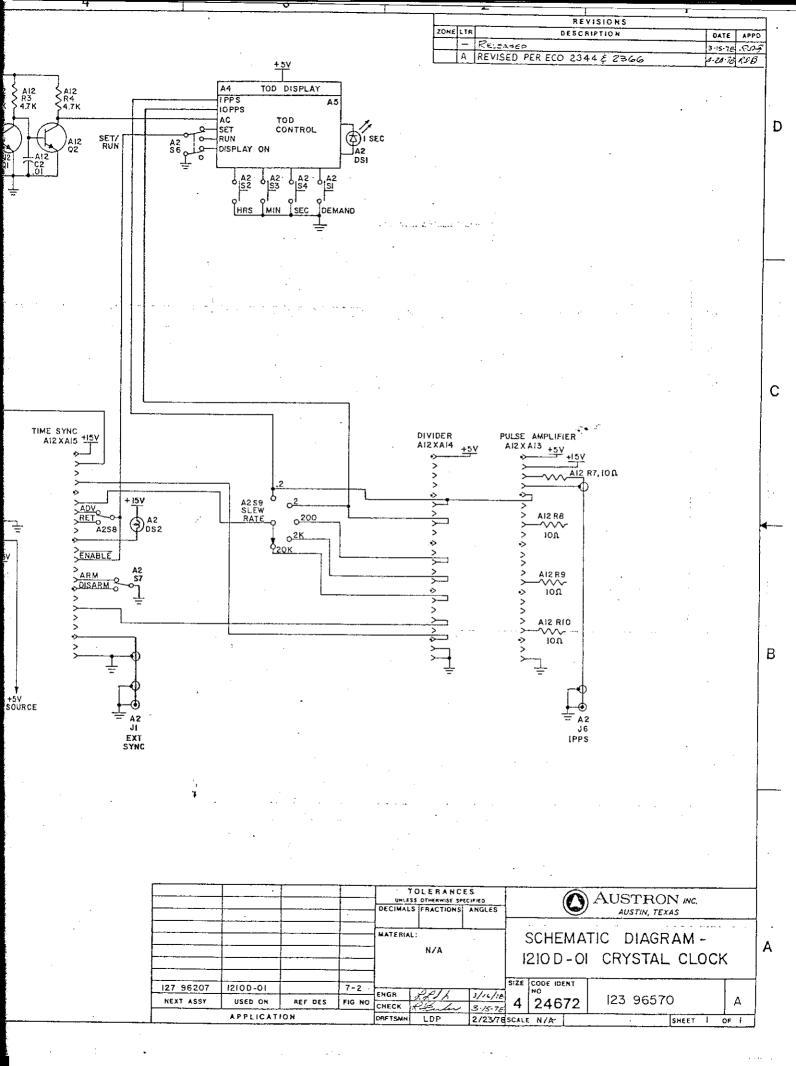
		SILKS	CREE	N	VAR	ATIO	N S		
DASH				POS	ITION		!		ARTWORK
NO.	А	В	С	D	E	F	G	Н	REV STATUS
NONE			(J2) 5MHZ	(J3) IMHZ	(J4) 1MHZ	(J5) IPPS		1210D	С
- 1	_	(J2) IOMHZ	(J3) 5MHZ	(J4) 1 MHZ	(J5) IOOKHZ	(J6) 1PPS	_	12100-01	В
-2		(J2) 5MHZ	(J3) 1MHZ	(J4) 60HZ	(J5) IPPS	(J6) {PP!0S	(J7) 60PPS	12100-02	D
~3	(J2) 10MHZ	(J3) 5MHZ	(J4) 1MHZ	-	(J5) 10.0 PPS	(J6) 1PPS	(J7) IPPM	12100-03	Α

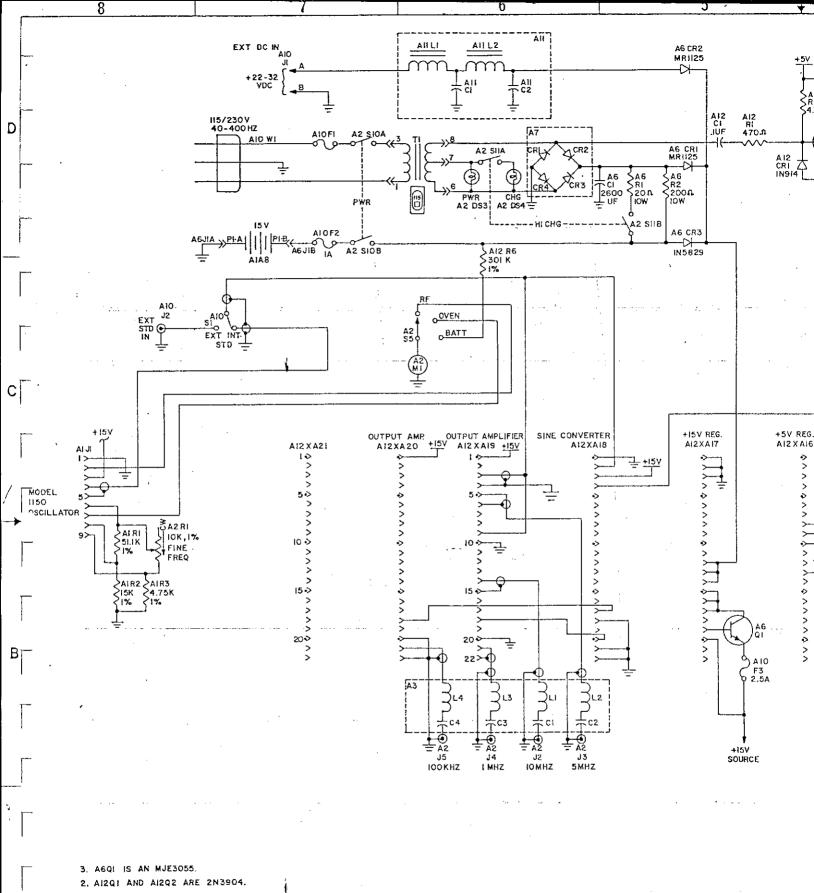


	H
	USTRON WC MODEL CRYSTAL CLOCK
DEMAND	ISEC
	FINE FREQ METER + FUNCTION
0 0	HRS MIN SEC OVEN BATT O SET SLEW RATE US/S 20K
	RUN EXT ADV 200 200 200
PWR_ON	DISARM OUTPUTS
OFF	1.0W - 573 673 673 673 673 673 673 673 673 673 6
	A B C D E F G
	APPLY NOMENCLATURE TO THESE POSITIONS PER CHART A

.

	APPLICAT	ION
NEXT ASSY	USED ON	REF
109 96193 🛠	1210D	IA

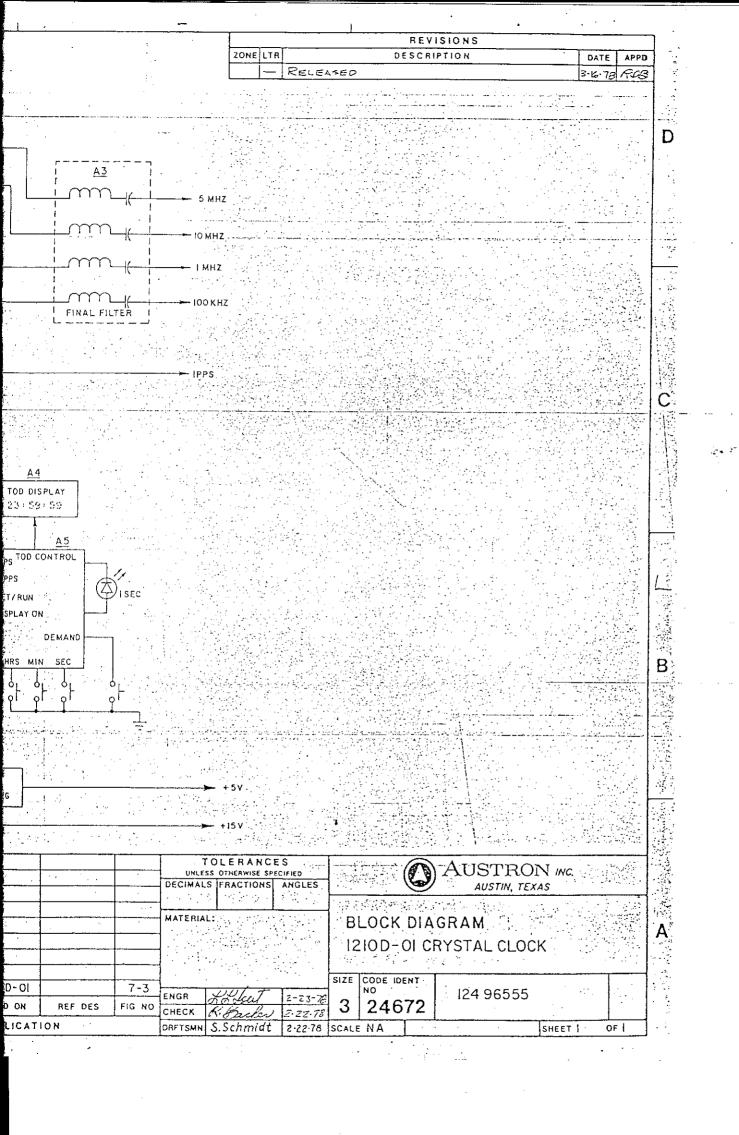


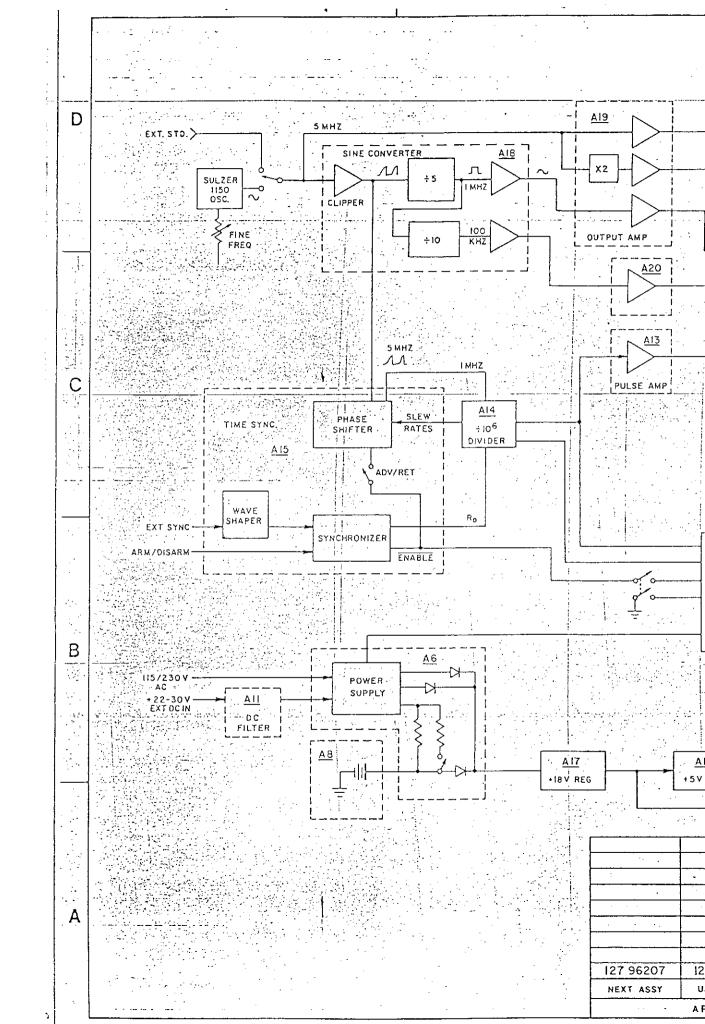


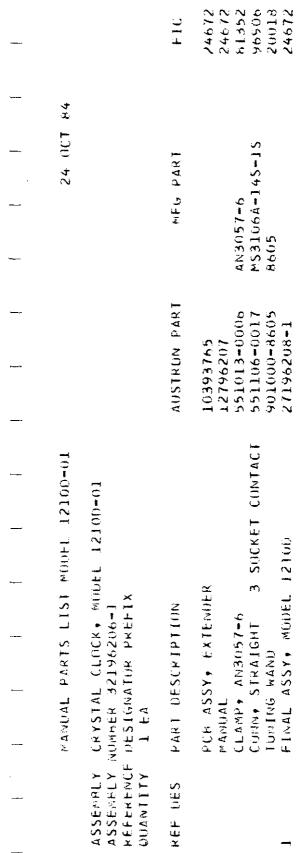
1. ALL RESISTORS 1/4W 10% UNLESS OTHERWISE SPECIFIED.

NOTES:

1







24072 27672 21942 21077 24612 24072 21942 24672 21447 06540 00240 06540 71977 2447 24672 24072 06540 21442 21047 71947 24672 21942 2447 21947 21942 24672 21447 24072 24032 24672 FFG PART 9228-55171-0 9228-55171-0 9228-55171-0 9228-55171-0 AUSTRUN PART 520830-0005 520830-0005 520830-0005 520830-0005 10996193-1 10394595-1 2-60296100 -60296100 01096753 10397355 10396186 00396601 6919660 10396188 11096511 10394664 12096212 80296818 2996195 0396614 10394618 0396025 10394601 10397342 10397359 00794304 14196050 10396197 17096741 02096741 12096741 PC BUARD ASSY-LOOKHZ DUTPUT AMP PC HUARD ASSY. +5 VDC REGULATUR PCH ASSY 10,5,1MHZ UUTPUT AMP PC BUARD ASSY-PULSE AMPLIFIER KOLIND RUUMB ROUND PCH ASSY+ +15 VDC REGULATOR ATLUSC, 1150 (70-85 PEG) PCS ASSY SINE CUNVERTER ASSY I.C.D. CONTRUL PCB ASSY, FINAL FILTER PCH ASSY, INTERCUNNECT SPACER PC BOARD ASSY, DIVIDER SPACER SPACER SPACER ASSY, TUU DISPLAY PC BILAKU ASSY-BRIDGE SYNC PCH ASSY, UC FILTER FINAL ASSY, MUDEL RATTERY PACK ASSY REFERENCE DESIGNATOR PREFIX PAMEL ASSY, FRUNT PANEL ASSY, RFAR PART DESCRIPTION ASSEMBLY NUMBER 27196208-1 COVER-INSTRUMENT COVER-INSTRUMENT PLATE ASSY (46) HAR RECTANGULAR MASHER, PRELUAD MASHER, PRELUAD RASHER. PRELUAD MASHER PRELUAD PCH ASSY, TIME CUVER. FRUNT .171x1/4x1/2 .171X1/4X1/2 5/1X4/1XI/15 5/1x5/1x1/5 V T ASSEWRLY CUANTITY REF DES MP12 (E) div 75 9 9 9 0 [ø 7 1 7 412 A 13 4] 4 8 T V 416 417 ALA 6 T V 024 E d K Ç d ≅ ტ ქ χ. Ο 5 MP6 100 6 V 24 24 24 24 ASA 83 77

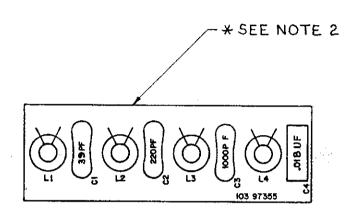
10K4-1501F

520205-0005

SHUCK FUCK! BUSHING

	MANUAL PARTS LIST MODEL 12100-01		54 UCT 84	
ASSEMBLY FIMAL ASSEMBLY NUMBER REFERENCE DESIG QUANTITY I EA	ASSEMBLY FINAL ASSY• MUDEL 1210D ASSEMBLY NUMBER 27196208•1 REFERENCE DESIGNATOR PREFIX 1 QUANTITY 1 EA	(CON)		
REF DES	PART DESCRIPTION	AUSTROM PART	SHO PAKE	FIC
31dw	SHOCK MOUNT HUSHING	520205-60005	1084-15618	14519
MP16	SHOCK MOUNT BUSHING	520205-0005	1084-15018	4144T
2 Tdw	SHOCK MOUNT RUSHING	520205-0005	1084-15018	14519
MP18	SHOCK DRIVE WASHER	520205-0010	10841500	14519
61dm	SHUCK DRIVE MASHER	520205-0010	10841560	14519
MP20	SHUCK URIVE WASHIK	520205-0010	10841500	7451S
MP21	SHOCK DRIVE WASHER	520205-0010	10841500	1,4519
MP22	FUMPER. RUHRER WIMARE	520210-2198	2398	13330
mP23	PUMPER. RUBBER W/MARINARE	520210-2198	219H	83330
P.P.24	FUMPER. RUBBER W/HAKEWARE	520210-2198	21.96	XXXX0
NP25	KUNFER. KUKBEK M/HAKDYAKE	520210-2198	2198	63330

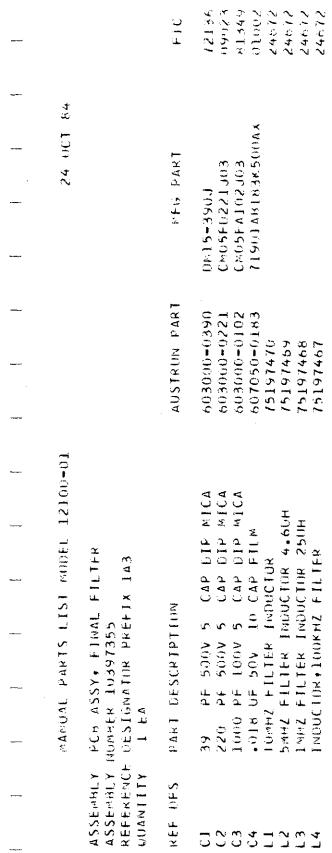
APPLIC	CATION		REVISIONS		
NEXT ASSY	USED ON	LTR	DESCRIPTION	DATE	APPROVED
109 96193-1	12100-01		RELEASED	11-19-76	
10996193-3	1210D-03	A	C1 WAS 22 PF; C4 WAS . OI UF, PER ECO 1866		
		В	ADDED -1 PER ECO # 2328	4-26-78	ROR



- 2.P/N 10397355 AS SHOWN, FOR P/N 10397355-1 OMIT L4 AND C4. (ADD "-1" TO P/N MARKING AS REQD.)
- 1. MOUNT CHOKES WITH NYLON ROD.

NOTES:

- [AUSTRON INC.	AUSTIN, TEXAS
			ENGINEER	PC BOARD ASSY - FINAL FILTER	
_	A3 REF DES	7-4 FIG NO		1 CODE IDENT 1 NO.24672 103 973	55 * B
				SCALE 1:1 SHEE	T OF

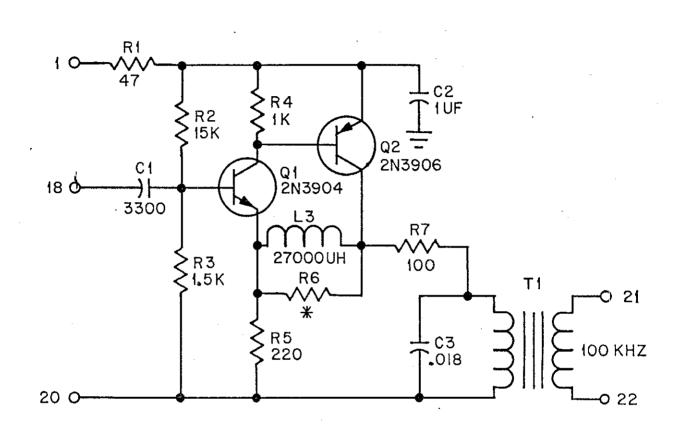


- 7.3.2 100 kHz Output Amplifier -- The pcb (A20) has an output amplifier capable of supplying 1 V RMS into a 50 **Q**hm load.
- 7.3.2.1 The transistors Ql and Q2 form a two-stage commonemitter, collector-tuned amplifier with negative feedback. The feedback ratio is determined by R5 and R6, which are used to set the output level. The output amplifier has been designed to allow a minimum level change for a maximum load change.

		REVISIONS		-
ZONE	LTR	DESCRIPTION	DATE	APPD
		RELEASED	11-18-76	RD18

	UNLESS	LERANO			(6	\widehat{A}	USTR	RON INC.		-
<u> </u>	DEC	FRAC	ANG				AUSTIN.	TEXAS		ļ
	MATEI	RIAL:		l	HEMATIC KHZ (LIFIER		A
7-5		Caute			CODE ID		107.0	\		
FIG N	O CHECK	KKH1	11-30-76	2	NO 246	672	123 8	97357		
		BARKER		SCAL				SHEET	OF	

 E_{-}



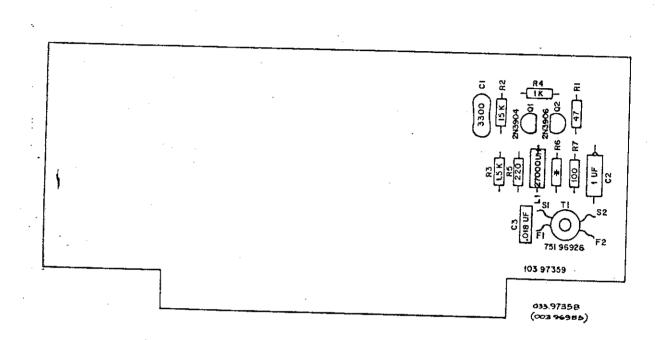
<u> </u>		
103 97359	1210D-01	A20
NEXT ASSY	USED ON	REF DES
	APPLICAT	ON

FOR OUTPUT LEVEL IV RMS INTO 50 OHMS.

В

		REVISIONS		
ZONE	LTR	DESCRIPTION	DATE	APPD
		RELEASED	11.23-76	ROB
	А	ADDED SI,S2,FI,F2 PER ECO. 1996	6.28.77	
		PARTS LIST CHANGE PER ECO 2271	4-14-73	803
		ADDED NOTE 1 PER ECO 3920	10.5.81	ensis

		LERANO THERWISE S FRAC				At	JSTR AUSTIN,	ON INC.	:	<u> </u>
	MATER	IAL:			C BOAR UTPUT) KHZ	7
7-6 IG NO	ENGR CHECK	LLI] KOB	//-31-76 //-32-76	SIZE 2	CODE IDENT	2	103 9	7359	(C C
	DRFTMN			SCALE	1:1			SHEET	OF	\neg



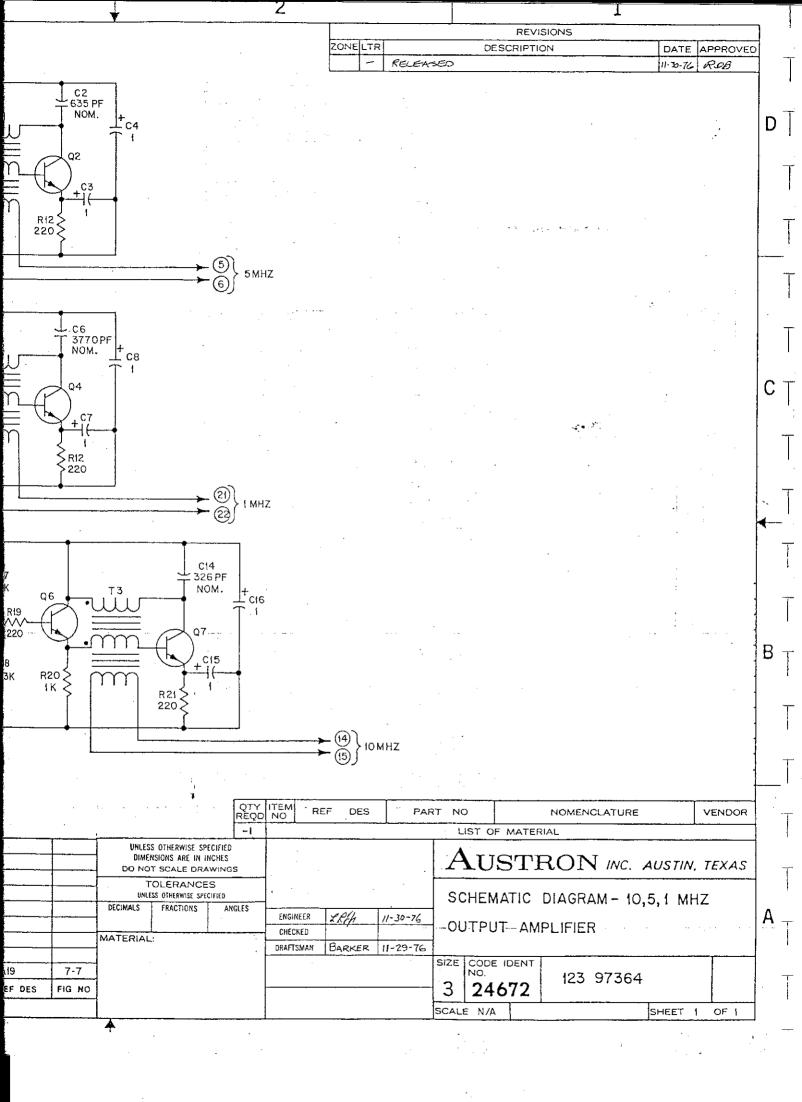
HEN INSTALLING TI, CONNECT "F1" LEAD

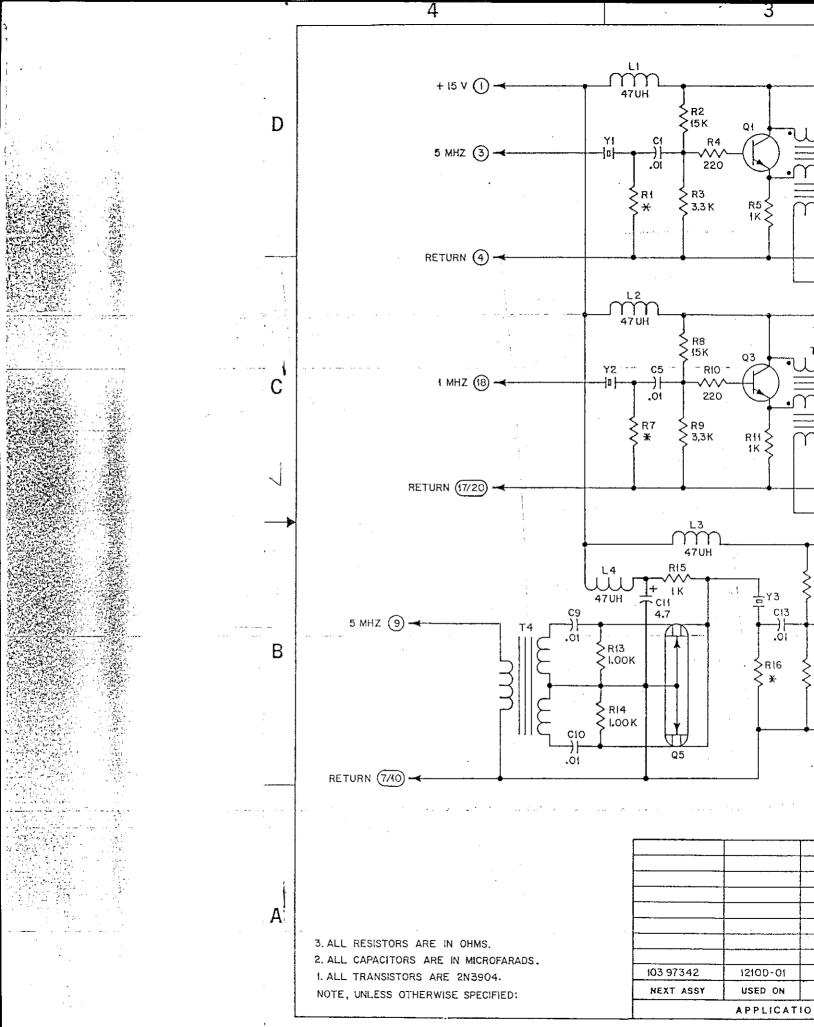
TES:

	T .	
		·
271 96208-1	12100-01	A20
NEXT ASSY	USED ON	REF DES
	APPLICATI	ON

	~	_		_	Yerren	_	_				_
	MARKUAL	MARGUAL PARTS LIST		MPEL 1	RUDEL 12100-01	7				24 DCT 84	
ASSEMBLY PC BUARD ASSY-100 ASSEMBLY NUMPER 10397359 REFERENCE DESIGNATOR PREFIX GUANTITY I EA	PC BUA NUMPER DESIGN	PC BUAKÛ ASSY-100 UMFER 10397359 DESIGNATOR PREFIX I EA	7-100кн2 39 СЕГХ 1A	н2 (30ТР 1A20	жыг фотрот <i>ам</i> р : 1820						
REF DES	D TARA	PAPT DESCRIPTION	MO1.				AUSTRUM PART	PART	4	MFG PART	FIC
C.1	3300 P	3300 PF 500V	4 C A P	AP DIP MICA	ICA		603000-0332	1332	CM06F0332303	32,403	81349
C.2	1 0	UF 35V	ب	AP TAMI			608017-0105	5010	CS138F105K	05K	X 3 34 U
C3	. U18 UF	F 50V	10 CAP	CAP FILM			607050-0183	183	71901An	7190146183k500AX	91002
t. 1	27000UH	I	CHUKE	نفة			751104-0273)273	MS90537-66	100	9000K
 	0.31× 10-92	26-01	XSIR	SNAN	S SH		7022n3964	74	243904		アナグース
0.2	0.31w IU-92	10-92	XSIR		SSH		702243906	90	2N3906		K4CIN
K.1	KES FXD COMP	COMP	4	47 UHM	M 7 / 7	10*	651102-0470	1470	KC076F470K	70K	21344
R2	RES FX0	COMP	T	ς. ×	1/4%	}0ſ	651102-0153	1.53	RC076+153K	53K	91349
ሞ	XES FX	XO CCMP	Ţ	1.5 K	1/414	1() [651102-0152	1.52	RC076r152K	52K	ガサウース
R 4	RES FX	o Cümp	~	¥	1/44	10%	651102-0102	7.05	KCO 76F 192K	02K	81.54ÿ
ጽን	RES FXD COMP	D COMP	~	20 UH	220 UHM 1/4W	104	651102-0	1221	RC076+221K	X	ア15年ン
よ ひ	SELECT	SELECTEU1/4W	10 RES FXD COMP	FXD C	OMP		651102-SEL	11			
R.7	KES FXO COMP	O COMP	-	HD 00	100 UHM 1/4w	%OT	551102-0101	101	RC076F101K	0.1×	カサヤドス
TI	IRAHSFI	TRANSFURMER TOOKH	00KHZ				75196926	•			24672

- 7.3.3 10 MHz, 5 MHz, and 1 MHz Output Amplifier -- The Output Amplifier (Al9) contains three similar amplifiers, plus a multiplier. This description will consider the 10 MHz amplifier.
- 7.3.3.1 Transformer T4 and dual transistor Q5 (along with their associated componenets) form a push-push doubler. The resulting 10 MHz signal is filtered by one-third (1/3). Since the signal from the emitter follower (Q6) is feed to the common-emitter, collector-tuned amplifier (Q7) through a winding on transformer T3, the stage has ac feedback. This amplifier is capable of supplying 1 V RMS into a 50 ohm load and is releatively insensitive to load changes.





		REVISIONS		
ZONE	LTR	DESCRIPTION	DATE	APPD
	_	RELEASED	11-18-76	ROB
	Α	ECO 1991: C14A WAS 56PF	6-28-77	ATOB.
	B	REVISED PARTS LIST PER ECO. 2886	4-16-79	ROB
	C	ADDED NOTE I PER ECO 3691	4-16-81	ROR

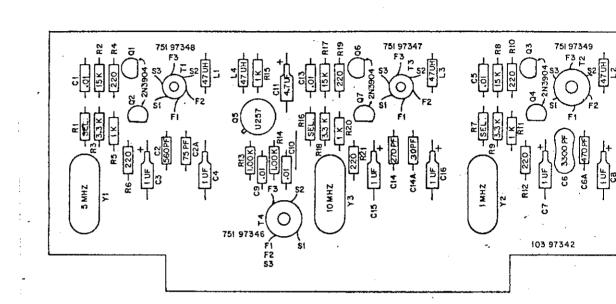
TOLERANCES
UNLESS OTHERWISE SPECIFIED AUSTRON INC. FRAC ANG AUSTIN, TEXAS PCBOARD ASSY-10,5,1 MHZ MATERIAL: OUTPUT AMPLIFIER SIZE CODE IDENT
2 NO 24672 A19 7-8 103 97342 ENGR REF DES FIG NO CHECK SCALE |:/ OF SHEET

C

P

A





I. R 13 AND R14 ARE IK 1/8W 1% RESISTORS NOTES:

271 96208	1210D-01
NEXT ASSY	USED ON
	APPLICAT

ASSEMBLY PCH ASSY 10,5,1MHZ COTPUT AMP ASSEMBLY NUMBER 10397342 REFERENCE DESIGNATOR PREFIX 1A19

IY I EA	
UUANTIIY	•

8 T	FARE DESCR	IPILIN			AUSTRON PART	NEG PART	F)(
C1 C2	AP CERA A ELECTED	XL X7R .01 CAP DIP	UF 100V FICA	10%	601205-0103	CK12BX103K	81349
C3	UF 35	V 10 CAP JAME			608037-0105	CS13HF105h	45 EX
C4	1 UF 35	V 10 CAP			608017-0105	CS138F195K	↑ 5
C5	P CERA A	XL XTR .01 UF	UF 100V	XOT	601205-0103	CKIZEXIDBE	カーストン
	SELECTED	CAP DIP MICA	MICA				
C.7	UF 3	V 30 CAP TANT			608017-0105	CS138F105K	147
63	1 OF 35)	10 CAP			608017-0105	CS136F1.05N	1
60	CAP CFRA AX	L X7R .01	UF 100V	10°4	601205-0103	ChiZhalo3k	240.00
010	AP CEPA	L X7K .01	UF 100V	30 T	601205-0103	CK 12HX J D 3K	64547
C11	۲,				608017-0475	CS135F475K	F4550
C12	001 USED						1
C13	CERA A	XI. X7P .01	UF 100v	10%	601205-0103	CK12EXTUBK	₩ 4 + 1 × 5
C14	SELECTED	CAP LIP	MICA				i
(15	UF 35	1.0			608017-0105	CSIBBFIOSE	7 4 5 T X
C16	1 UF 351	V IC CAP TANT			608017-0105	\$13FF105	41344
۲1	HI) 25	CHIKE			751101-0470	1025-60	UNVER
1.2	47 HH	CHOKE			751101-0470	1025-60	このおかか
L3	40 14	CHÜKE			751101-0470	1625-60	00865
L.4.	4/ (9)	CHUKE			751101-0470	1025-60	CORSE
<u>ن ا</u>		STR	NPNS SH		7022N3904	2N3904	61349
7.7	10-9	ASTR			7022N3904	50.390.4	8134 ⁴
63	1x 10-9	STR			7022n3904	2n3904	X 3 3 4 4
0.4	25-11 KTE 0	SIR	45 SH		7022M3964	2N3904	335CX
©2	1981 FF	I. TRANSISTOR			1020251	0257	17695
90)	<u>) </u>	XSTR	IDDNS SH		7022N3904	2N3904	8134
13	3	XSTR	NPAS SH		7022N39U4	21,3904	81349
x2	FX()	ip 15 K	1/45	10%	651102-0153	RCU76F153K	81344
æ 33	ES FXD	m)	1/48	%OT	651102-0332	RC076F332K	- 1
¥4	rs fXD	220	OHW 1/4W		651102-0221	RCOTGFZZIK	かせがゴス
π. •	ES FXD C	- *	1/45		651102-0102	RC076F102K	81344
x x	RES FXU COM	4P 15 K	1/4%	10%	651102-0153	RC076F153K	グサケーダ
	;.						

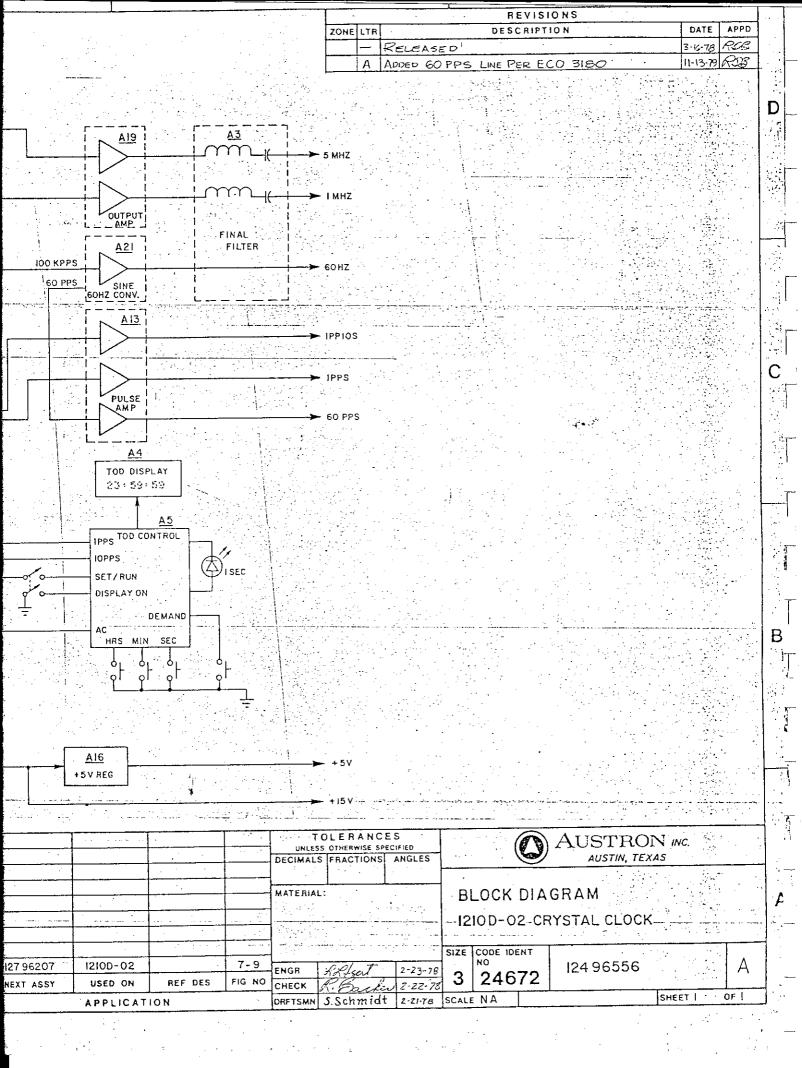
ASSEMBLY PCB ASSY 10+5+1MHZ FIUTPHT AMP ASSEMBLY NUMBER 10397342 REFERENCE DESIGNATUR PREFIX 1A19

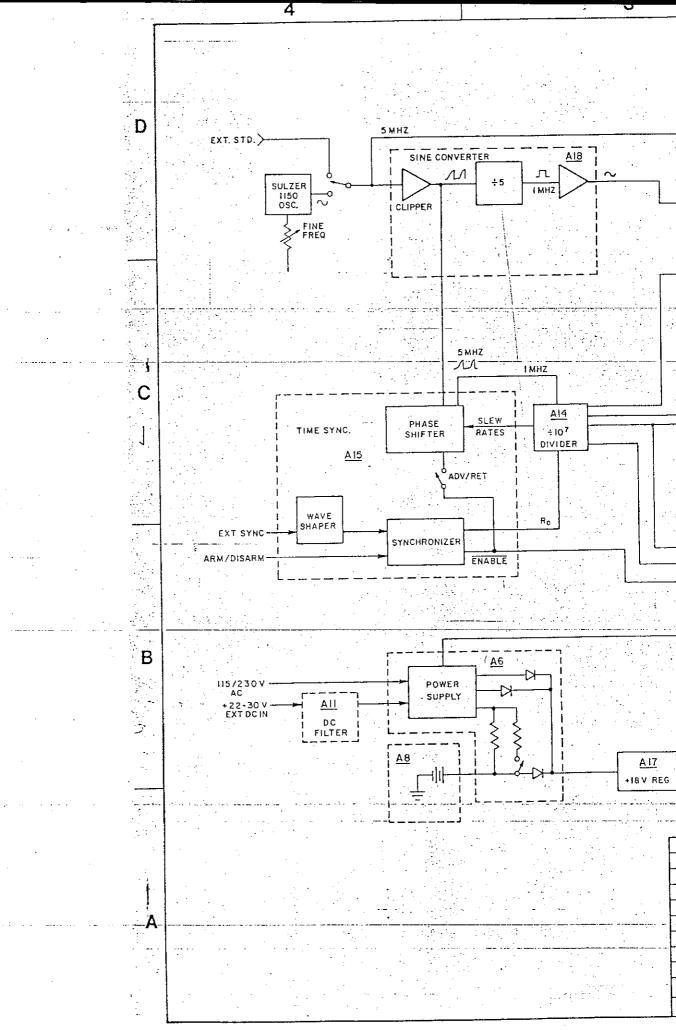
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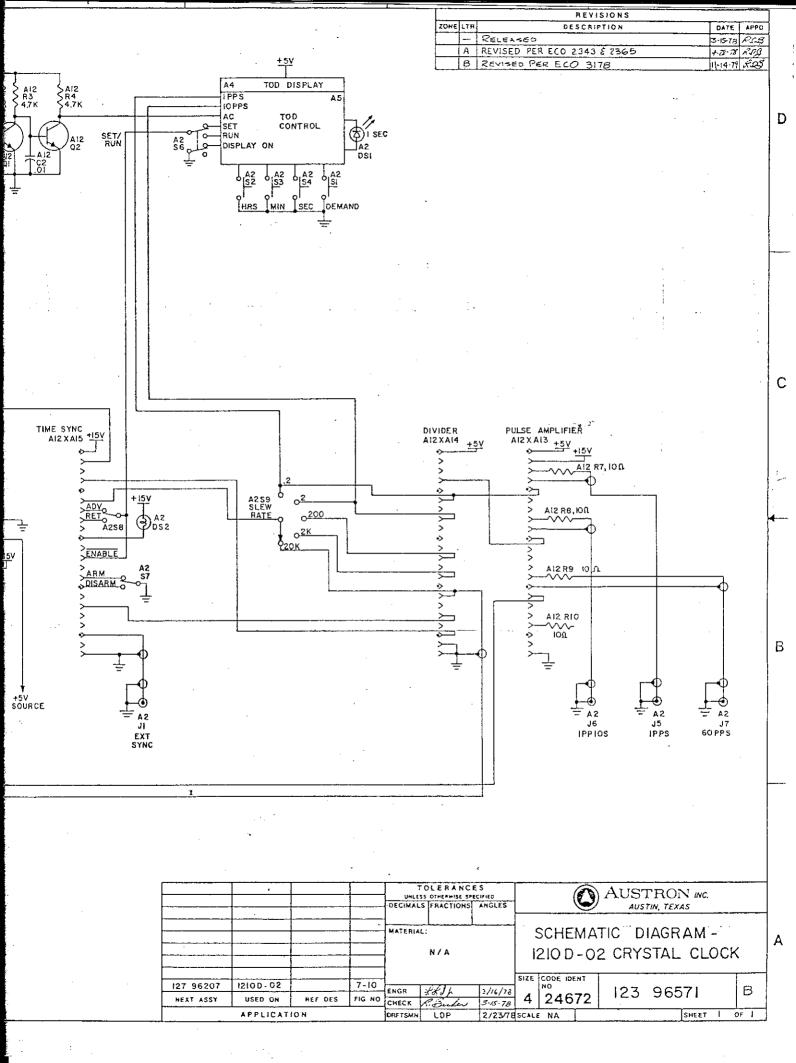
(COME)

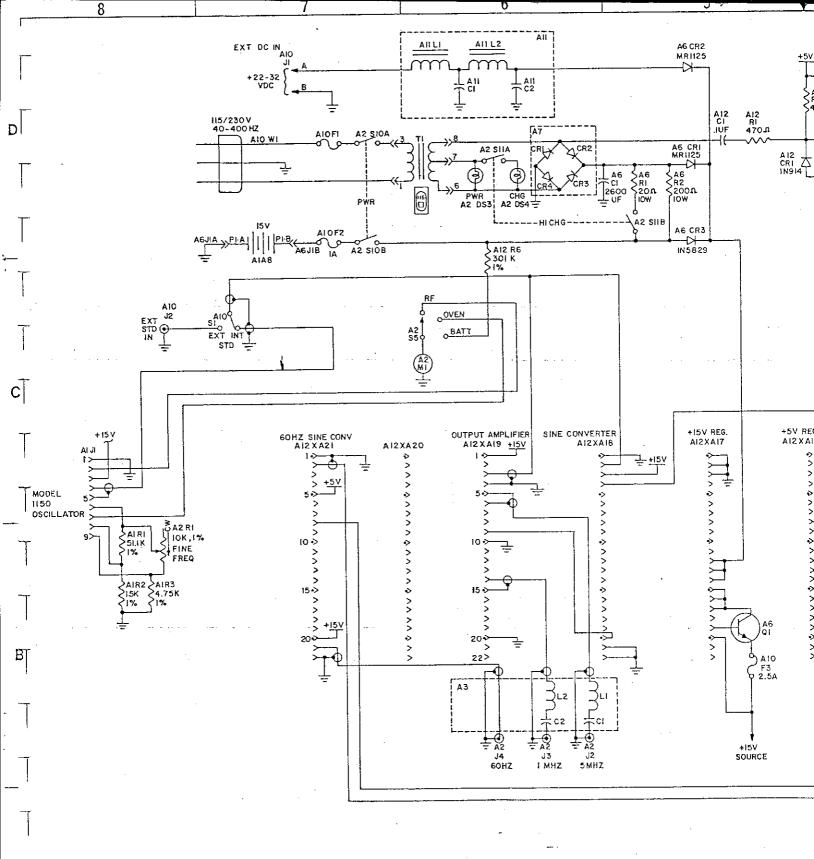
REF UFS	PART DESCRIPTION	AUSTRUN PART	MFG PART	FIC
810 810	RES FXU COMP 220 (HM 1/4M 10%	651102-0221	RC076F221K	41343
R11.		651102-0102	RC076F102K	X 3 3 4 5
R12	220 OHM 1/4W	651102-0221	RC076F221K	81349
K13	1 K 1/8W 1 RES FXD FILM	653001-1001	CI41K1%	24546
ሉ]4	1 K 1/8m 1 RES FXU FILM	653001+1001	CT41K1%	24546
k15		651102-0162	RC076F102K	81,544
*15	SELECTEUI/4W 10 RES FXO COMP	651102-SEL		
R17			RC076F153K	54% [%
K18	KES FXD COMP 3.3 K 1/4% 10%	651102-0332	RC076F332K	K1344
€19			RCU76F221K	61349
R20			KC07GF102K	67618
Ř21		651102-0221	RC076F221K	81349
11	TRANSFURMER-5MMZ OUTPUL W/FEFDSK	75197348		24672
12	XFMR-1 MHZ UNIPUT W/FEEDFACK	75197349		24672
13	XFAR 10 FMZ GUTPUT W/FEFDBACK	75197347		24672
14	XFMR WIDE BAND PHASE SPLITTING	75197346		24672
۲۱	XIAL, 5.000000 MHZ FILTER	75285000000	4051122-0	14306
Y2	XIAL, 1.000000 MHZ FILTER	75241000000	4051122	74306
۲3	X14L+ 10.000000 MHZ	752416006000	4051122	74306

7.4.1 The model 1210D-02 Portable Crystal Clock has 5 MHz, 1 MHz, and 60 Hz sinusoidal outputs, as well as 1 PPS and 1 PP10S pulse outputs. These outputs retain the specifications detailed in section 1.3. This version is the same as the model 1210D, with the addition of a 60 Hz Sine Converter pcb (A21), as well as the modification of the Divider pcb (A14), the Pulse Amplifier pcb (A13), and the Final Filter pcb (A3).



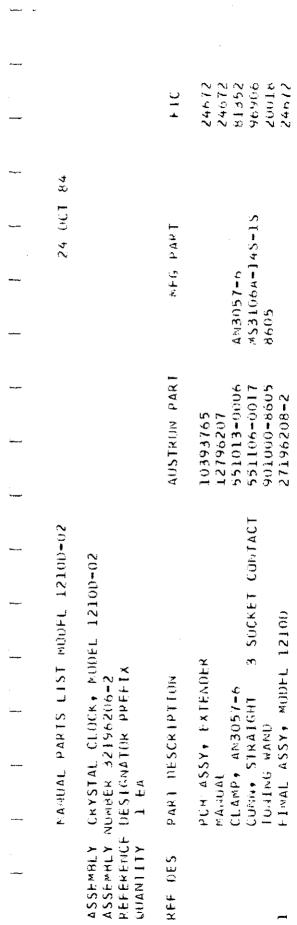






- 3. A6QI IS AN MJE3055.
- 2. AI2Q1 AND AI2Q2 ARE 2N3904.
- I. ALL RESISTORS 1/4W 10% UNLESS OTHERWISE SPECIFIED.

NOTES:



ASSEMBLY ASSEMBLY REFEMENCH QUANTITY	FIMAL ASSY+ MUDE NUMBER 27196208-2 H DESIGNATOR PREFI I EA	JEL 12100 -2 :Tx 1			
REF DES	PART DESCRIPTION	N.	AUSTRUN PART	MFG PAKT	P.1.C
۵1	RUT USED				
A2		01 12160	10996193-02		
ν3	SY+ 0		10396735-1		24672
A4	(A)	IS	10396186		24012
45	PC# ASSY 1.0.H.	CONTROL	10396188		24672
から	4SS4		11096211		24672
47	YSS1	RIDGE	10394664		24072
48	CK	ASSY	71296071		21942
40	.50	(70-85 DEG)	30296818		24612
Alt	¥.	ж.	10996195		24672
Δ1.1		LIFK	10396200		24872
A12		INTERCONNECT	10396769		24672
A13	\mathcal{I}	ULSE AMPLIFIER	10354595-3		24672
4 I 4	188Y+	DIVIDER	10396614-1		24013
A15	1-41	SYMC	1039461B		240/5
416	SSY	+5 VOC REGULATUR	10374607		27942
A17	+ 15	DC REGULATOR	10396197		24072
A]x	SIE	COMVERTER	10396025-1		24072
415	HIF	ANPLIFIER	10396031-1		24672
A26	MOT RISHD				
A21	PC0 455Y., 60HZ	SIME COMVERIER	10396517		24472
I div	COVER. FROM		00794304		24072
FP2	COVER-INSTRUMENT	<u>-</u>	00746209-1		24672
RUN	C D V fr + I N S T A U P F N	!	2-60296100		24572
N.P.4	CAR RECTANGULAR		01096753		24617
AP5	.171×1/4×1/2	SPACER ROUMD	520830-0005	9228-55171-0	0.6540
ክ P ድ	(1X1/4)	SPACER ROUND	520830+0005	9228-85171-0	06540
MT 7	<u>, </u>	SPACER ROUND	520830-0005	9228-55171-0	06540
RPS	71×174		520830-0005	9226-55171-0	Uh540
648	MASHER, PRELUAU		14196020		740.77
2010	5. 4.				. 6

24672 24072

24072

02096741 12796741 02096741

PRELEAD PRELUAD PRELOAD

KASHER WASHER . * ASHER.

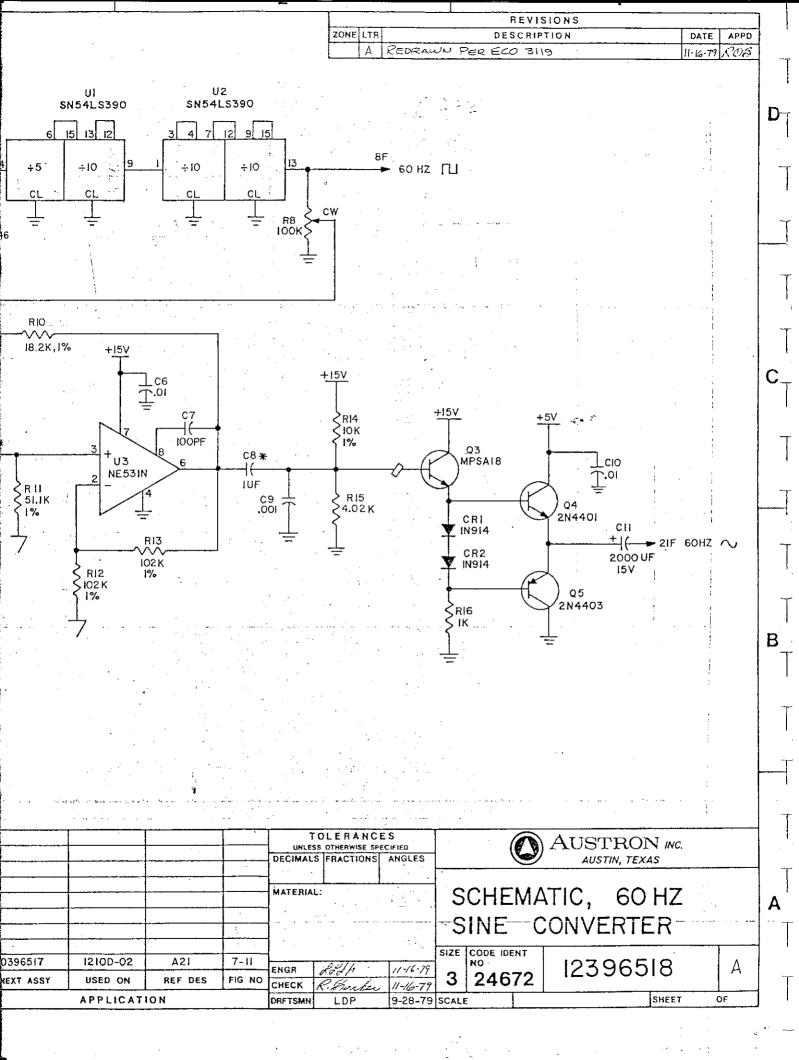
MP10 MP11 MP12

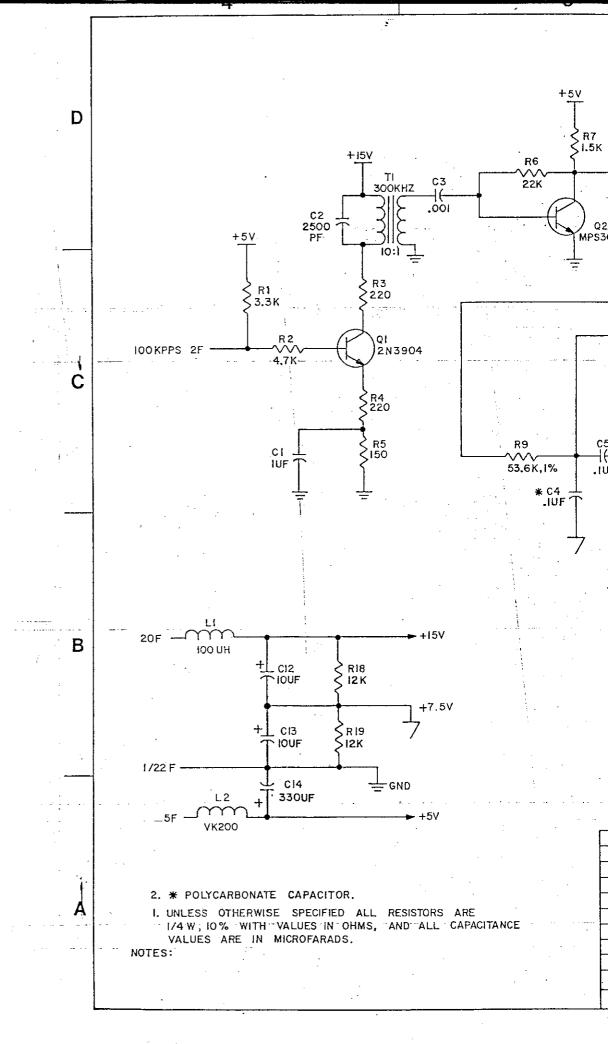
ASSEMBLY FINAL ASSY+ MODEL 1210D ASSEMBLY NORMER 27196208-2 REFERENCE DESIGNATUR PRÉFIX I QUANTITY LEA

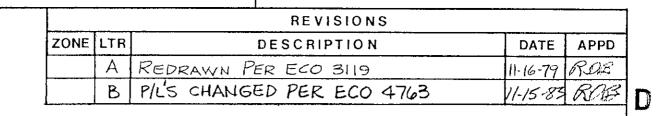
(CON))

	AUSTROM PART	**************************************	D14
TALLER	520205-0005	1084-15018	14514
FULL	520205-0005	lok4+1501E	14519
SHUCK MOUNT BOSHING	520205-0005	1084-15018	14519
HUCK MOUNT BUSHING	520265-0005	1084-1501B	14519
HICK DKIVE NASHER	520205-0010	10841500	41241
SHUCK URIVE WASHER	520205-0010	16841500	51 4 51
OCK DRIVE MASHER	520205-0010	10K41500	14519
SHOCK DRIVE MASHER	520205-0010	10841500	14519
FUNDER - KUHBER WIHAKOWAKE	520210-2198	2198	63330
SUSPER - KUGBEK WIHARDWARE	520210-2198	2194	85530
HUMPER - KUBBER WIHAKUWARE	520210-2198	2198	65350
RUNTER DEBEK NIHAKUNAKE	520210-2198	2198	(2) 有限的

- 7.4.2 60 Hz Sine Converter -- This pcb (A21) generates a 60 Hz sine wave from a 100 kHz TTL input.
- 7.4.2.1 A 100 kHz square wave is applied to the base of Ql which is a common-emitter, collector-tuned amplifier, which is tuned to the third harmonic, 300 kHz. Therefore, there is a multiplication of three in this stage.
- 7.4.2.2 Transistor Q2 is a saturated clipper which is used to generate a fast fall time suitable for driving the TTL dividers.
- 7.4.2.3 The integrated circuits Ul and U2 are cascaded dividers which divide the 300 kHz signal to 60 Hz.
- 7.4.2.4 Op Amp U3 is a second-order VCVS bandpass filter which has a Q=10 with G=10 at fo=60 Hz. In a filter of this configuration the op amp and resistors R12 and R13 form a voltage controlled source. Resistor R12 and R13 set the gain while capacitors C4 and C5 and resistors R9, R10 and R11 set the center frequency as well as the Hi and Lo roll off response.
- 7.4.2.5 Transistor Q3, whose base bias is derived from resistors R14 and R15, is a driver stage for the push-pull, capacitor-coupled output amplifier, which is composed of CR1, CR2, Q4, Q5 and associated components. This amplifier is capable of supplying 1 V RMS into a 50 ohm load.



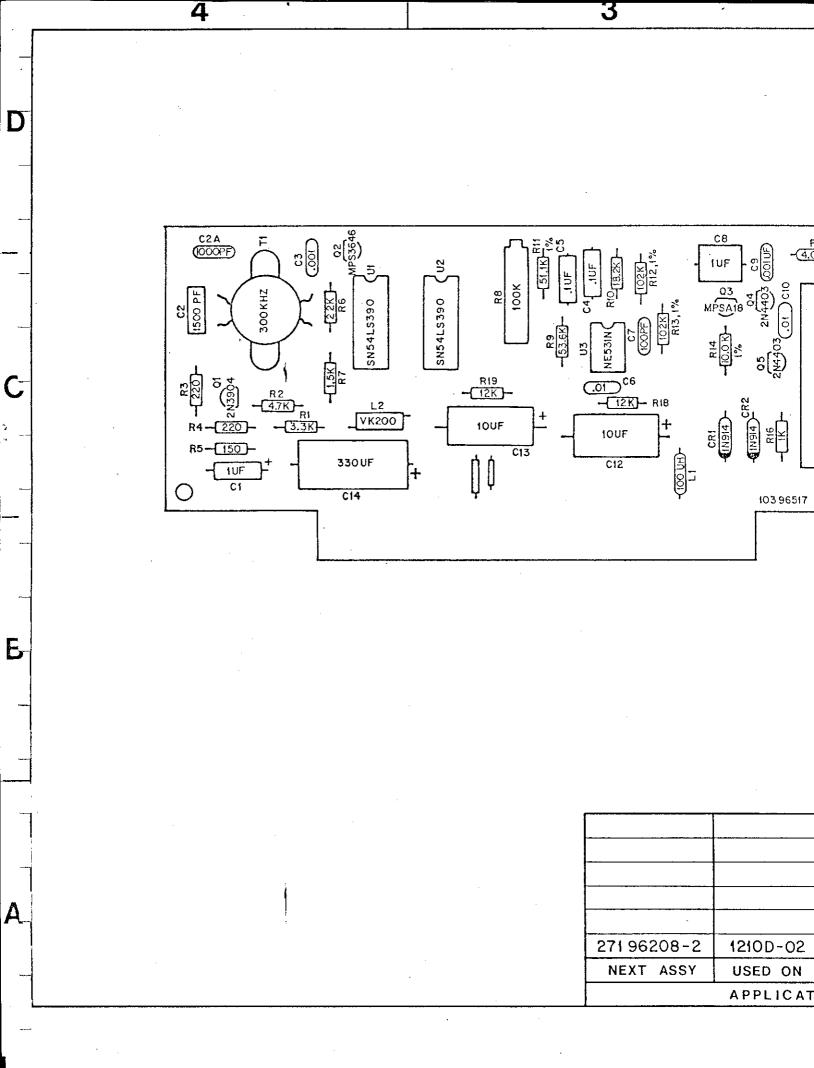




E

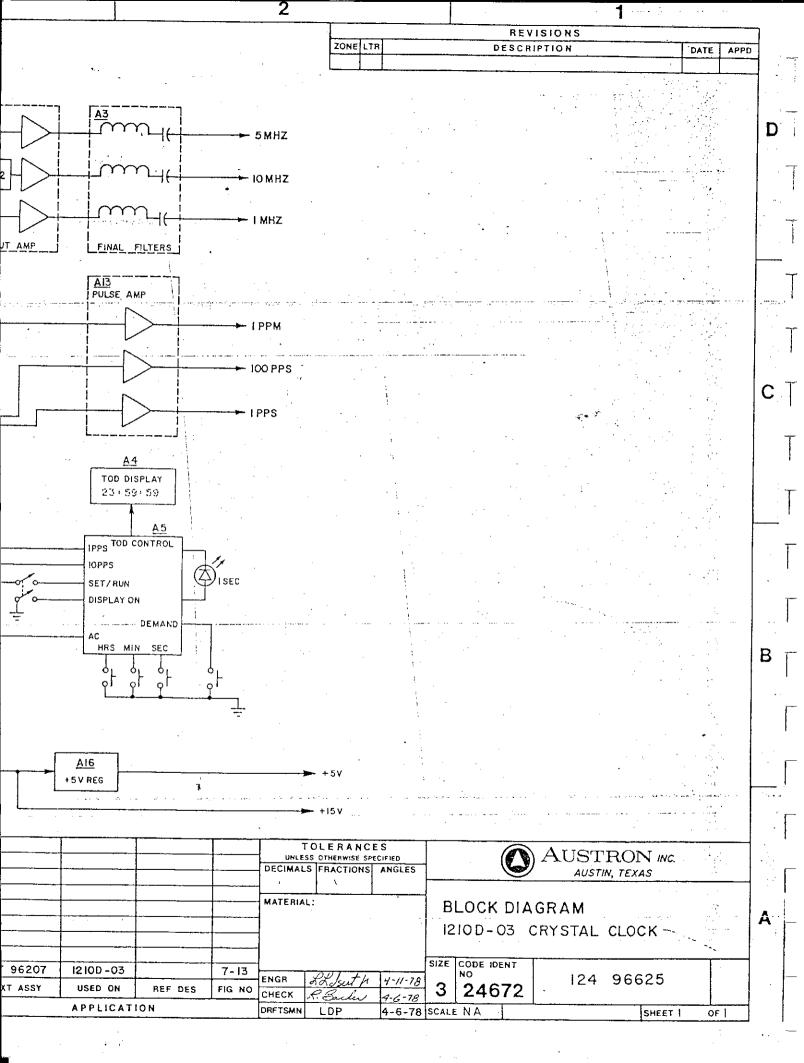
2000UF 15V C11

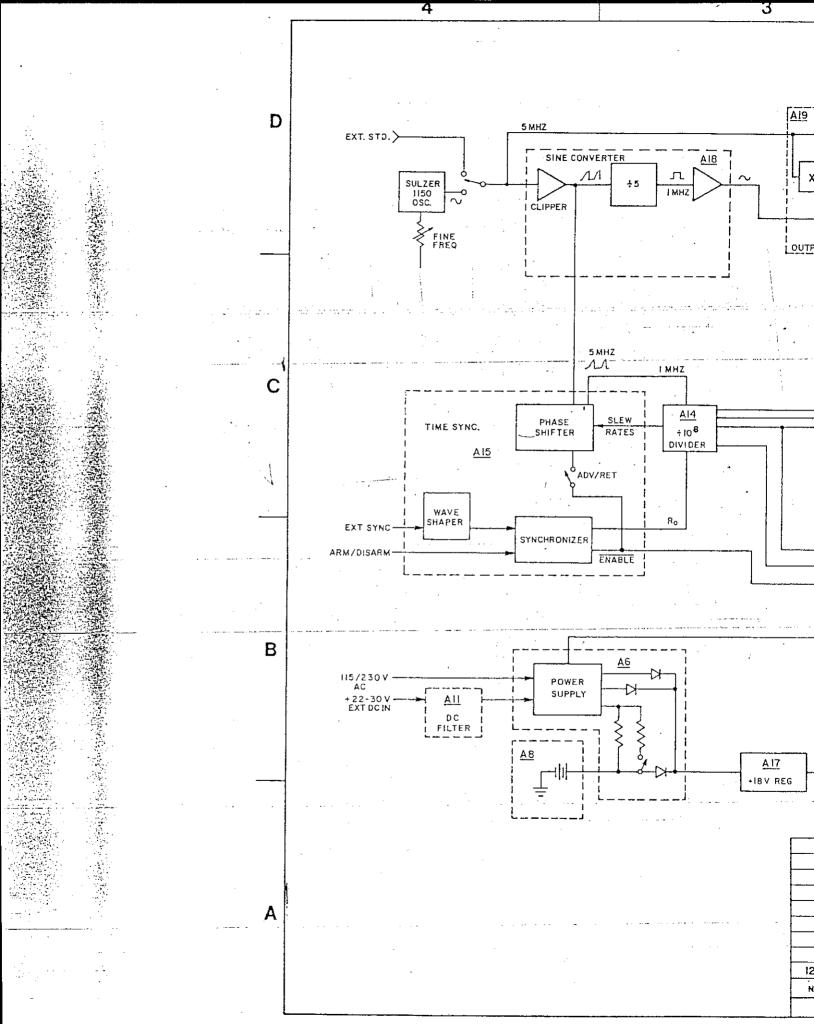
			LERANC THERWISE S		igwedge Austron inc.	
: 		DEC	FRAC	ANG	AUSTIN, TEXAS	
	1				PCBOARD ASSY,	
		MATER	HAL:		1 CBOARD ASSI,	
					60 HZ SINE CONVERTE	7
A21	7-12	1	,		SIZE CODE IDENT	~
REF DES	FIG NO	ENGR CHECK	Lough	11-16-79	2 No 24672 103 96517	B
O N			BARKER	2 11-16-79	SCALE NONE SHEET OF	= {

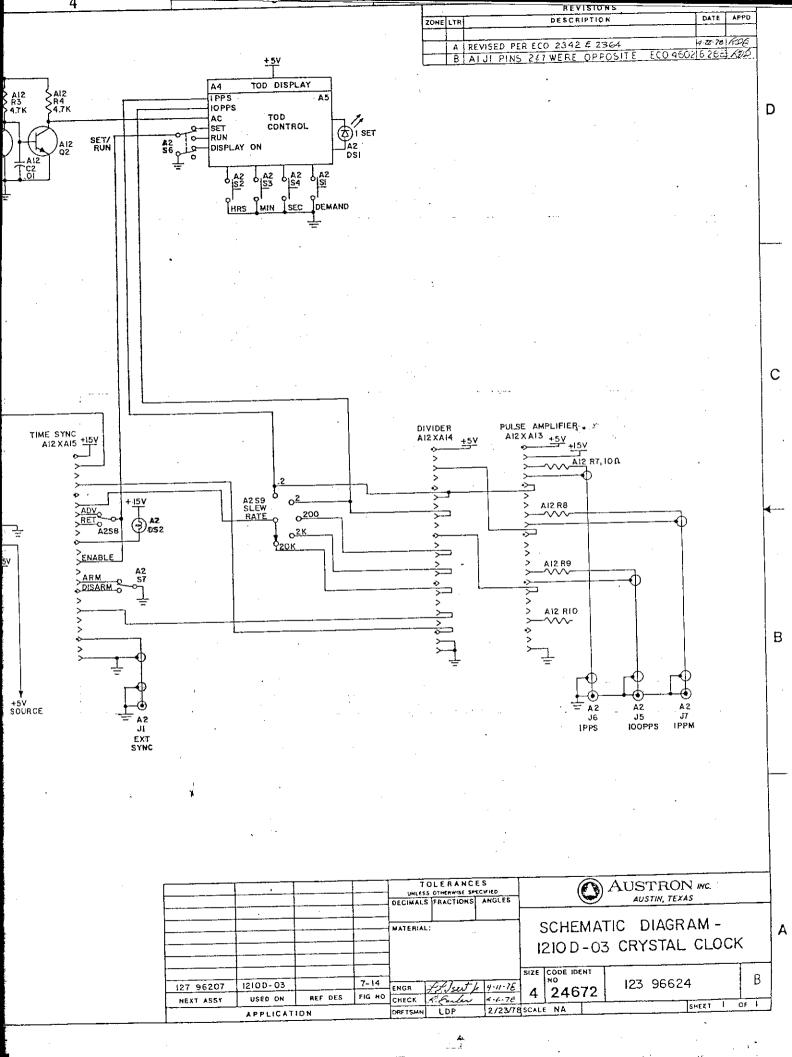


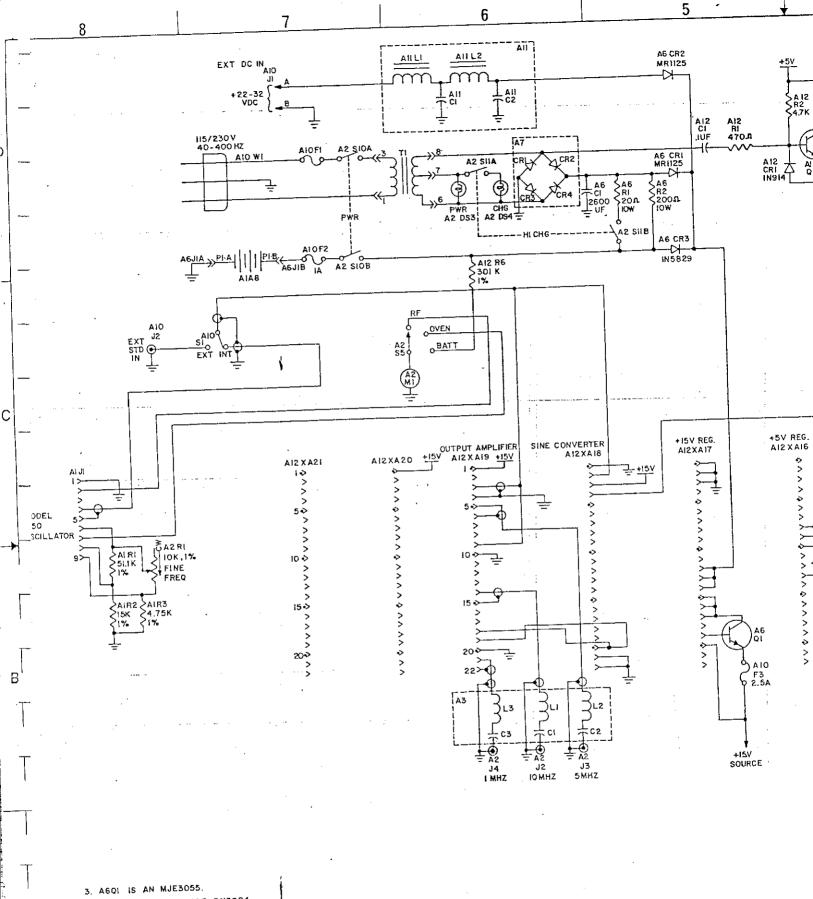
-	RAMUAL PARTS LIST	I PARTS L		((((n-02			MODEL 12100-02 24 OCT 84	24 ()	24 OCT 84	_
ASSEMBLY PCR ASSY., 60HZ SINF COASSEMBLY NUMBER 10396517 REFERENCE DESIGNATOR PREFIX 1A21 GUANTILY EA	PCR ASSY., 60H2 S NUMBER 10396517 DESIGNATOR PREFIX EA	Y., 60H 0396517 IOR PRE	2 SINF CO FIX 1A21	INF COMVERTER 1A21	FR	(CONT)	-				
REF DES	PART DESCRIPTION	SCRIPII	<u>2</u>			AUSTRU	AUSTRUM PART	<u> </u>	MFG PAST		FIC
R13	102 K	1/8M]	RES FXD FILM) FILM		65300	653001-1023	C14102k1%	ي. ج		24546
K]4	10 K	1/8W 1	RES FX	ES FXD FILM		65300	653001-1002	C [410]K1×	*		2454ú
R15	4.02K	1/8% 1	KES FX	ES EXD FILM		65300]	653001-4021	C144.U2K]%	× × ×		24546
816	KES FXD	はどりし	-		1/4W 10%		651162-0102	RCU feF1028	02k		81545
ķ] 7	KES FXO	こじゅか	12 K		1/4M 10%	10% 651102-0123	2-0123	KCU76F1238	238		645£0
x T	RES FXD COMP	COMP	12		1/4m 10%	10% 651162-0123	2-0123	KCUTGE123K	23K		H13549
	TRANSFU	UMFR+ 30	TRANSFURMER+ 300KHZ 10:1	_		75158118	118				24072
UI	IC DUAL	DECADE	IC DUAL DECAME COUNTER			7035NJ	7035N74L5390	SN (41, 5390m	200		くんどじつ
02	1C DUAL	DECABE	IC DUAL DECADE COUNTER				0350741.5390	SN74LS3900	20%		562TO
63	ІС нІ6н	SLEM K	IC HIGH SLEW RATE UPFRATIONAL AMPLE	NT I OPEAN	L AMPLF	703NF531V	5.31.V	NESSIN			18324

7.5.1 The Model 1210D-03 Portable Crystal Clock has 10 MHz, 5 MHz and 1 MHz sinusoidal outputs as well as 100 PPS, 1 PPS and 1 PPM outputs. The outputs retain the specifications detailed in section 1.3. This version is the same as the model 1210D, except that the Output Amplifier (Al9) has been replaced with the 10 MHz, 5 MHz, 1 MHz Output Amplifier (AUSTRON Part Number 10397342), which is covered under section 7.3.3. The Final Filter (A3) has been replaced by AUSTRON Part Number 10397355-1, covered under Figure 7-4. The Pulse Amplifier (A13) is AUSTRON Part Number 10394595-3 covered under section 4.9. The Divider (A14) has been replaced by AUSTRON Part Number 10396614-1.





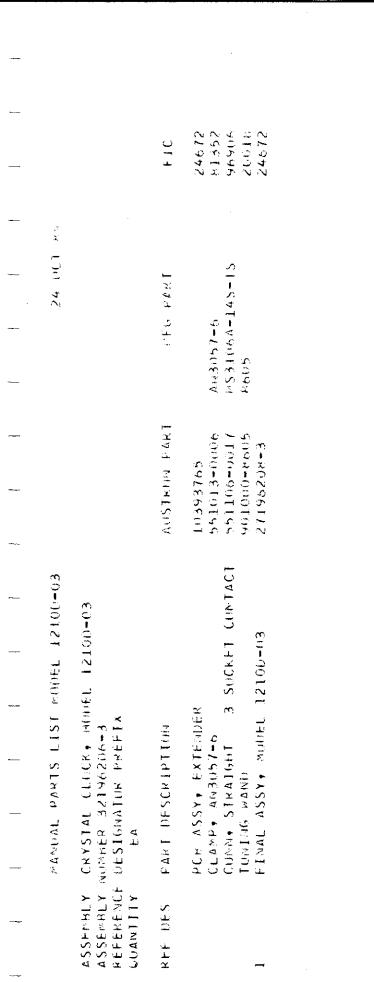




2. AI2Q1 AND AI2Q2 ARE 2N3904.

I. ALL RESISTORS 1/4W IO% UNLESS OTHERWISE SPECIFIED.

NOTES:

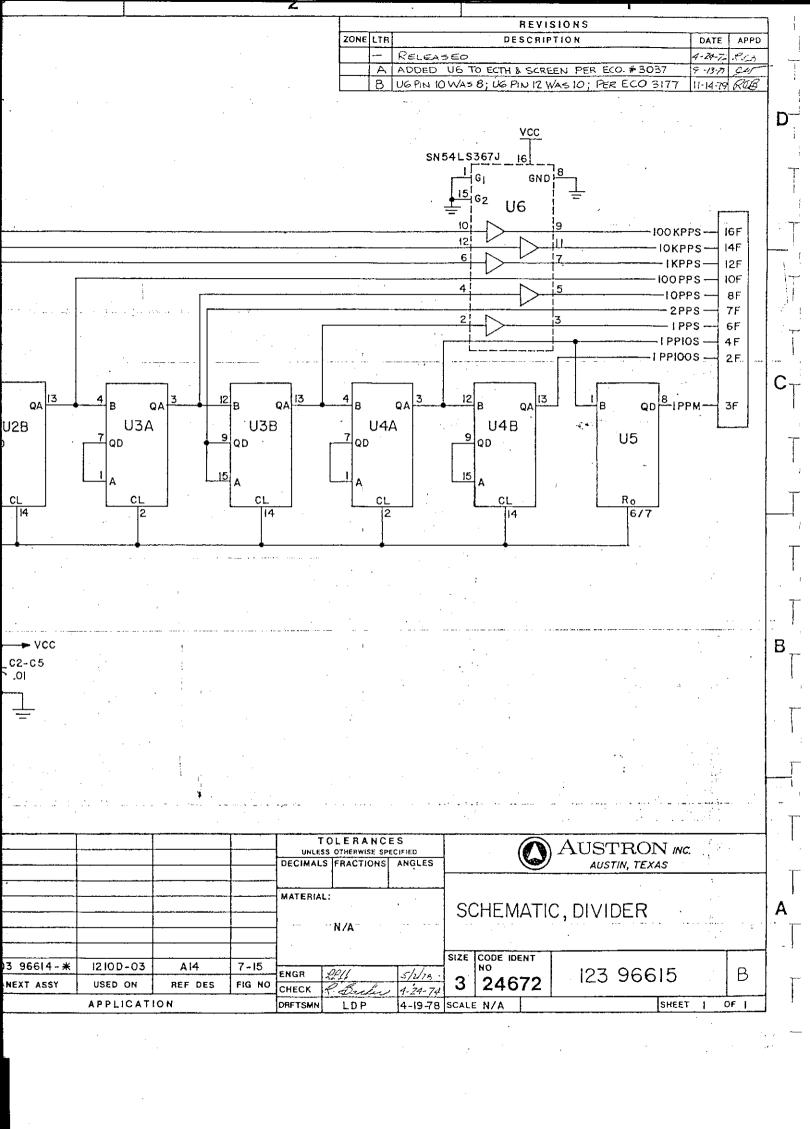


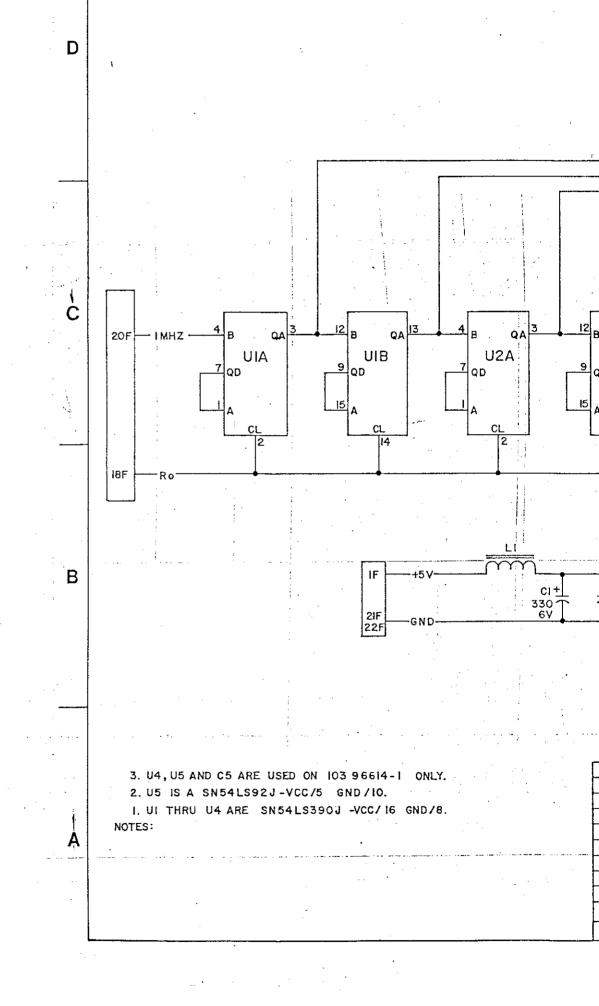
ASSEMBLY FIMAL ASSY, MUDEL 1210L-03 ASSEMBLY NOMBER 27196208-3 REFERENCE DESIGNATOR PREFIX 1 QUANTITY EA

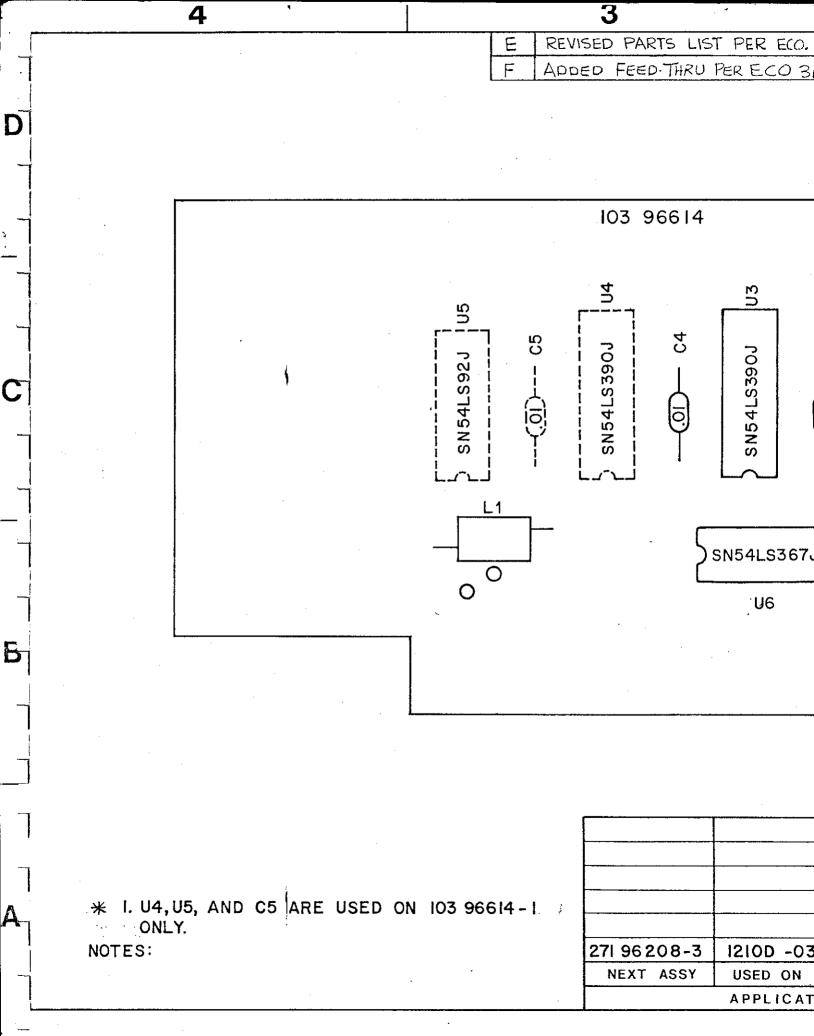
REF DES	PART OFSCRIPTION	AUSTOUN PART	TEAG DIS	F 1 C
δ.]	NOT USED			
24	PAMEL ASSY FRINT	10996193-3		24072
A 3	PCH ASSY. FIGAL FILTER	- 1		24672
9	ASSY, TOD DI	10396186		7447
AS	ASSY 1,0.0.	10396188		24672
Λé	PLOTE ASSY (A6)	11096211		7447
74	PC EHAPL ASSY-HRIDGE	10394664		24672
AA	HALTERY PACK ASSY	12096212		24672
64	XTLUSC+ 1150 (70-85 0EG)	30296818		24672
Δ10	PAMEL ASSY, RFAR	10996195		24472
4]]	PCR ASSY+ DC FILTER	10396200		52447
412	PCA ASSY INTERCONNECT	30396769		74612
613	FC BHARD ASSY-PULSE AMPLIFIER	10394595-3		24672
A14	PC HUARD ASSY. DIVIDER	10396614-1		24672
415		10394618		24672
9[0	RUARU ASSY	10394607		24672
717	155×	T0395197		24077
&~ ₹	ASSY. SIM	10396025-1		24472
A19	PCS ASSY 10.5.1342 BUTPUT AMP	10397342		24672
¥50	IISED			;
A21	(1451) ION			
Mp]	~	00794304		24672
5 d v	COVER-INSTRUMENT	00796209-1		24672
F.P.3	COVER-1MSTRUMENT	00796209-2		24612
70 %	BAP RECTANGULAR	01696753		24672
× 05		520830-0005	9228-55171-0	06540
9d-1	.171x1/4x1/2 SPACEP RUUND	520830+0005	9228±55171±0	0.6540
KP7		520830-0005	9228+55171-0	06240
ಚರಣ	1/4	520830+0005	9228-55171-0	1)6541)
かんだ		197096743		24672
MP10	SHER	02096743		24672
to J	σ.	14798650		24012
~ Z I d ~	SHFR	72096741		240.12

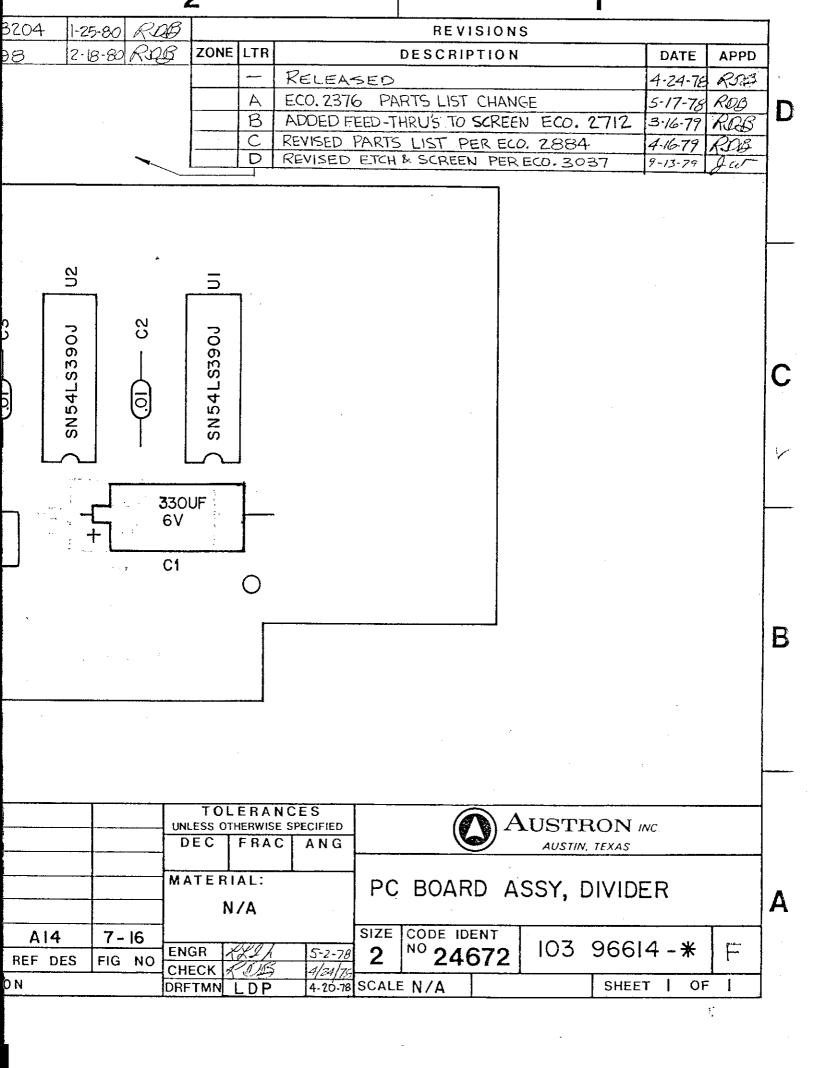
	~	<u></u>	_		_	-	_							1
	MANUAI	L PARI	MAMUAL PARTS LIST		*Ont. 12100-03	H-03					7	24 UCT 84	्रो अं	
ASSEMBLY FINAL ASSY, MODEL ASSEMBLY MOMRER 27196208-3 REFERENCE DESIGNATOR PREFIX OUANIITY EA	FINAL NUMMER • DESIG	ASSY. 27196 UATUR	FINAL ASSY, MODEL HUMMER 27196208-3 DESIGNATOR PREFIX EA	12100-03	0=03		((.)	(CONT)						
REF DES	ранг	PART DESCRIPTION	₽TIO∿				AUS	AUSTROM PART	AR T	-Z	MF W DART	<u>.</u>		31. 4
MP14	SHÜCK	SHOCK MODEST	HSIIISH	50%			520	520205-0005	05	1084-15018	50) ¤			14513
MF.1.5	SHUCK	SHUCK MOUNT	ONTHSOH	N.			520	529205-0005	65	1084-1501x	5010			34510
81 dw	SHIJCK	SHIJCK MOUNT	BUSHING	N.G.			5/0	620202-60008		1084-1501E	5.1			14519
Lidn	SHEICK	LWH(HA		NG.			520	520205-60005	S	1084-15018				34514
F-P18	SHUCK	SHUCK DRIVE	WASHER	œ			920	520205-0010	Ç	10641500	Ę			14517
61dw	SHEICK	SHFICK URIVE	WASHER	¥			520,	520205-6016	10	1137415.00	1			14519
MP20	SHUCK	SHUCK DRIVE	アカヘエデス	.			520	520205-0010	10	10361501	1 1			14510
N.P.2.1	SHOCK	L# 1VE	SHOCK LETVE WASHER	¥			520	520202-0010	10	100015				14519
MP22	#Hebile	♣ PUB.	RUSPER, PURBER WIHARDWAKE	HARIDIA	A.F.		520	52021C-2198	٩ ٦	7 C :				83430
FP23	TENESCHE I	ROMPER, ROSKER W/		HAKENAKE	4.K.F.		541	2512-012029	τ. .J.	ã <u>o </u>				83330
MD24	MUMPER.		RUPBER W/	HARUWBRE	5R E		6211	8612-012029	7. Jr	210r				0888 888 888
MP25	+ Madwina		PUBBER W/	HARDWARE	AR E		520	520210-2194	¥ ሙ	8617				83350

- 7.5.2 Divider -- The Divider PCB (Al4) contains provisions for four cascade dual-decade dividers, as well as a divide-by-six stage.
- 7.5.2.1 The 1 MHz TTL output from the Time Sync card is divided (by means of three cascade dual-decade dividers) to 1 PPS. This and other intermediate rates are used by the TOD Control (A5), the Time Sync (A15), and the Pulse Amplifier (A13).
- 7.5.2.2 The fourth dual-decade divider and the divide-by-six stage are used to generate the 1 PP10S, 1 PP100S, and 1 PPM respectively. These are special options, and used on the 10396614-1 assembly only.
- 7.5.2.3 The Hex Driver (U6) is used to buffer the slew rates applied to the front panel slew rate switch (1A2S9). This prevents the capacative loading of the switch from giving the dividers a false count.
- 7.5.2.4 The R_{g} signal originates on the Time Sync card (Al5), and is used to reset all dividers when synchronizing the clock to external source.









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